

B0L002S, U22-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 27.4660

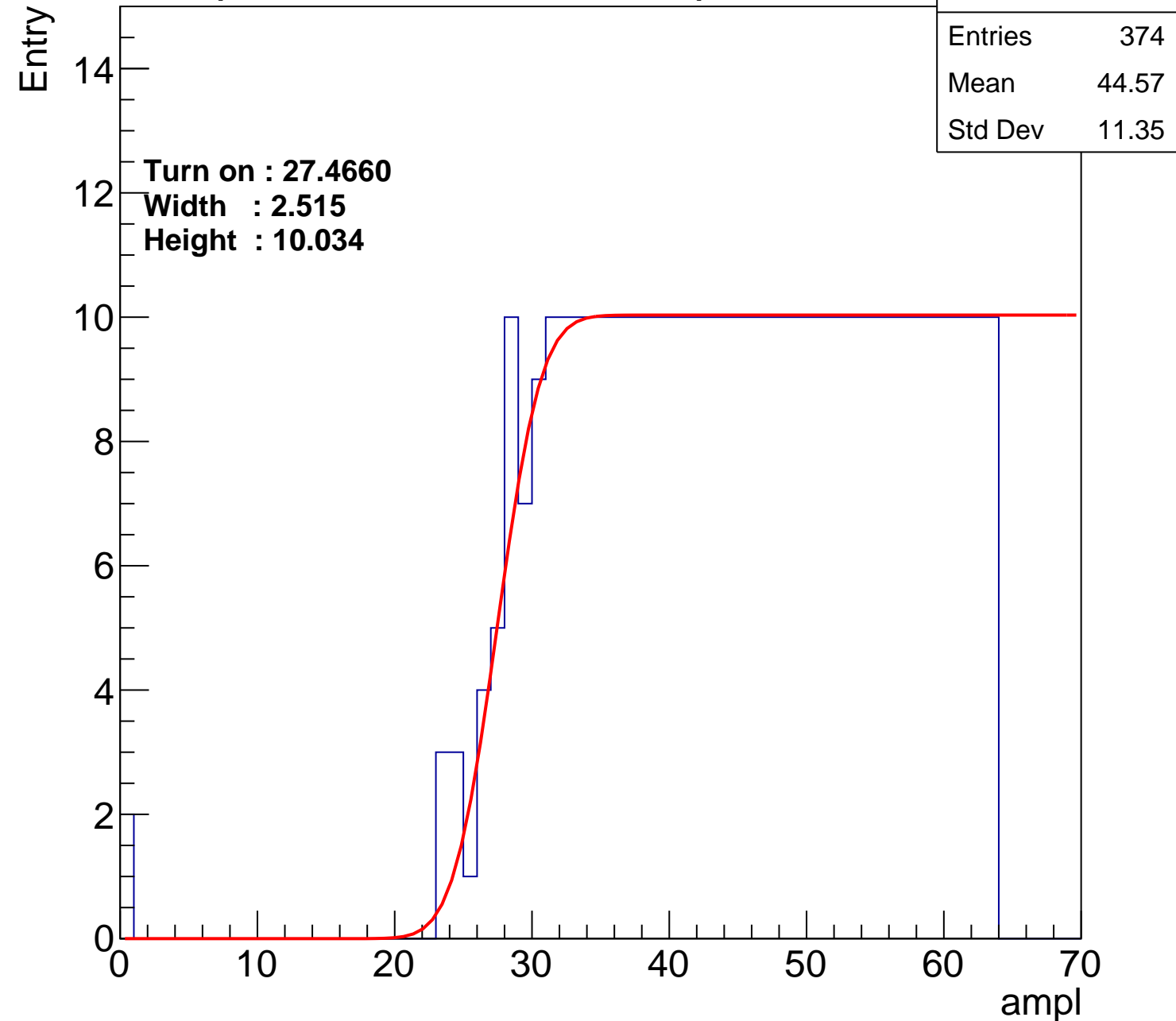
Width : 2.515

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch1

calib_packv5_042523_0143.root, FC#8, port C1

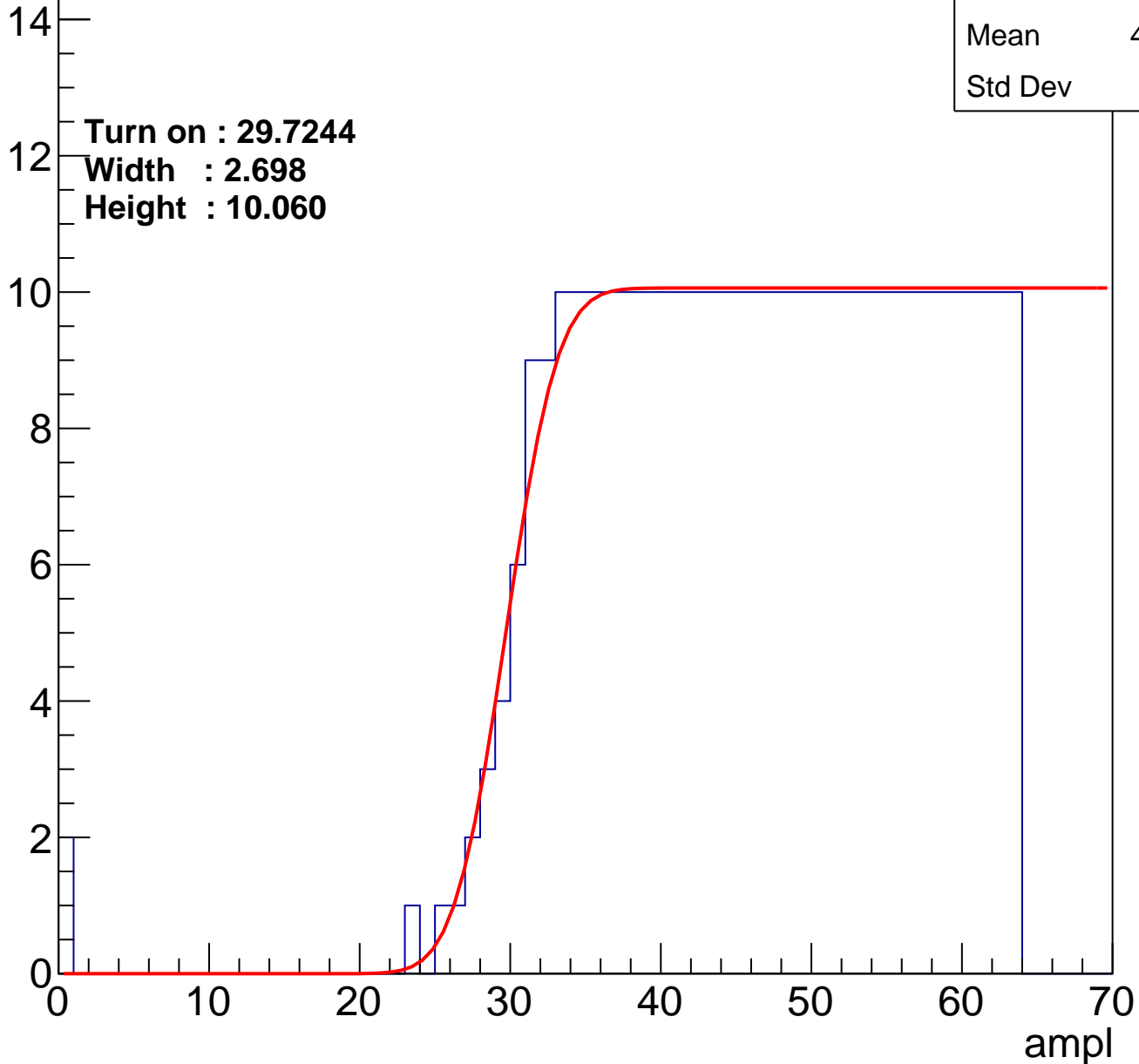
Entries	348
Mean	45.85
Std Dev	10.7

Turn on : 29.7244

Width : 2.698

Height : 10.060

Entry



B0L002S, U22-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Turn on : 28.6364

Width : 2.027

Height : 10.061

Entries	362
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Mean	45.26
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Std Dev	10.79
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B0L002S, U22-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.89
Std Dev	10.68

Turn on : 29.6715

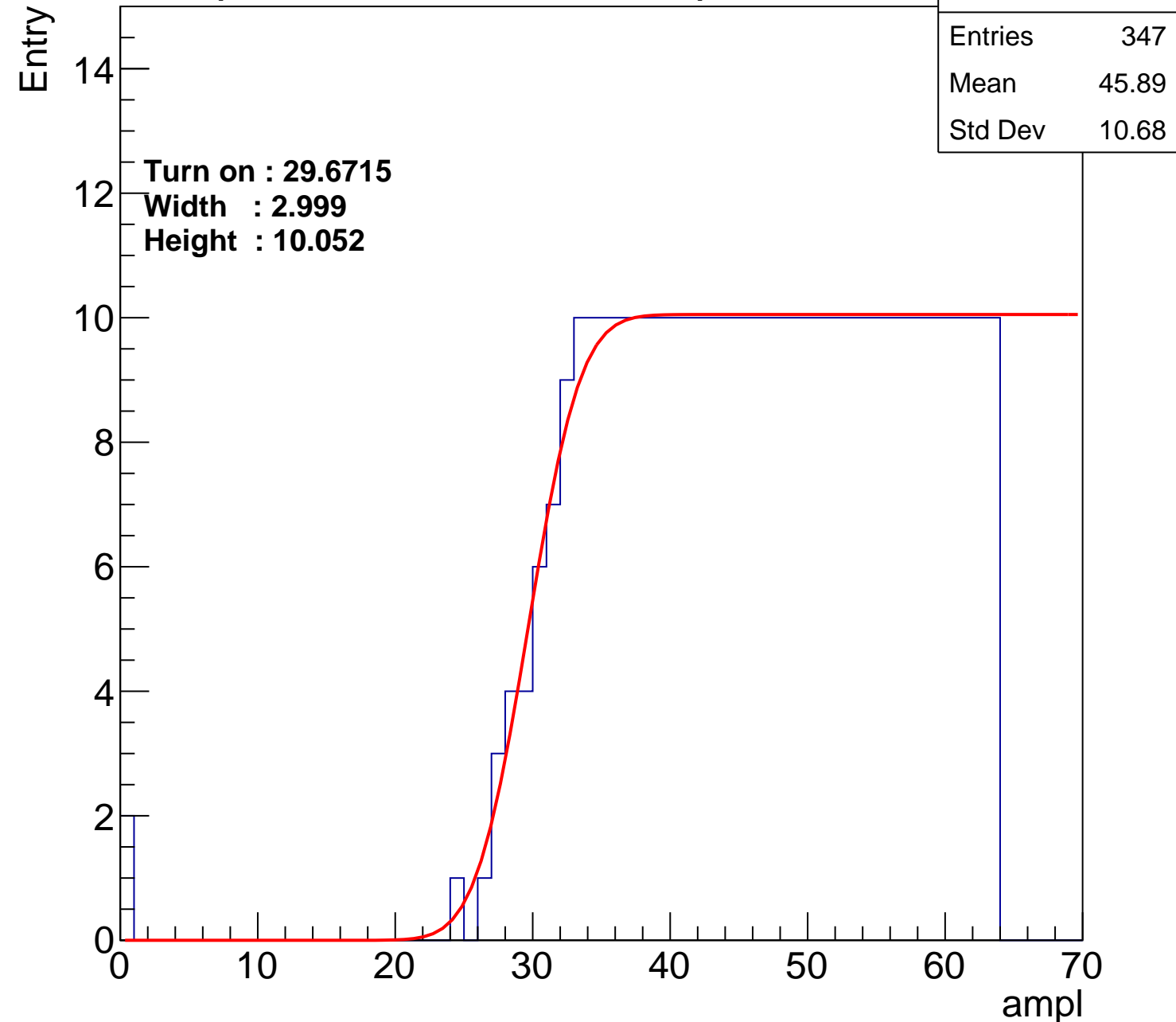
Width : 2.999

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.9
Std Dev	10.66

Turn on : 29.9379

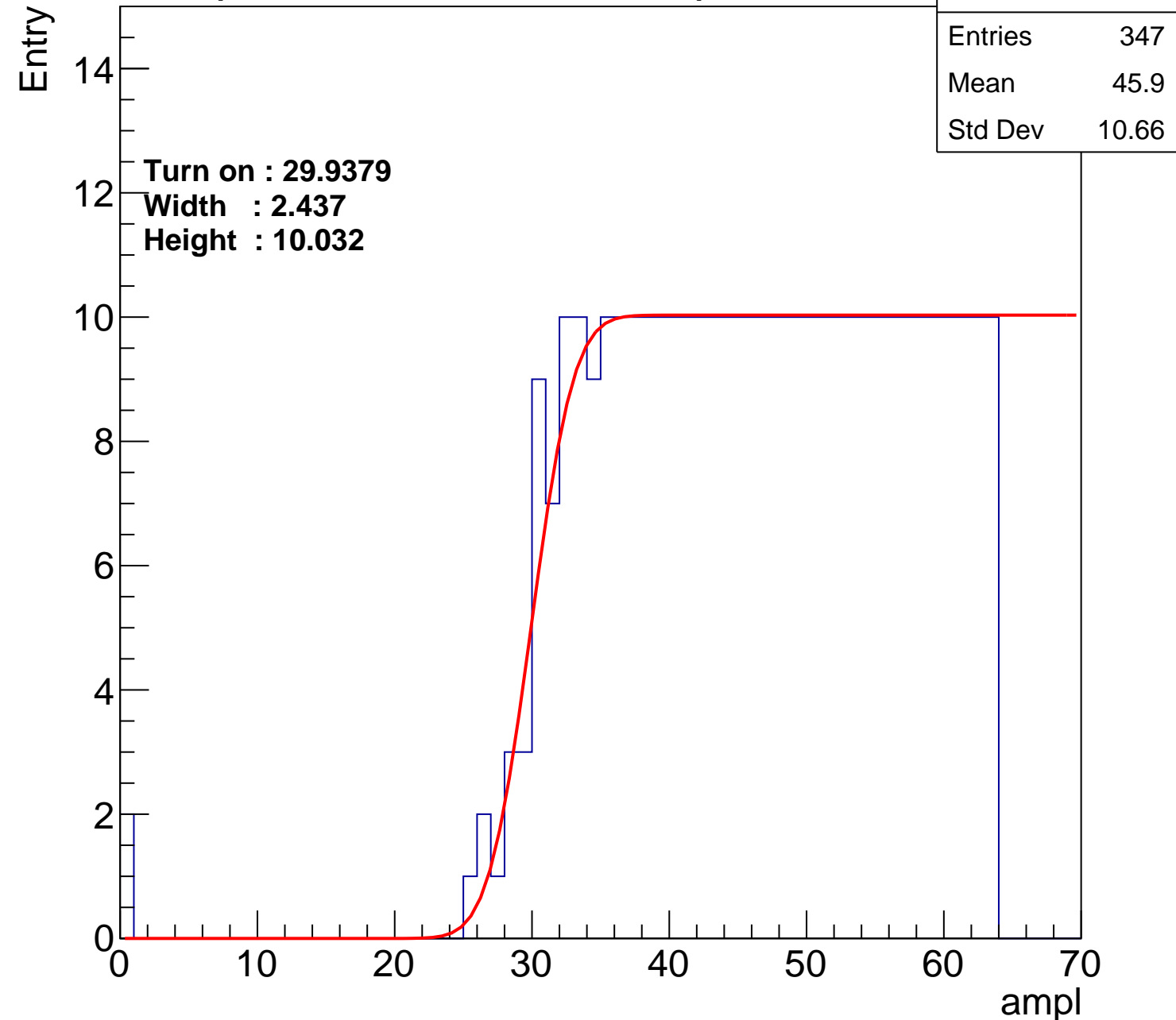
Width : 2.437

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.48
Std Dev	11.24

Turn on : 26.3071

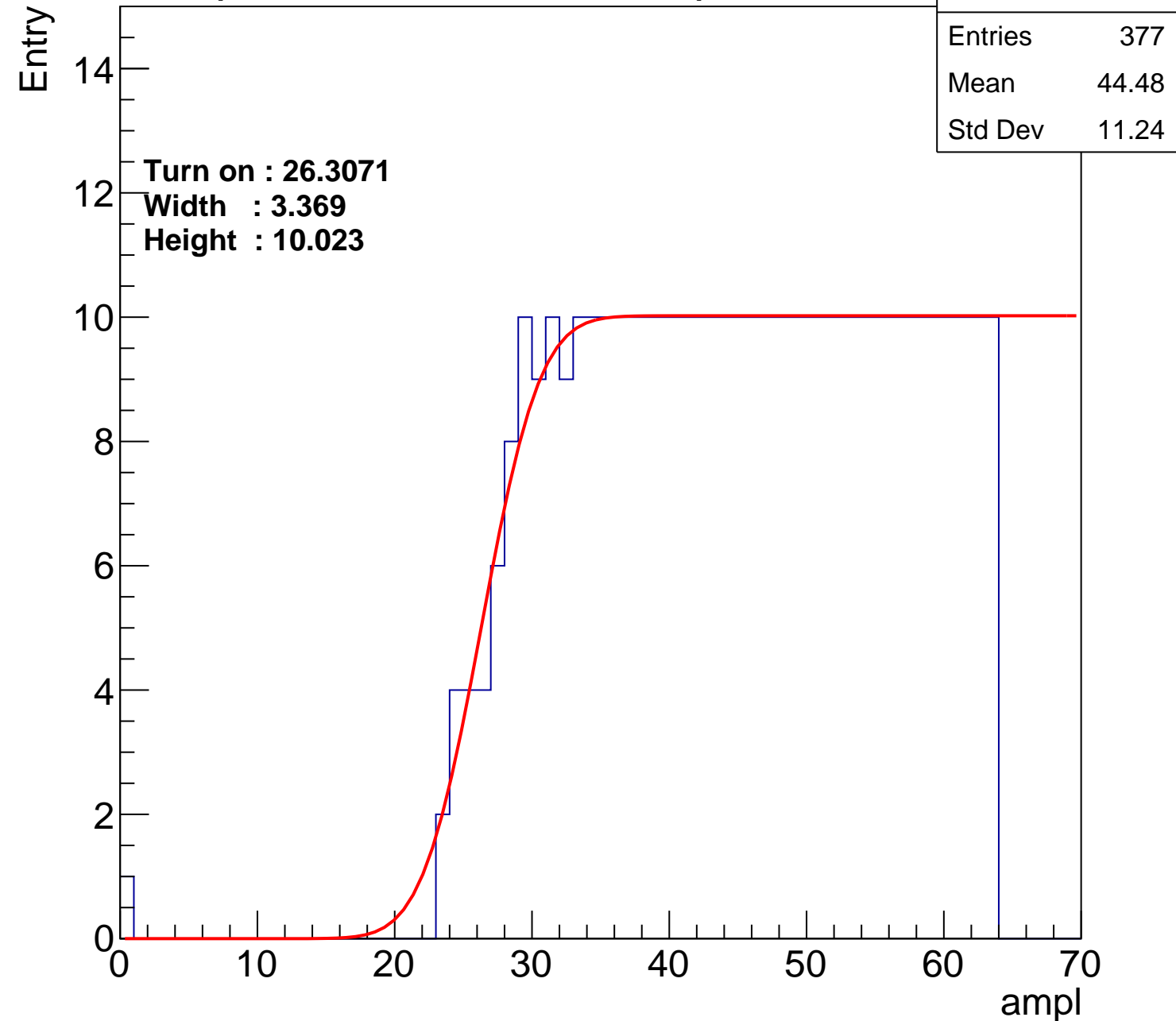
Width : 3.369

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.61
Std Dev	11.84

Turn on : 28.2870

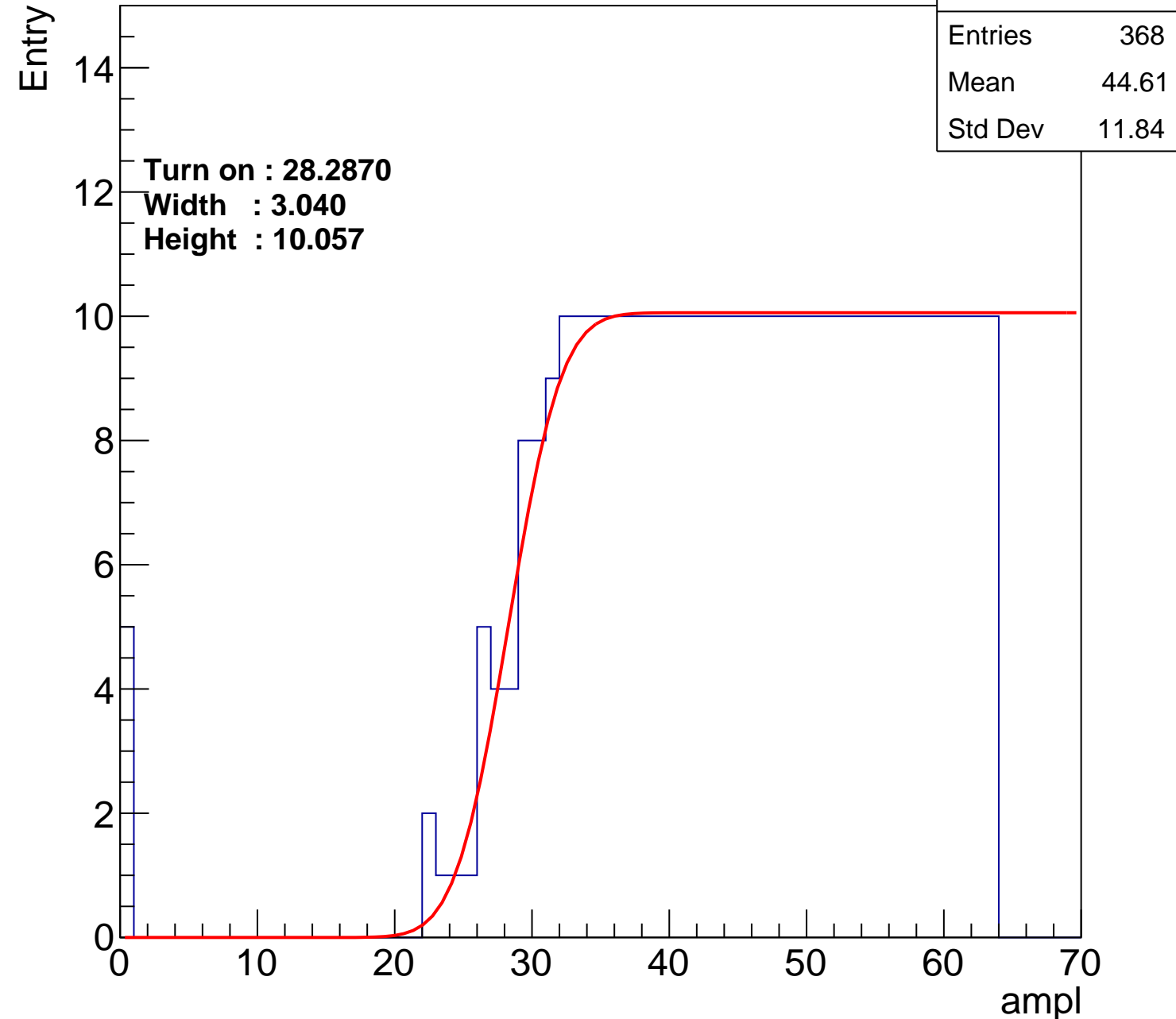
Width : 3.040

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.12
Std Dev	11.11

Turn on : 28.1602

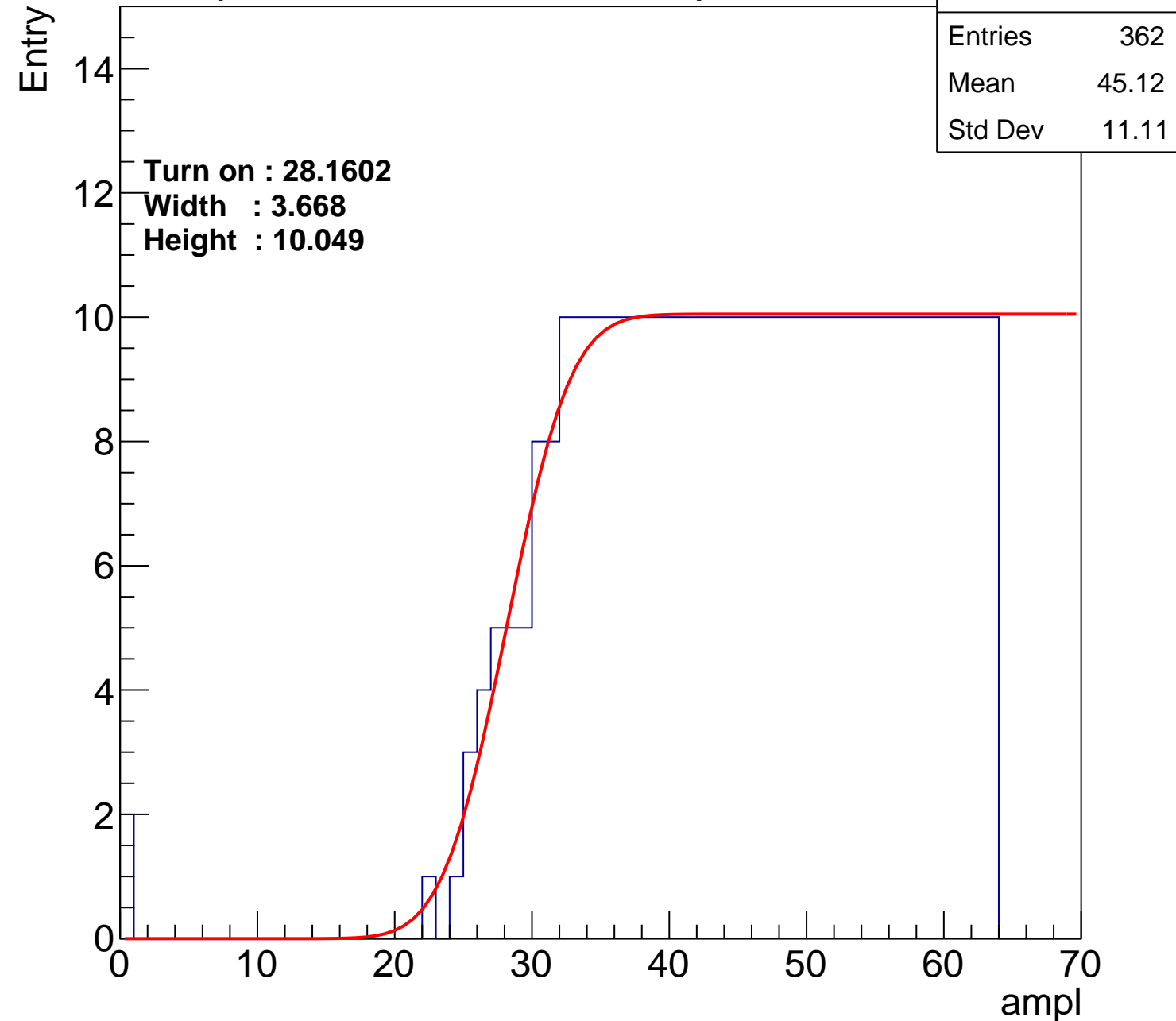
Width : 3.668

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch8

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.06
Std Dev	10.96

Turn on : 26.8780

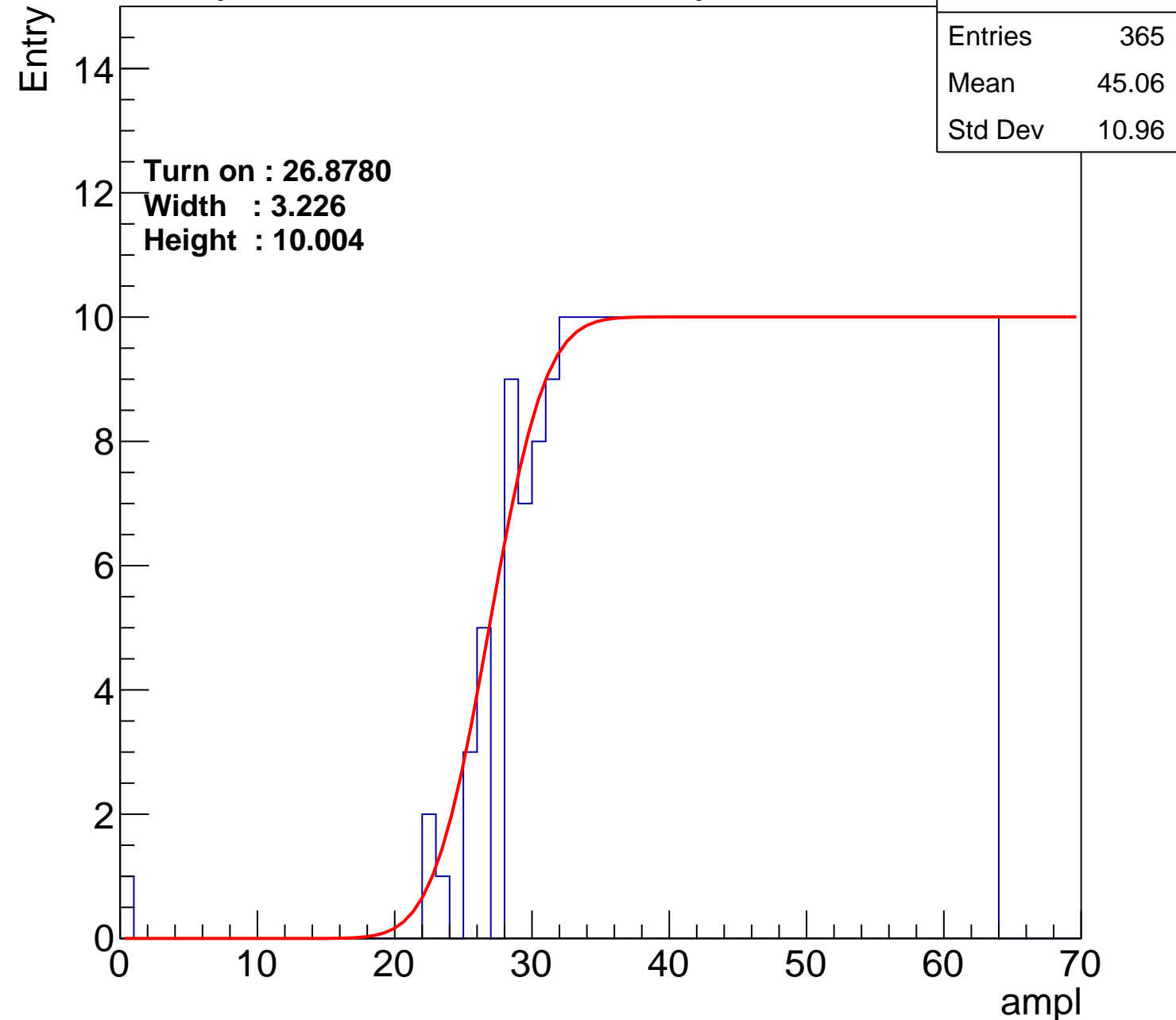
Width : 3.226

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.35
Std Dev	10.74

Turn on : 28.5457

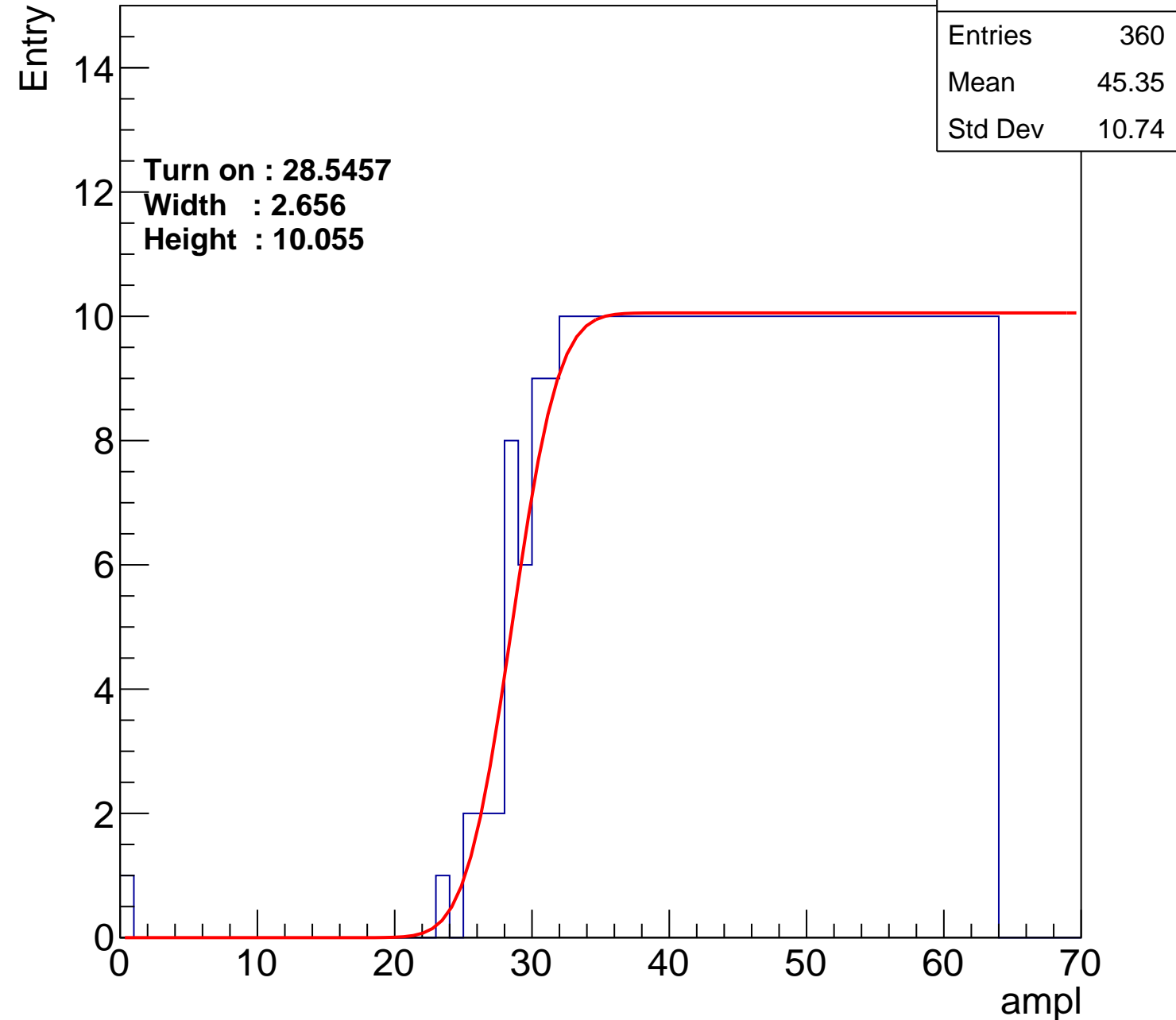
Width : 2.656

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.24
Std Dev	10.87

Turn on : 28.1844

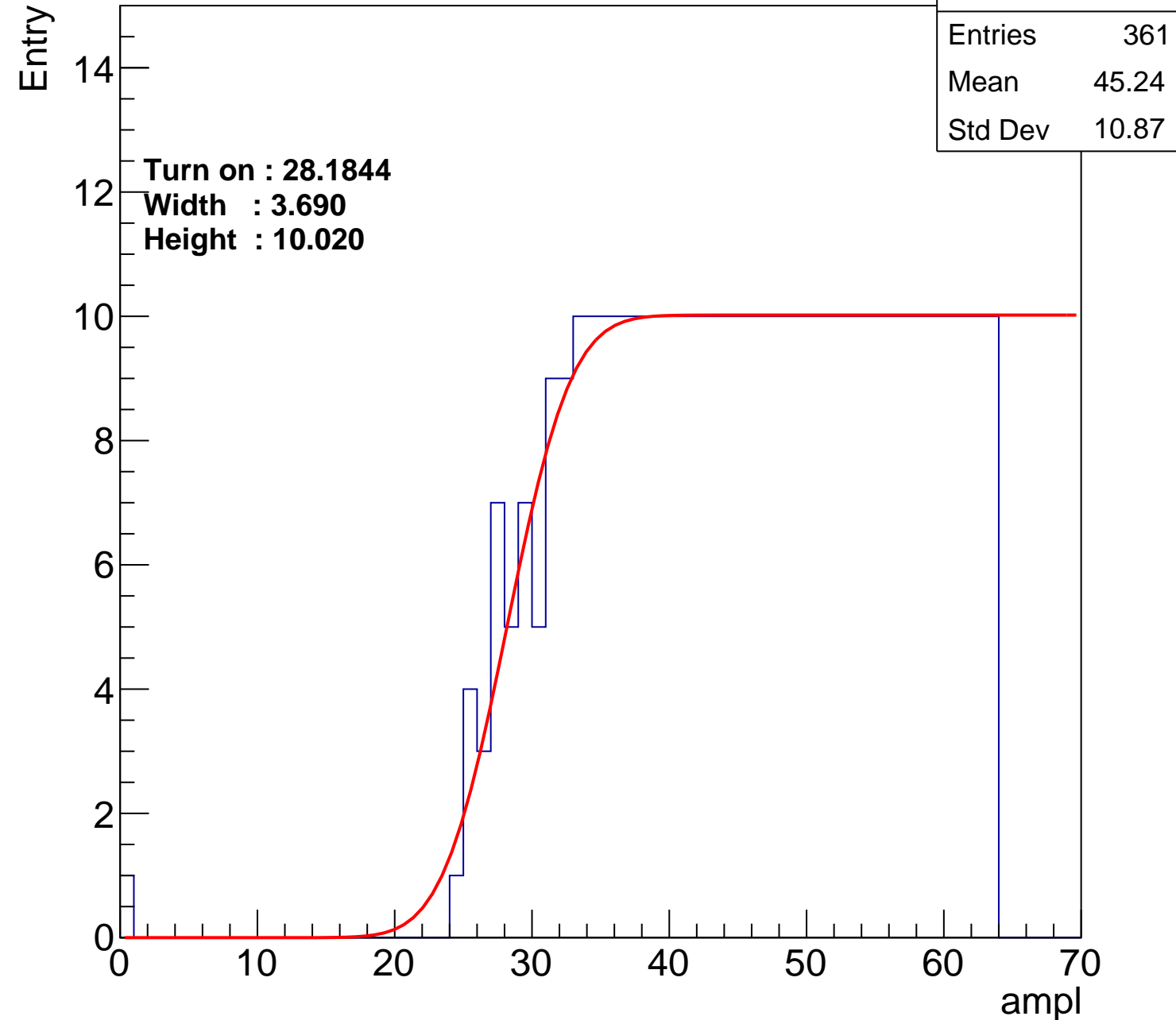
Width : 3.690

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.23
Std Dev	11.04

Turn on : 28.7056

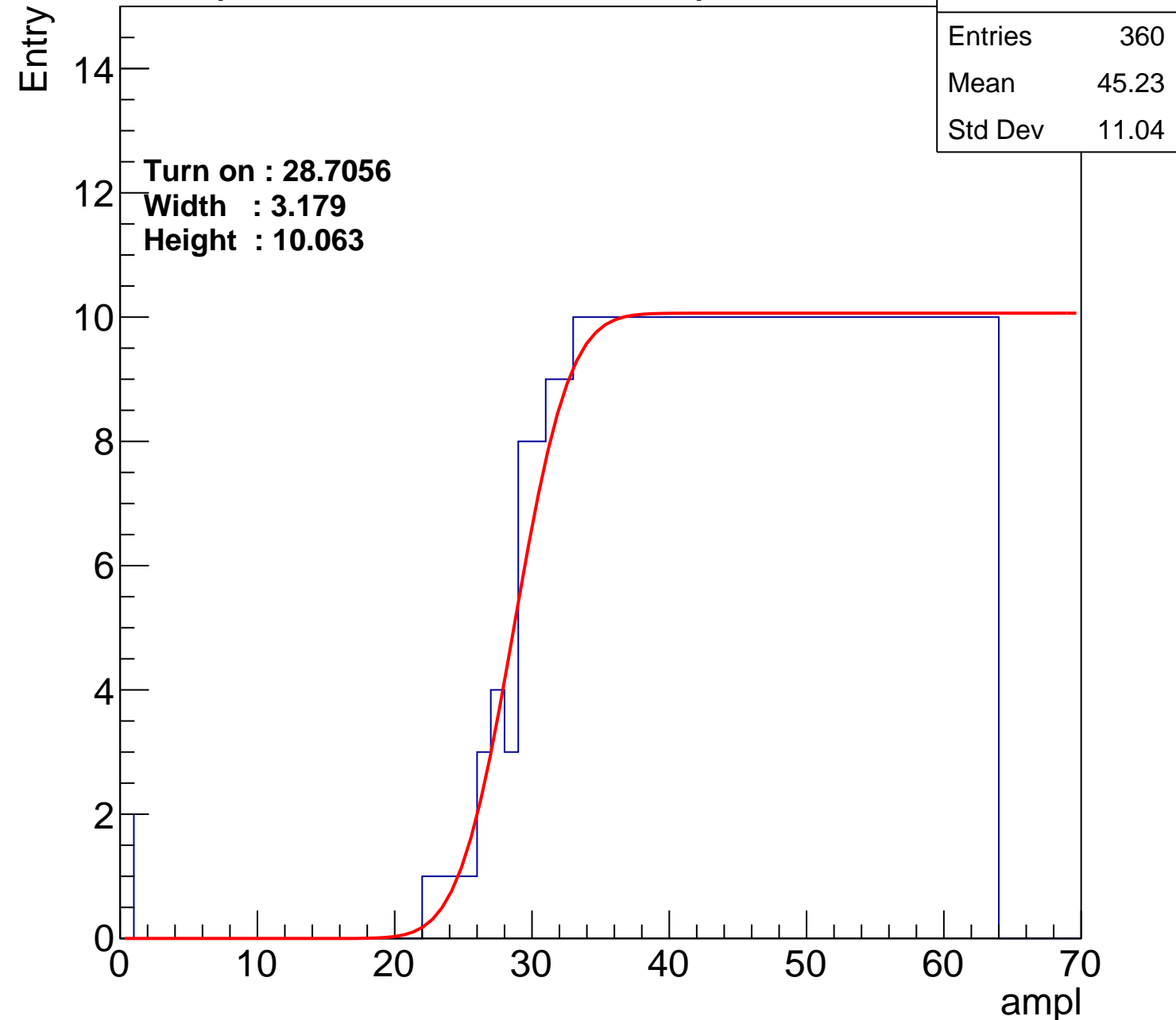
Width : 3.179

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.05

Turn on : 27.1861

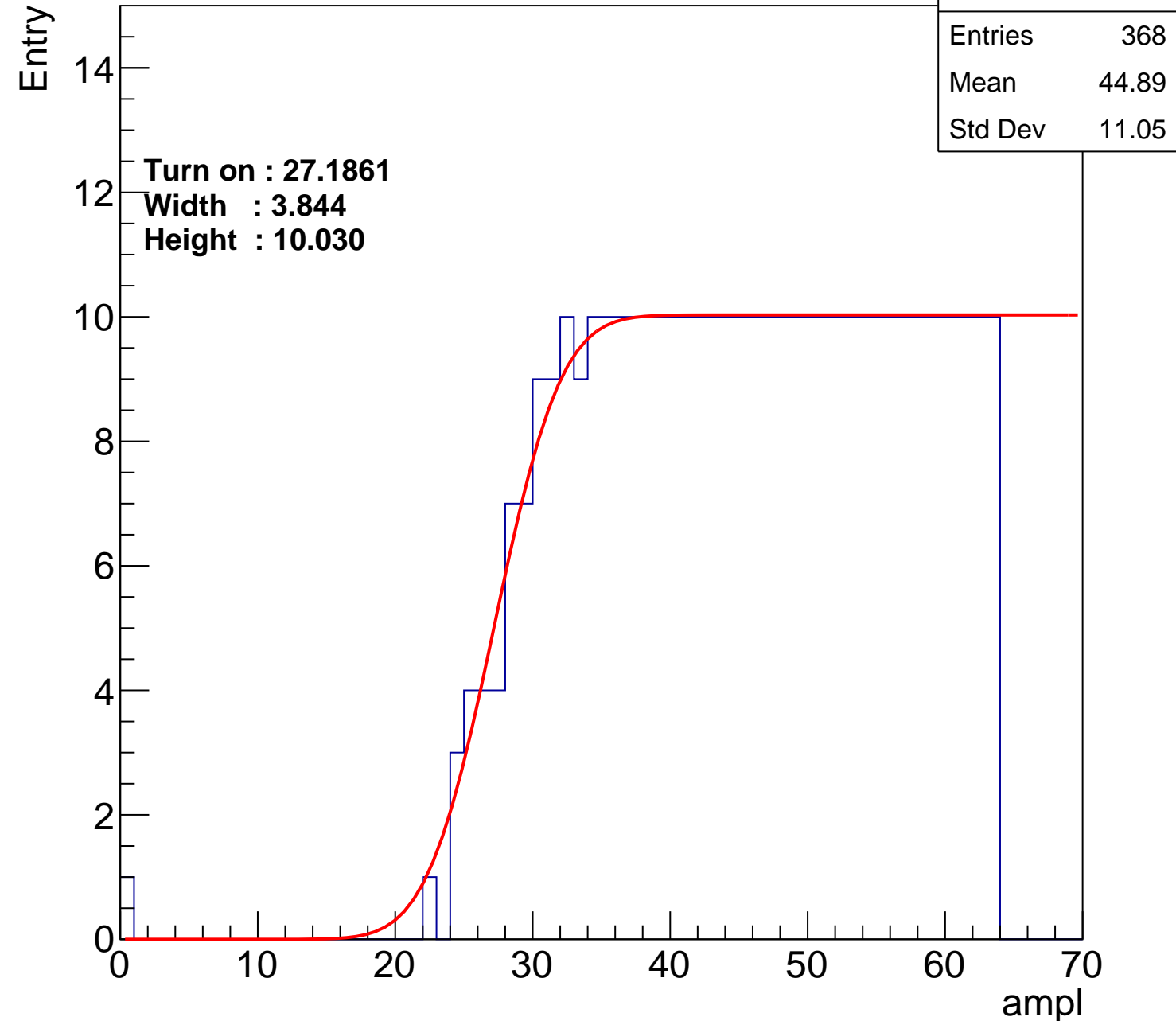
Width : 3.844

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch13

calib_packv5_042523_0143.root, FC#8, port C1

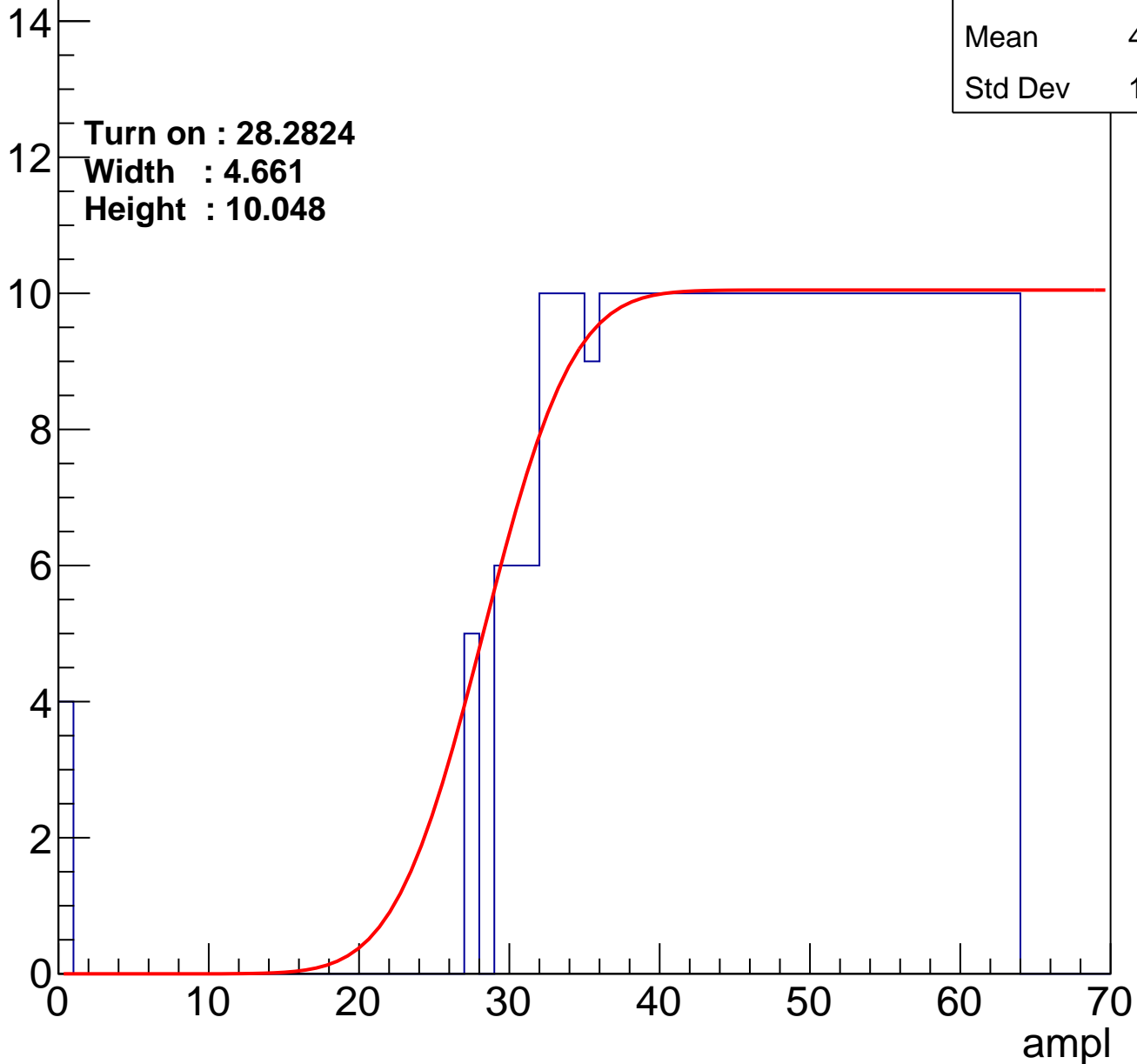
Entries	346
Mean	45.78
Std Dev	11.12

Turn on : 28.2824

Width : 4.661

Height : 10.048

Entry



B0L002S, U22-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 27.0993

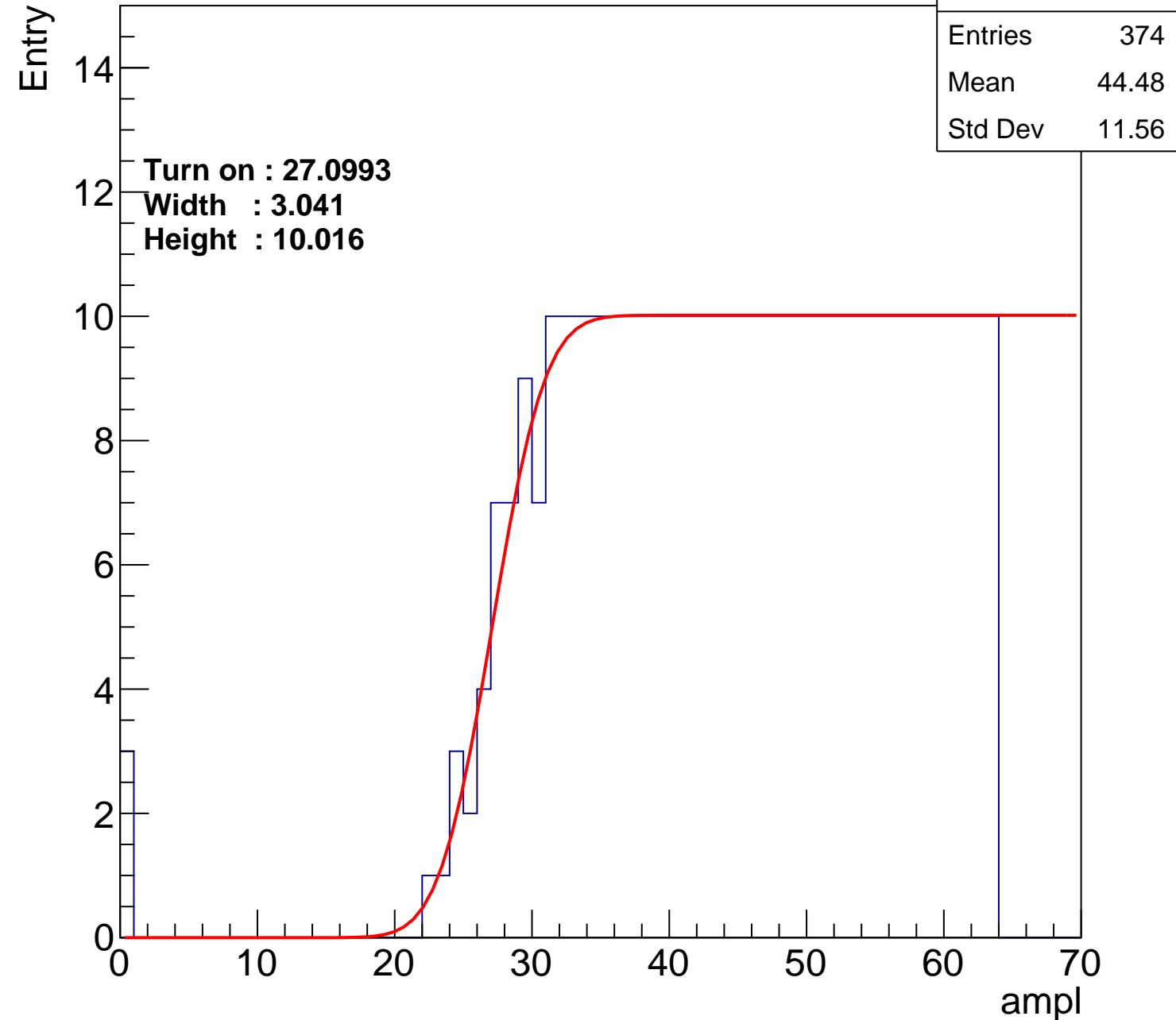
Width : 3.041

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch15

calib_packv5_042523_0143.root, FC#8, port C1

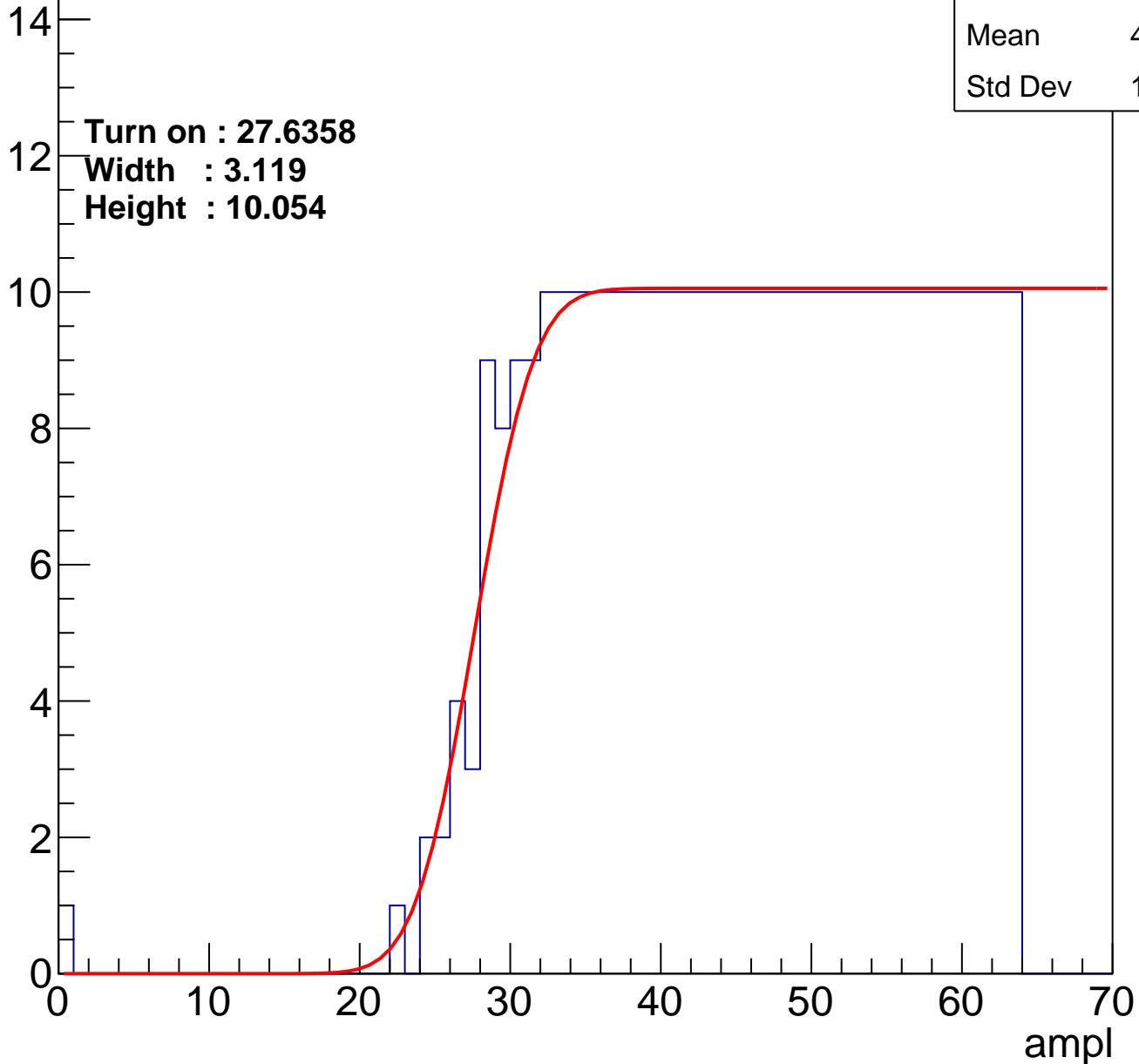
Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.6358

Width : 3.119

Height : 10.054

Entry



B0L002S, U22-ch16

calib_packv5_042523_0143.root, FC#8, port C1

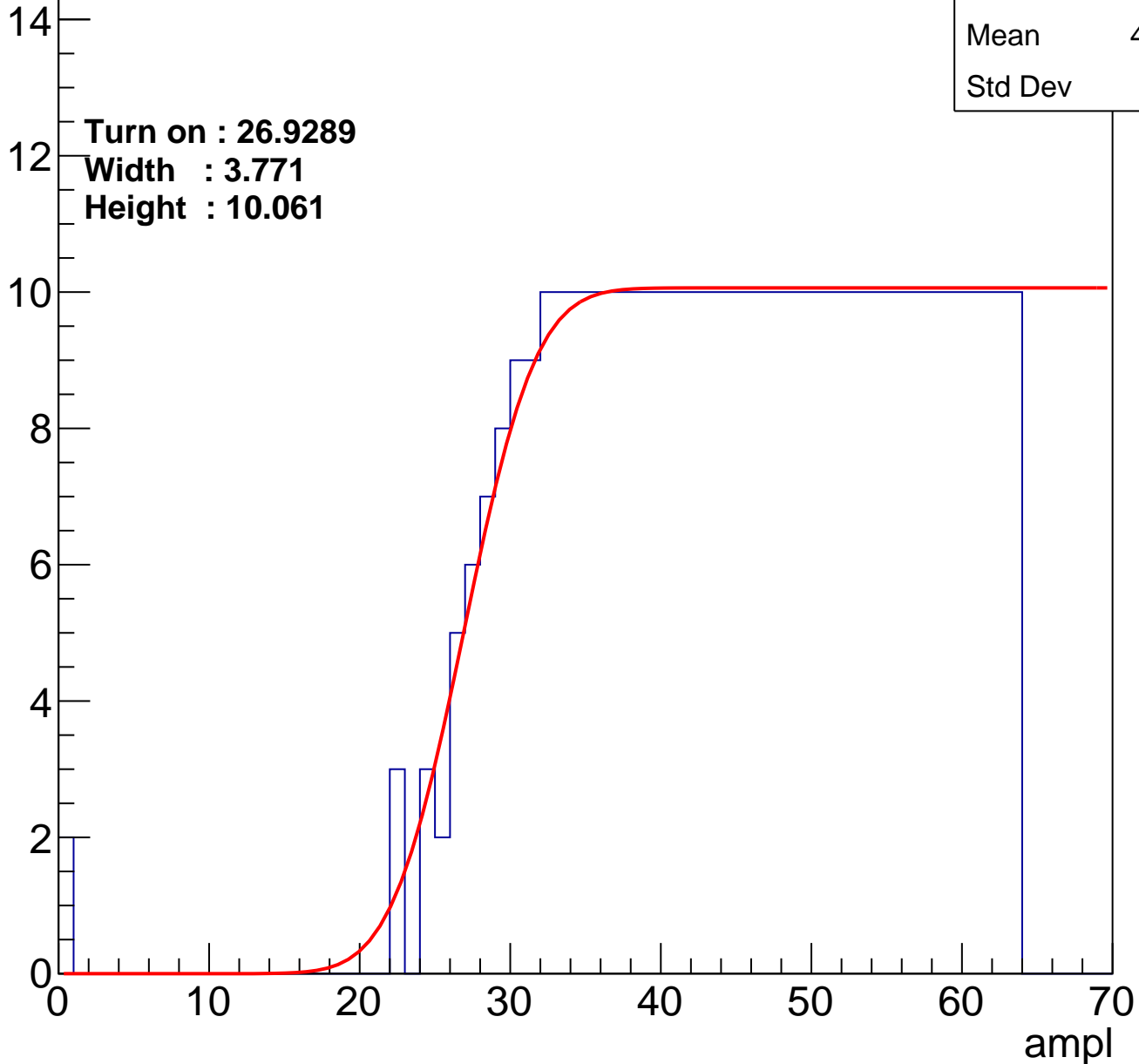
Entries	374
Mean	44.54
Std Dev	11.4

Turn on : 26.9289

Width : 3.771

Height : 10.061

Entry



B0L002S, U22-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.5605

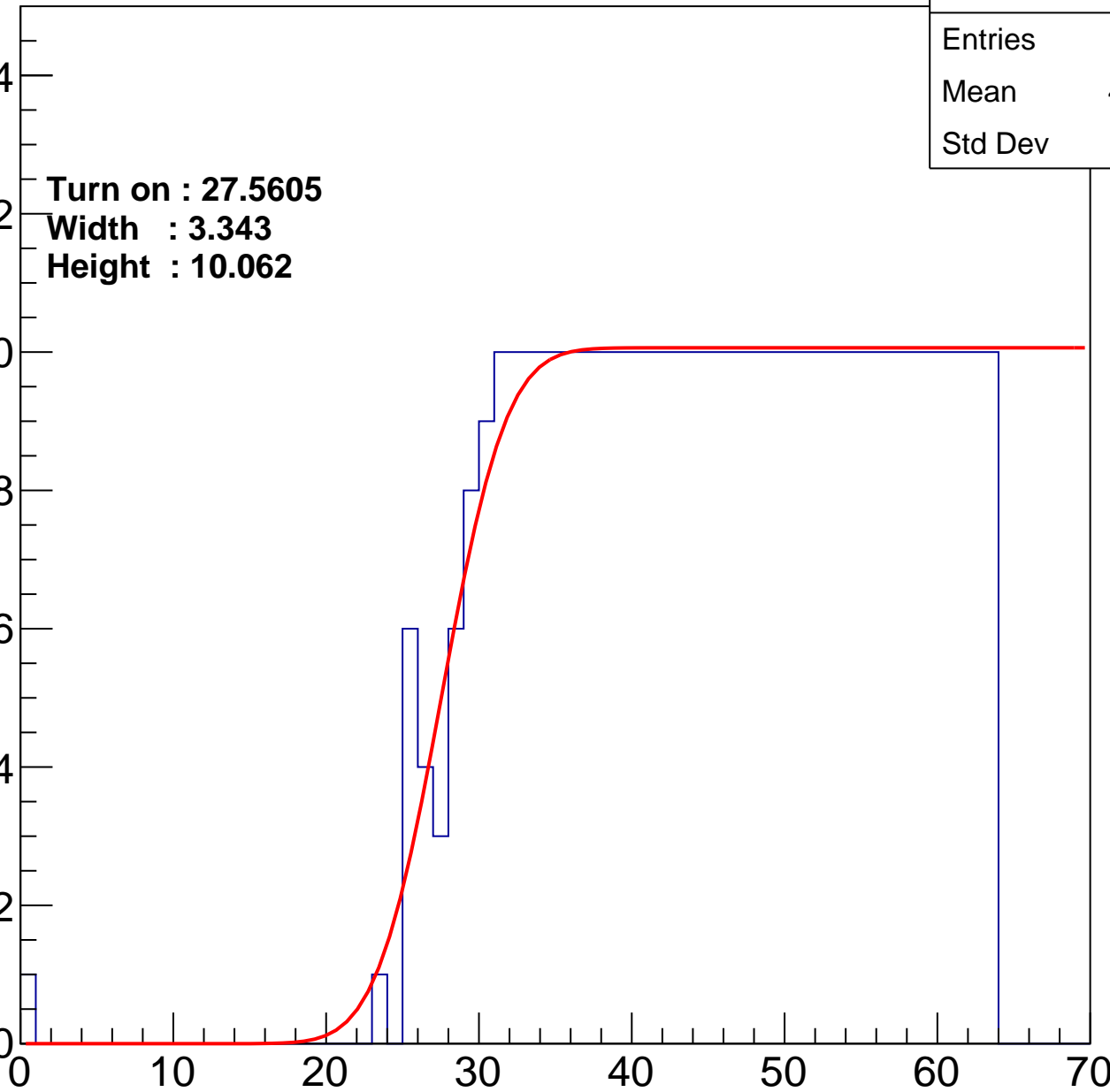
Width : 3.343

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.8502

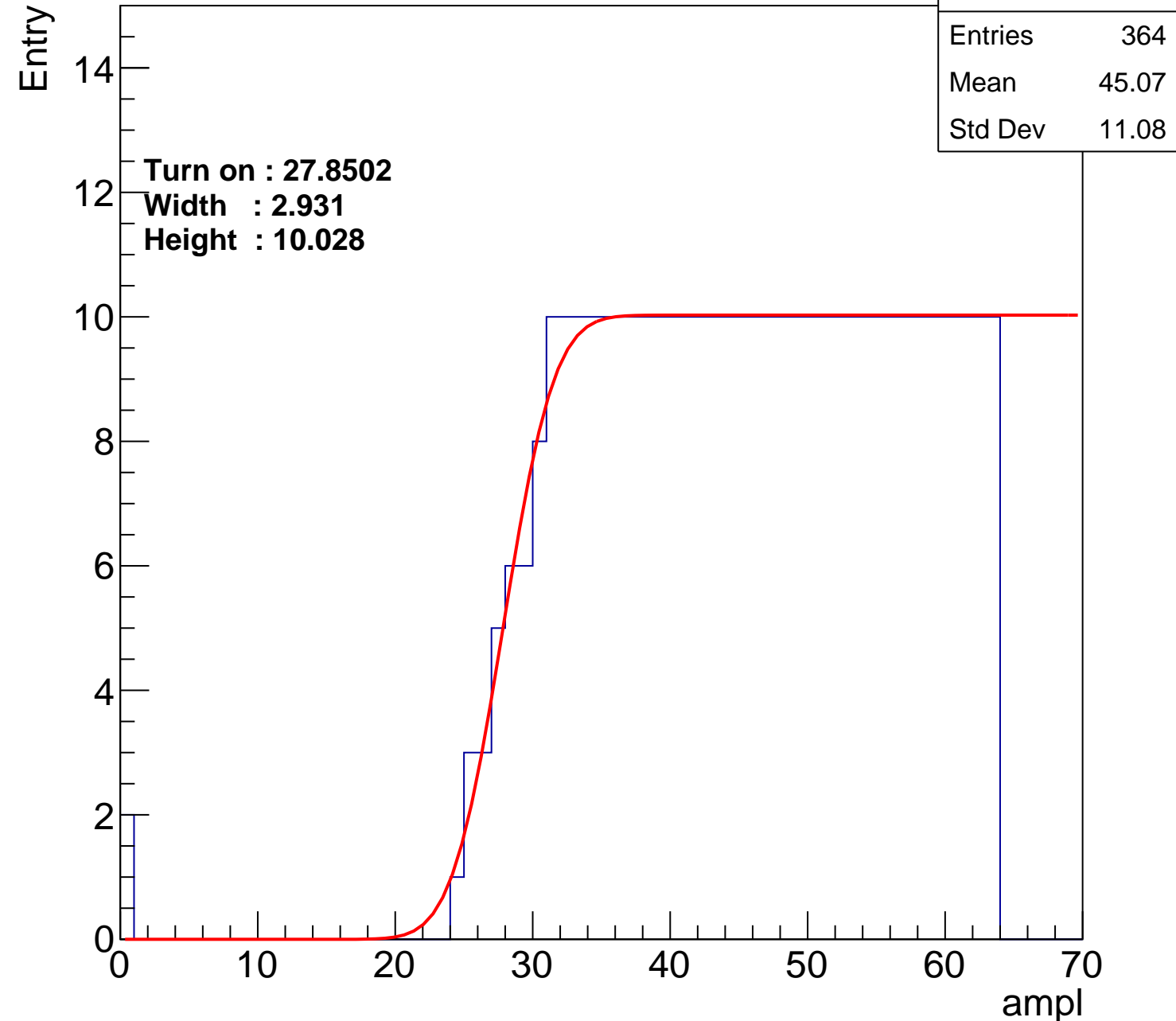
Width : 2.931

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.55
Std Dev	11.65

Turn on : 27.8131

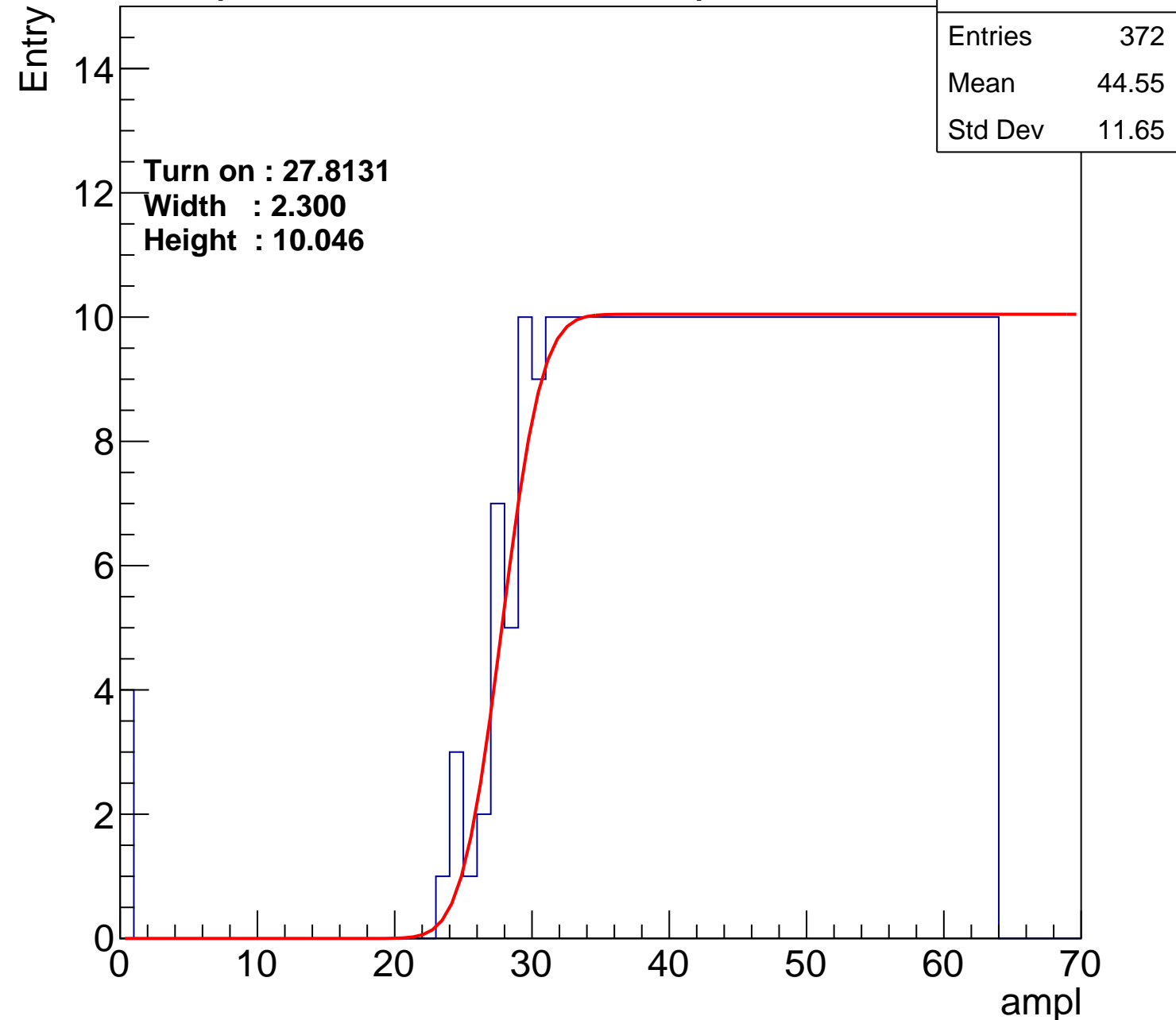
Width : 2.300

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch20

calib_packv5_042523_0143.root, FC#8, port C1

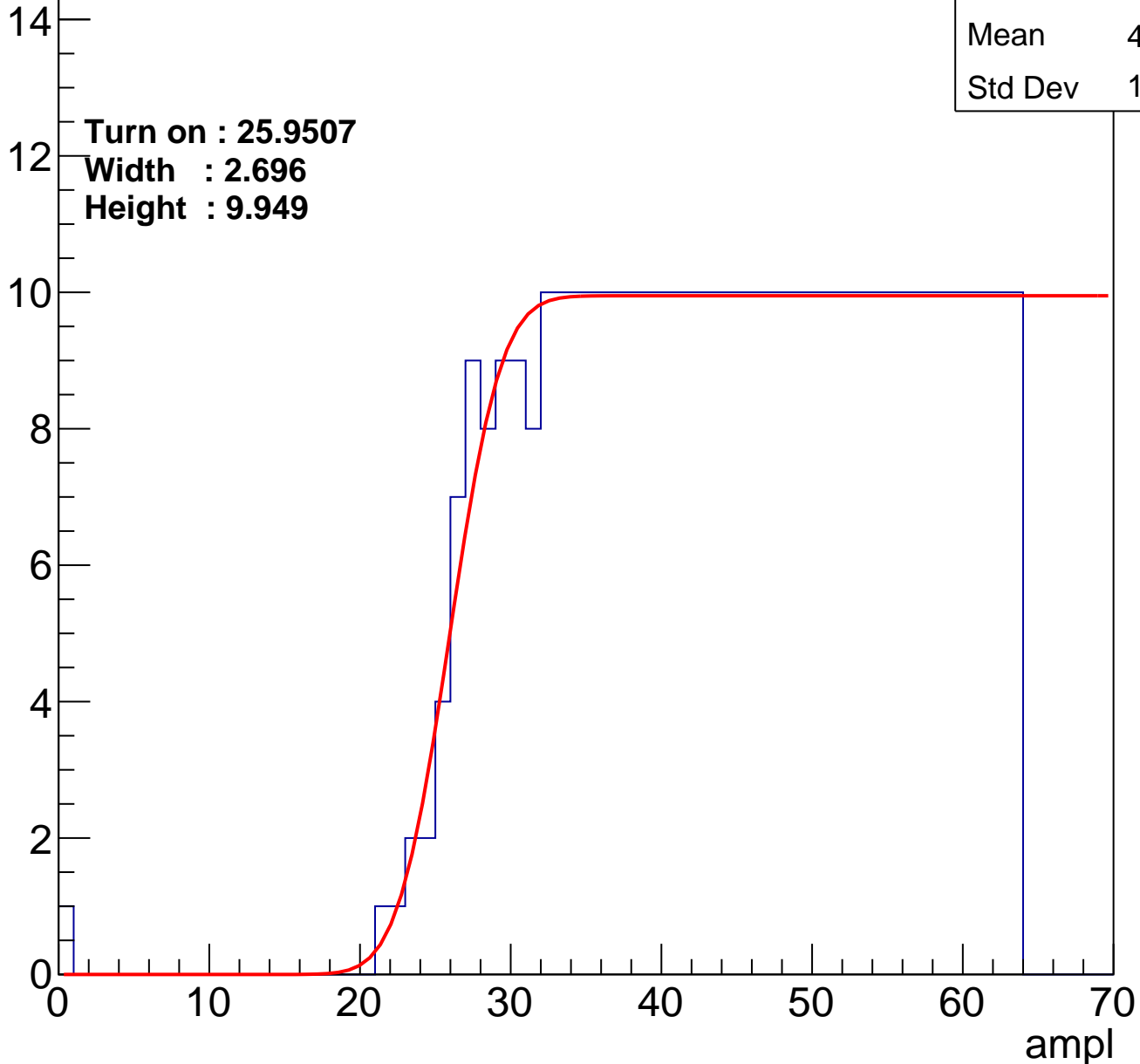
Entries	381
Mean	44.27
Std Dev	11.38

Turn on : 25.9507

Width : 2.696

Height : 9.949

Entry



B0L002S, U22-ch21

calib_packv5_042523_0143.root, FC#8, port C1

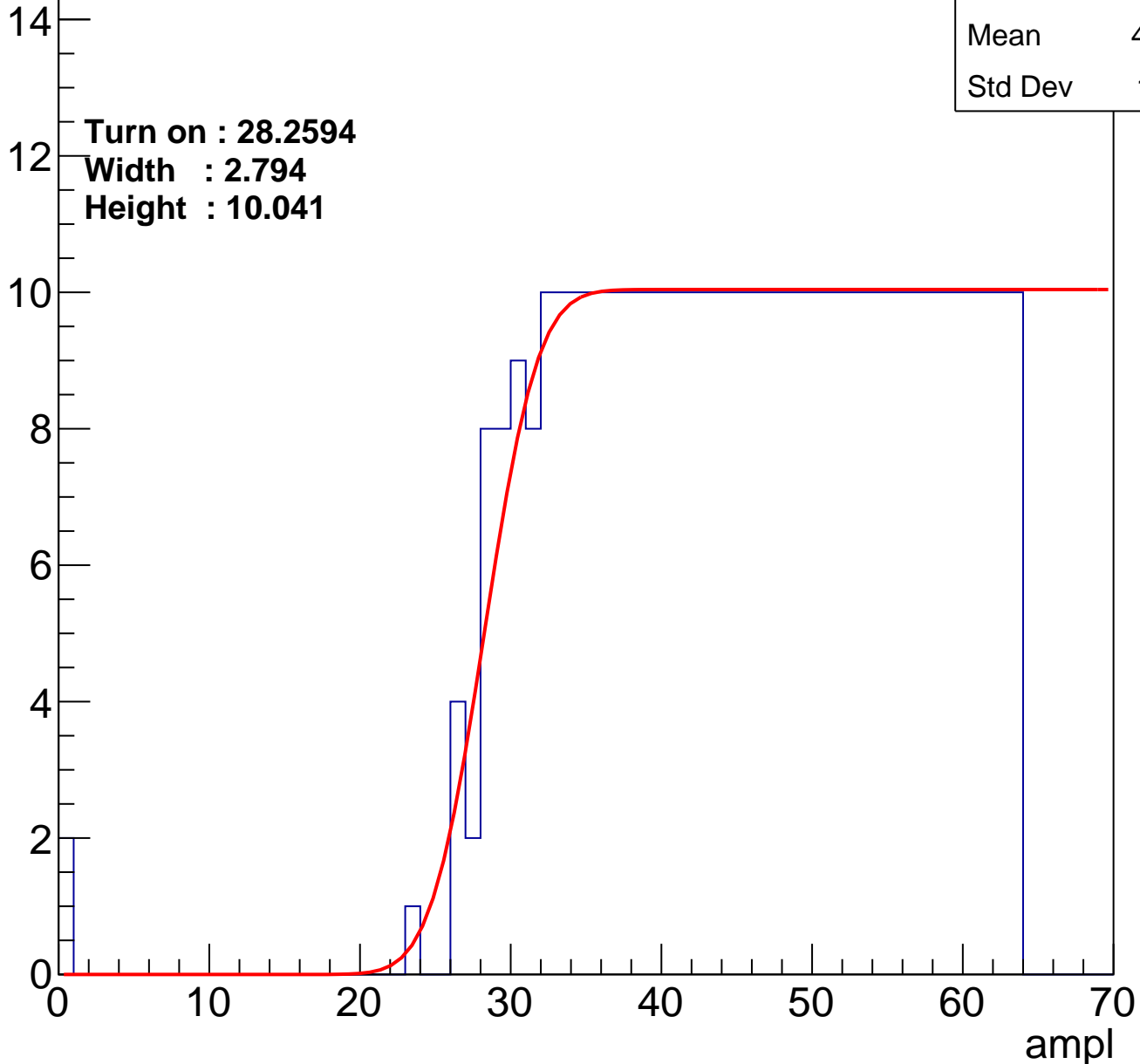
Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 28.2594

Width : 2.794

Height : 10.041

Entry



B0L002S, U22-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 27.1515

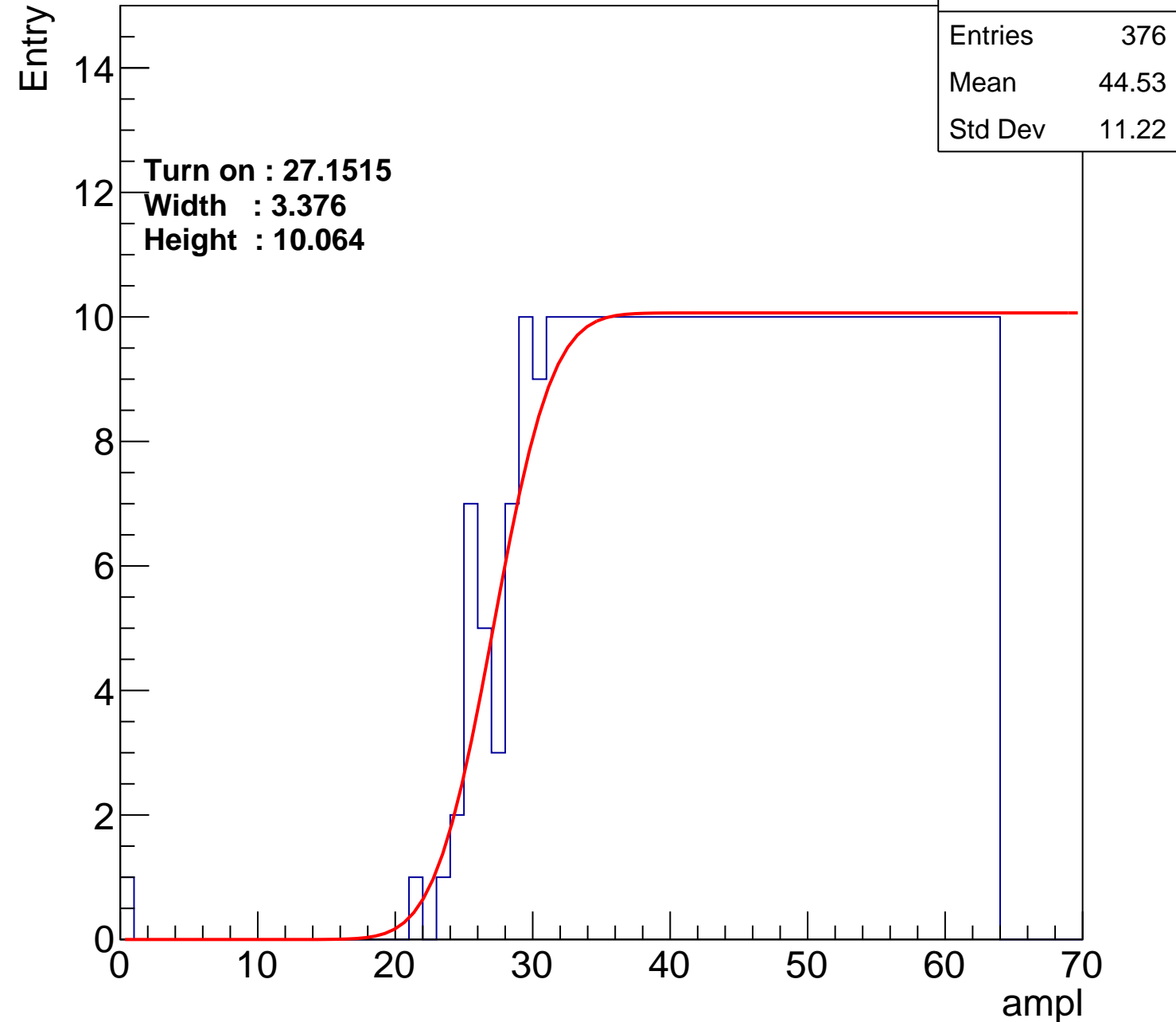
Width : 3.376

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.94
Std Dev	11.5

Turn on : 27.3901

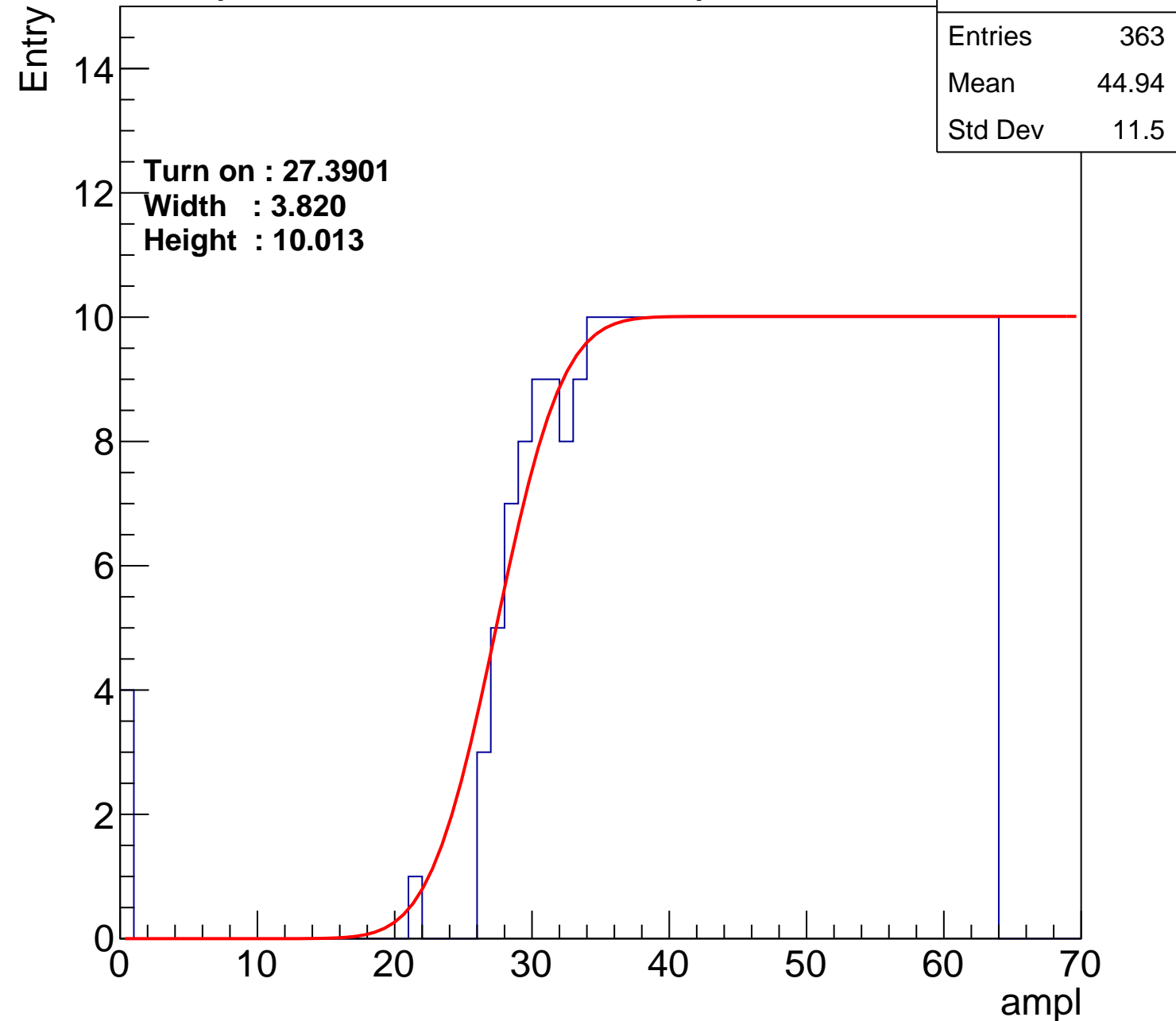
Width : 3.820

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.77
Std Dev	10.54

Turn on : 28.7836

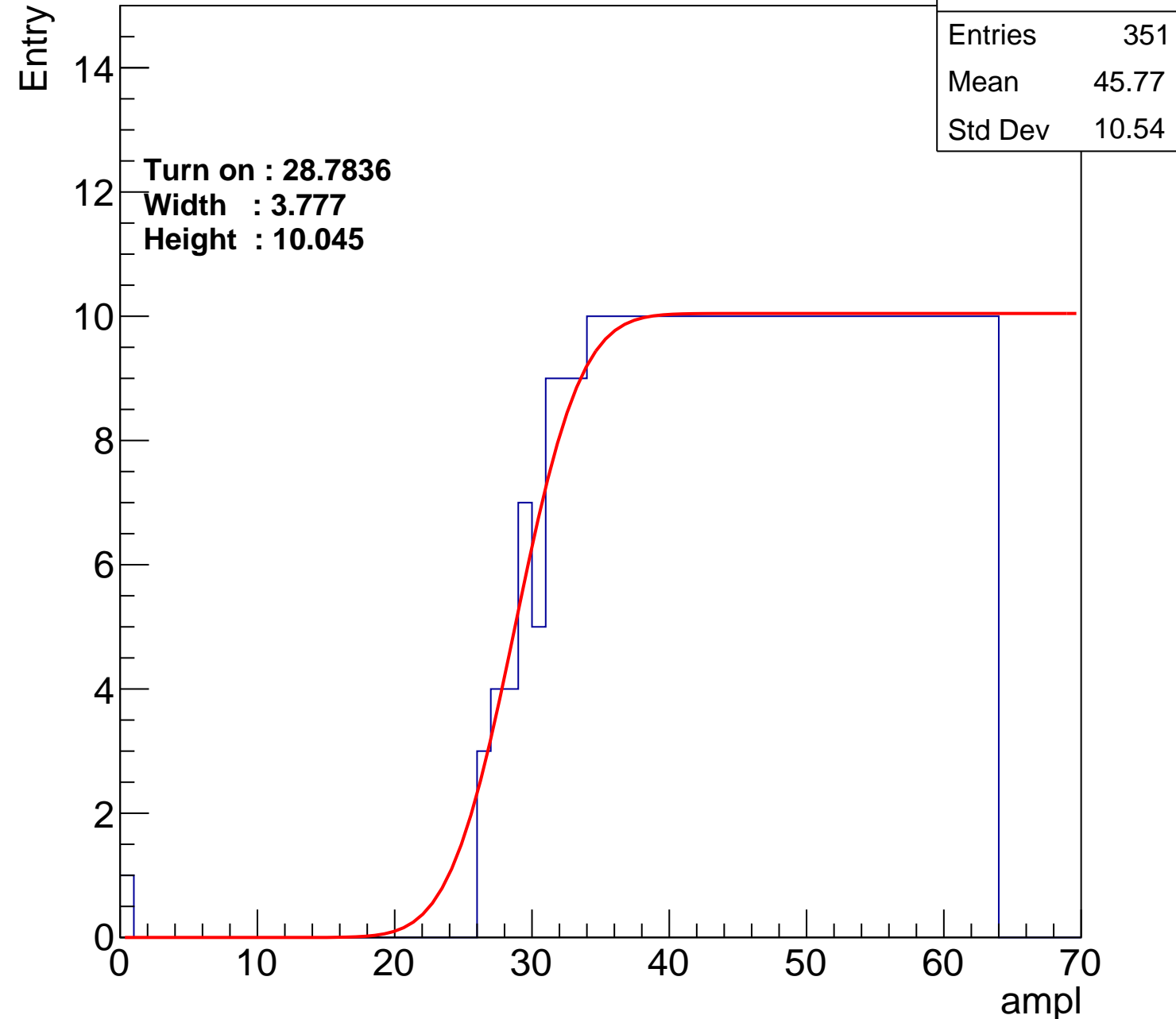
Width : 3.777

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch25

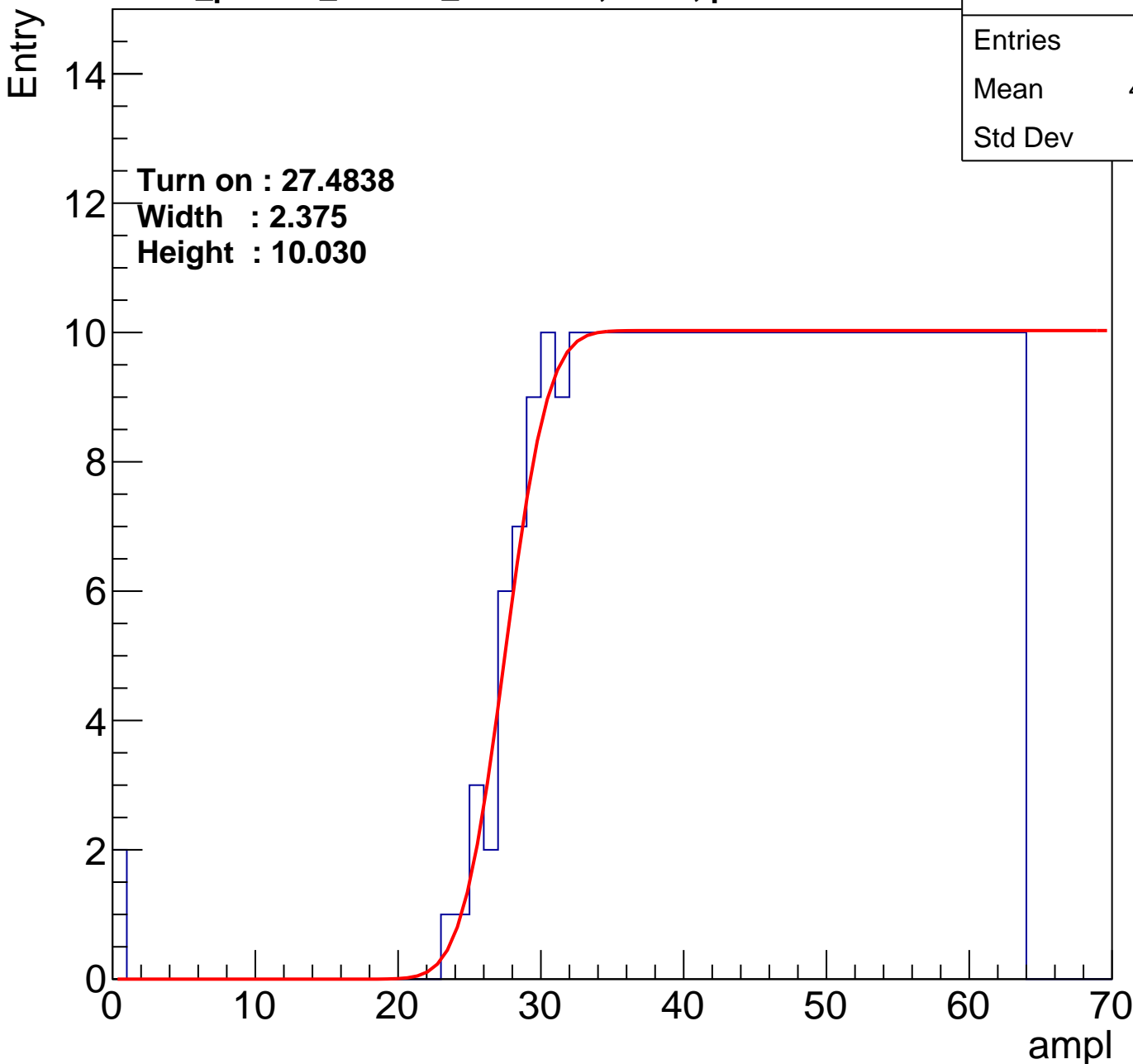
calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.79
Std Dev	11.2

Turn on : 27.4838

Width : 2.375

Height : 10.030



B0L002S, U22-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.66
Std Dev	10.77

Turn on : 29.2013

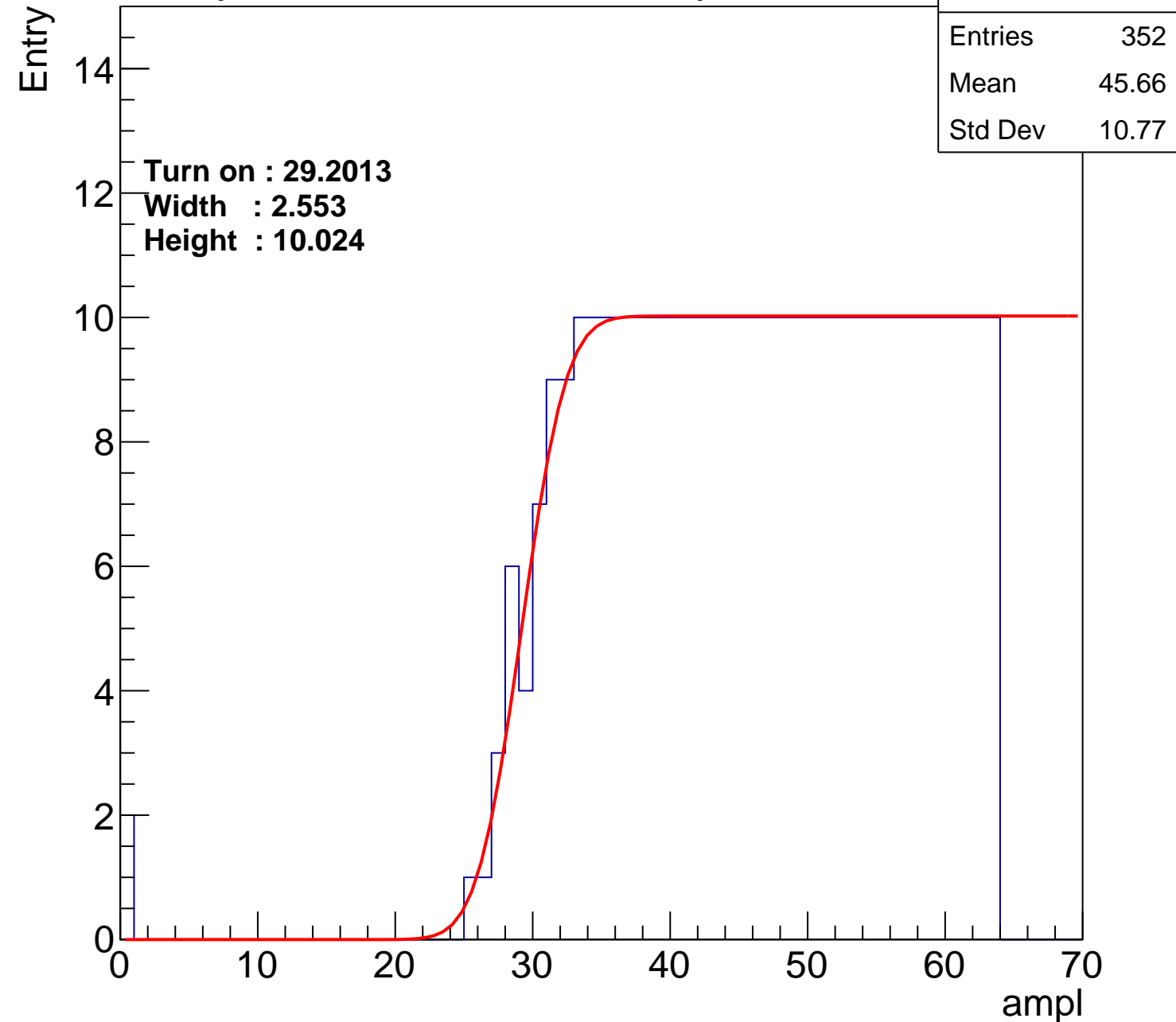
Width : 2.553

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.75
Std Dev	11.08

Turn on : 26.9330

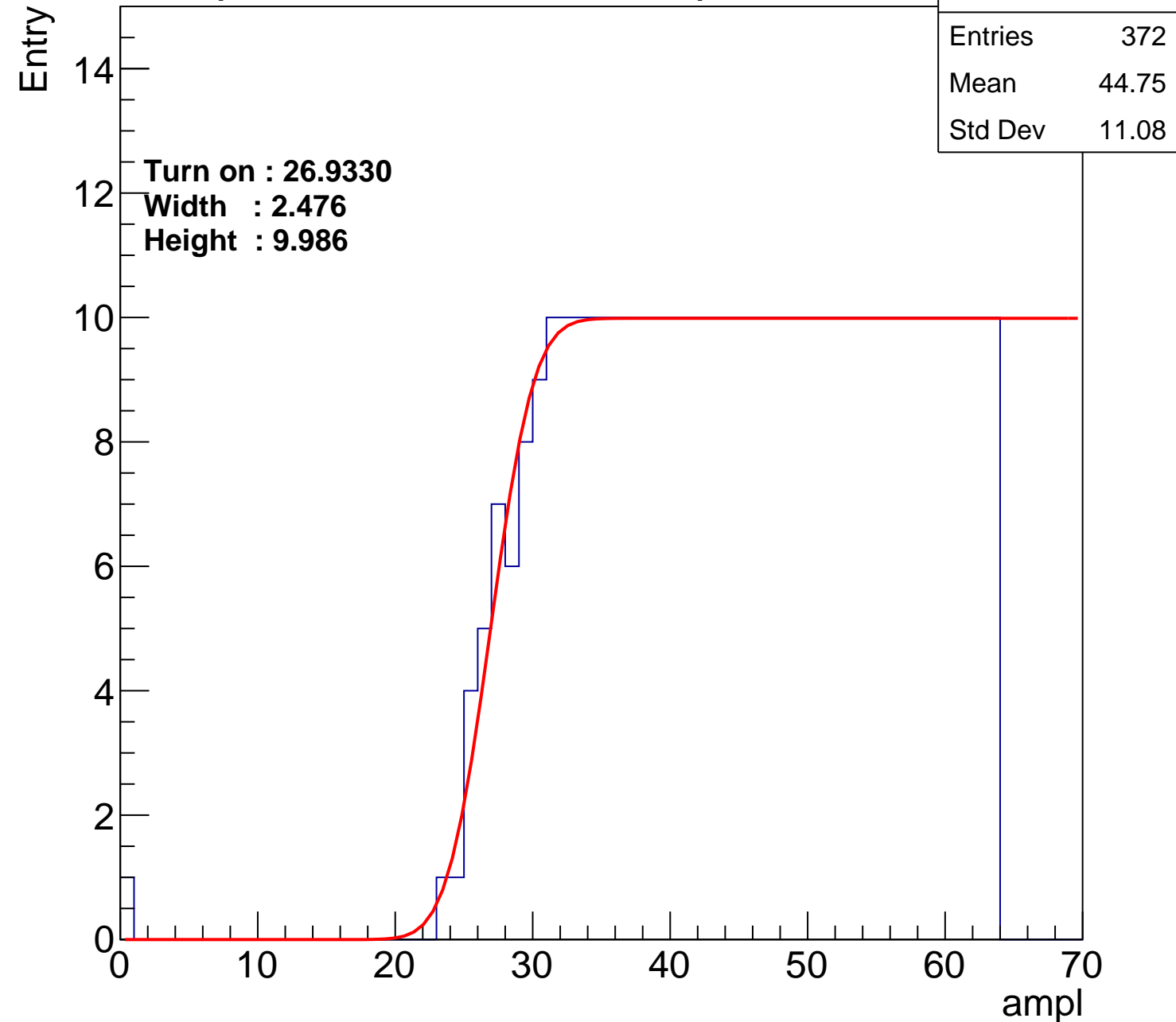
Width : 2.476

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch28

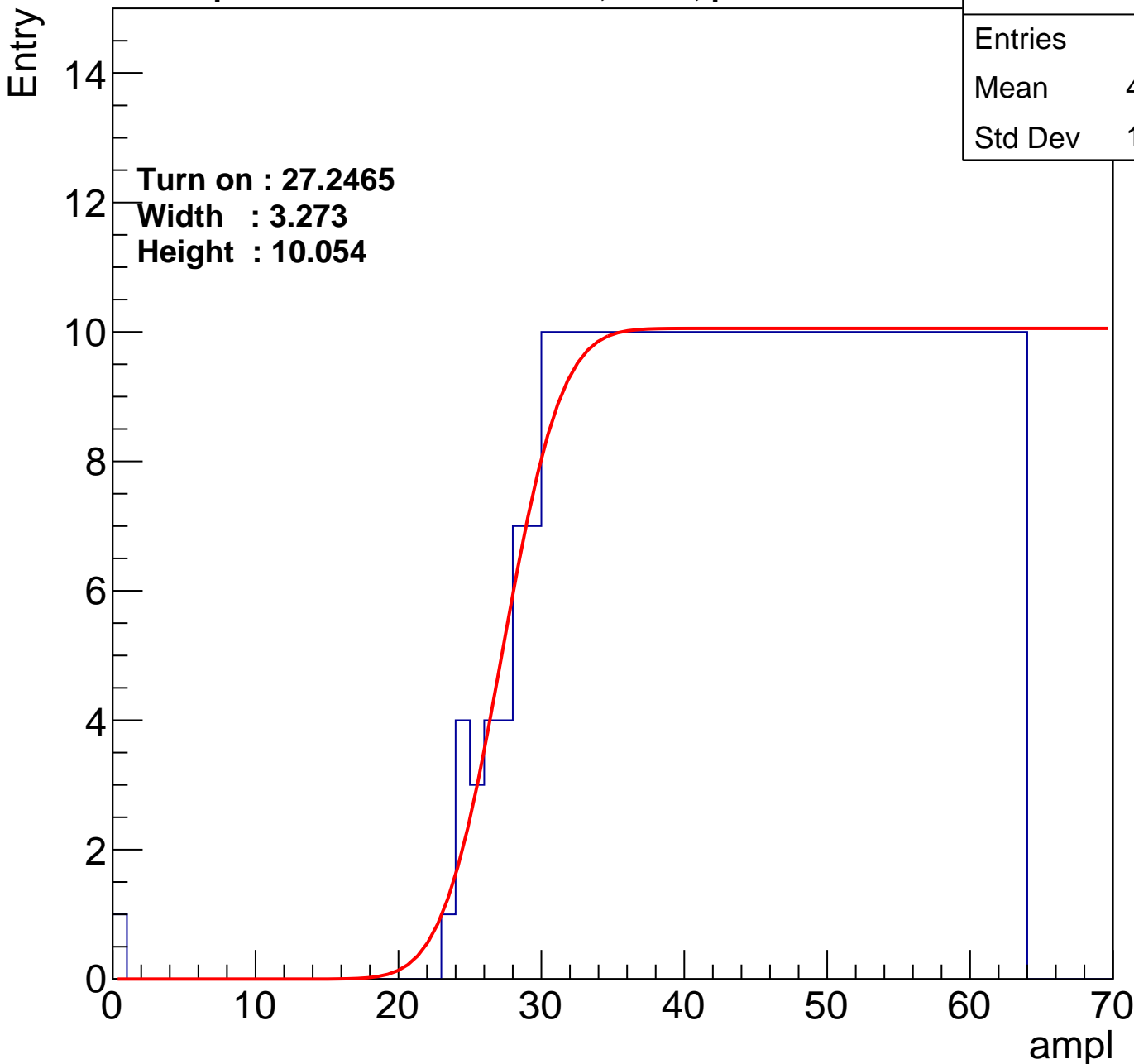
calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 27.2465

Width : 3.273

Height : 10.054



B0L002S, U22-ch29

calib_packv5_042523_0143.root, FC#8, port C1

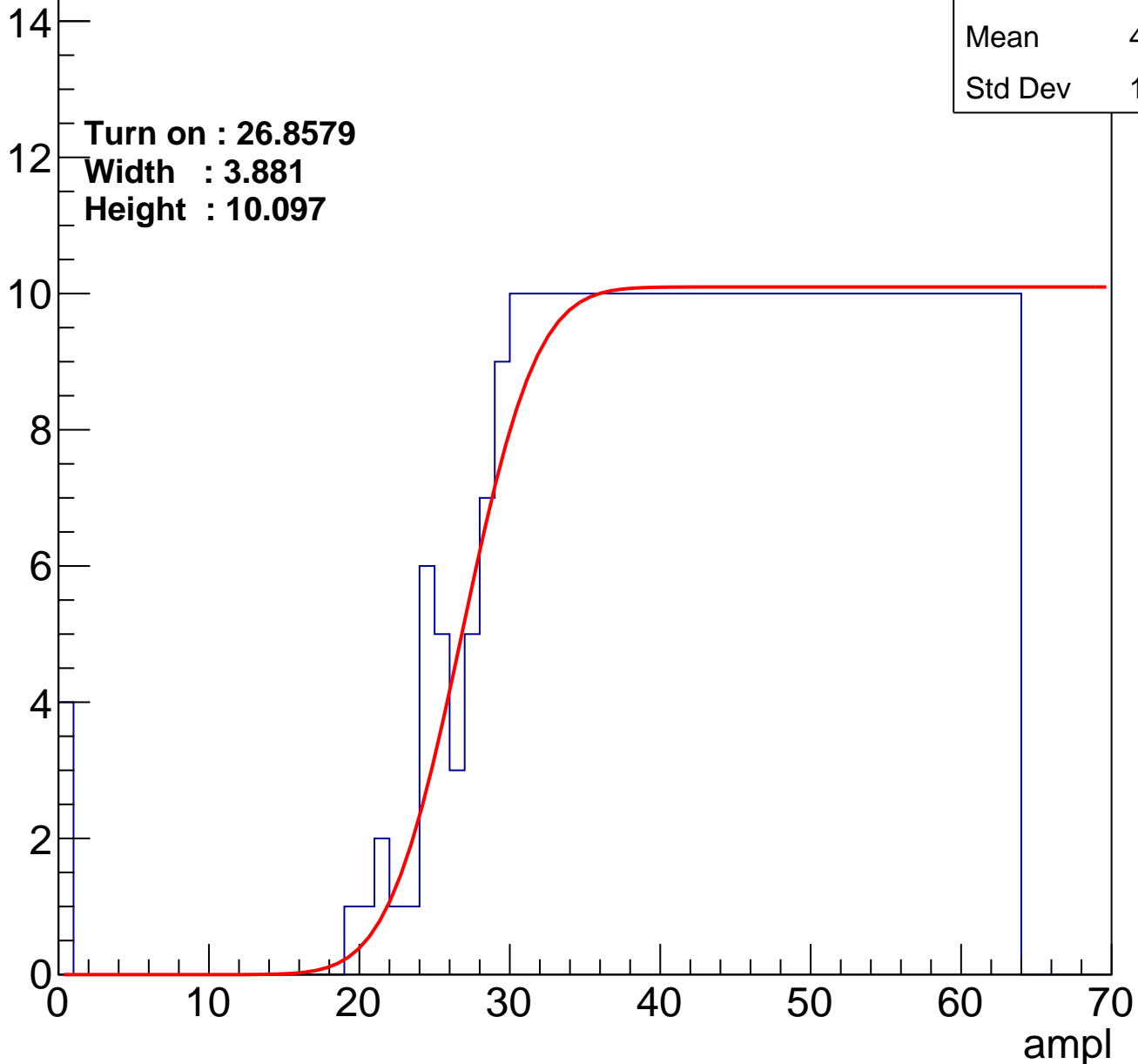
Entries	385
Mean	43.83
Std Dev	12.08

Turn on : 26.8579

Width : 3.881

Height : 10.097

Entry



B0L002S, U22-ch30

calib_packv5_042523_0143.root, FC#8, port C1

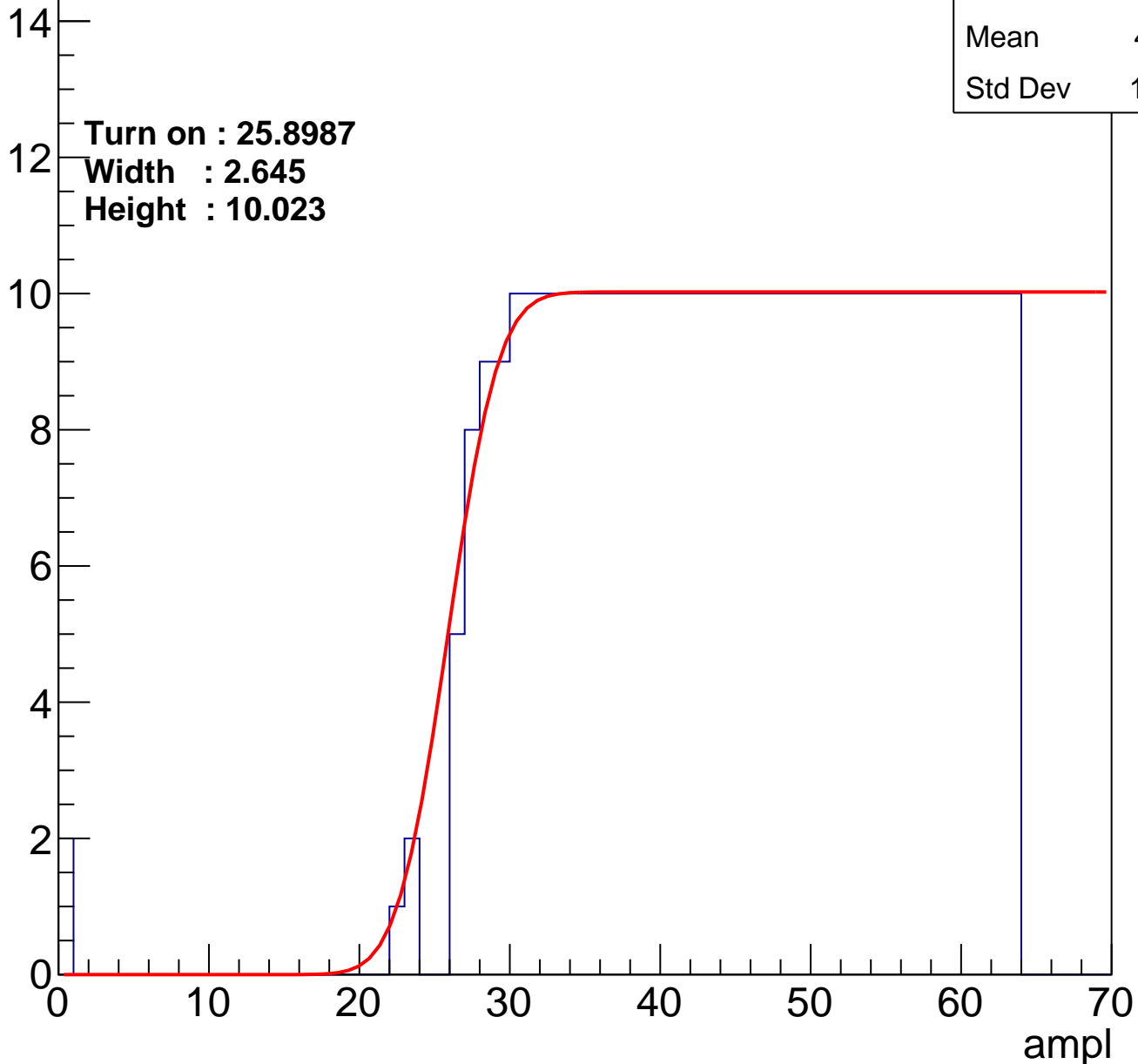
Entries	376
Mean	44.51
Std Dev	11.33

Turn on : 25.8987

Width : 2.645

Height : 10.023

Entry



B0L002S, U22-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.72
Std Dev	11.45

Turn on : 27.3588

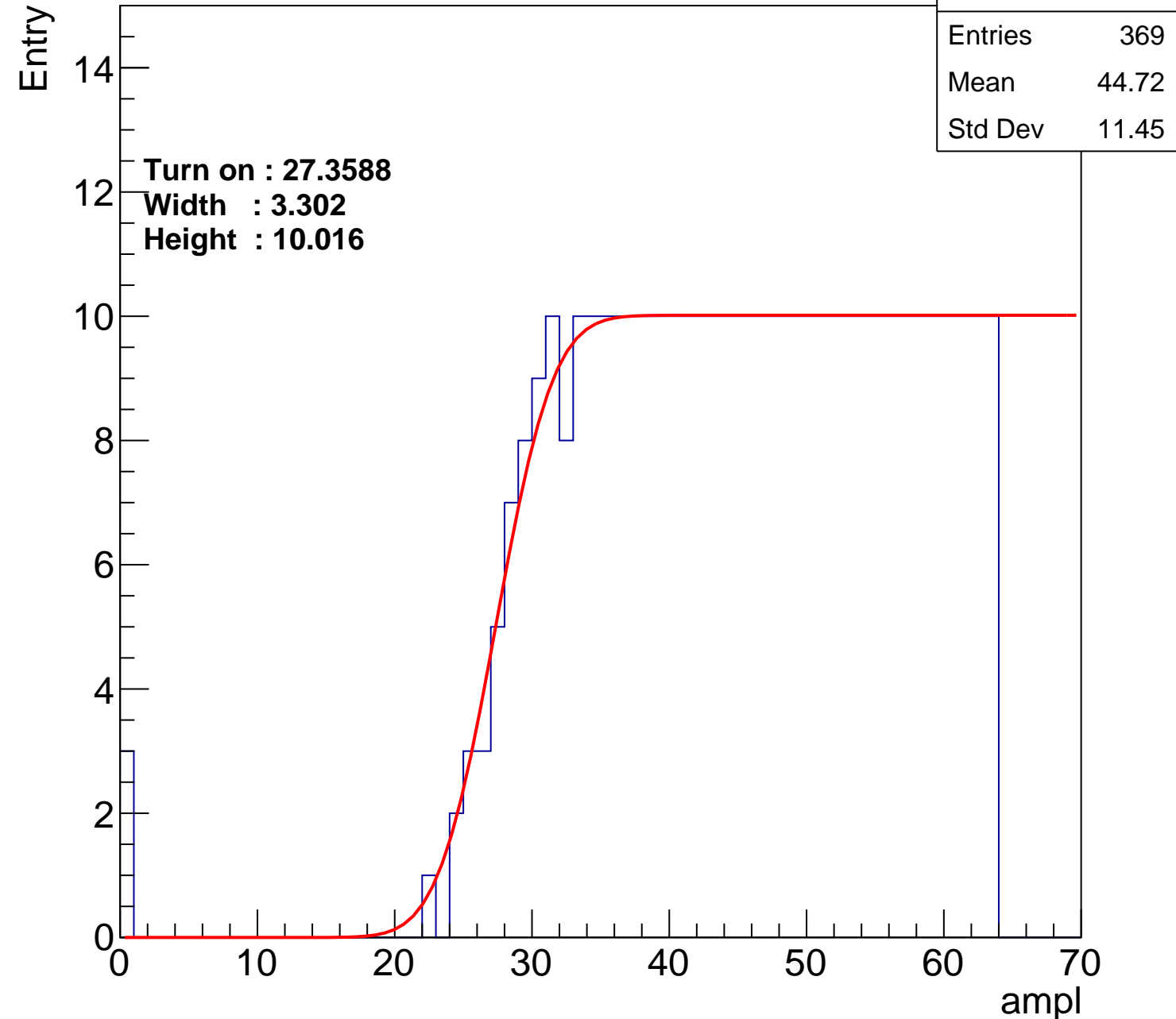
Width : 3.302

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.54
Std Dev	10.6

Turn on : 28.5948

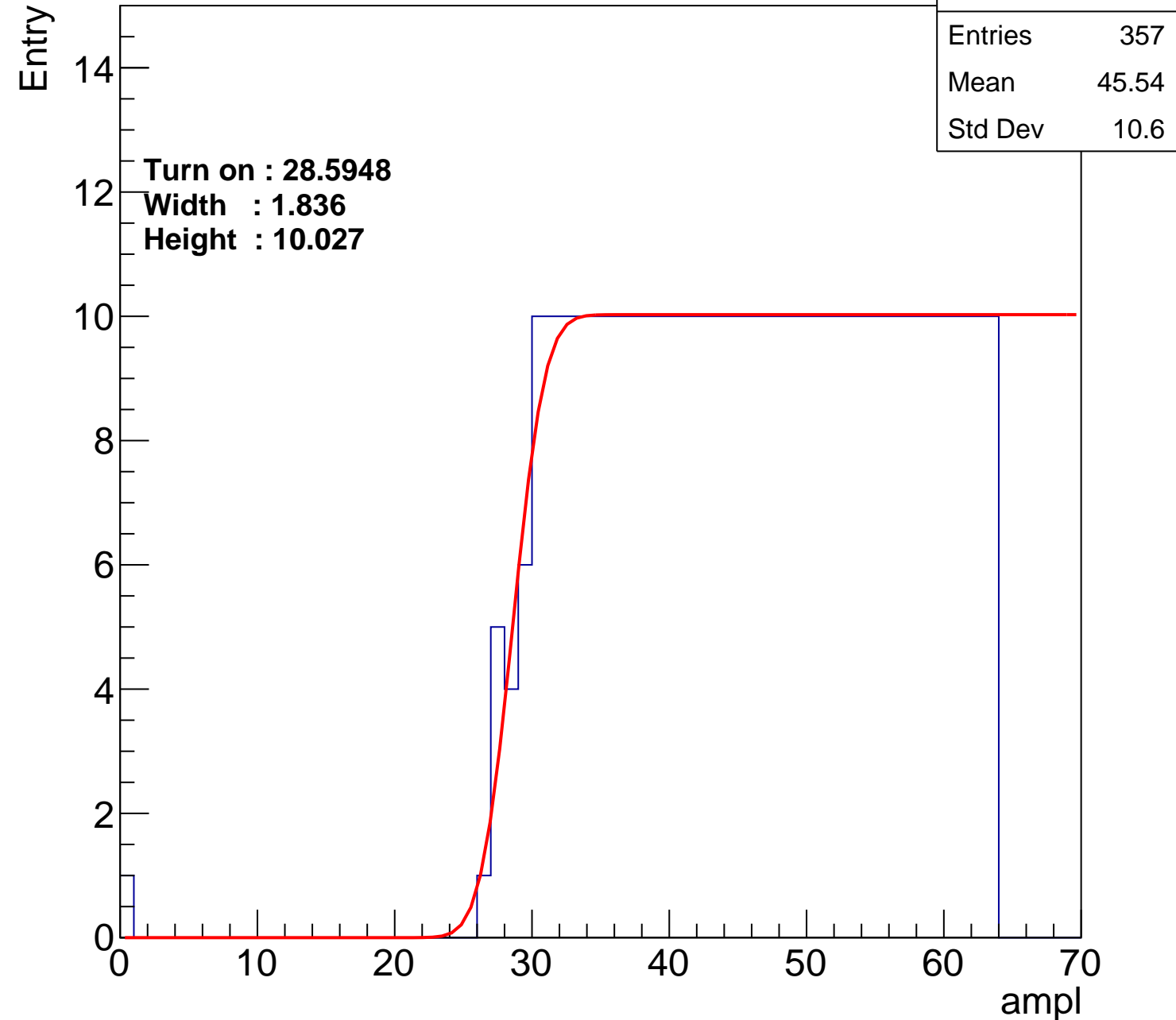
Width : 1.836

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 26.8555

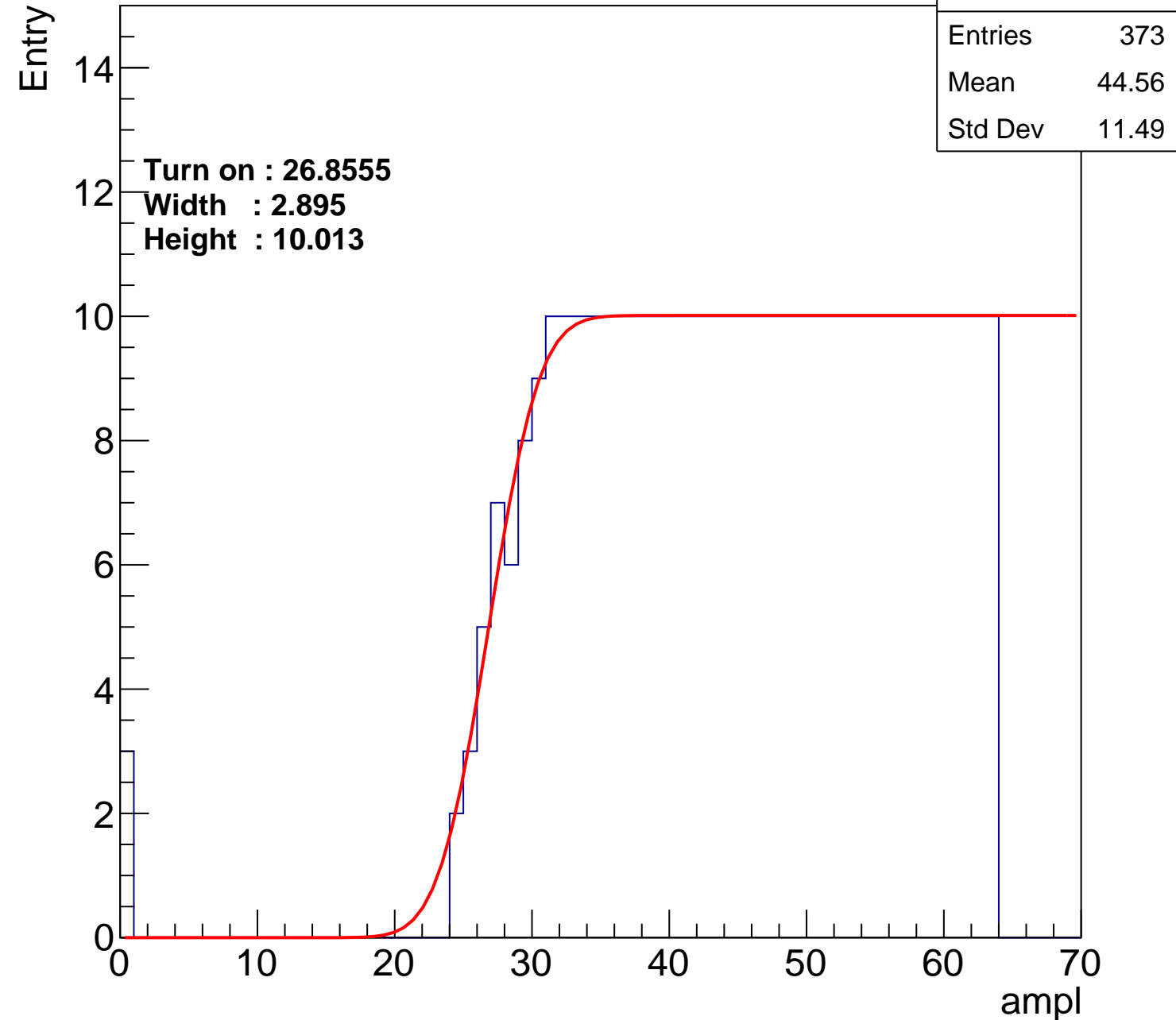
Width : 2.895

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.05
Std Dev	10.92

Turn on : 27.8440

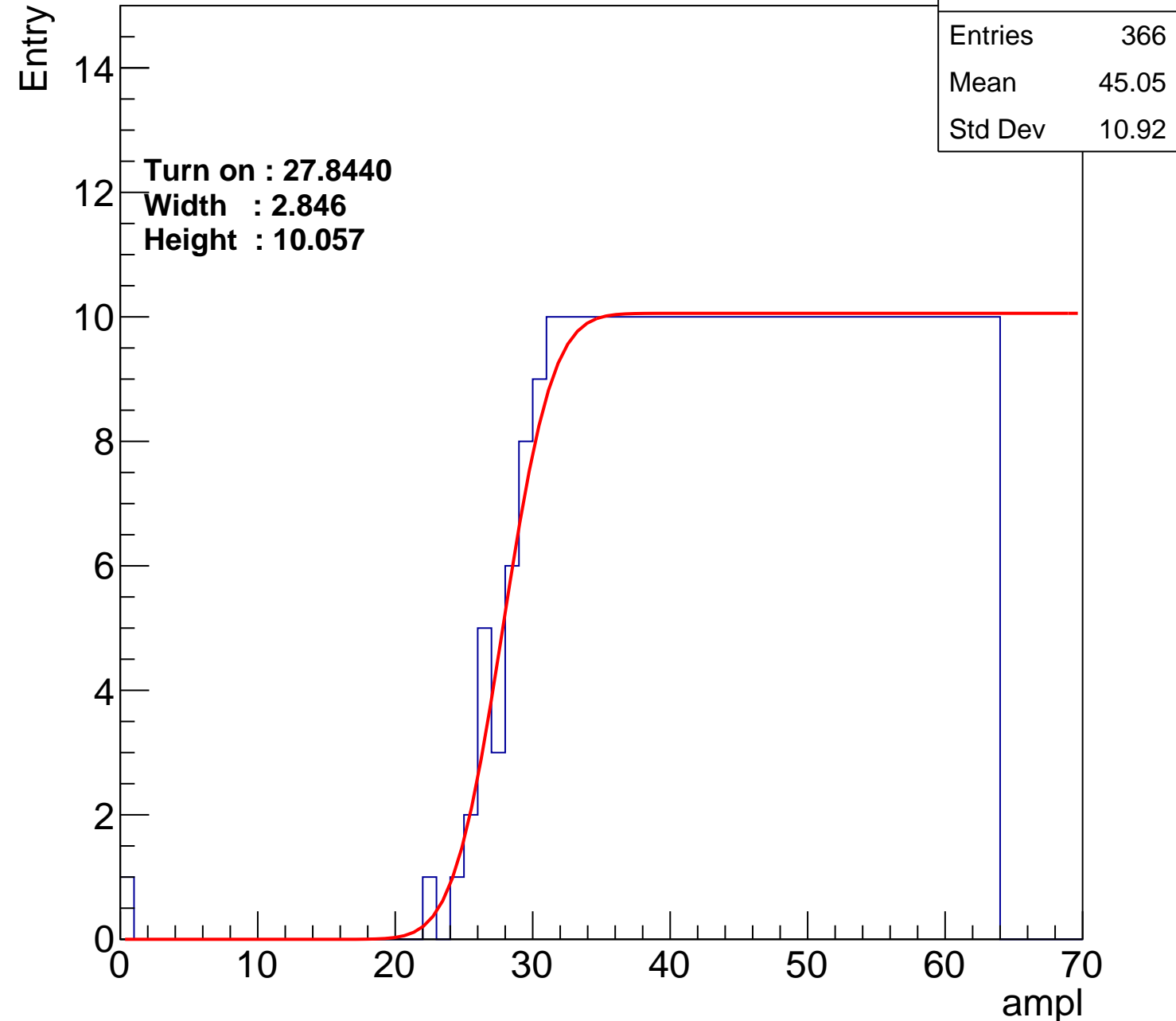
Width : 2.846

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.33
Std Dev	10.97

Turn on : 28.7277

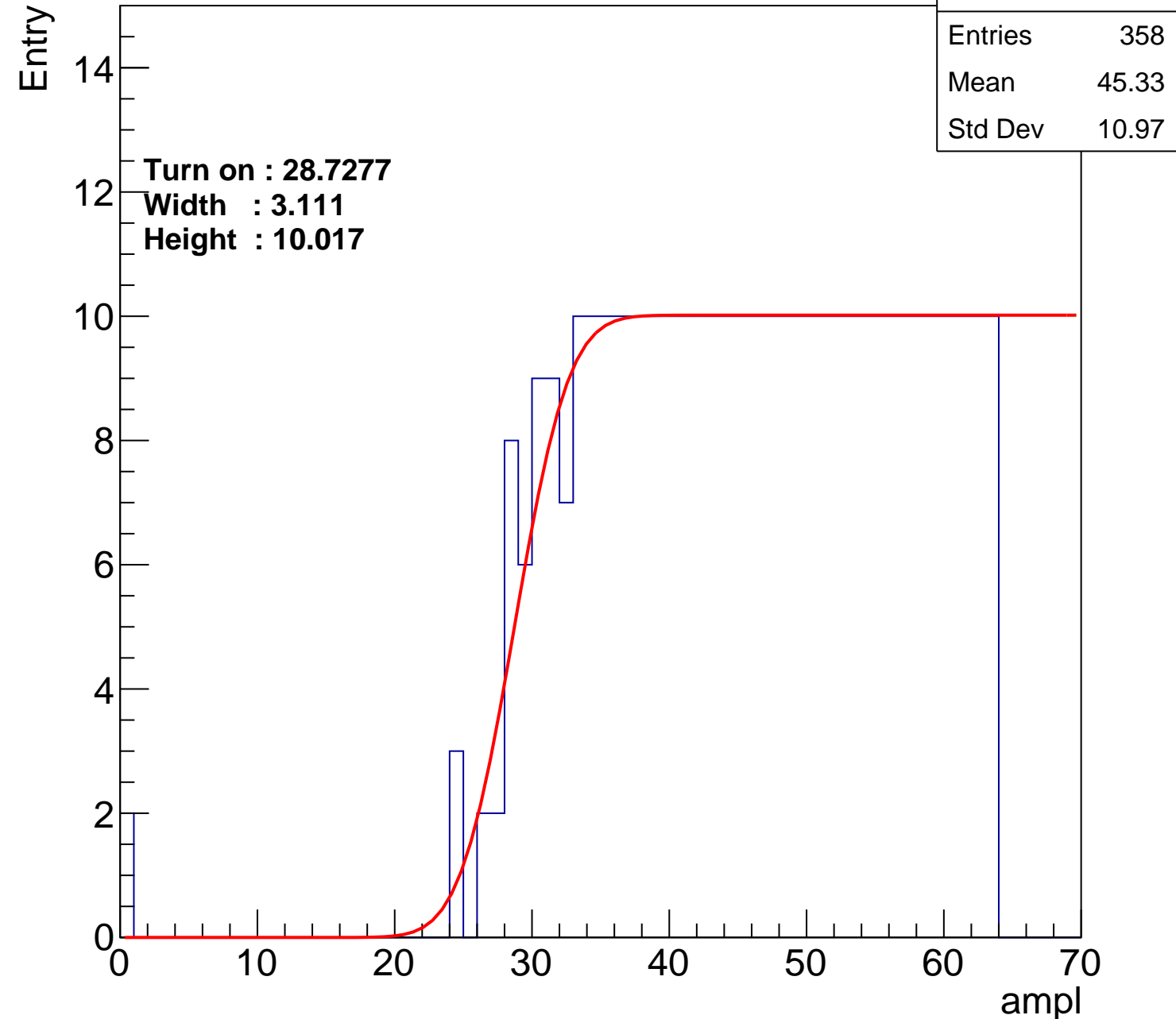
Width : 3.111

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.89
Std Dev	10.99

Turn on : 27.0519

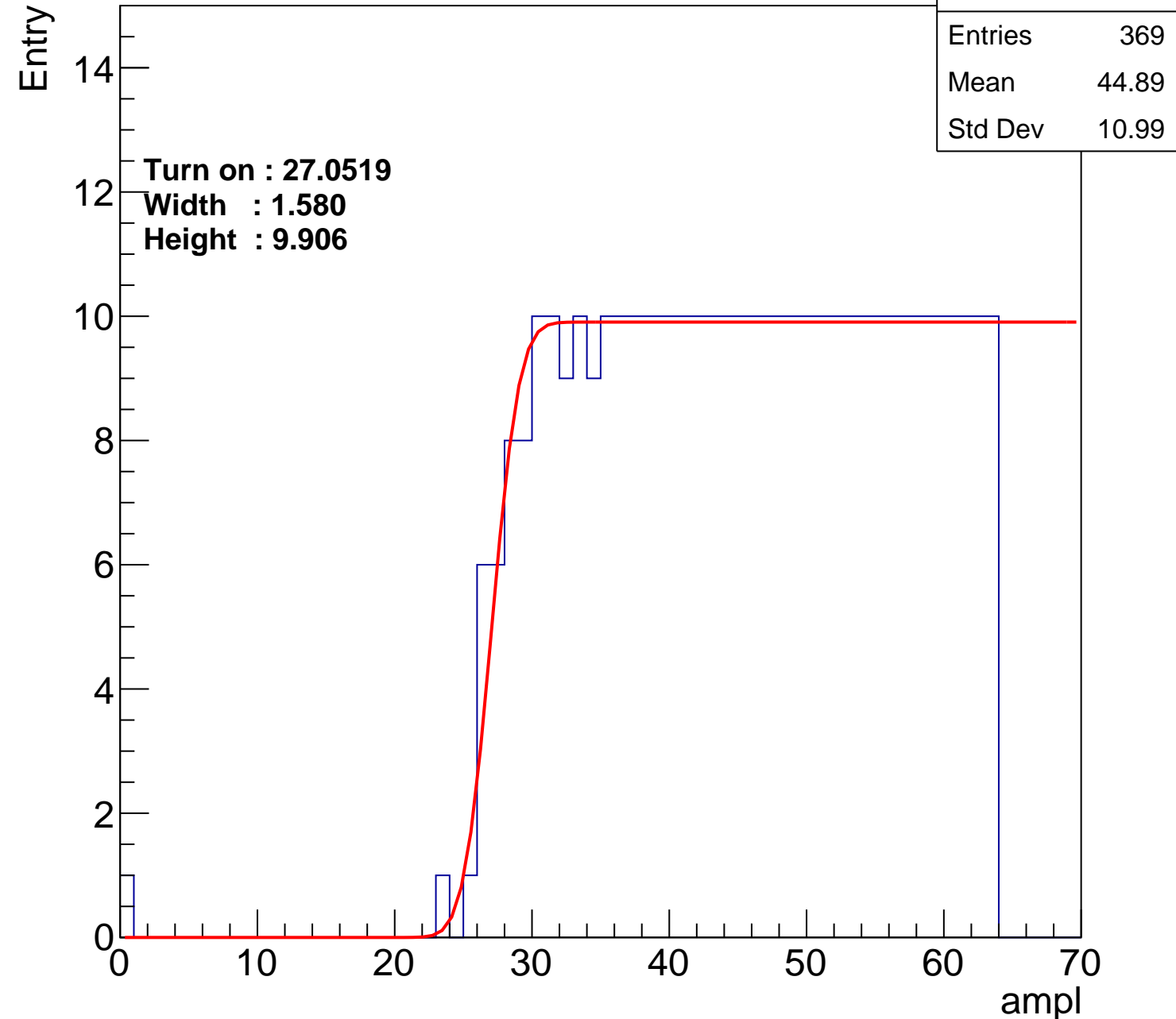
Width : 1.580

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 27.9012

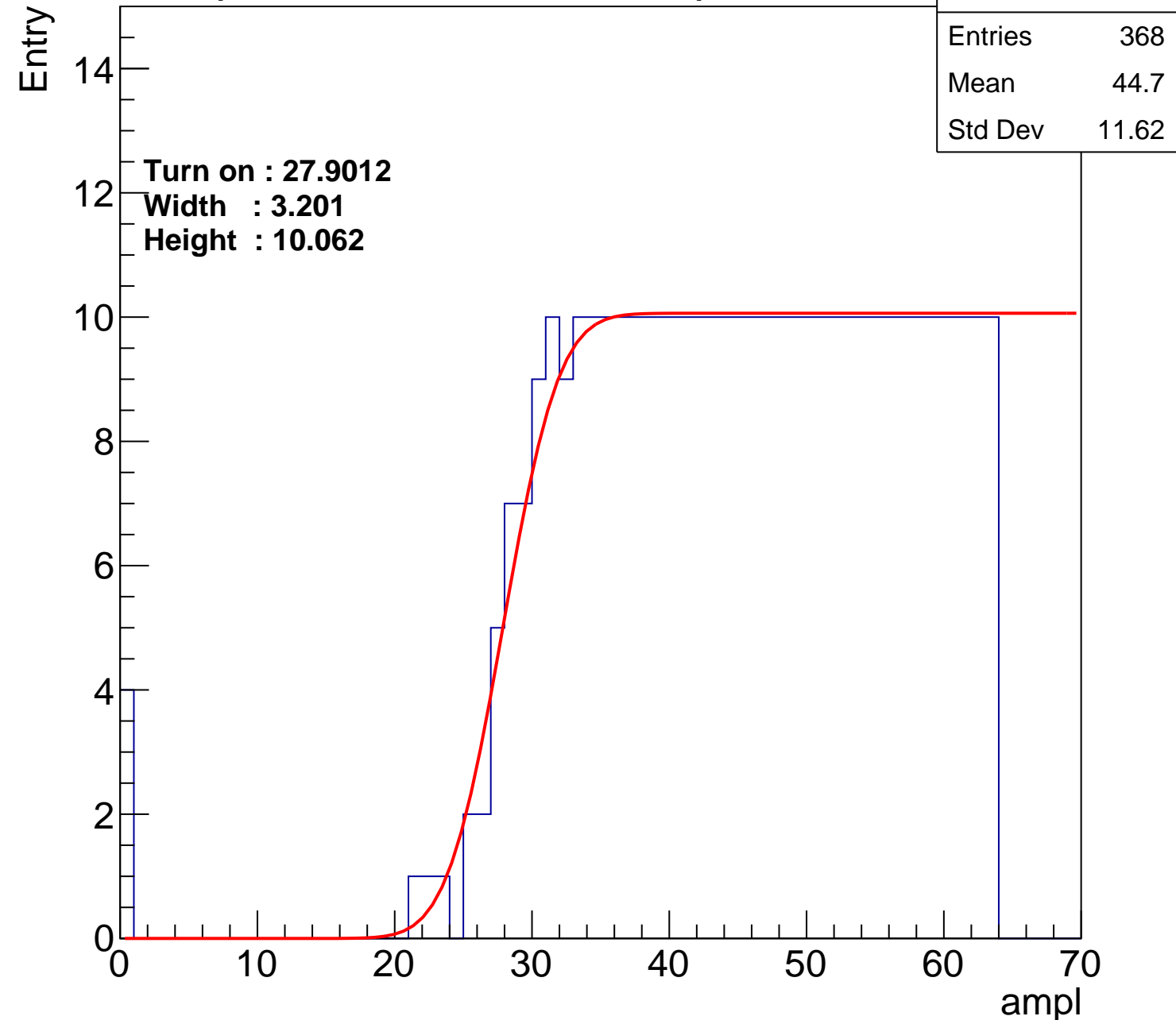
Width : 3.201

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.49
Std Dev	10.84

Turn on : 28.8120

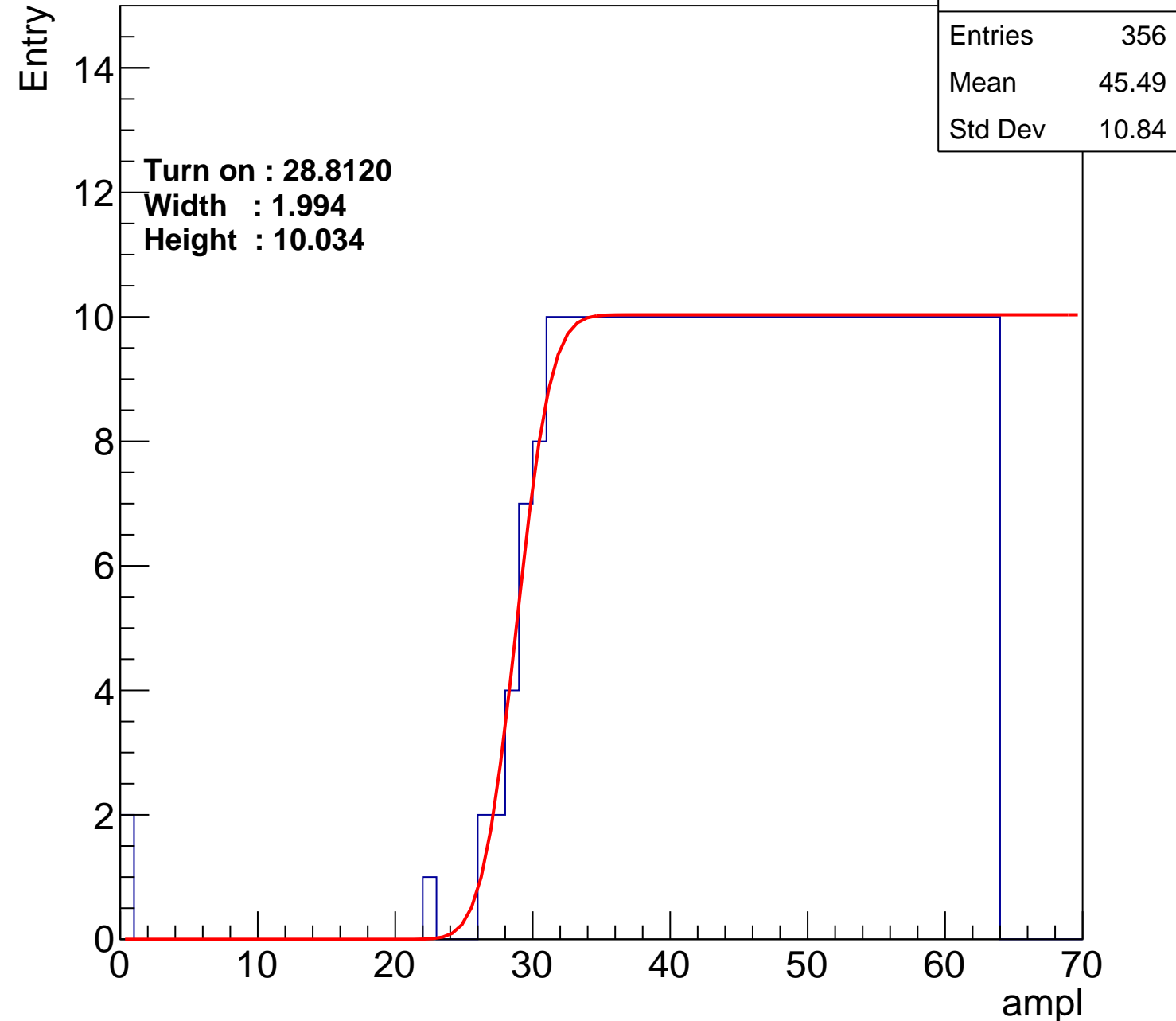
Width : 1.994

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch39

calib_packv5_042523_0143.root, FC#8, port C1

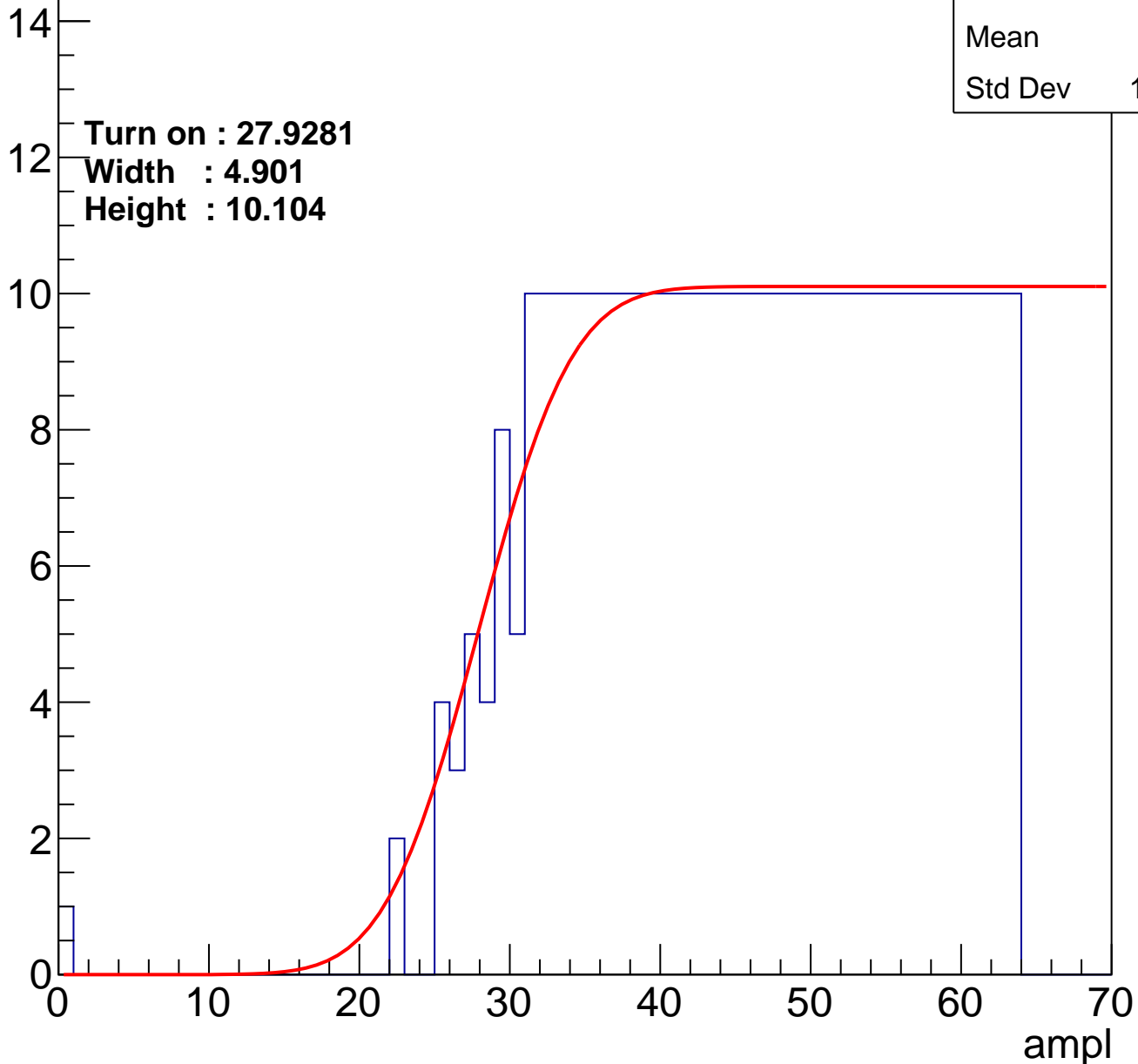
Entries	362
Mean	45.2
Std Dev	10.89

Turn on : 27.9281

Width : 4.901

Height : 10.104

Entry



B0L002S, U22-ch40

calib_packv5_042523_0143.root, FC#8, port C1

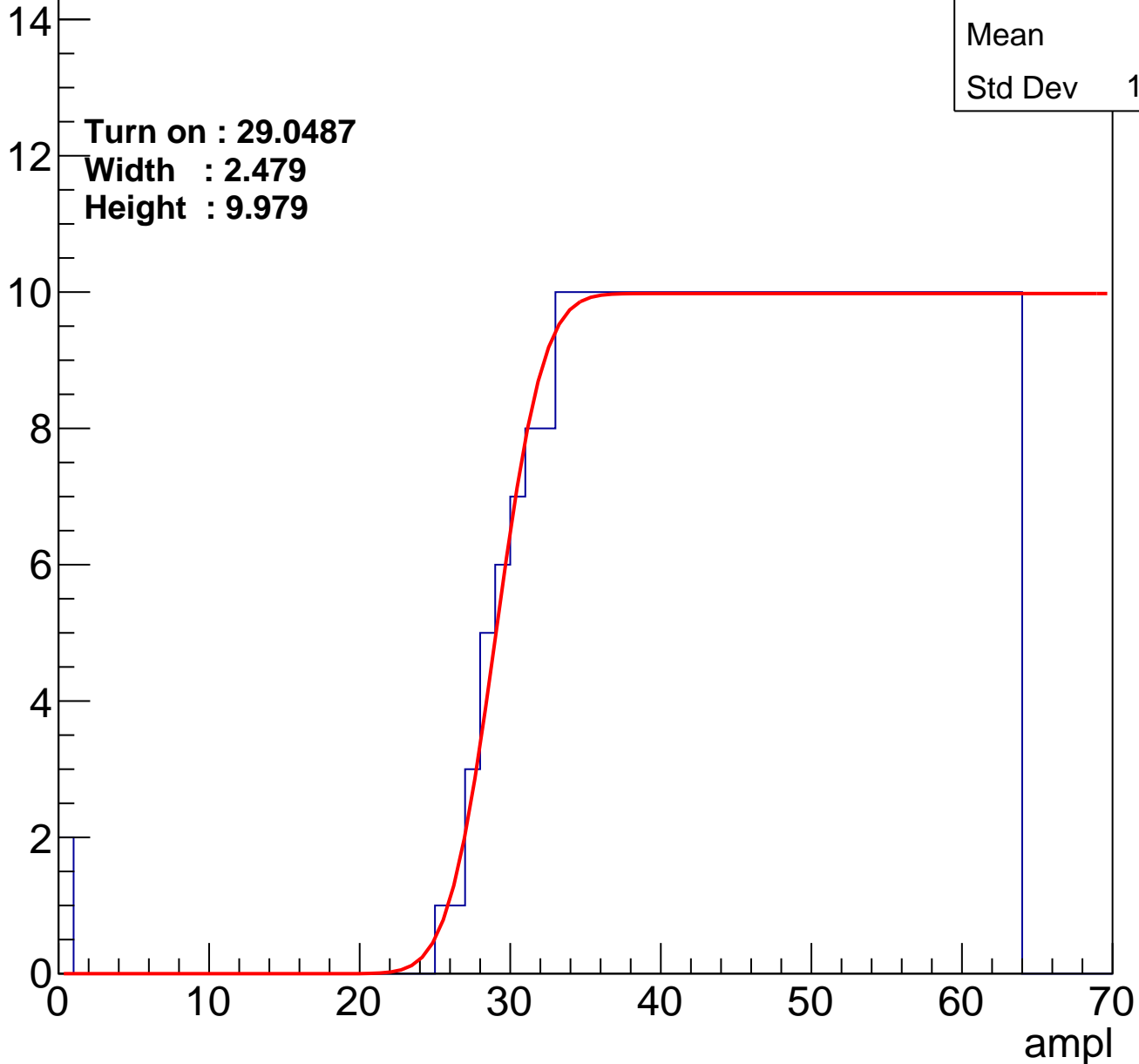
Entries	351
Mean	45.7
Std Dev	10.77

Turn on : 29.0487

Width : 2.479

Height : 9.979

Entry



B0L002S, U22-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.27
Std Dev	11.17

Turn on : 28.4292

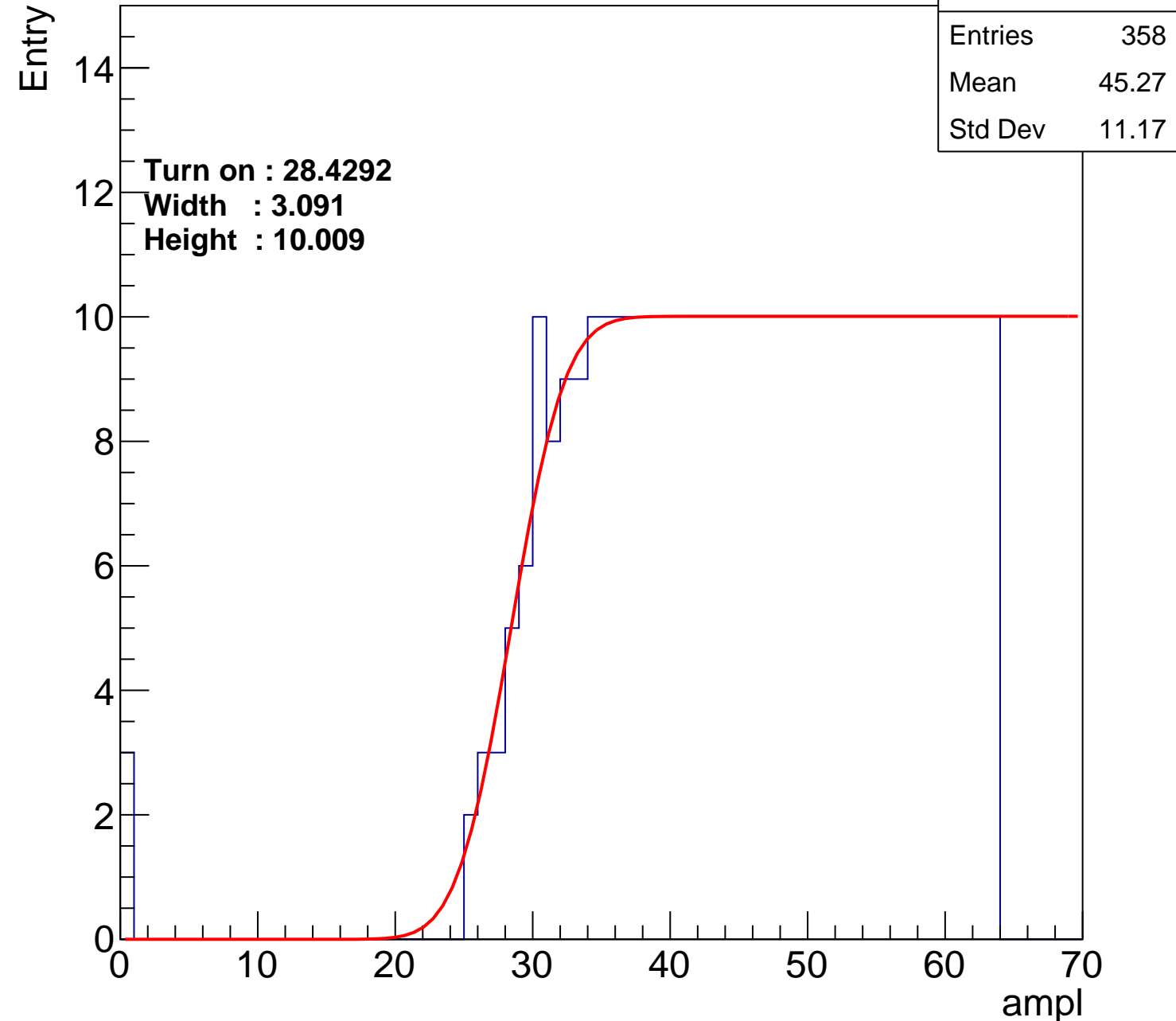
Width : 3.091

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch42

calib_packv5_042523_0143.root, FC#8, port C1

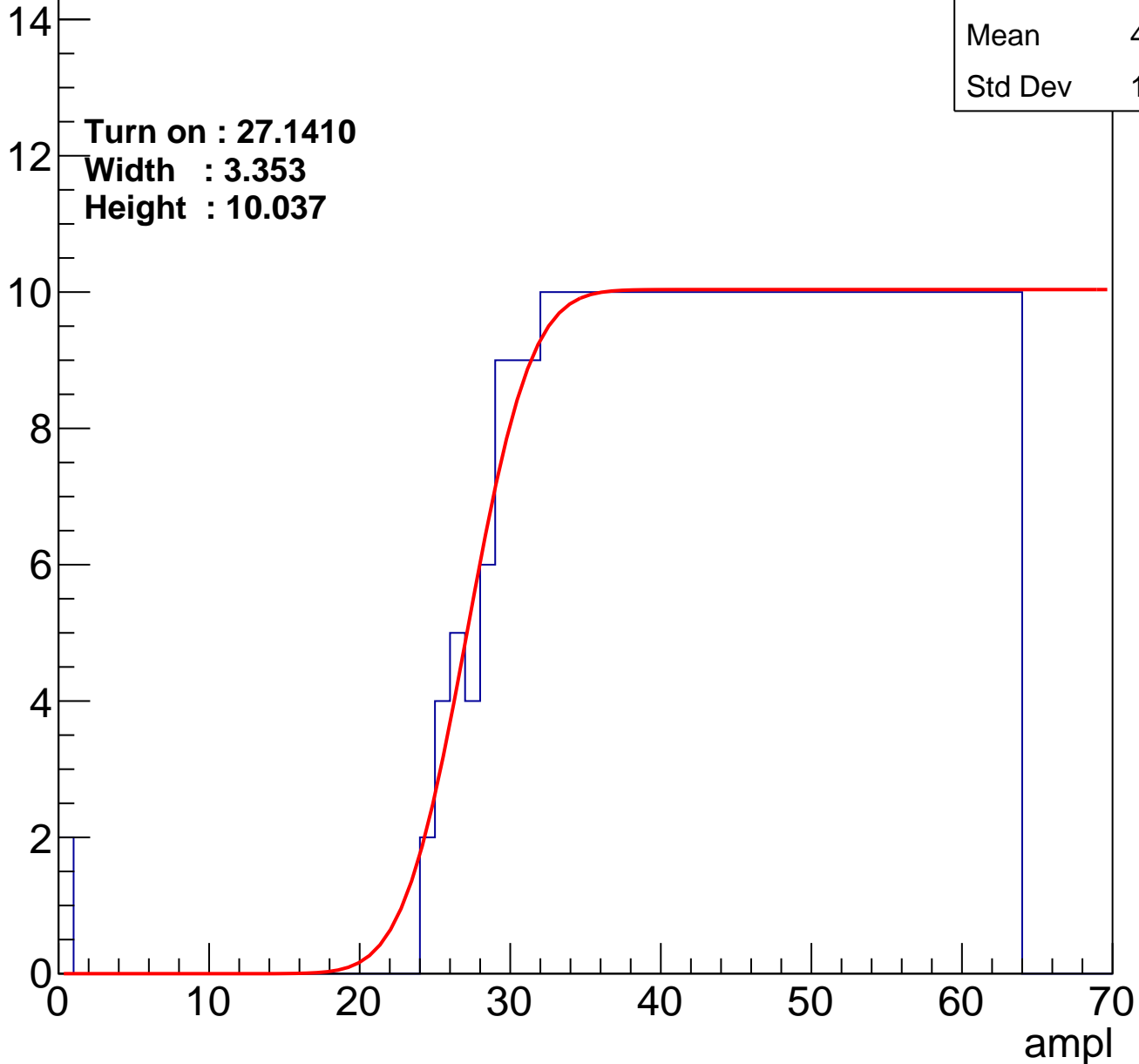
Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.1410

Width : 3.353

Height : 10.037

Entry



B0L002S, U22-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.92
Std Dev	11.87

Turn on : 25.6699

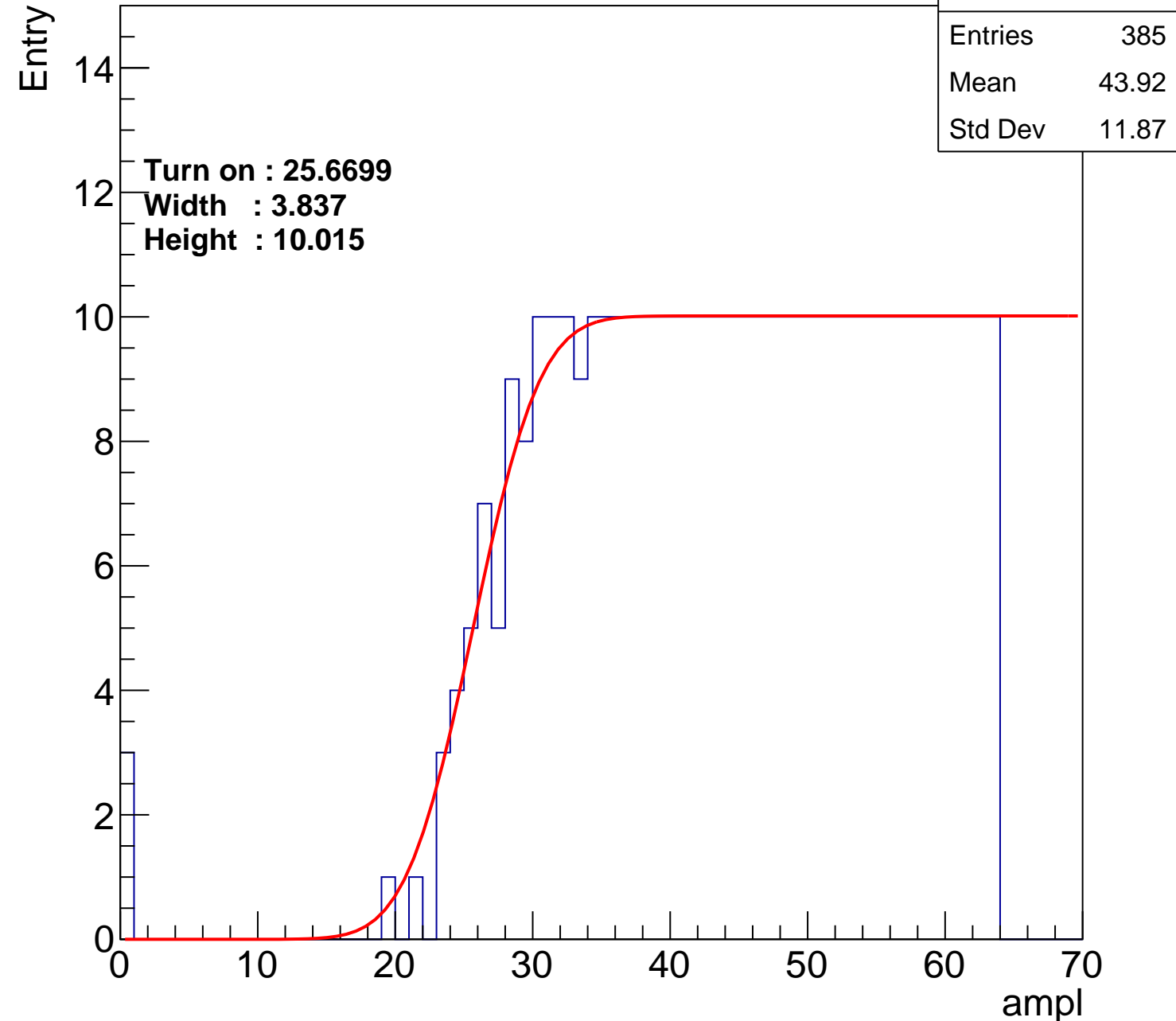
Width : 3.837

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.05
Std Dev	11.31

Turn on : 28.9766

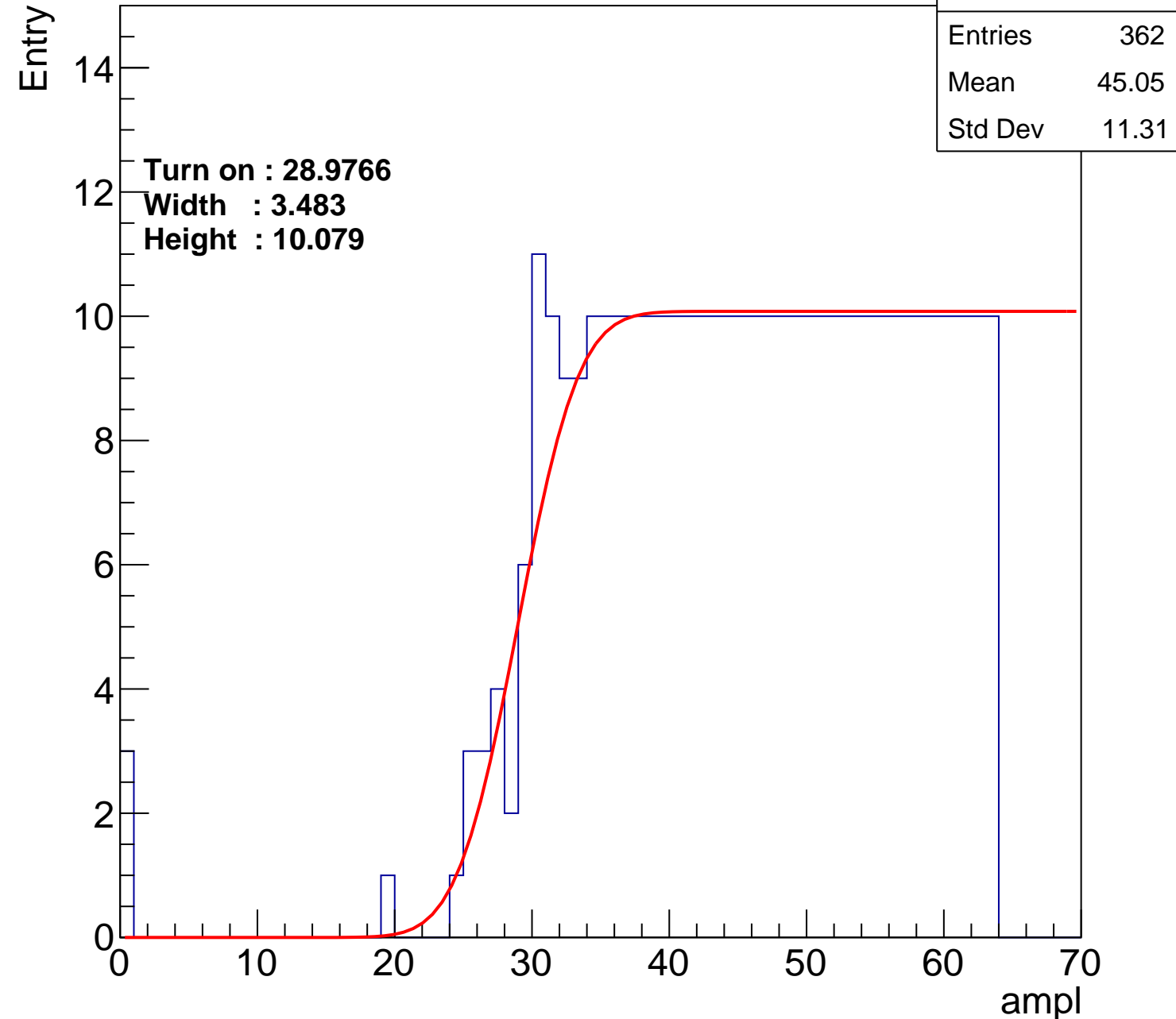
Width : 3.483

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.49
Std Dev	11.87

Turn on : 27.7381

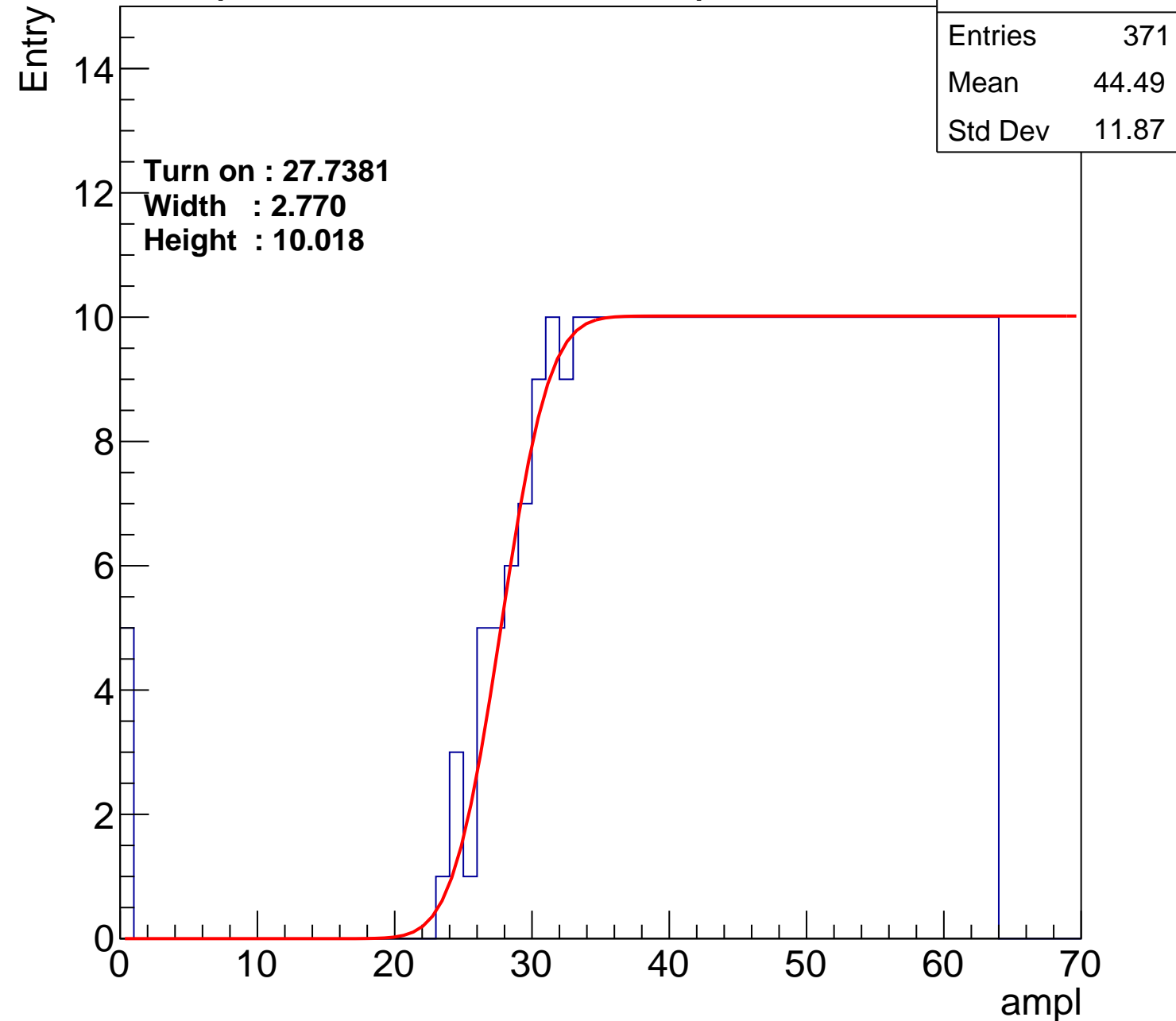
Width : 2.770

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch46

calib_packv5_042523_0143.root, FC#8, port C1

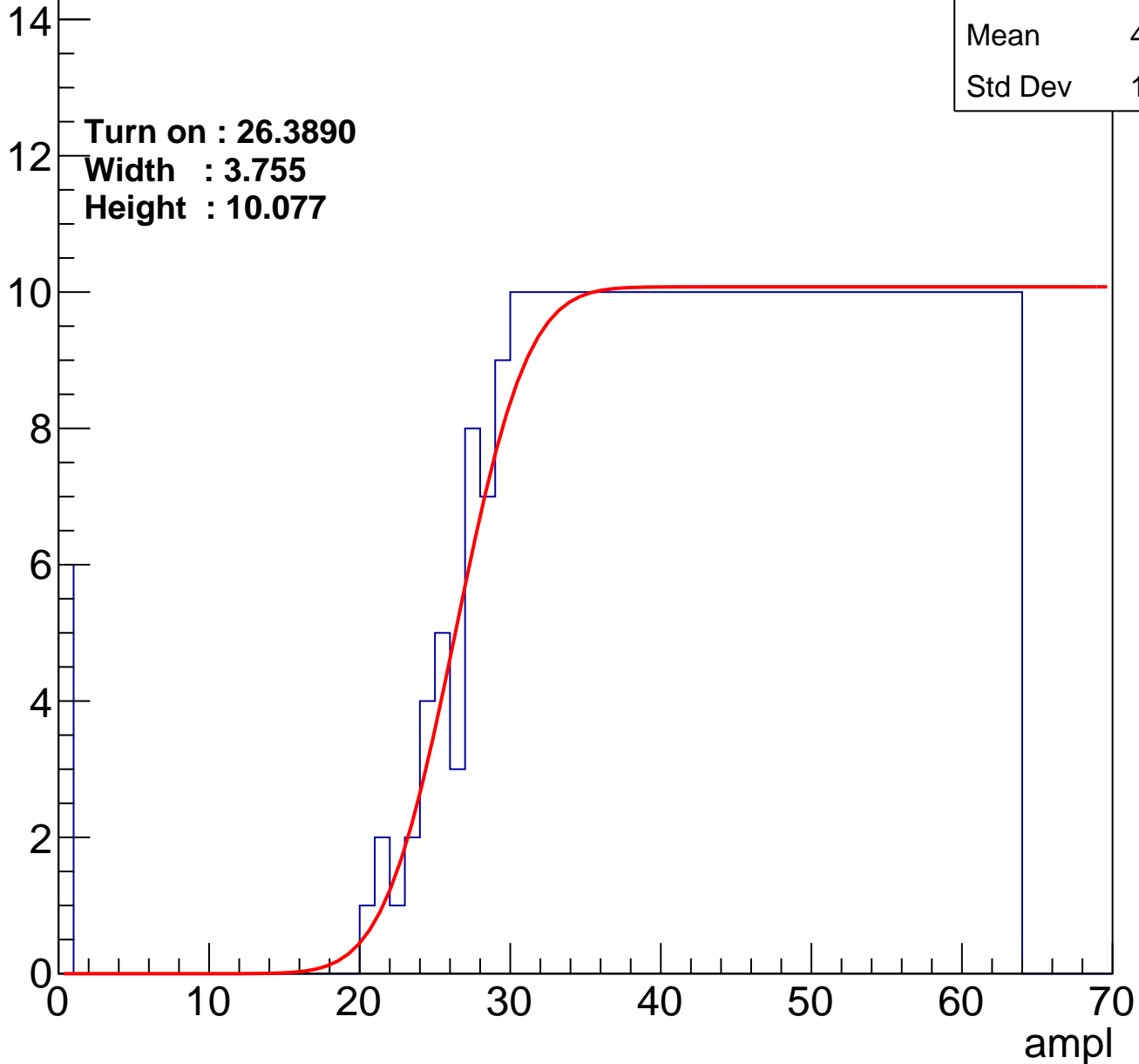
Entries	388
Mean	43.59
Std Dev	12.43

Turn on : 26.3890

Width : 3.755

Height : 10.077

Entry



B0L002S, U22-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.14
Std Dev	11.02

Turn on : 28.4175

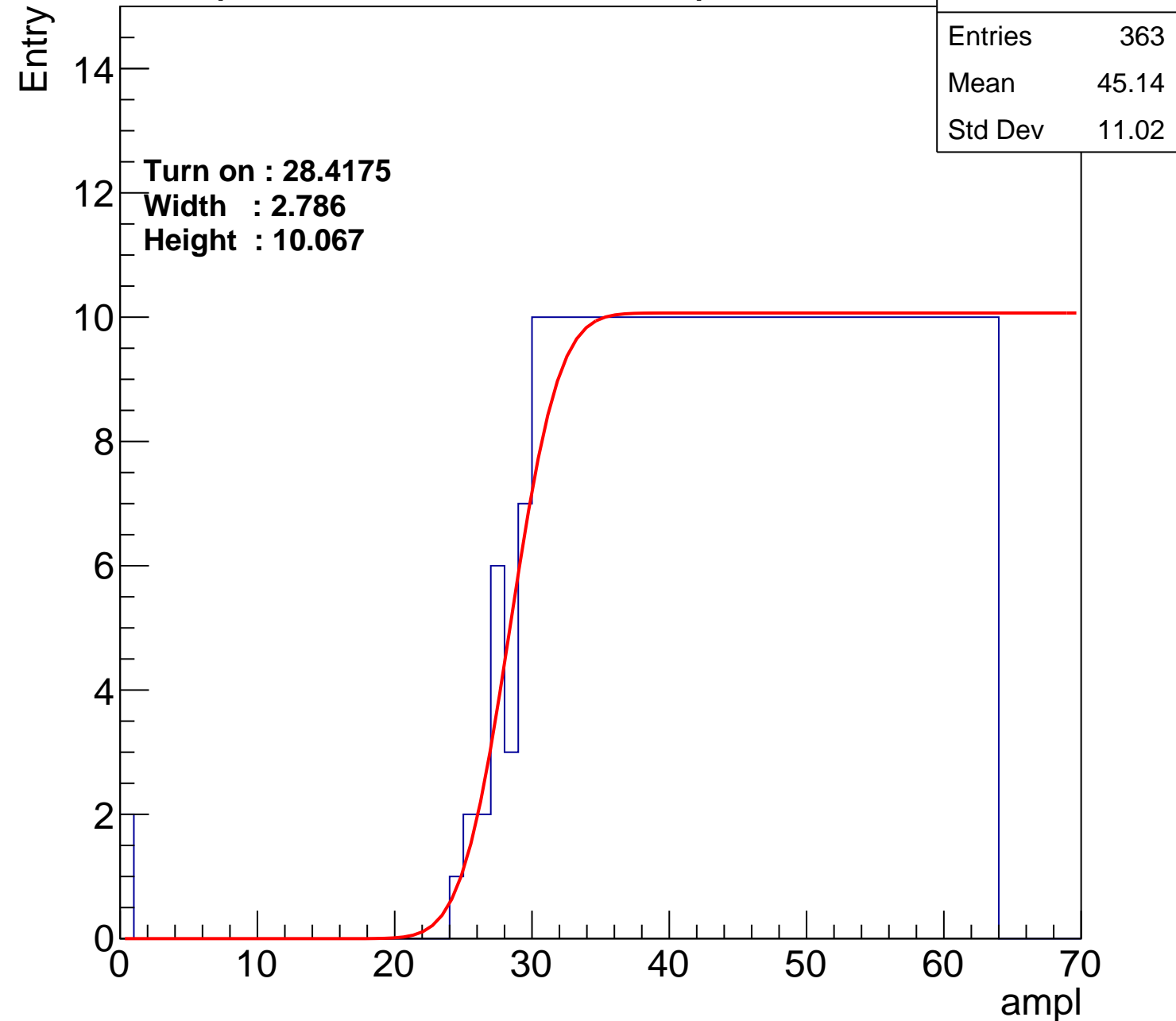
Width : 2.786

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.54
Std Dev	12.44

Turn on : 26.0370

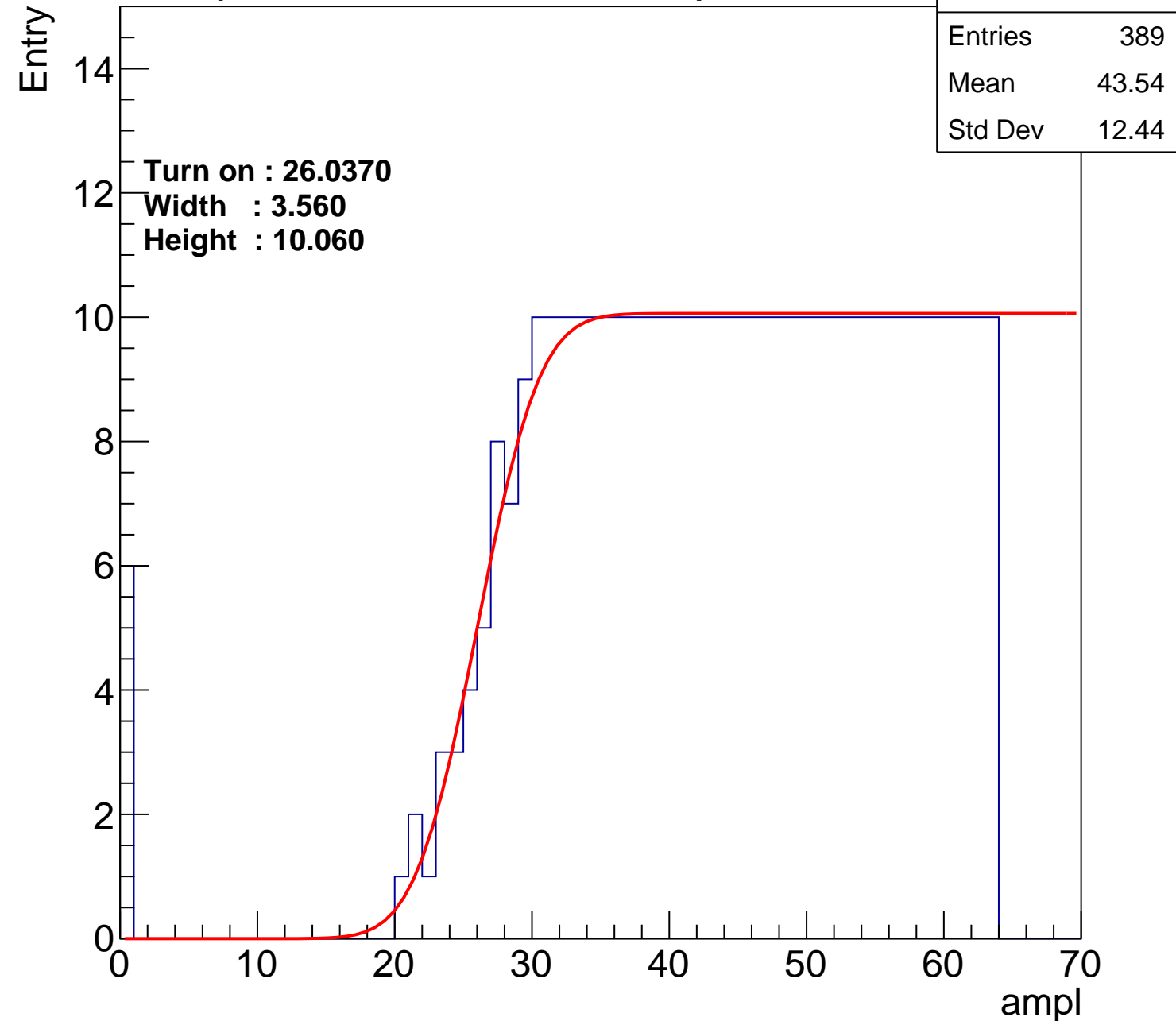
Width : 3.560

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 26.4899

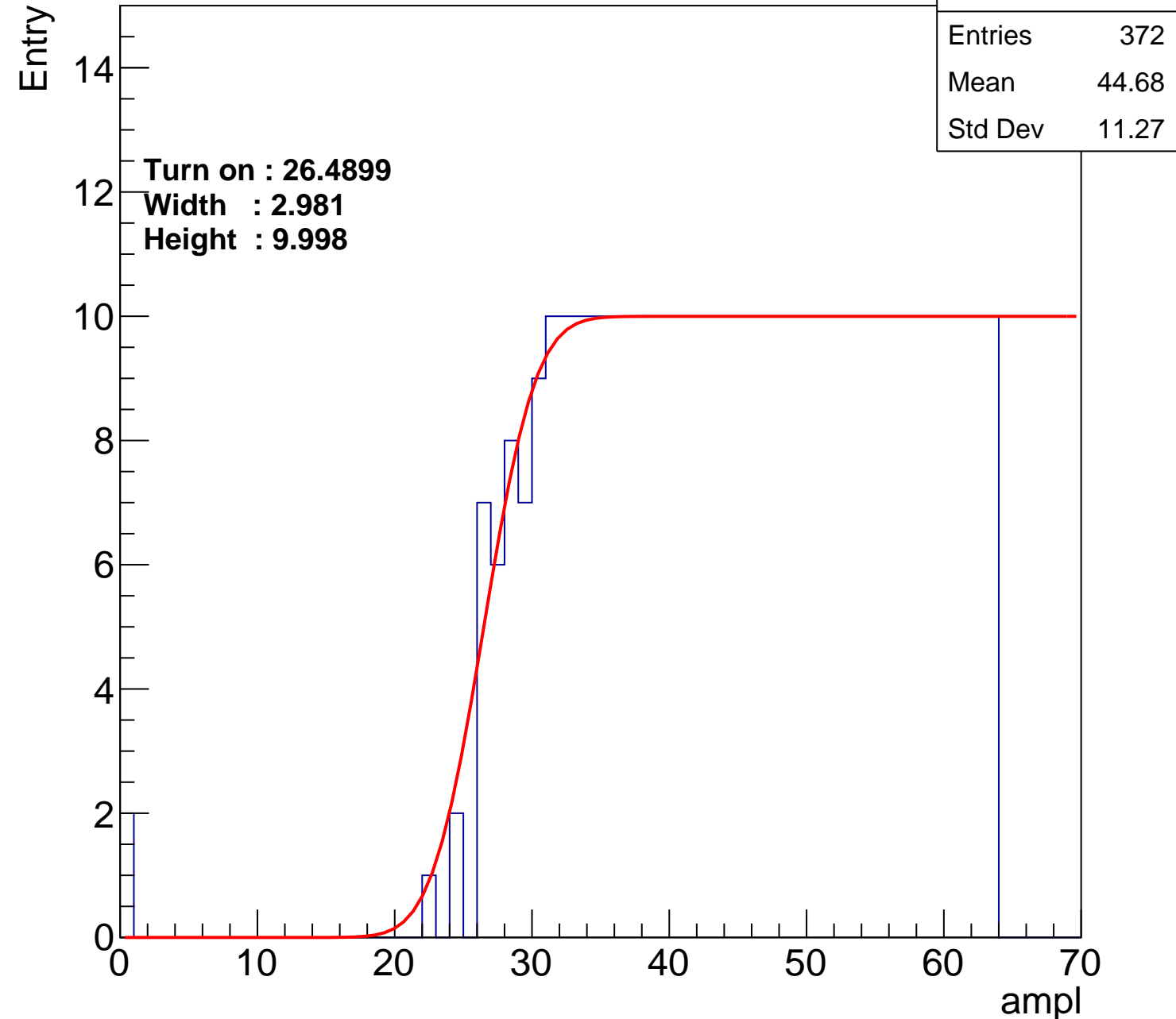
Width : 2.981

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 27.1832

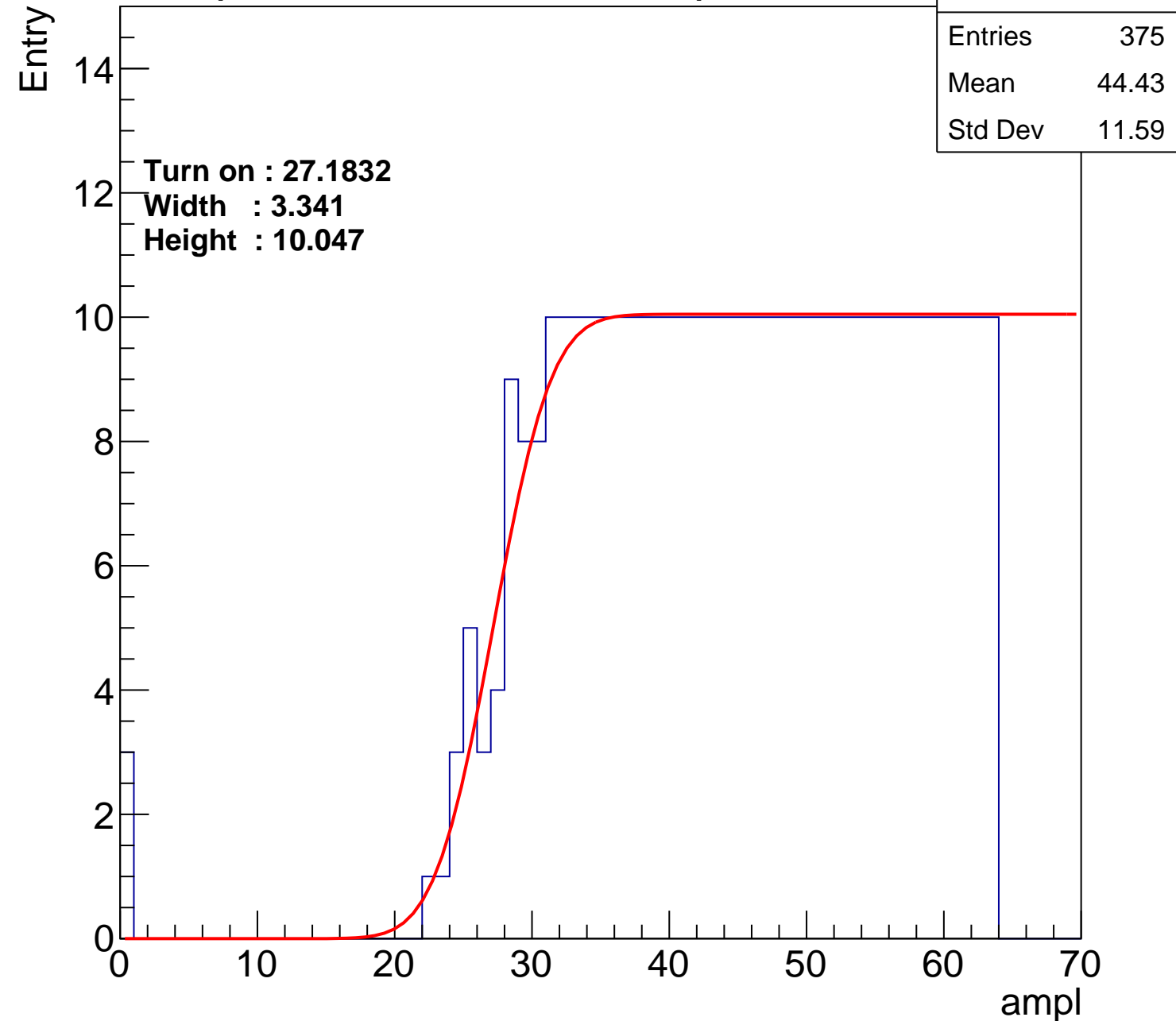
Width : 3.341

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch51

calib_packv5_042523_0143.root, FC#8, port C1

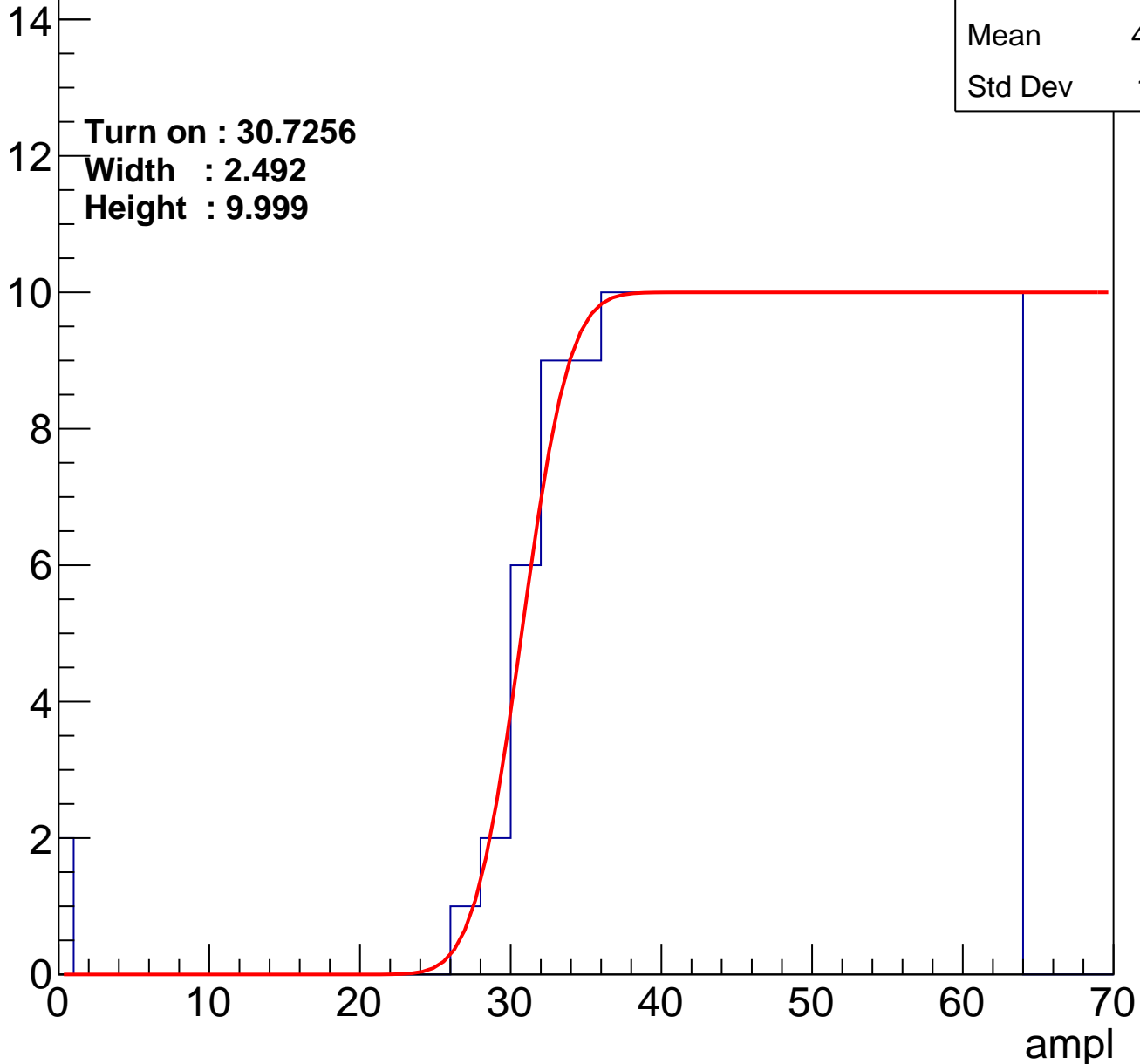
Entries	336
Mean	46.43
Std Dev	10.41

Turn on : 30.7256

Width : 2.492

Height : 9.999

Entry



B0L002S, U22-ch52

calib_packv5_042523_0143.root, FC#8, port C1

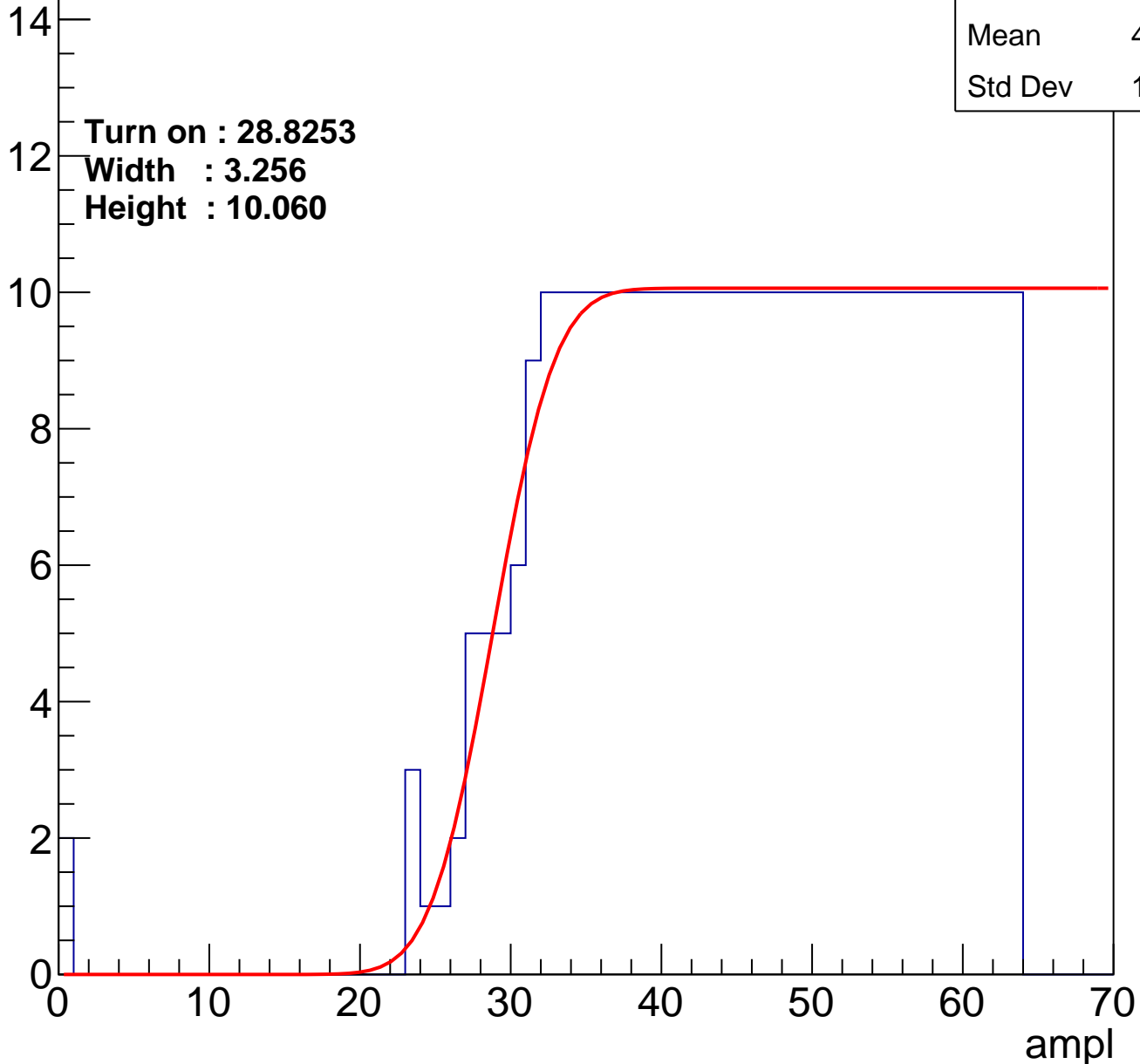
Entries	359
Mean	45.26
Std Dev	11.04

Turn on : 28.8253

Width : 3.256

Height : 10.060

Entry



B0L002S, U22-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.78
Std Dev	11.23

Turn on : 26.9434

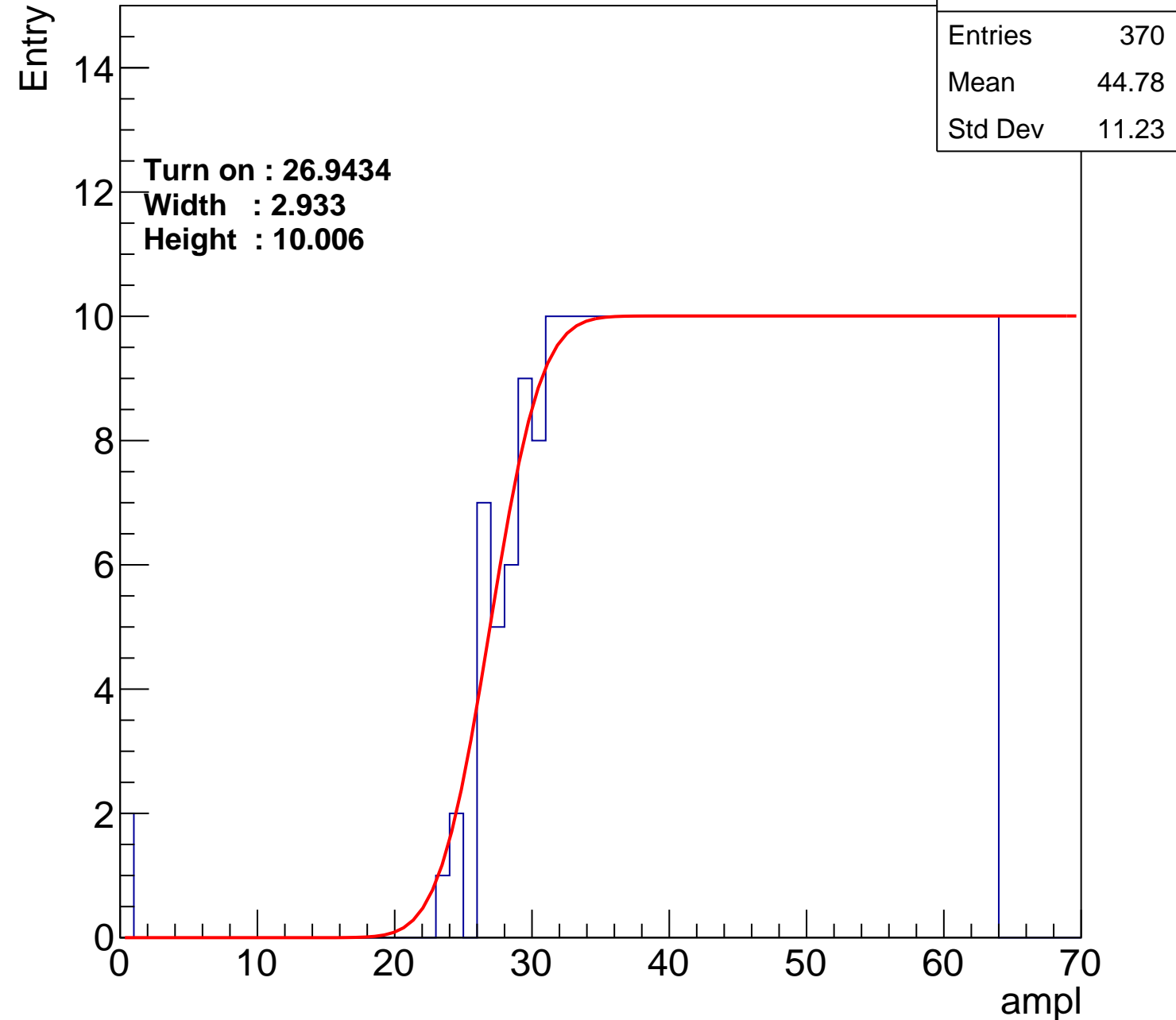
Width : 2.933

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch54

calib_packv5_042523_0143.root, FC#8, port C1

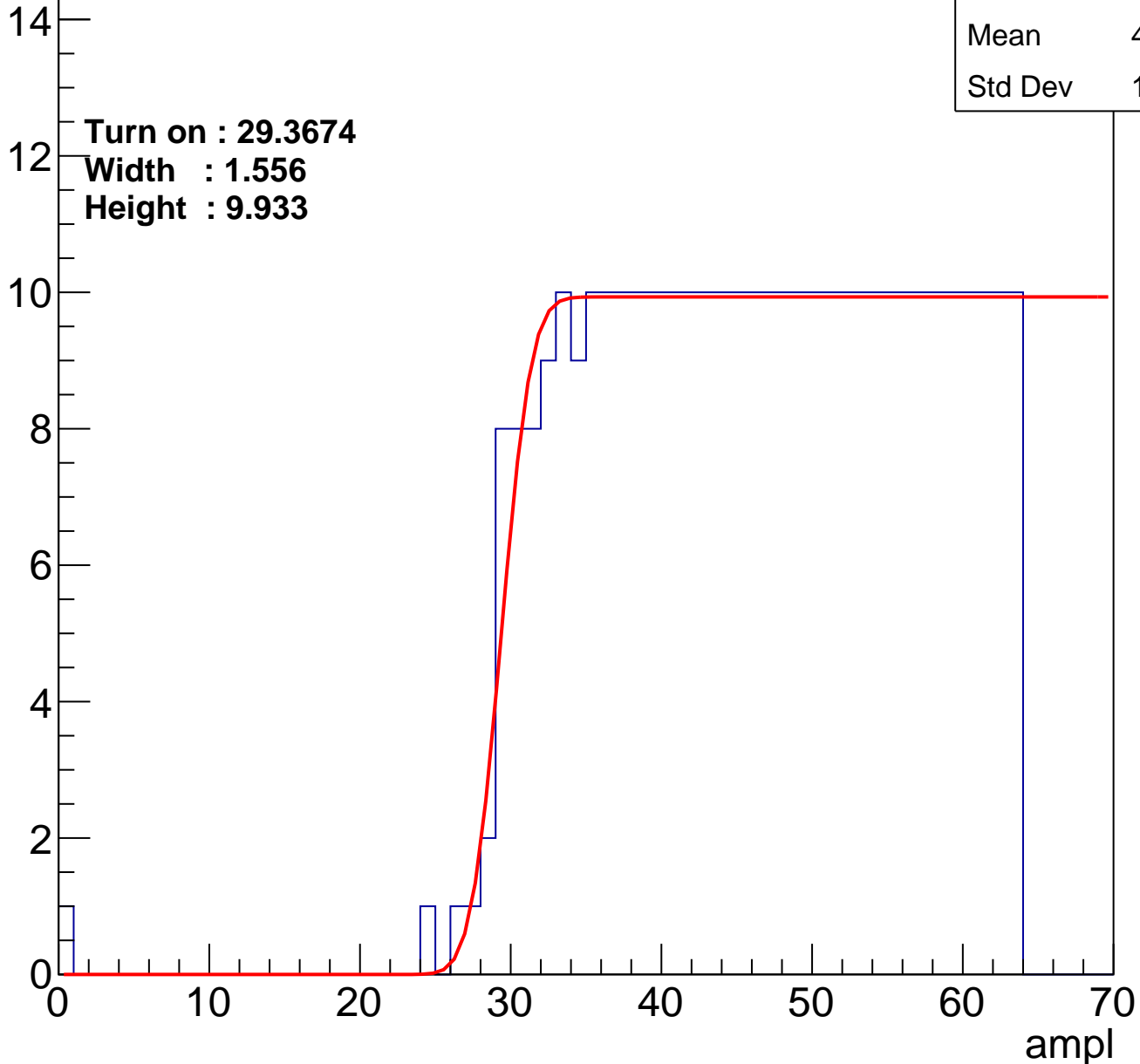
Entries	348
Mean	45.94
Std Dev	10.43

Turn on : 29.3674

Width : 1.556

Height : 9.933

Entry



B0L002S, U22-ch55

calib_packv5_042523_0143.root, FC#8, port C1

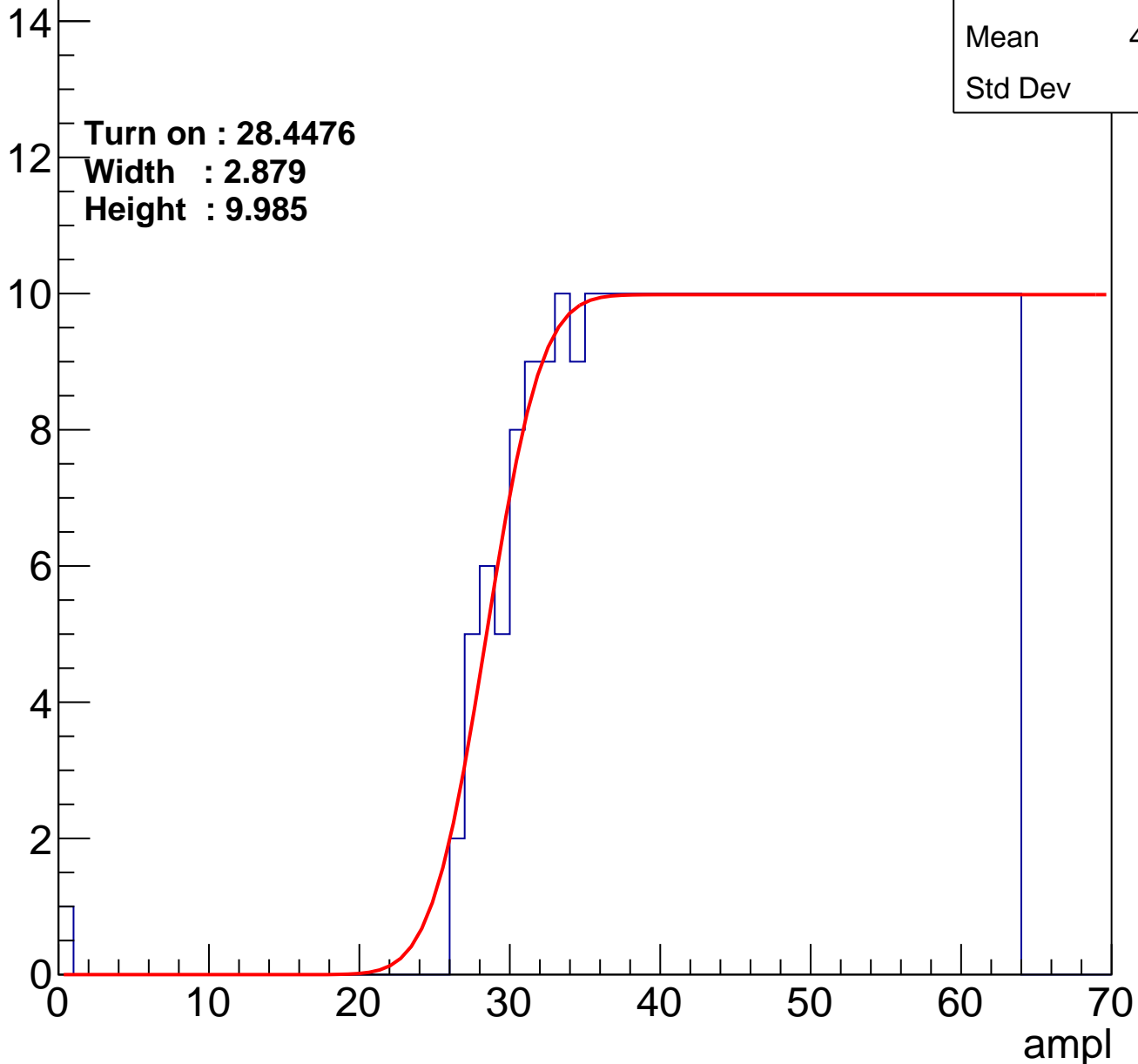
Entries	354
Mean	45.63
Std Dev	10.6

Turn on : 28.4476

Width : 2.879

Height : 9.985

Entry



B0L002S, U22-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.33
Std Dev	11.78

Turn on : 27.2359

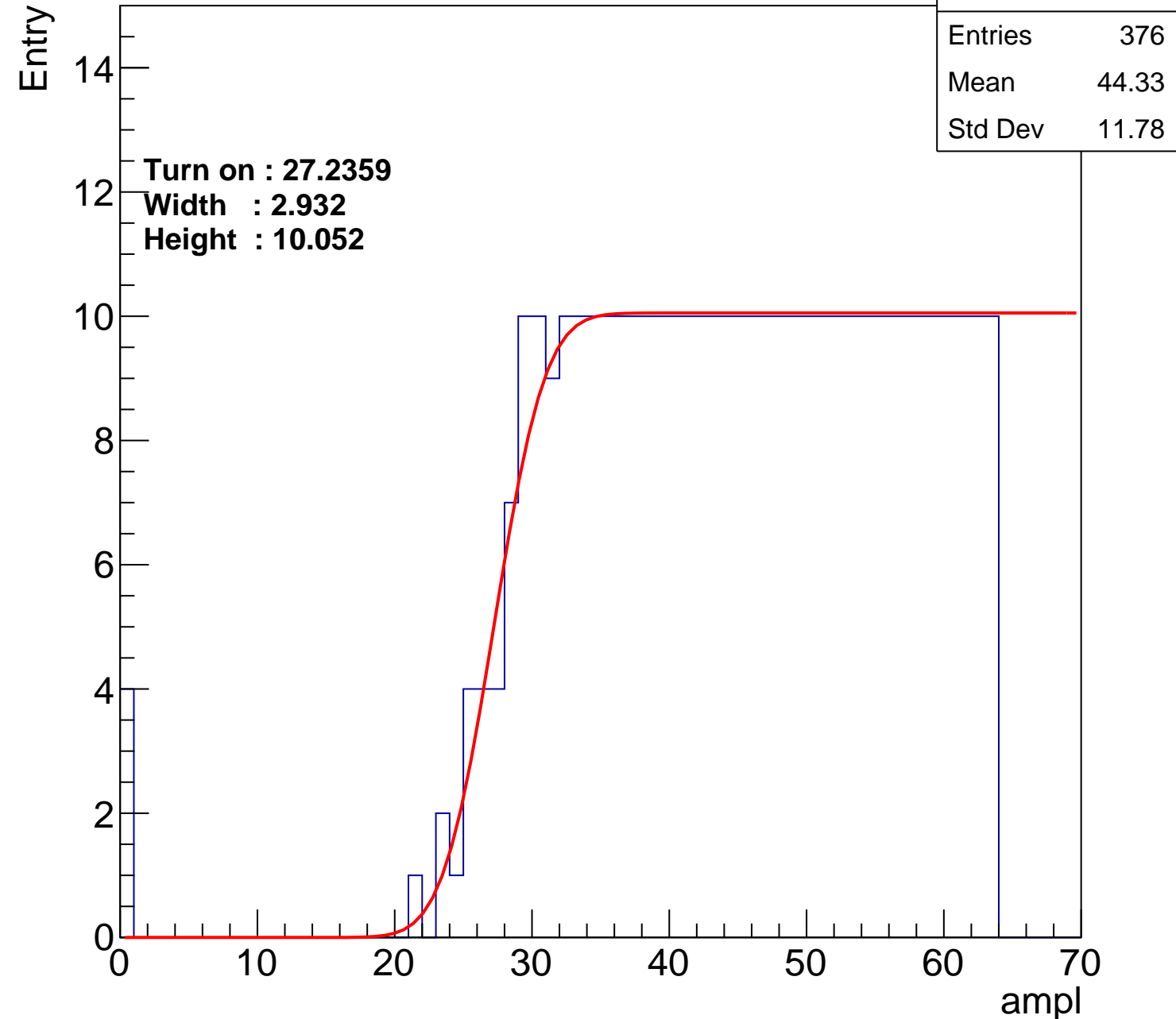
Width : 2.932

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.8629

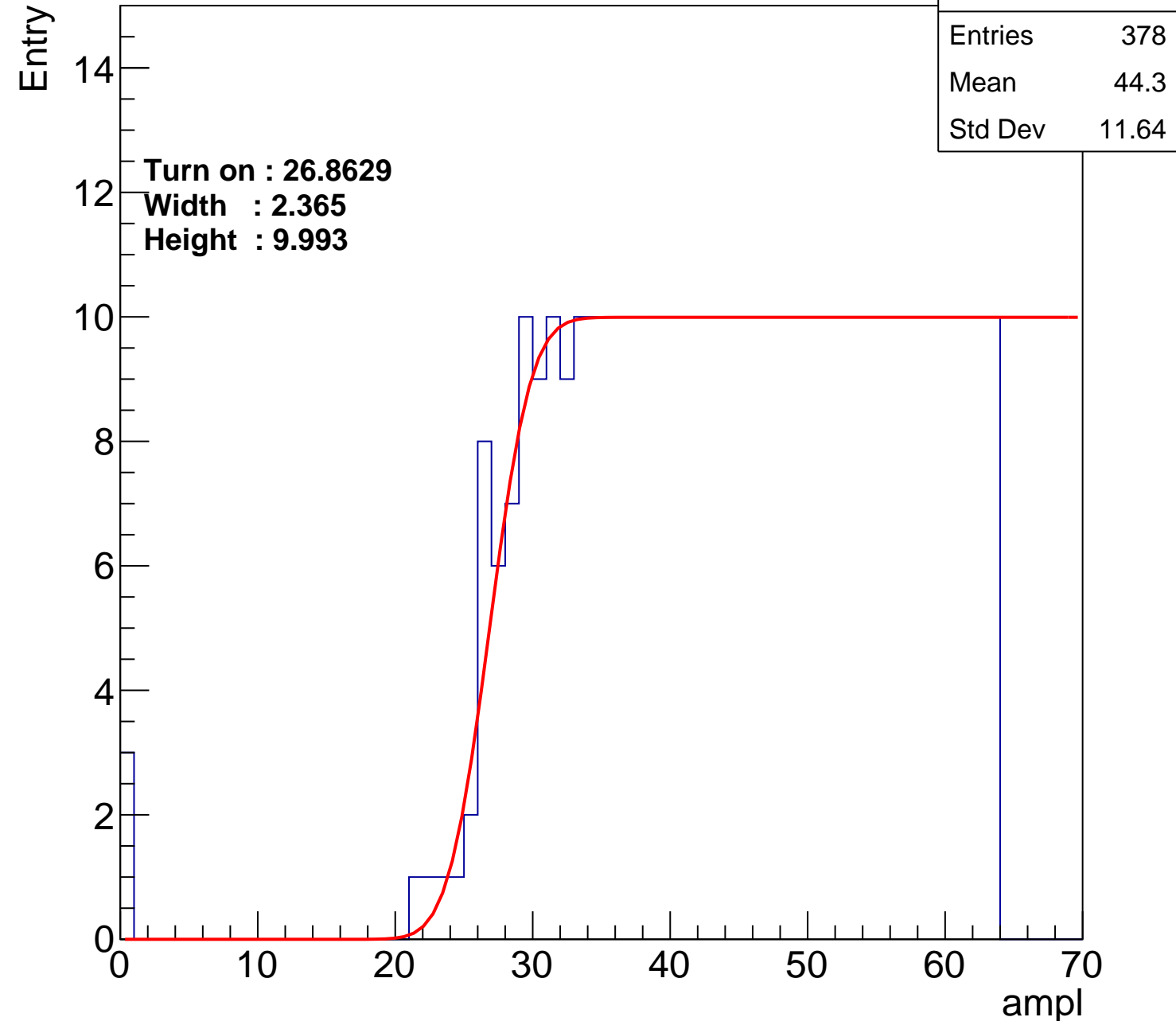
Width : 2.365

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.79
Std Dev	10.77

Turn on : 29.3485

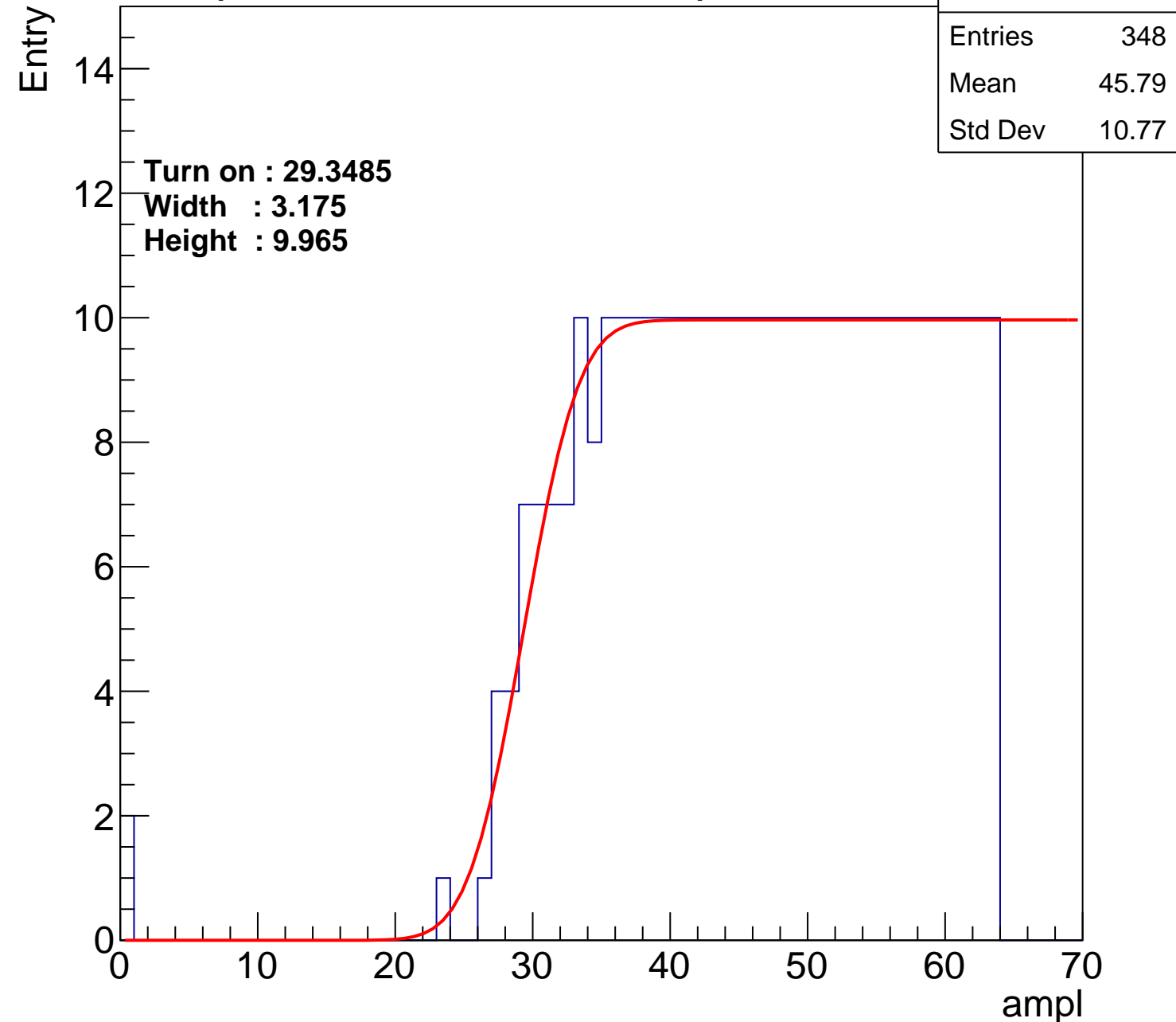
Width : 3.175

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.85
Std Dev	11.08

Turn on : 27.5569

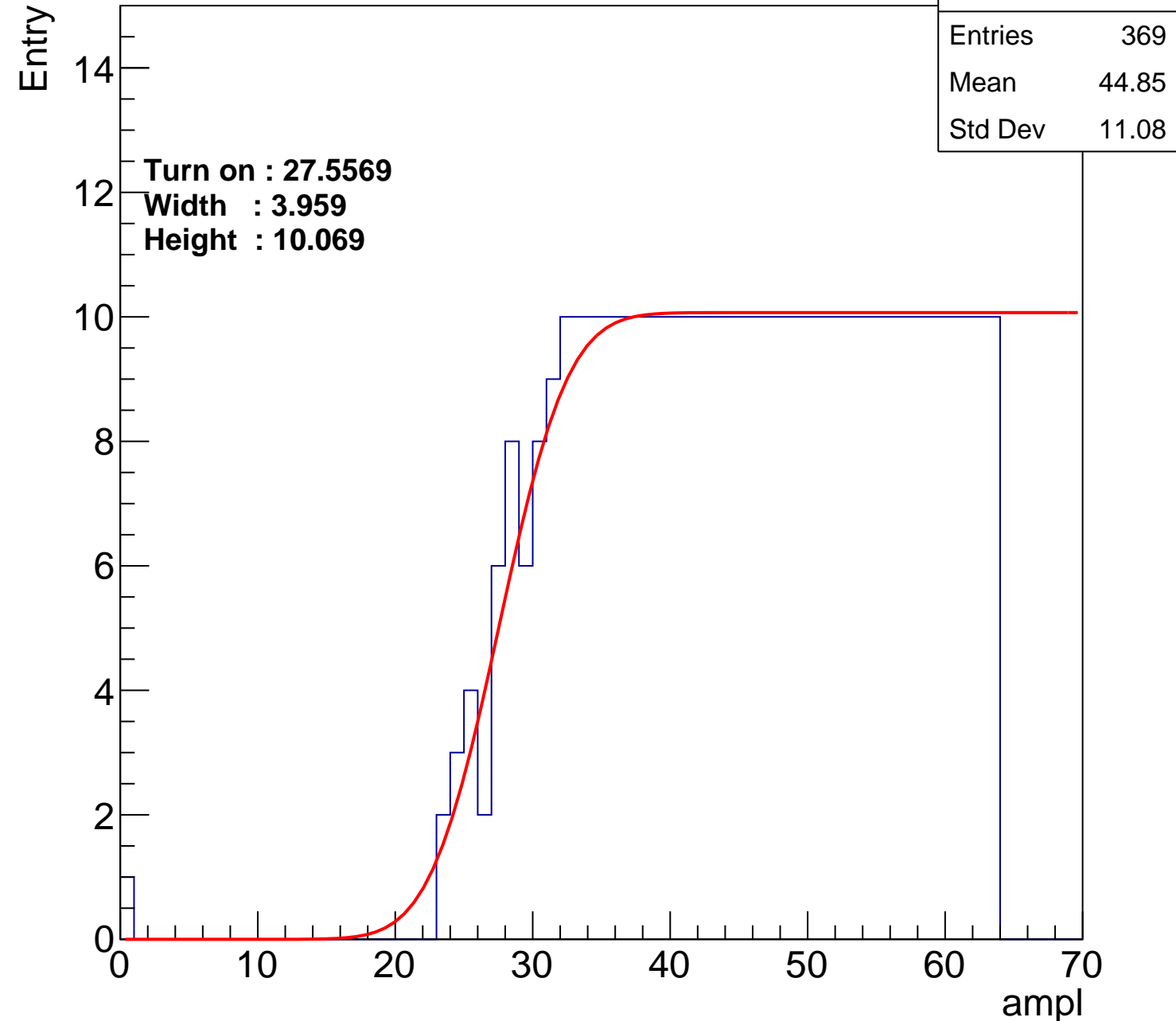
Width : 3.959

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 27.7949

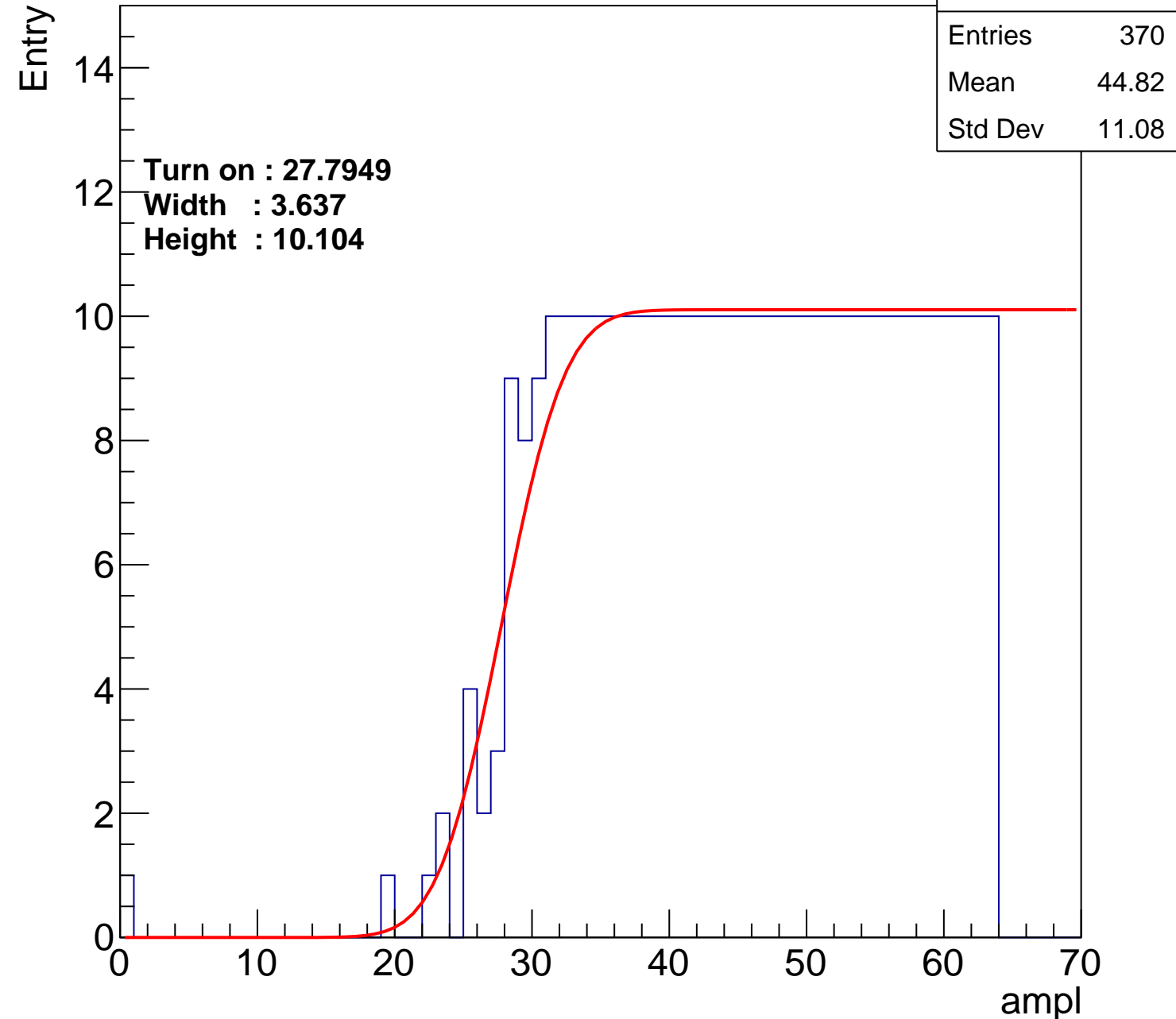
Width : 3.637

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.37
Std Dev	10.92

Turn on : 28.3835

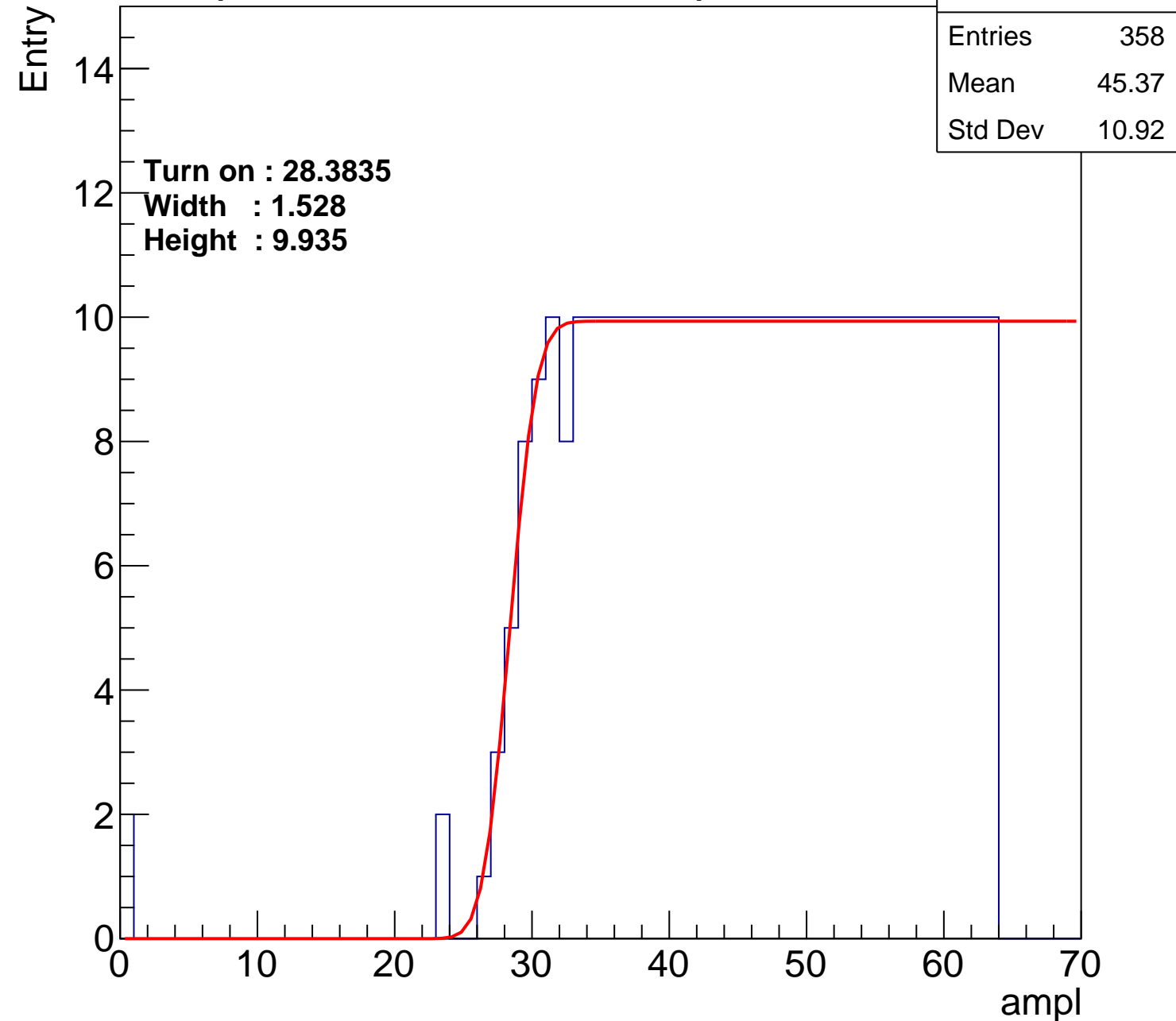
Width : 1.528

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.1156

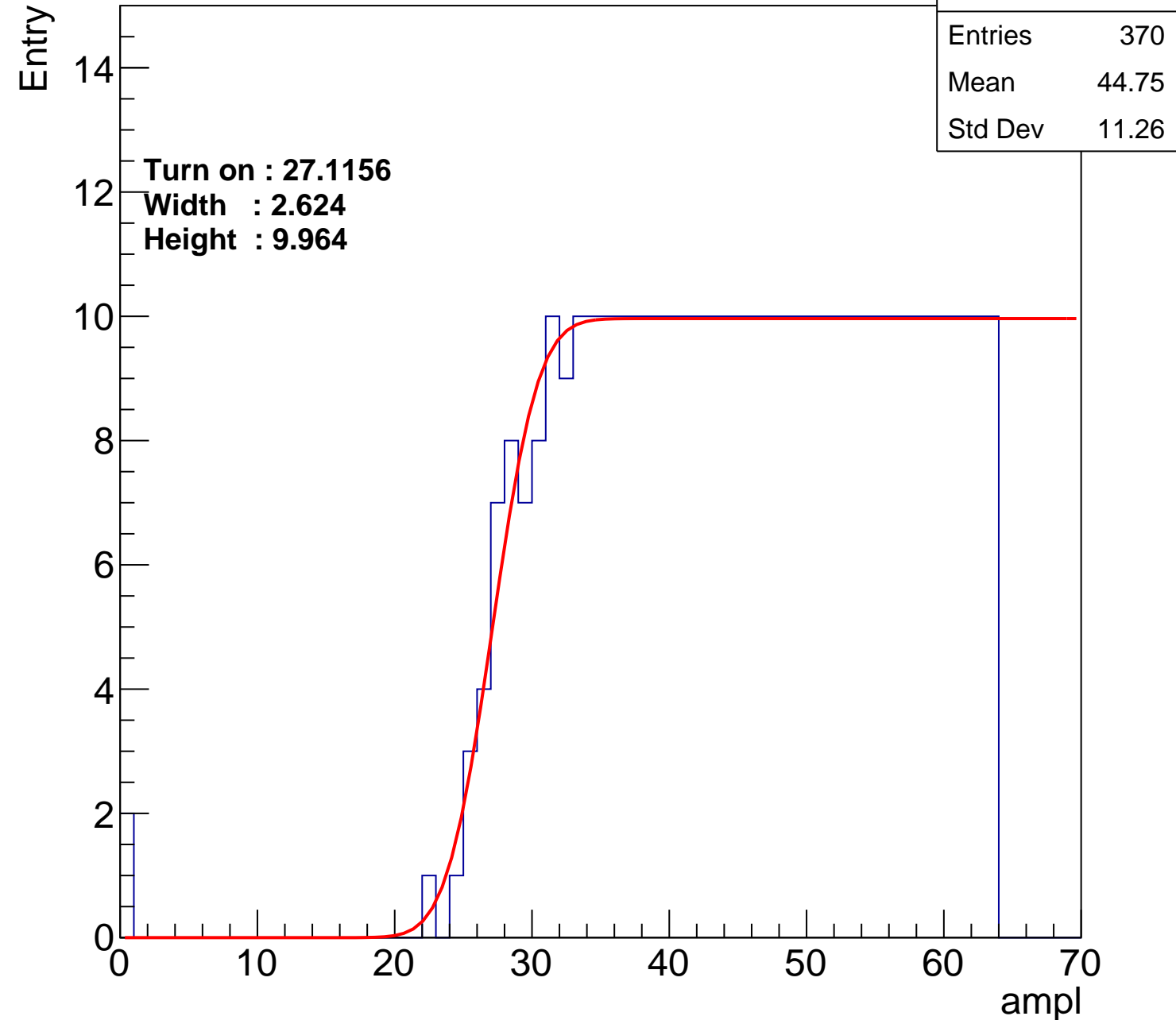
Width : 2.624

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.09
Std Dev	11.7

Turn on : 25.9632

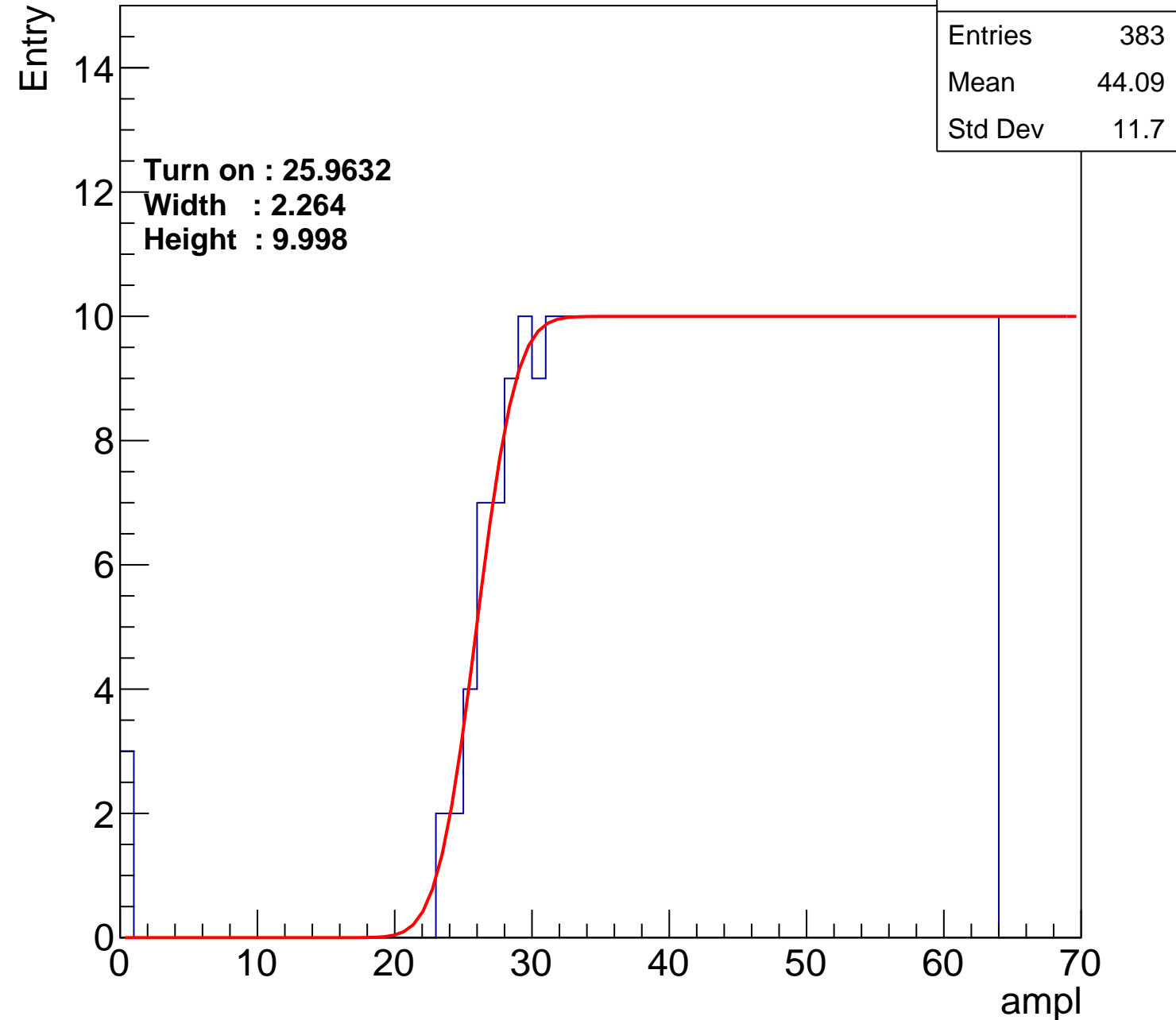
Width : 2.264

Height : 9.998

Entry

14
12
10
8
6
4
2
0

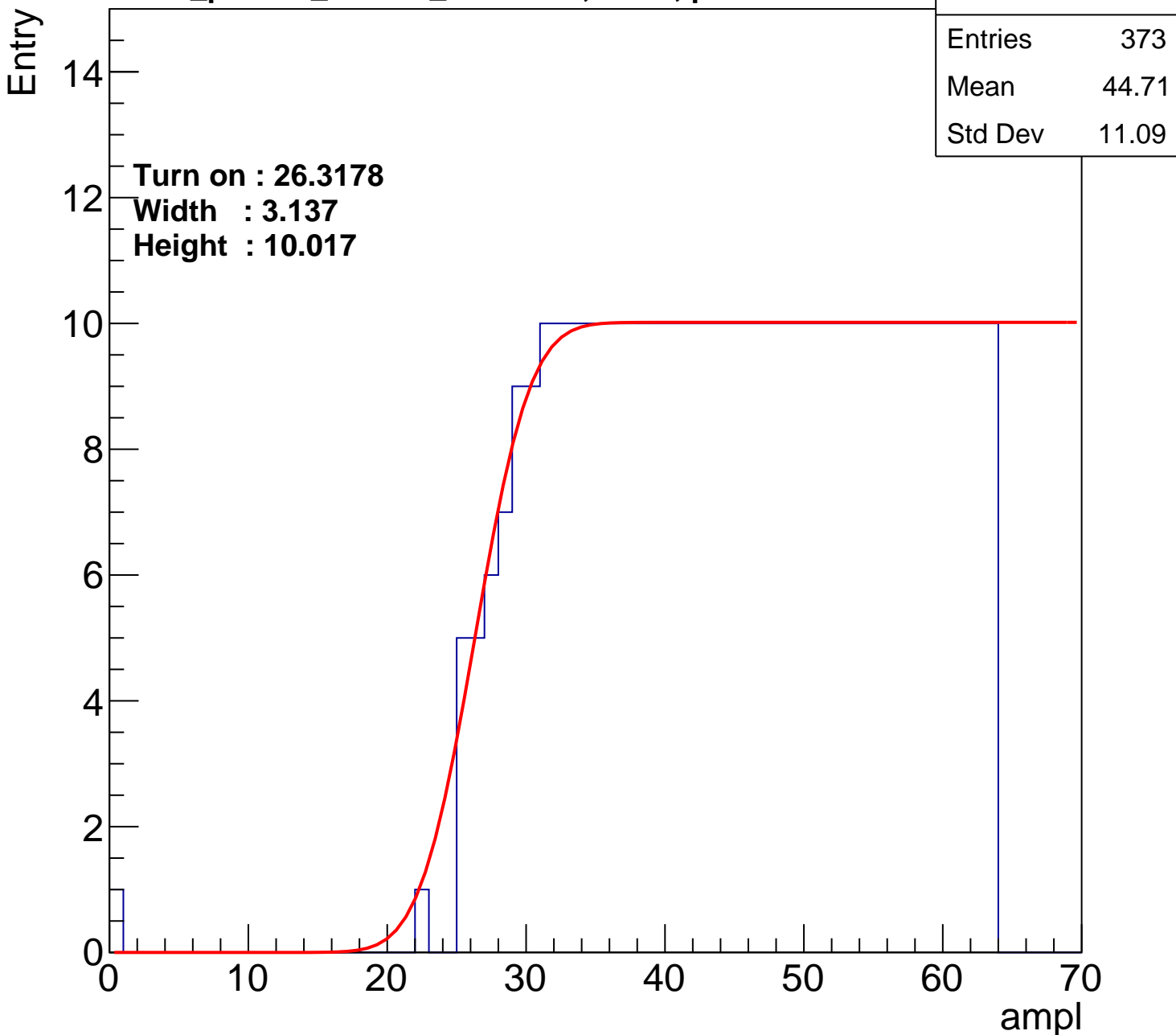
ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 26.3178
Width : 3.137
Height : 10.017



B0L002S, U22-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.44
Std Dev	11.88

Turn on : 27.2254

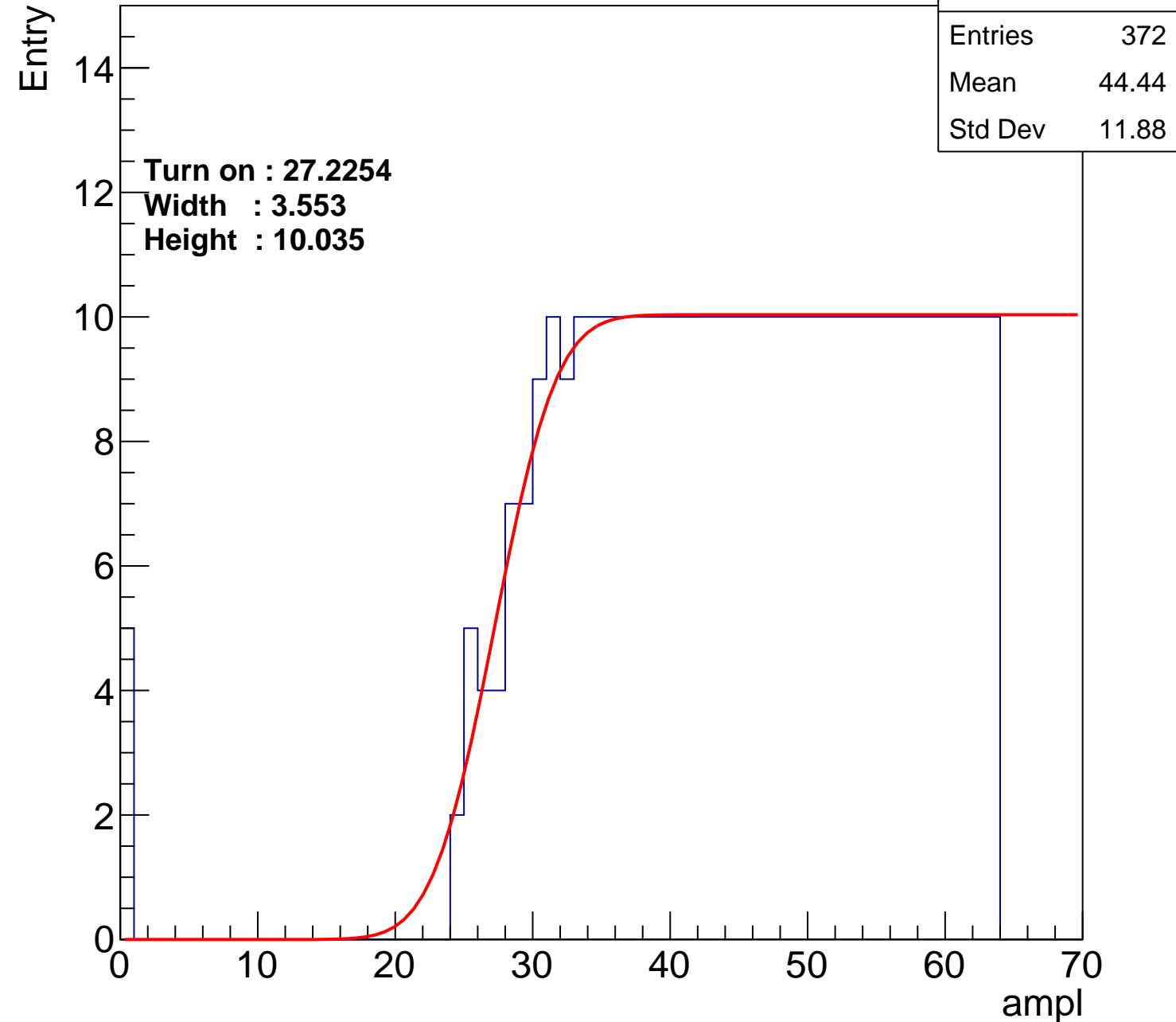
Width : 3.553

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.13
Std Dev	11.04

Turn on : 28.3679

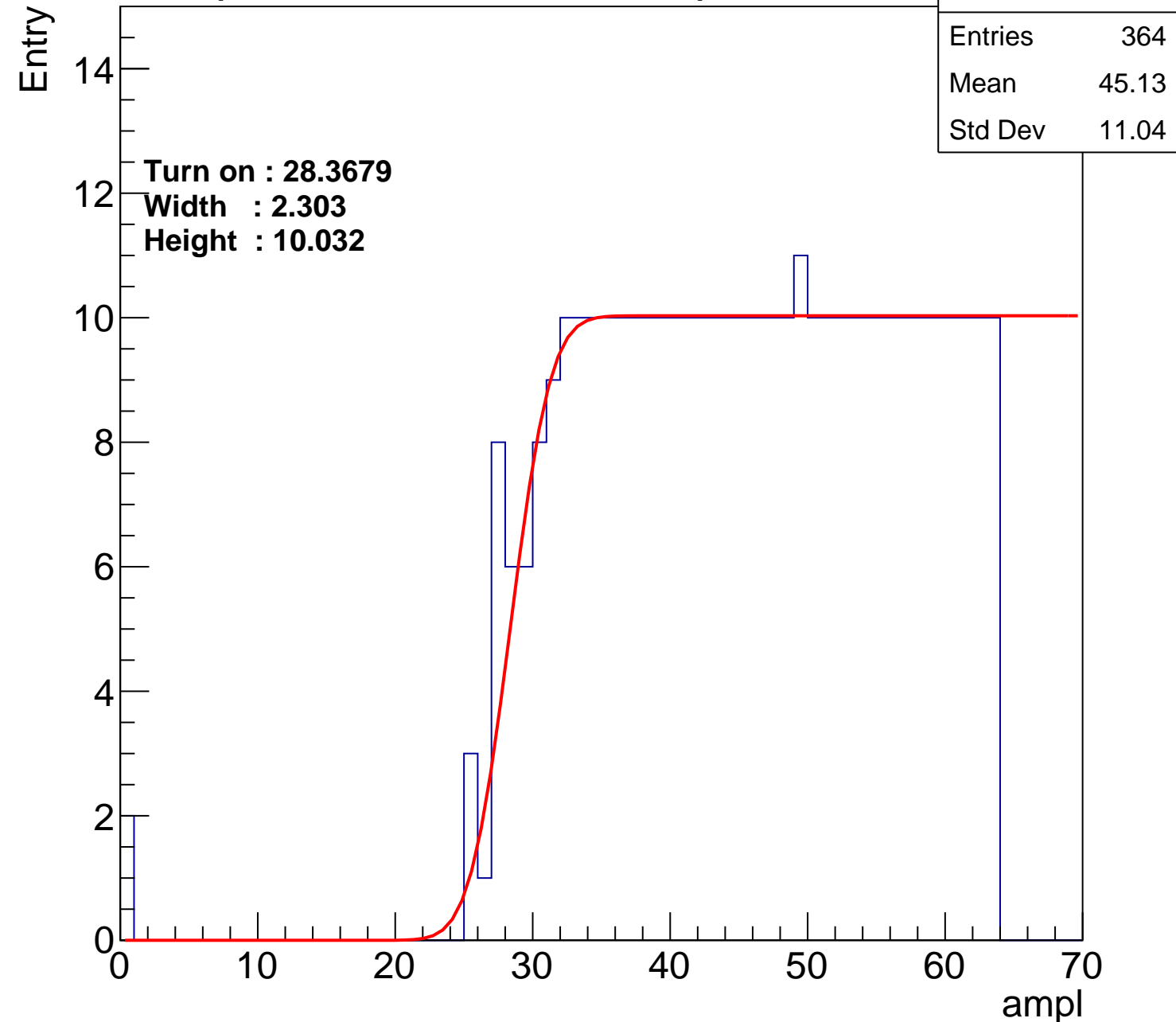
Width : 2.303

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch67

calib_packv5_042523_0143.root, FC#8, port C1

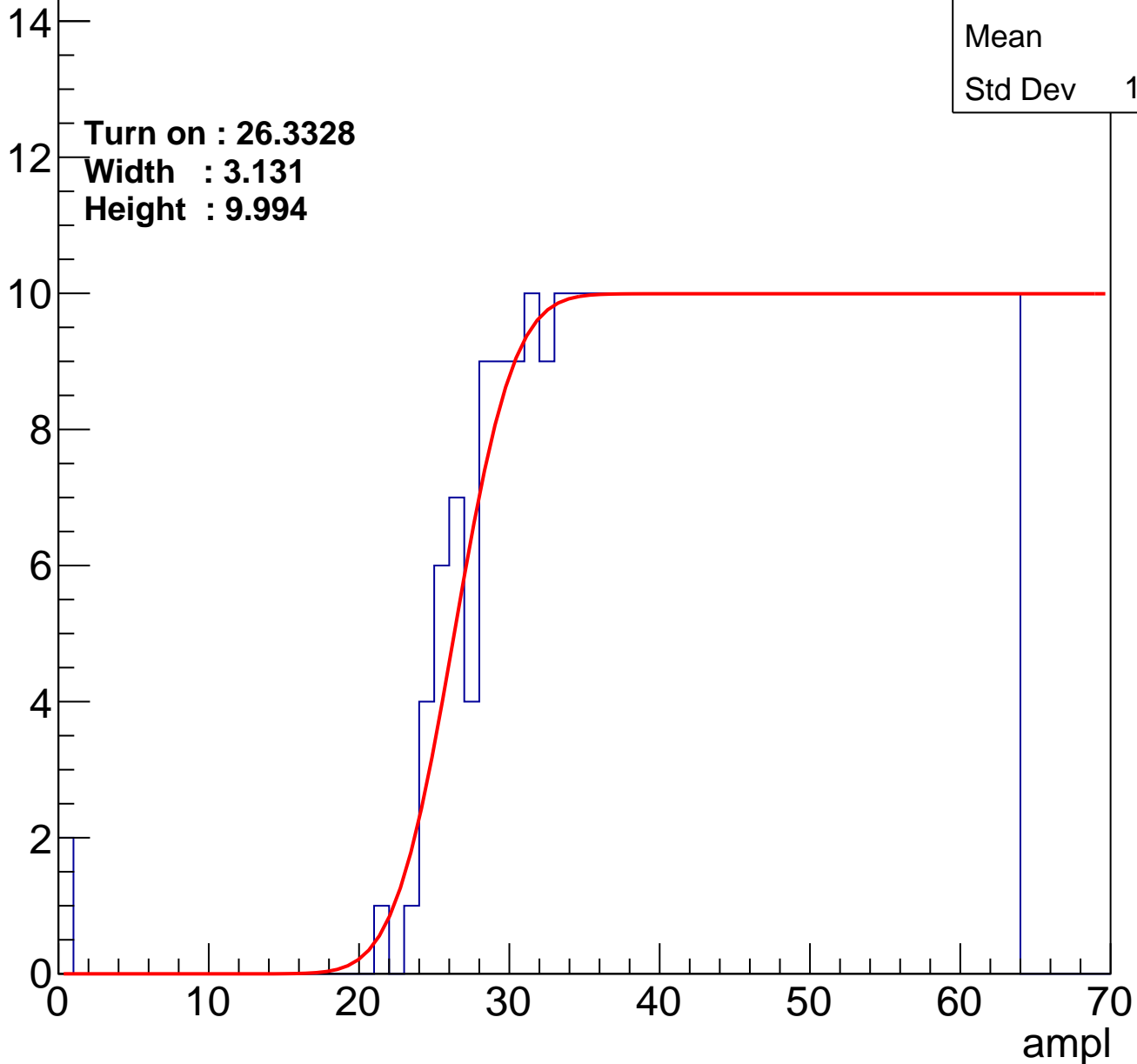
Entries	381
Mean	44.2
Std Dev	11.55

Turn on : 26.3328

Width : 3.131

Height : 9.994

Entry



B0L002S, U22-ch68

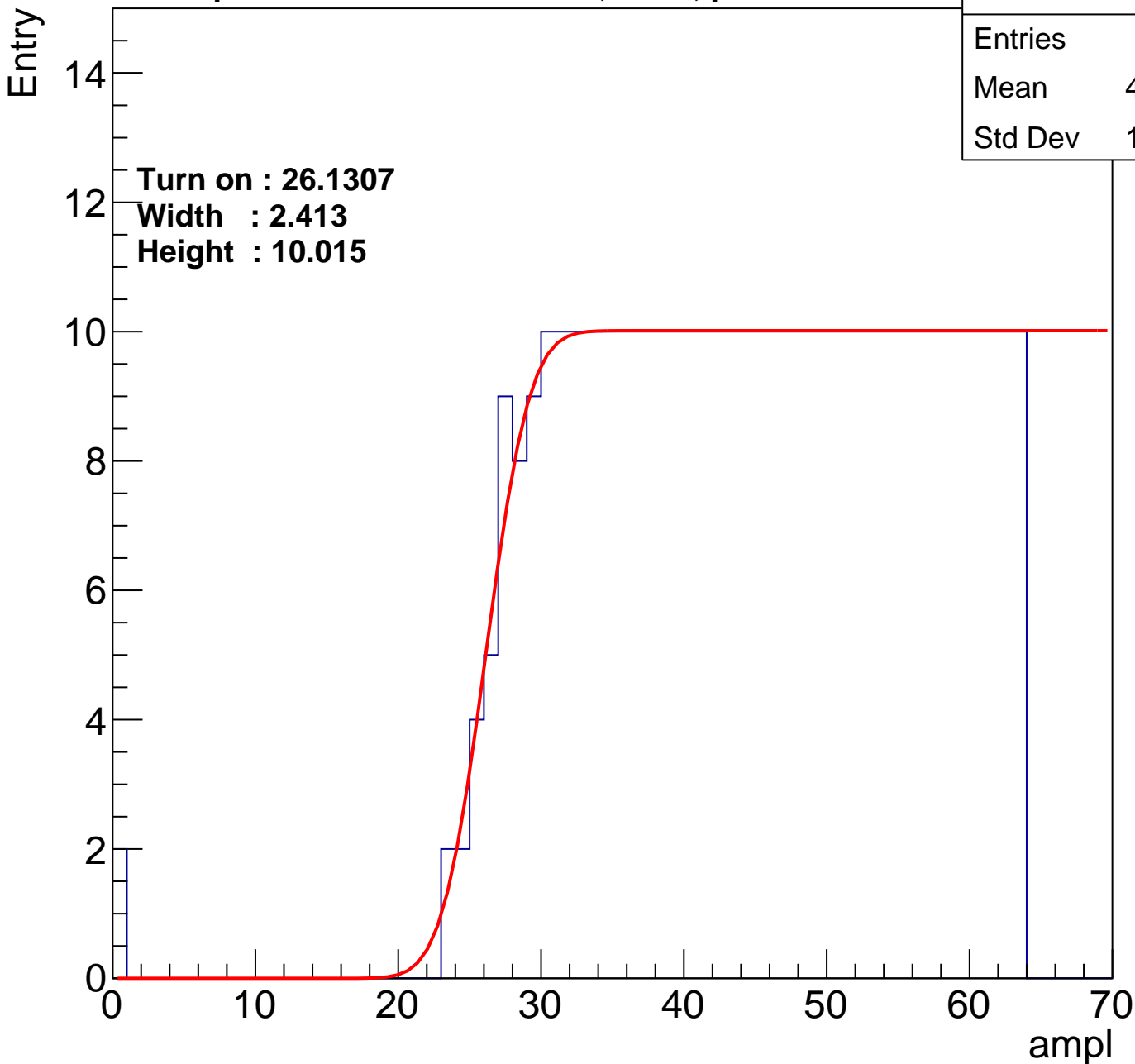
calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.26
Std Dev	11.47

Turn on : 26.1307

Width : 2.413

Height : 10.015



B0L002S, U22-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.37
Std Dev	11.57

Turn on : 26.7222

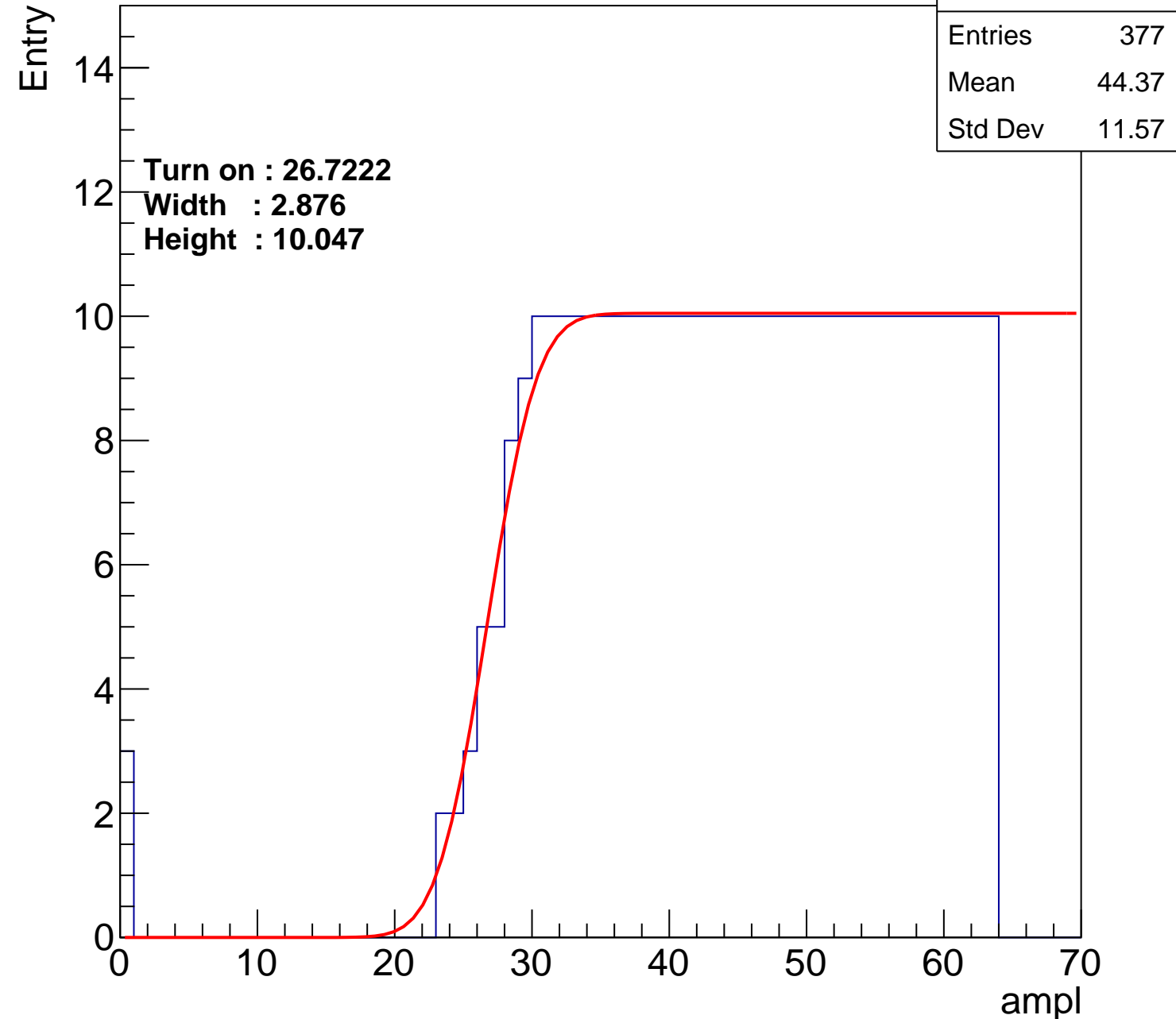
Width : 2.876

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch70

calib_packv5_042523_0143.root, FC#8, port C1

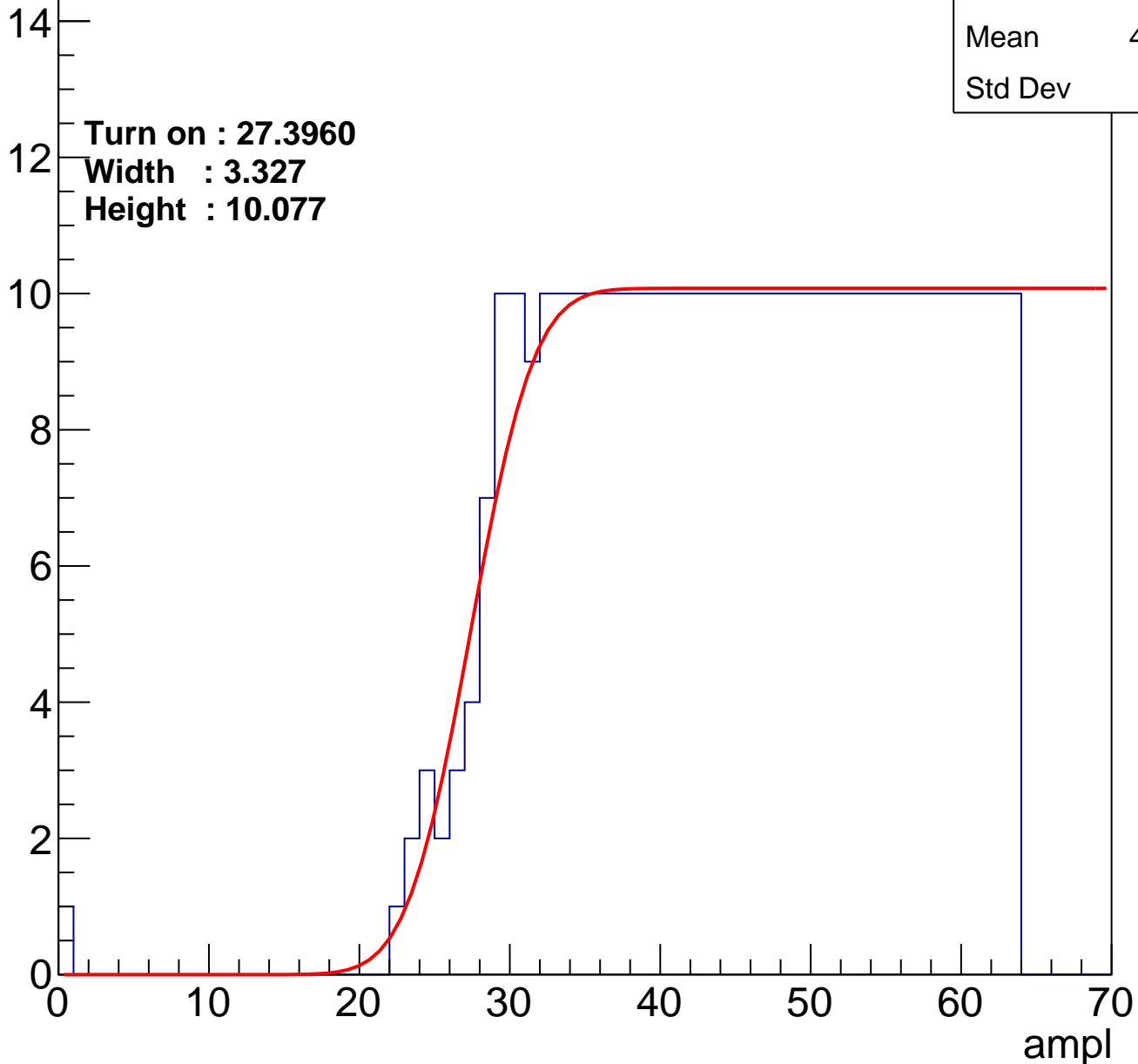
Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.3960

Width : 3.327

Height : 10.077

Entry



B0L002S, U22-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.02
Std Dev	10.96

Turn on : 27.3231

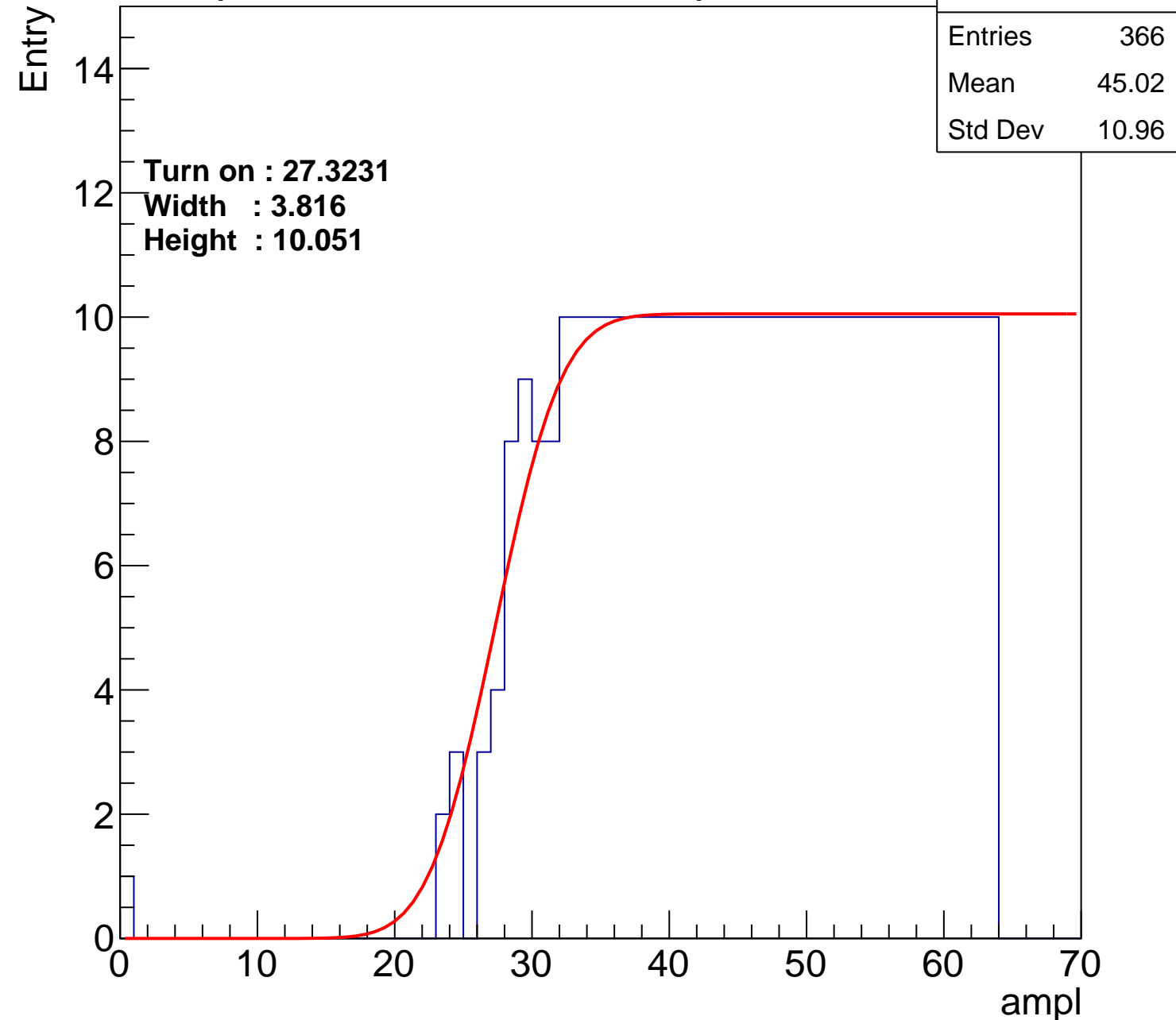
Width : 3.816

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.86
Std Dev	10.73

Turn on : 29.8286

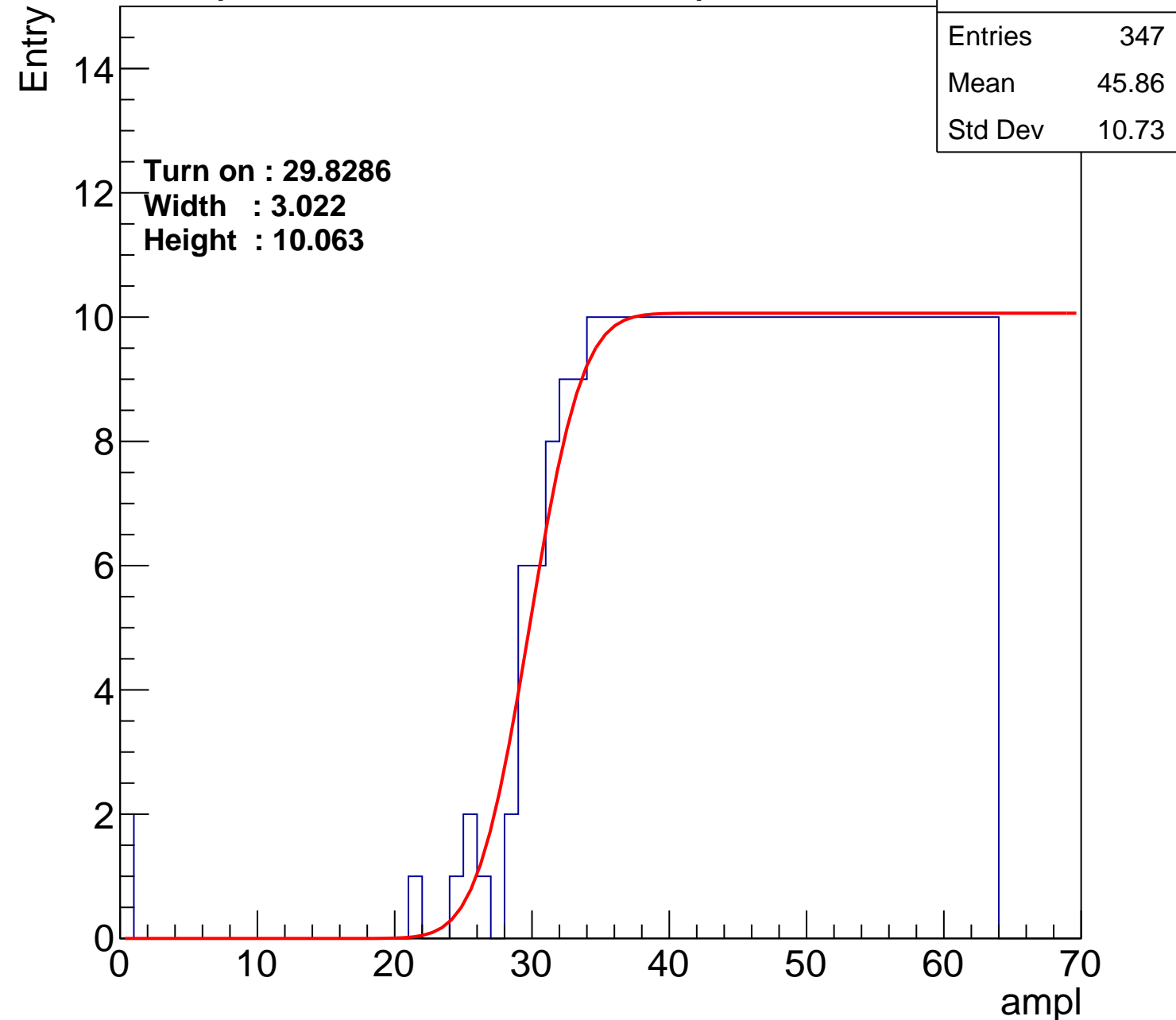
Width : 3.022

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.81
Std Dev	11.42

Turn on : 27.8872

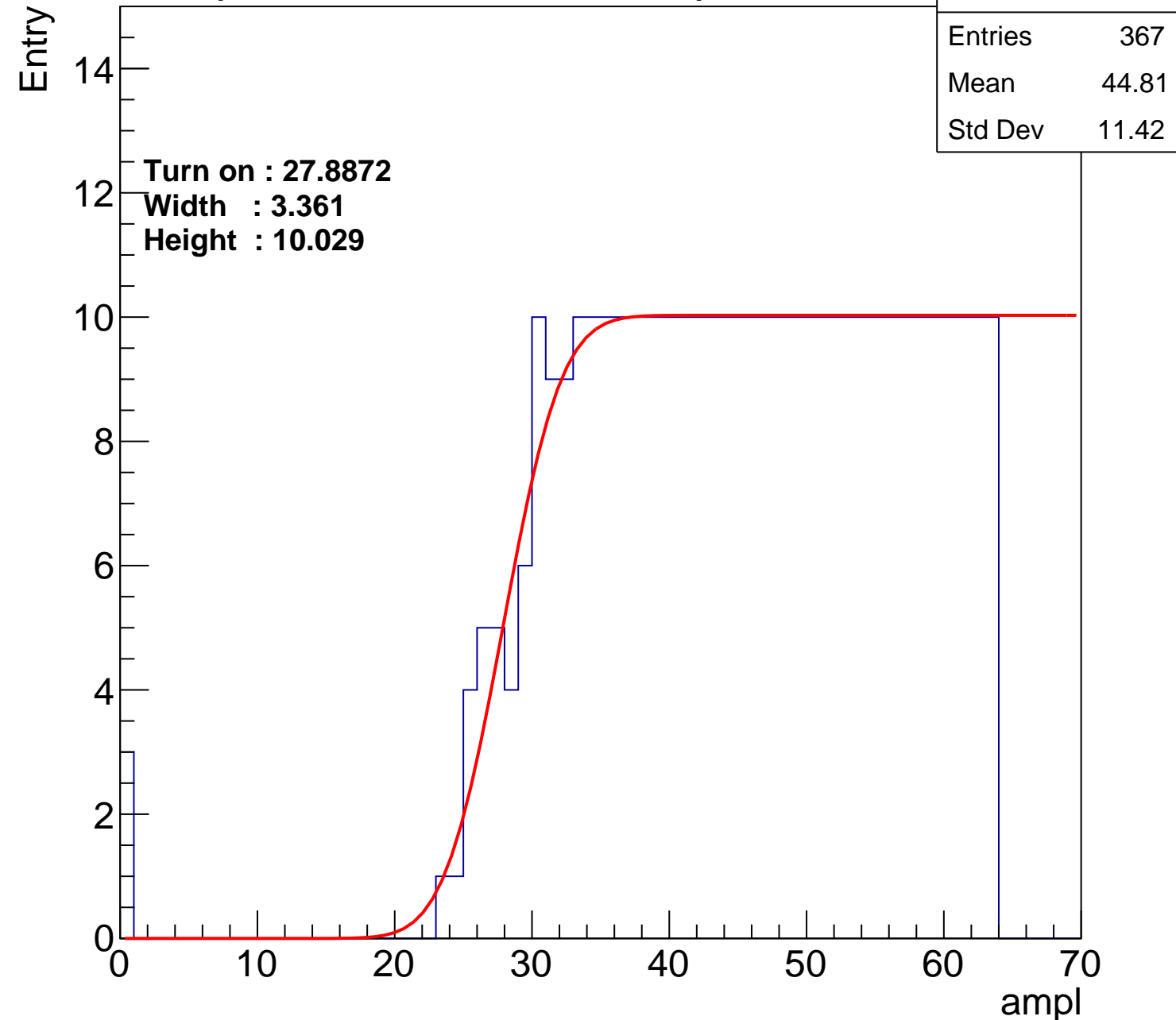
Width : 3.361

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.29
Std Dev	10.93

Turn on : 28.0303

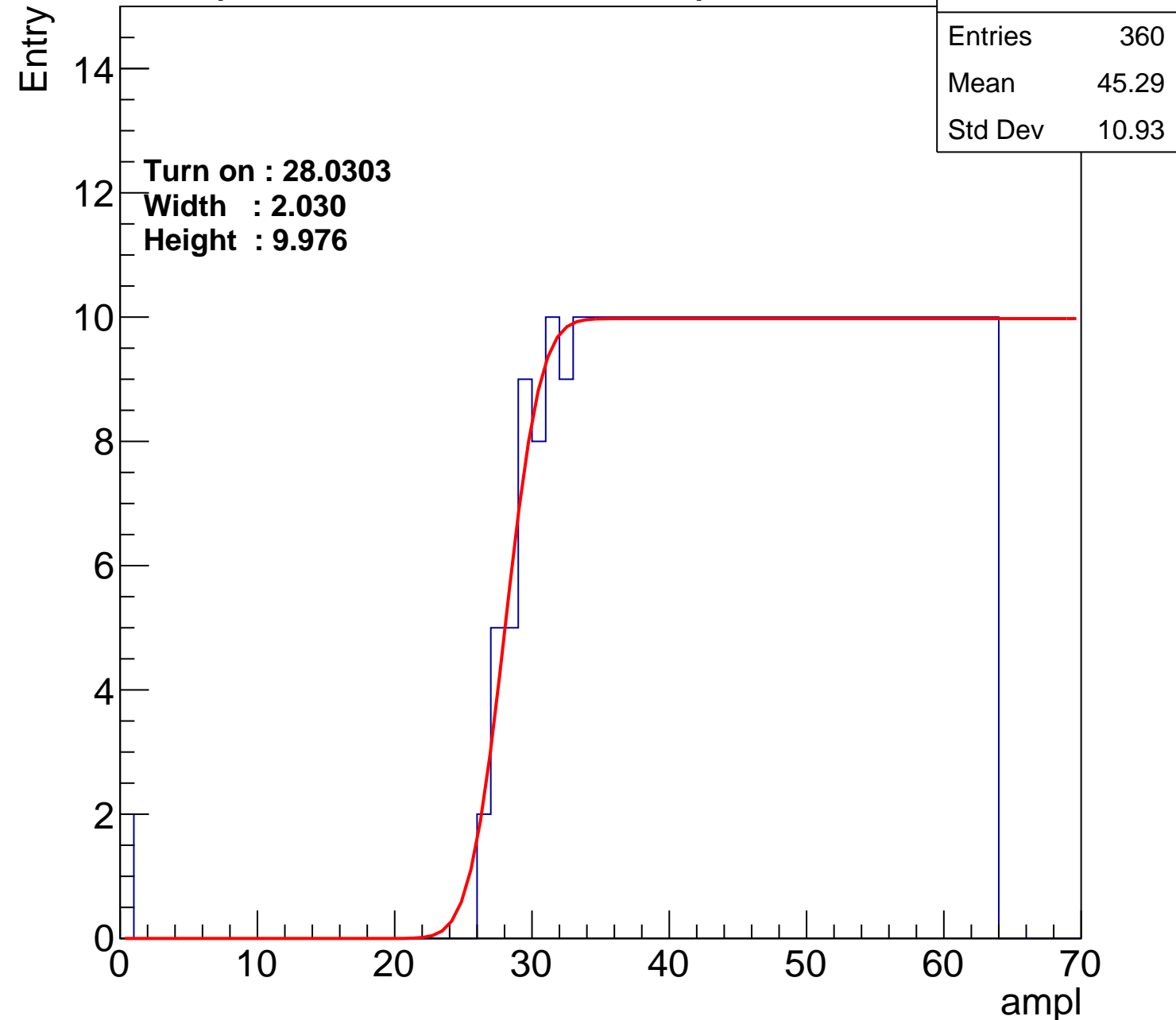
Width : 2.030

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch75

calib_packv5_042523_0143.root, FC#8, port C1

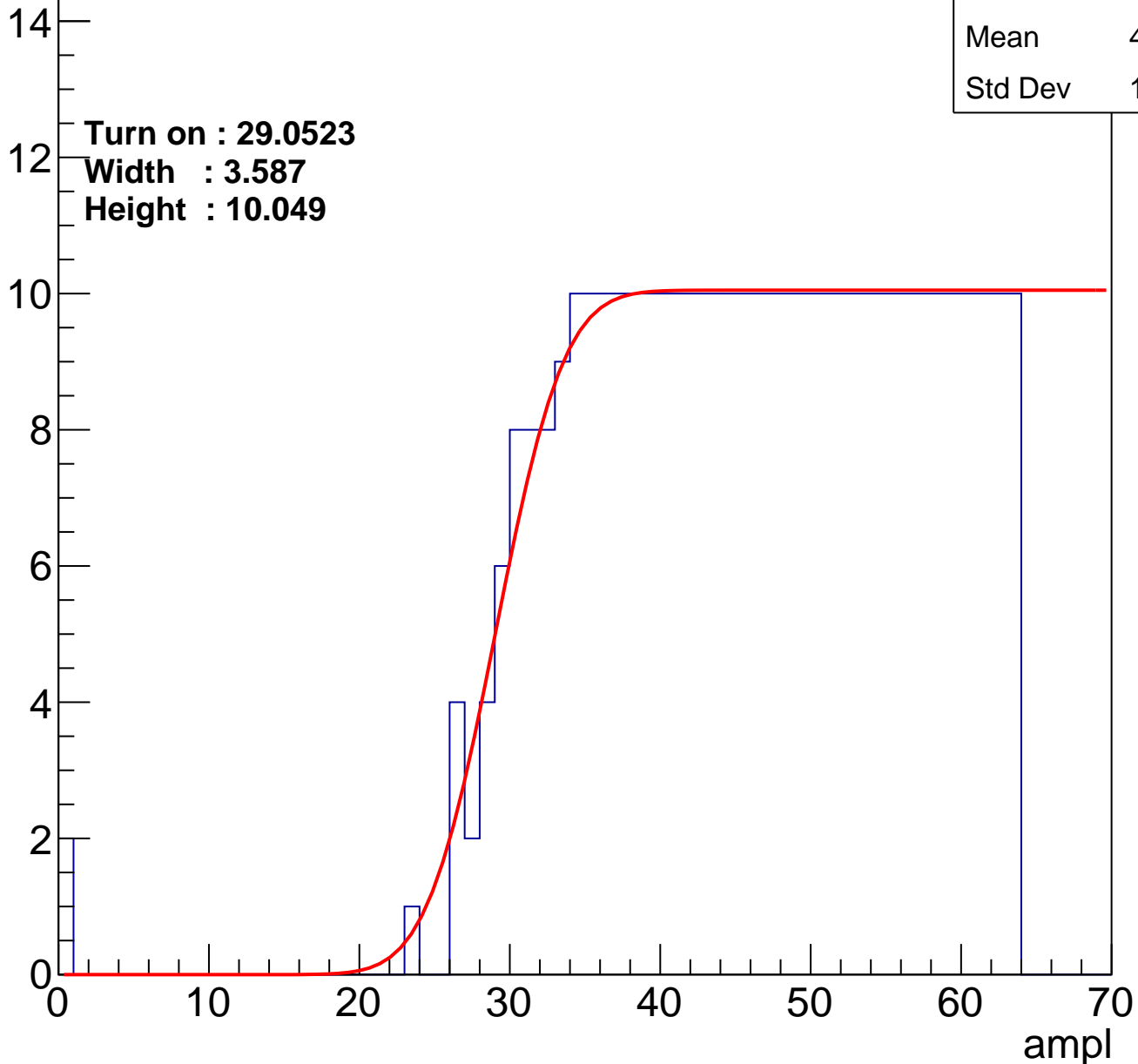
Entries	352
Mean	45.62
Std Dev	10.84

Turn on : 29.0523

Width : 3.587

Height : 10.049

Entry



B0L002S, U22-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	43.99
Std Dev	12.02

Turn on : 26.3694

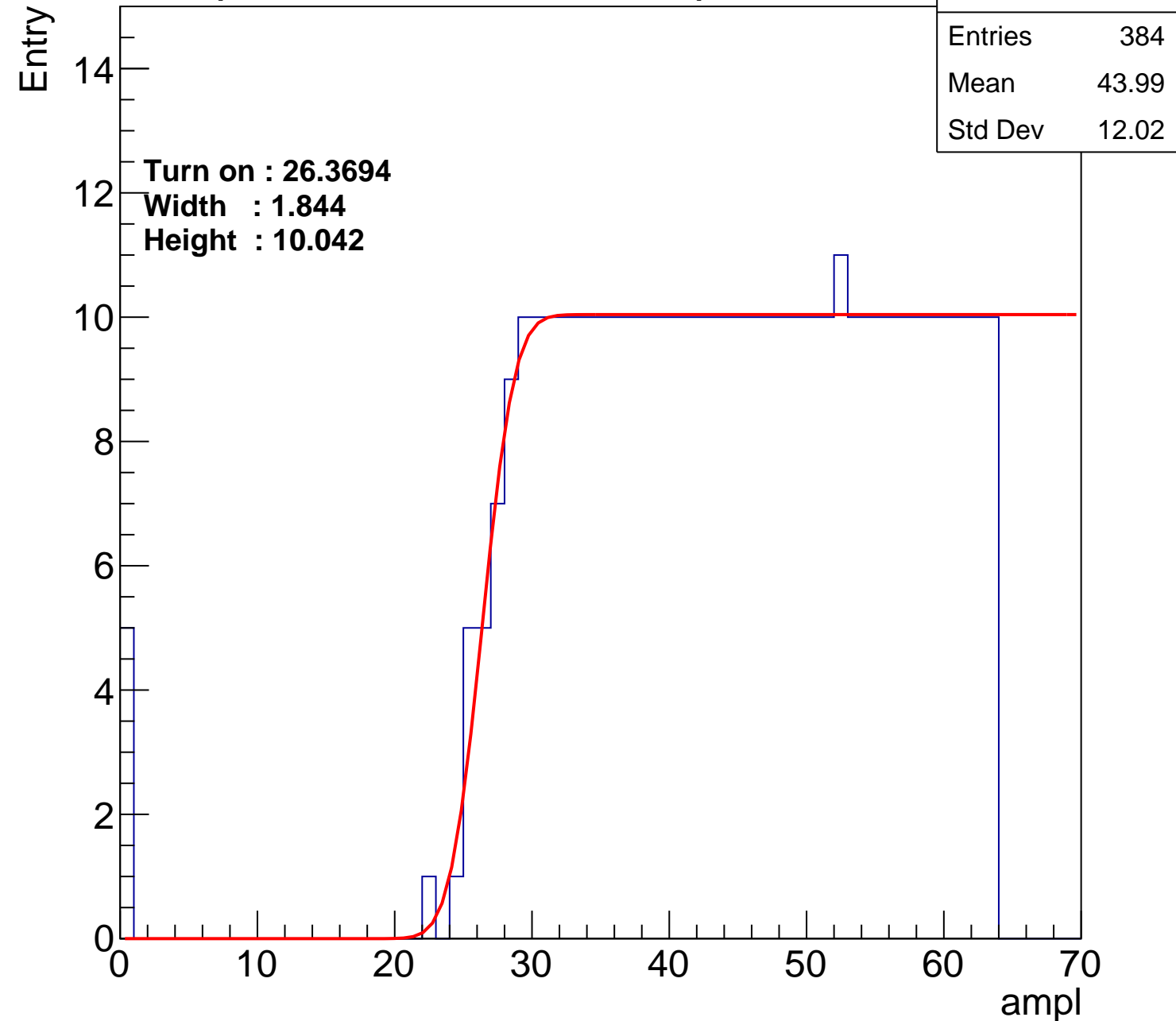
Width : 1.844

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.1114

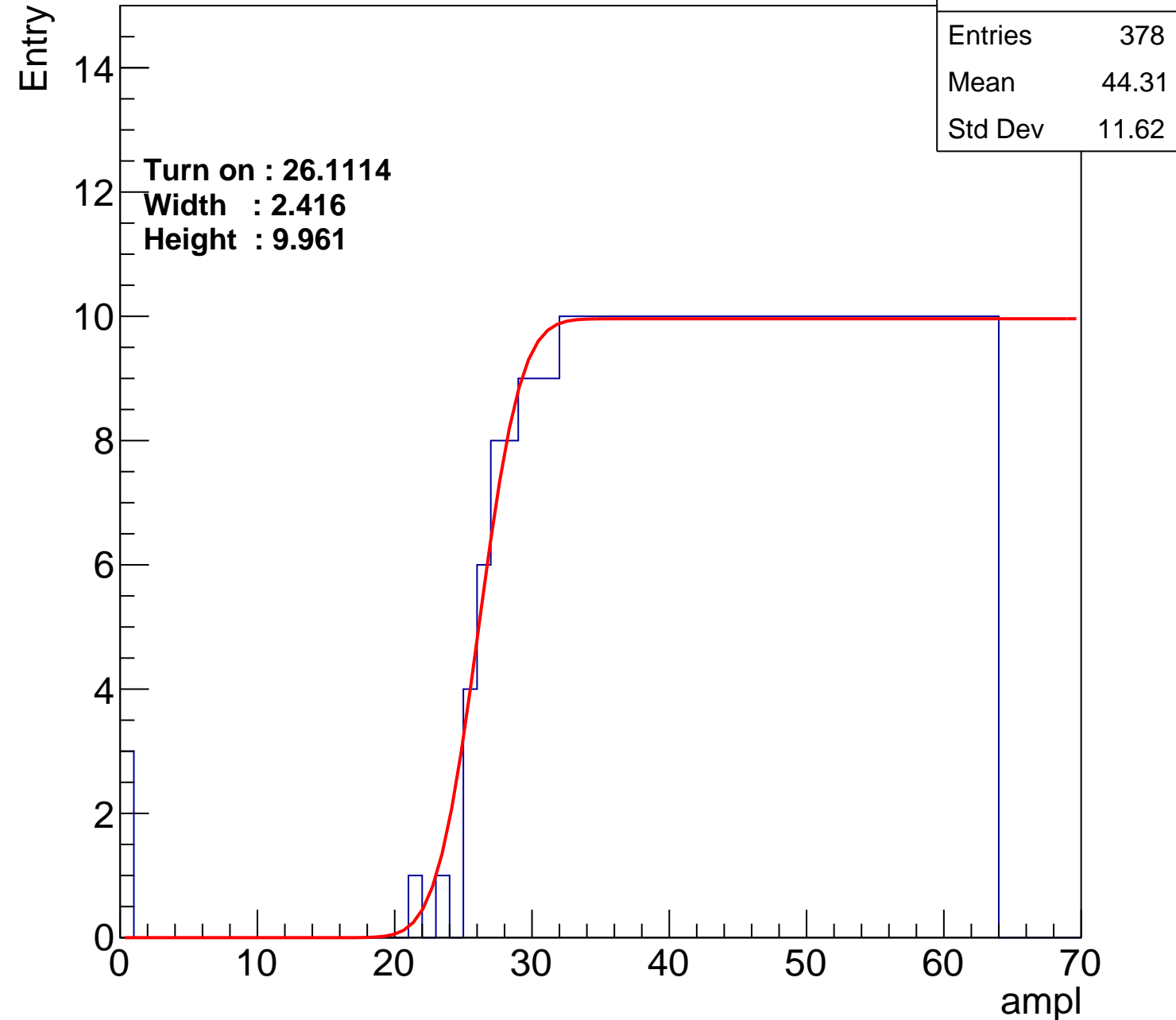
Width : 2.416

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch78

calib_packv5_042523_0143.root, FC#8, port C1

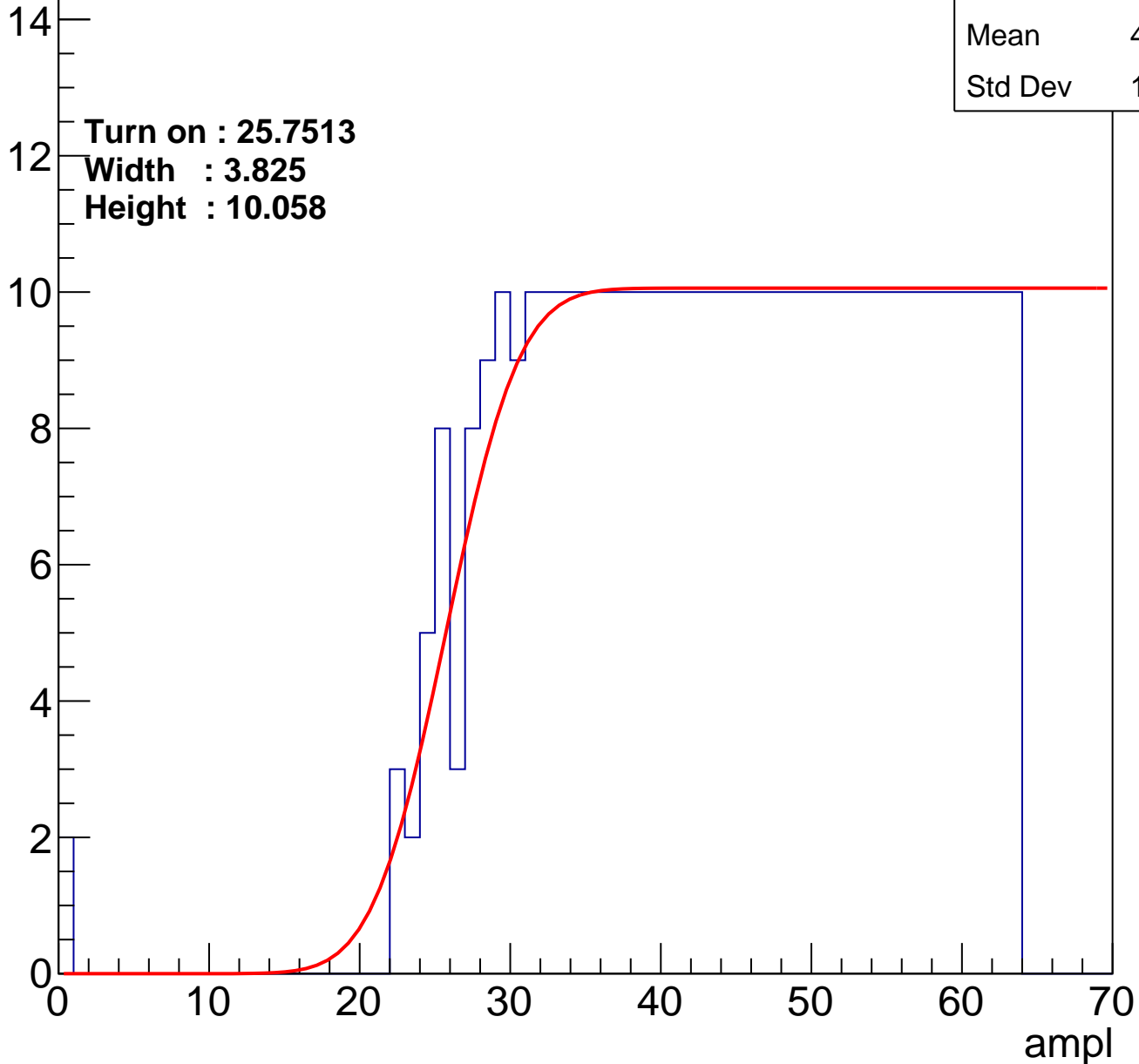
Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.7513

Width : 3.825

Height : 10.058

Entry



B0L002S, U22-ch79

calib_packv5_042523_0143.root, FC#8, port C1

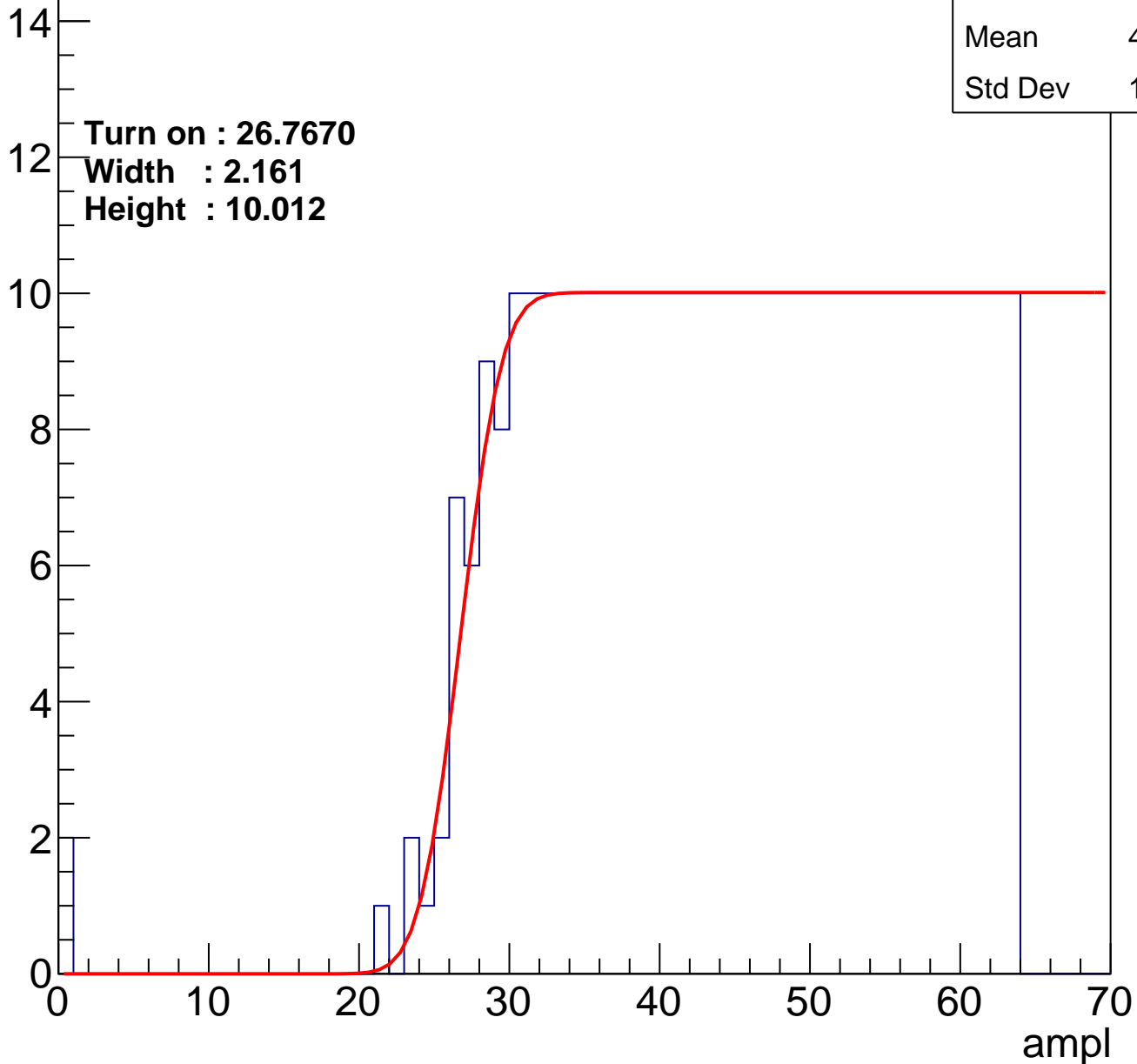
Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.7670

Width : 2.161

Height : 10.012

Entry



B0L002S, U22-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.3
Std Dev	10.83

Turn on : 28.7148

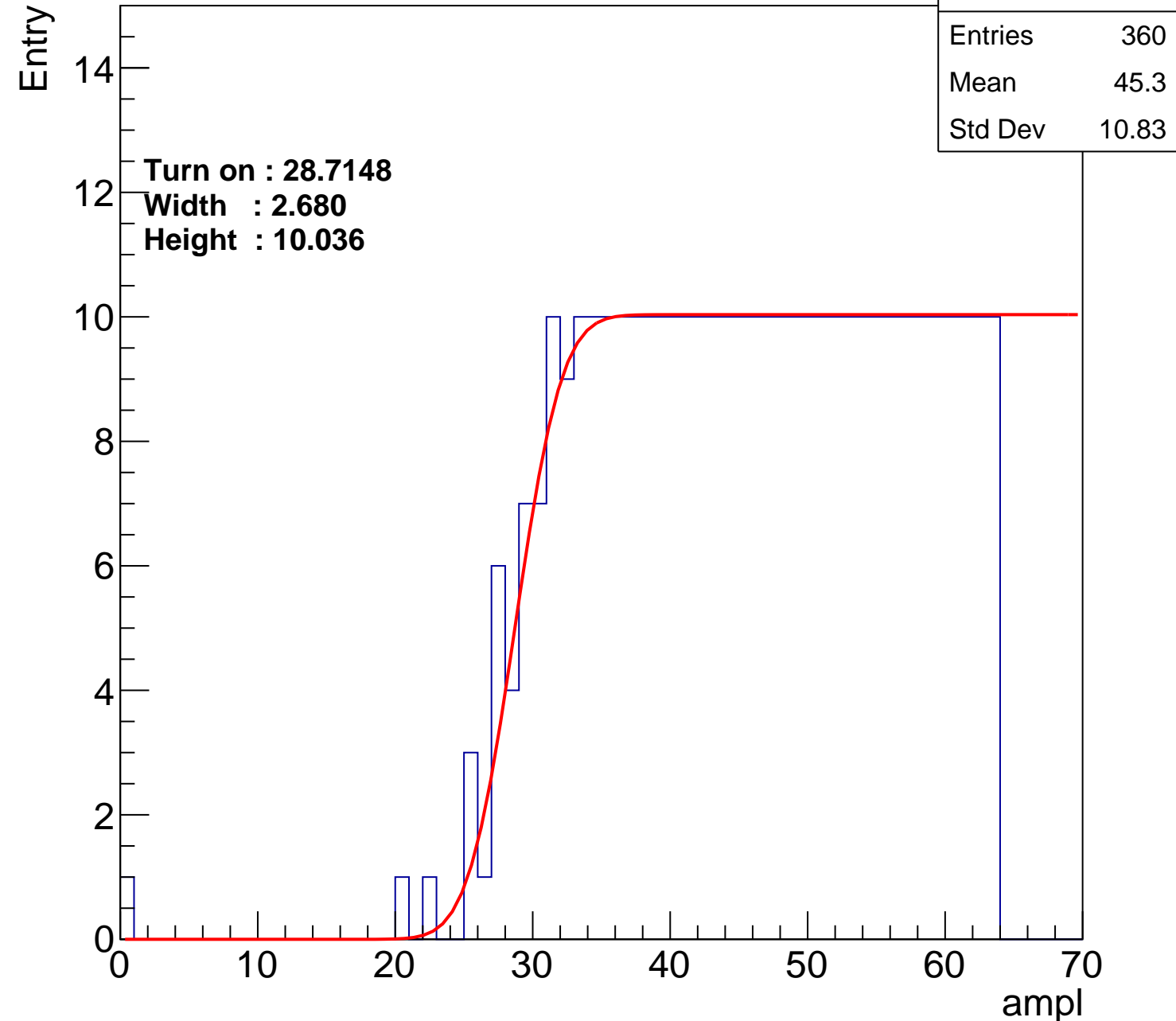
Width : 2.680

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.8423

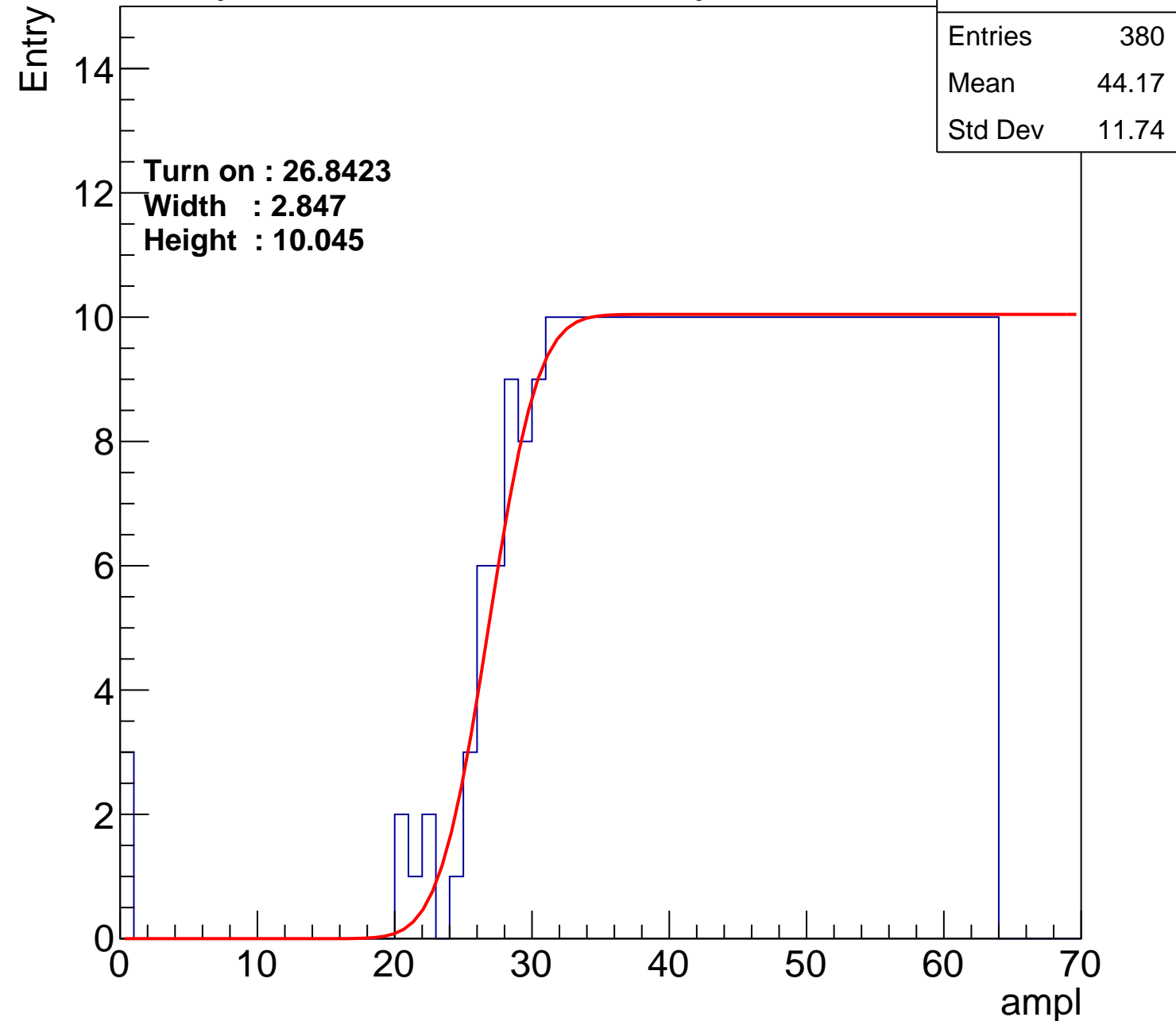
Width : 2.847

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.36
Std Dev	11.28

Turn on : 26.5609

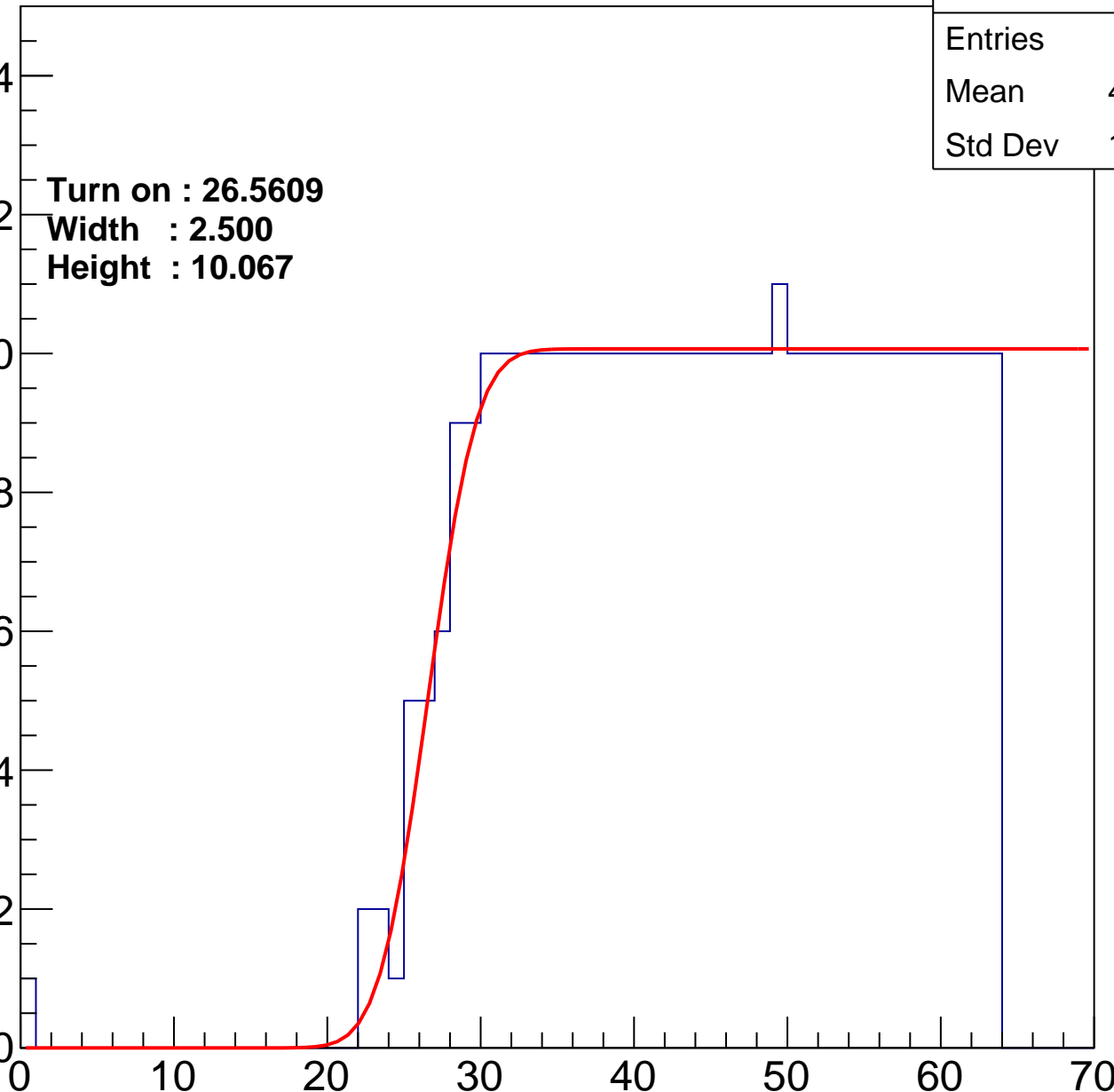
Width : 2.500

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.39
Std Dev	10.78

Turn on : 28.2889

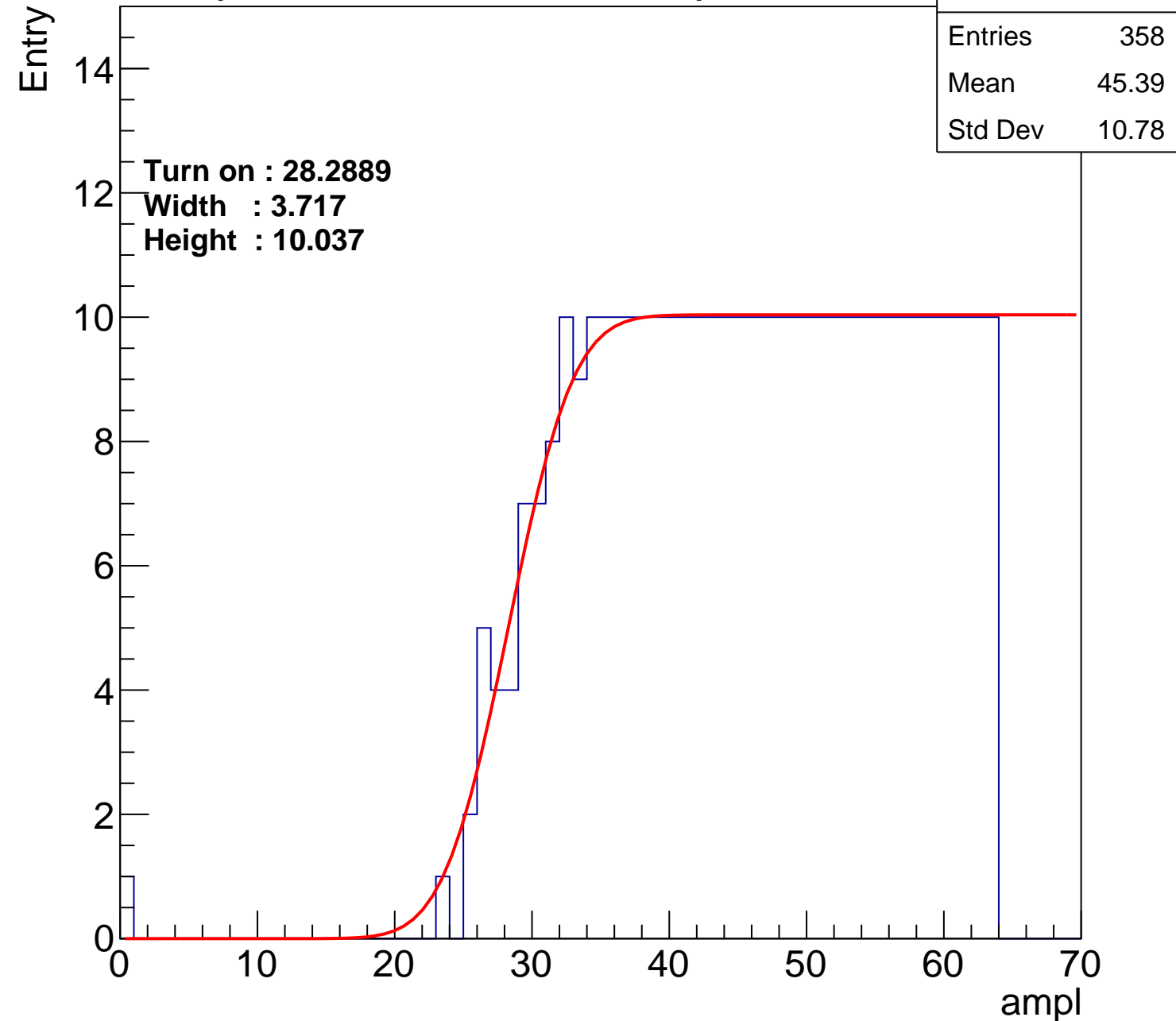
Width : 3.717

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.42
Std Dev	11.41

Turn on : 26.7094

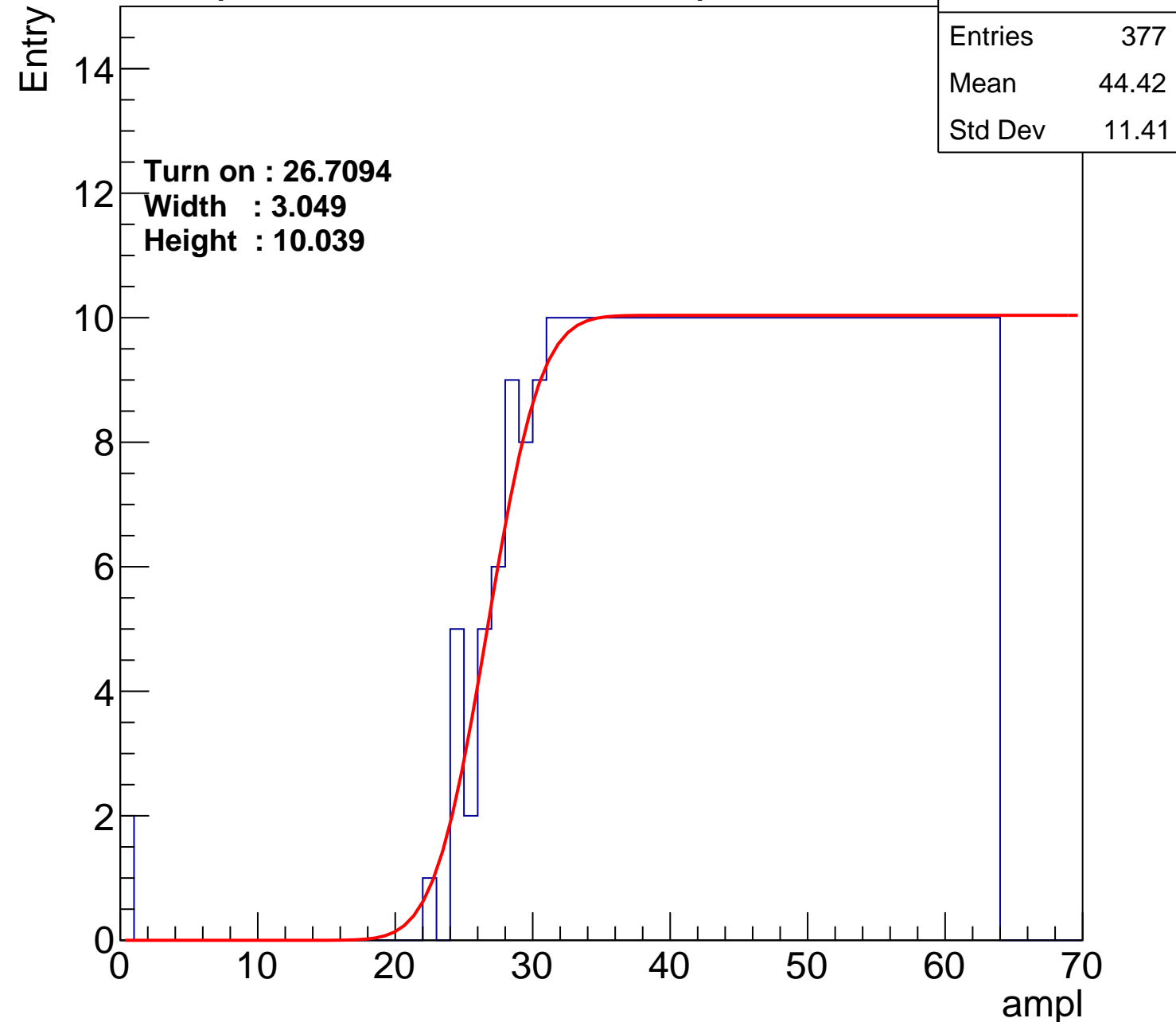
Width : 3.049

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch85

calib_packv5_042523_0143.root, FC#8, port C1

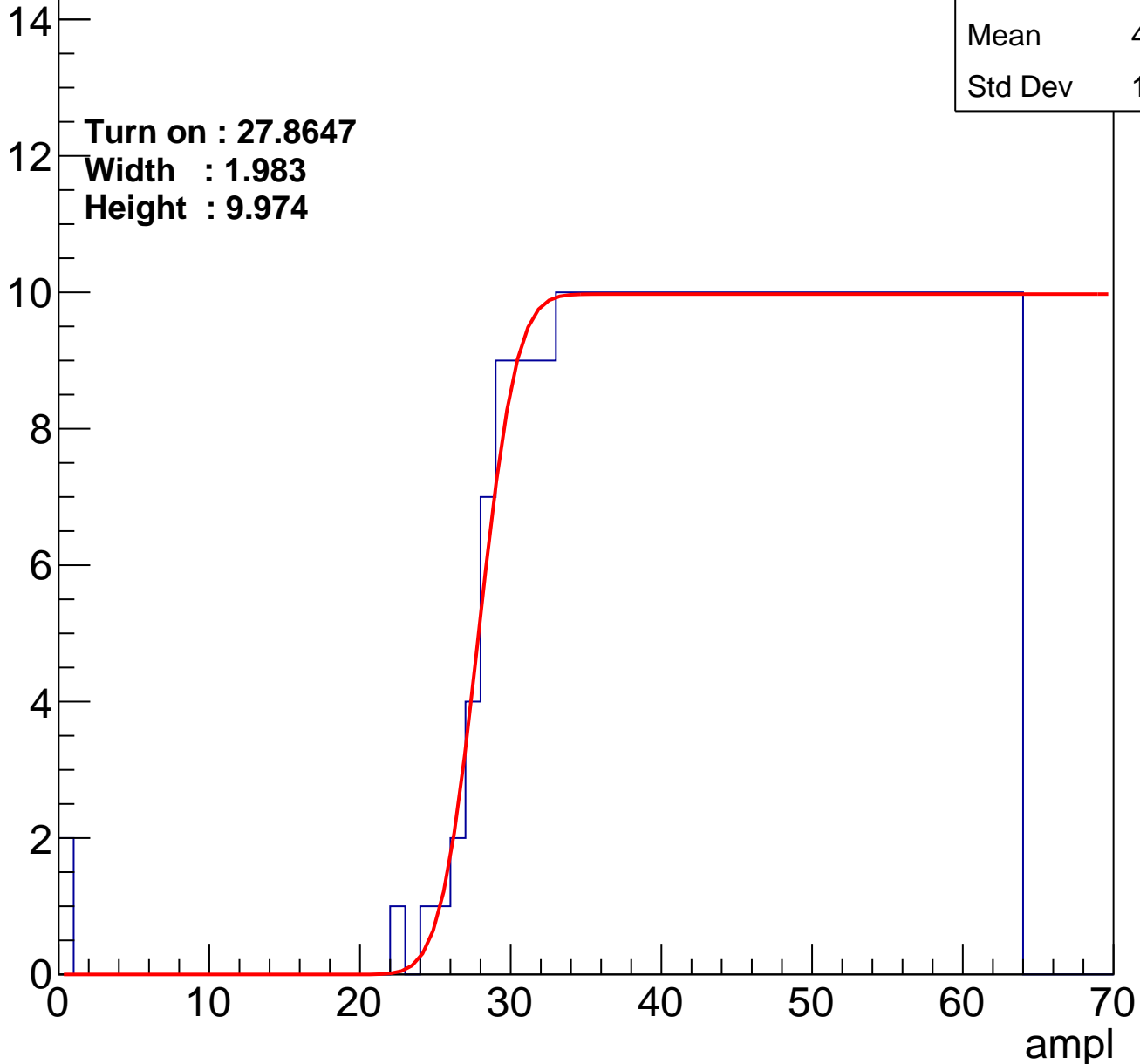
Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 27.8647

Width : 1.983

Height : 9.974

Entry



B0L002S, U22-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.74
Std Dev	11.28

Turn on : 27.5574

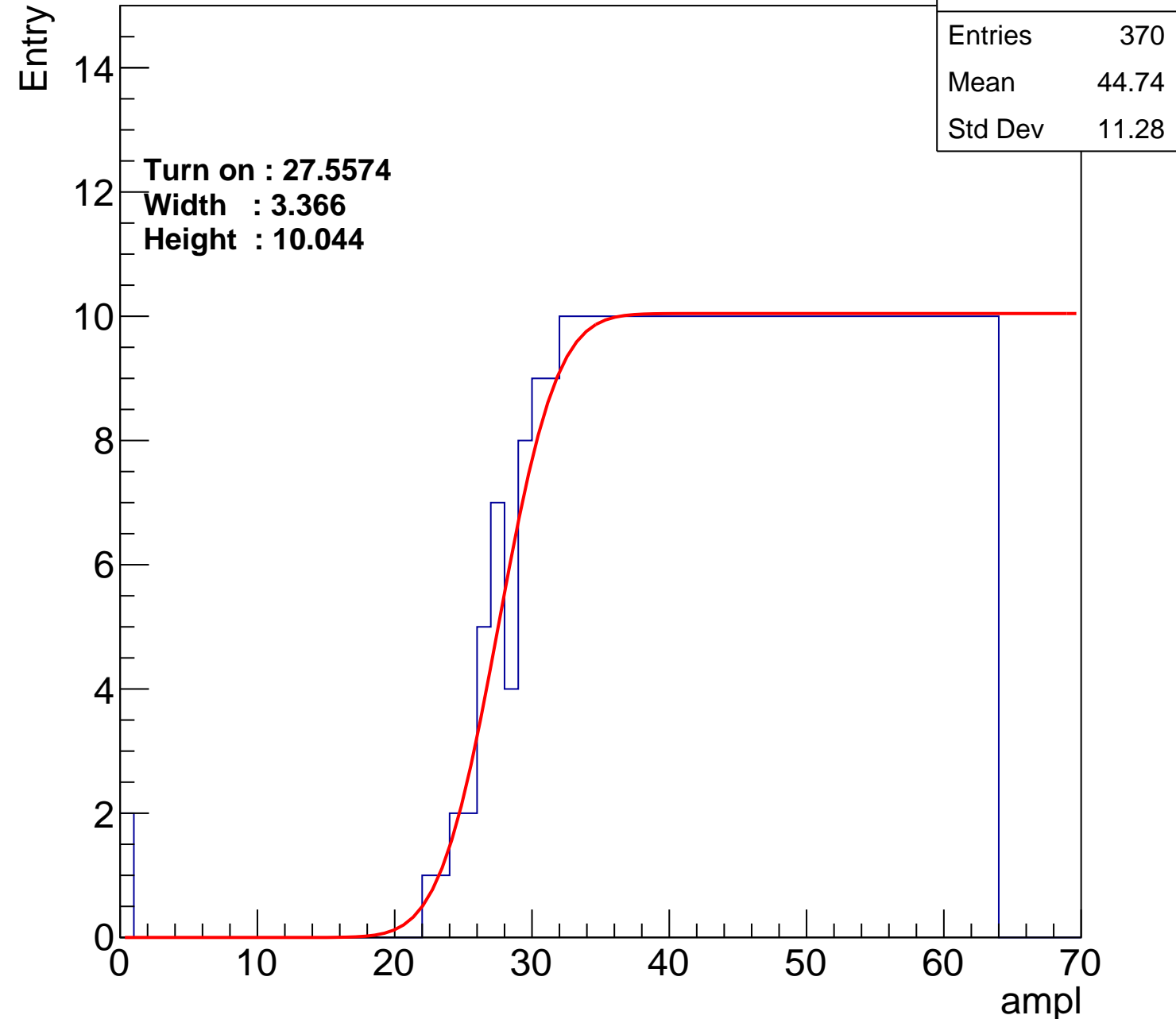
Width : 3.366

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.7
Std Dev	10.6

Turn on : 28.8934

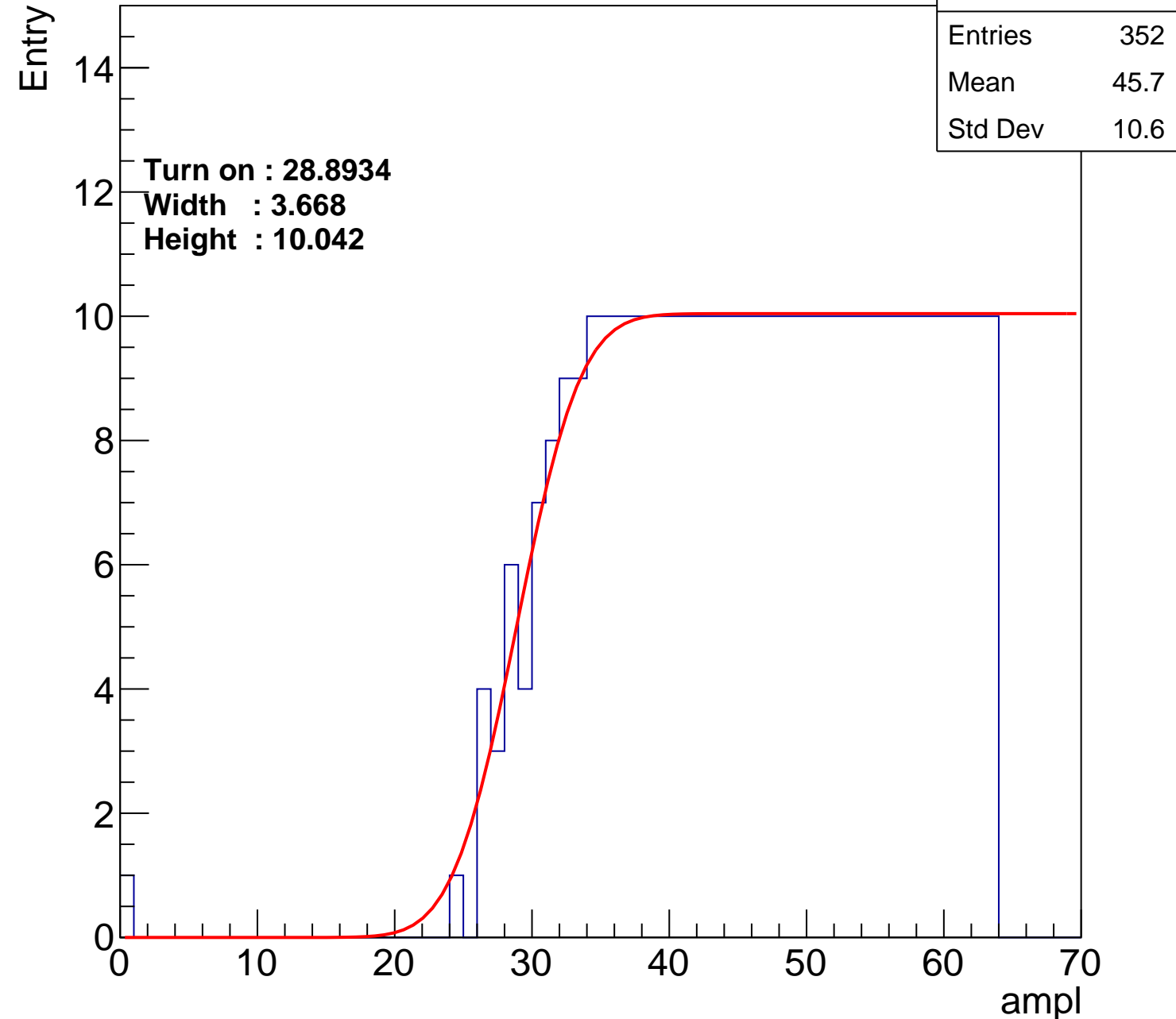
Width : 3.668

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.77
Std Dev	11.44

Turn on : 27.9759

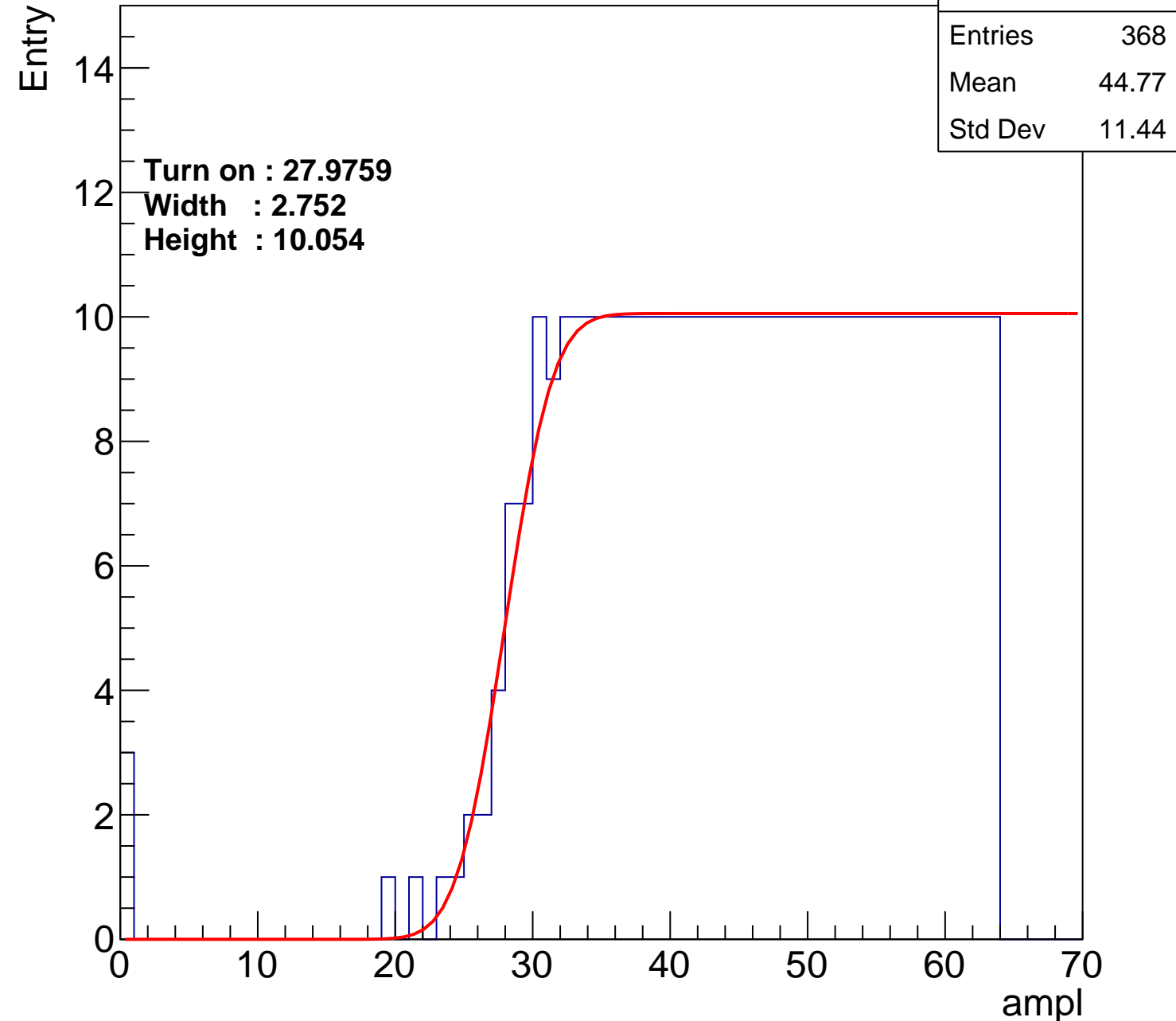
Width : 2.752

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.8
Std Dev	11.11

Turn on : 28.1845

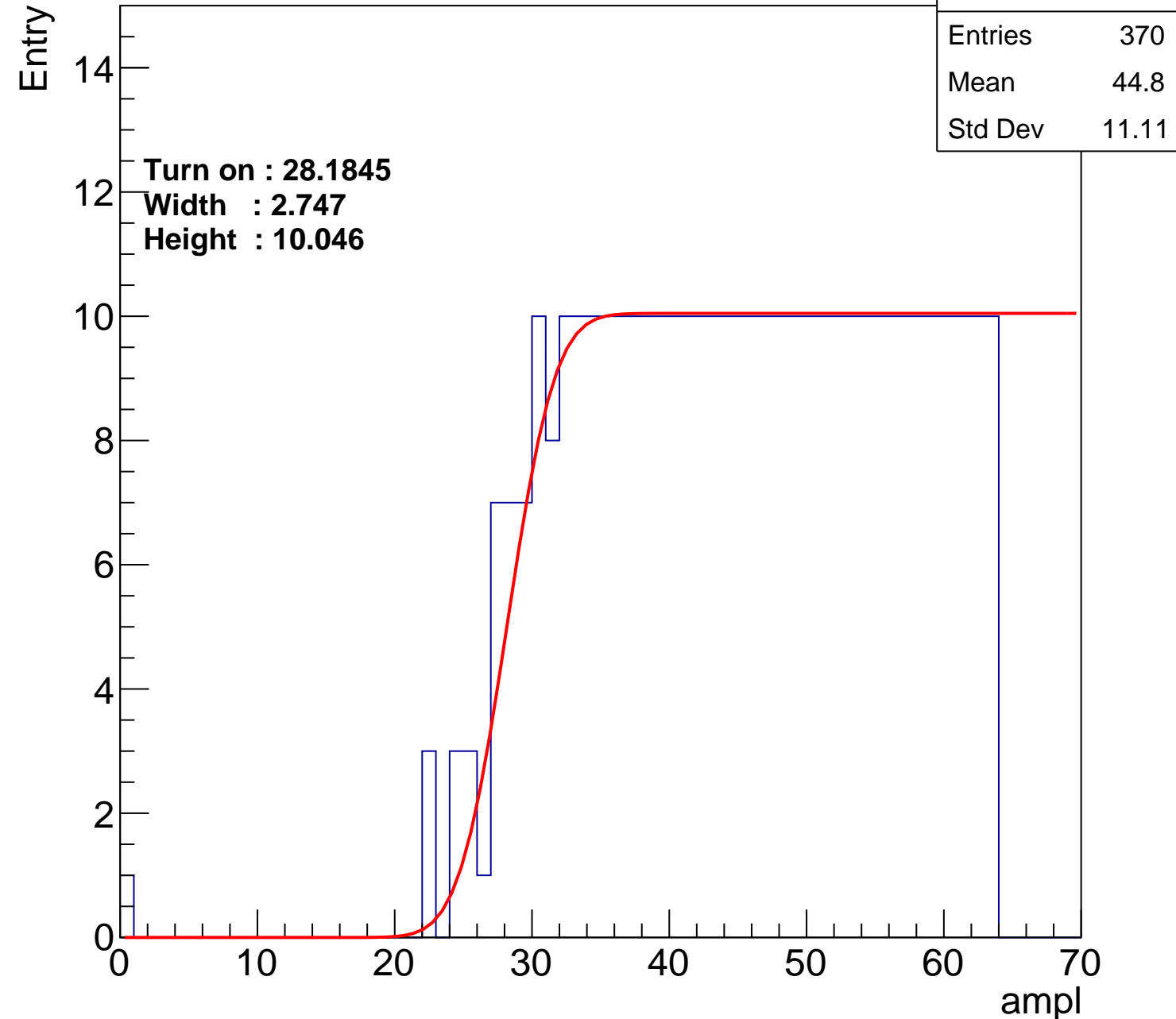
Width : 2.747

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl

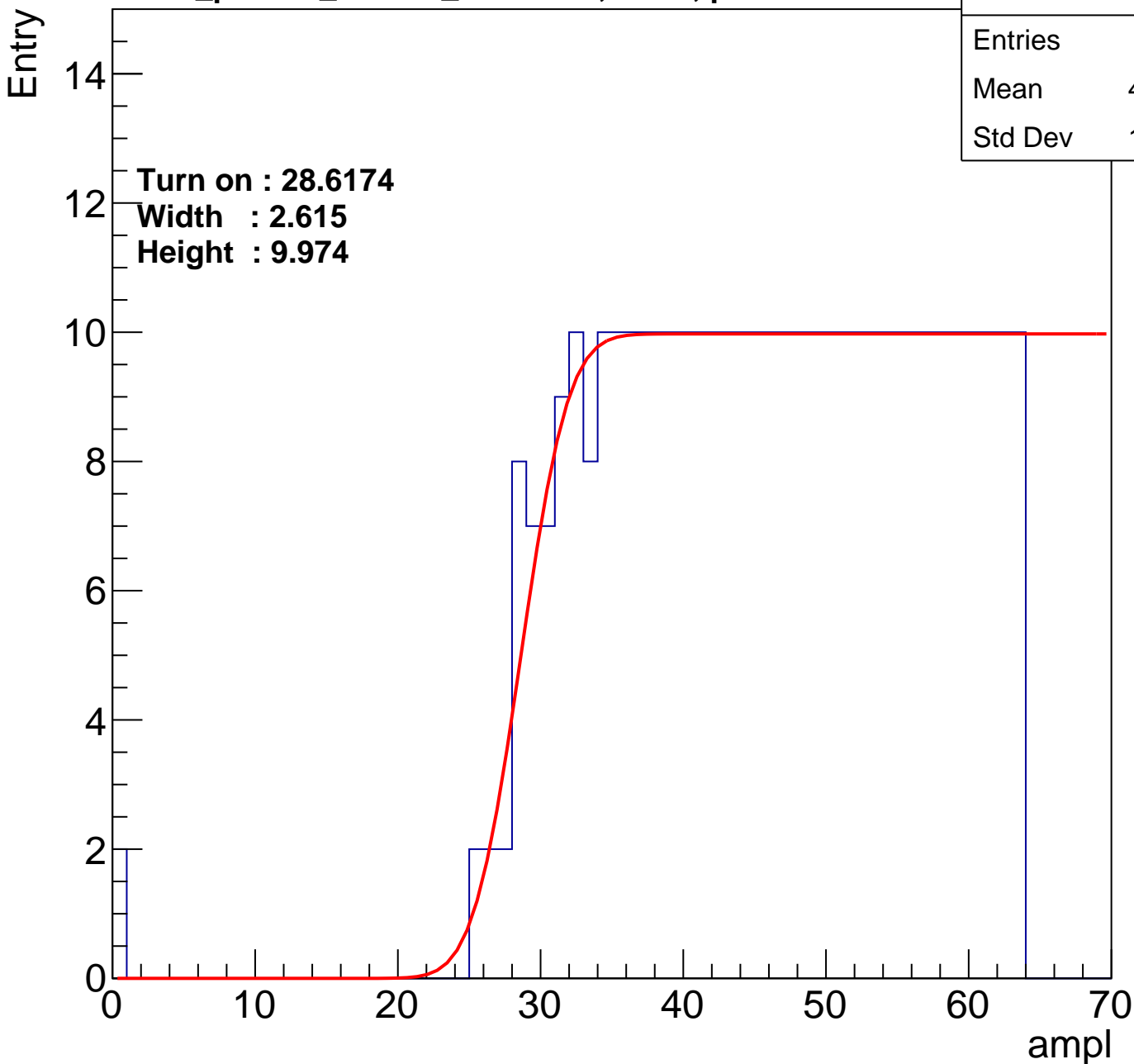


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.39
Std Dev	10.92

Height : 9.974



B0L002S, U22-ch91

calib_packv5_042523_0143.root, FC#8, port C1

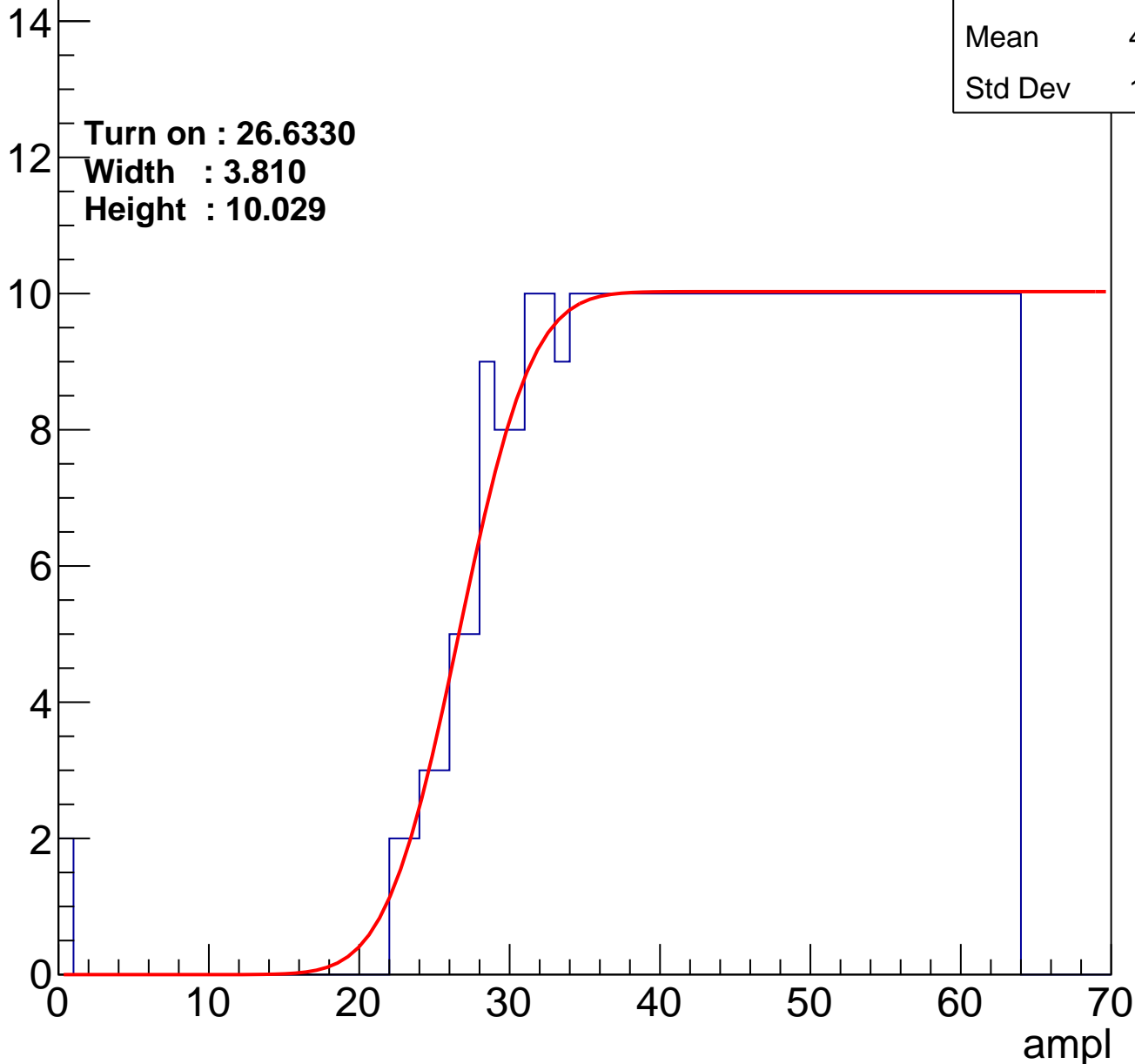
Entries	376
Mean	44.42
Std Dev	11.47

Turn on : 26.6330

Width : 3.810

Height : 10.029

Entry



B0L002S, U22-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.65
Std Dev	11.6

Turn on : 27.6794

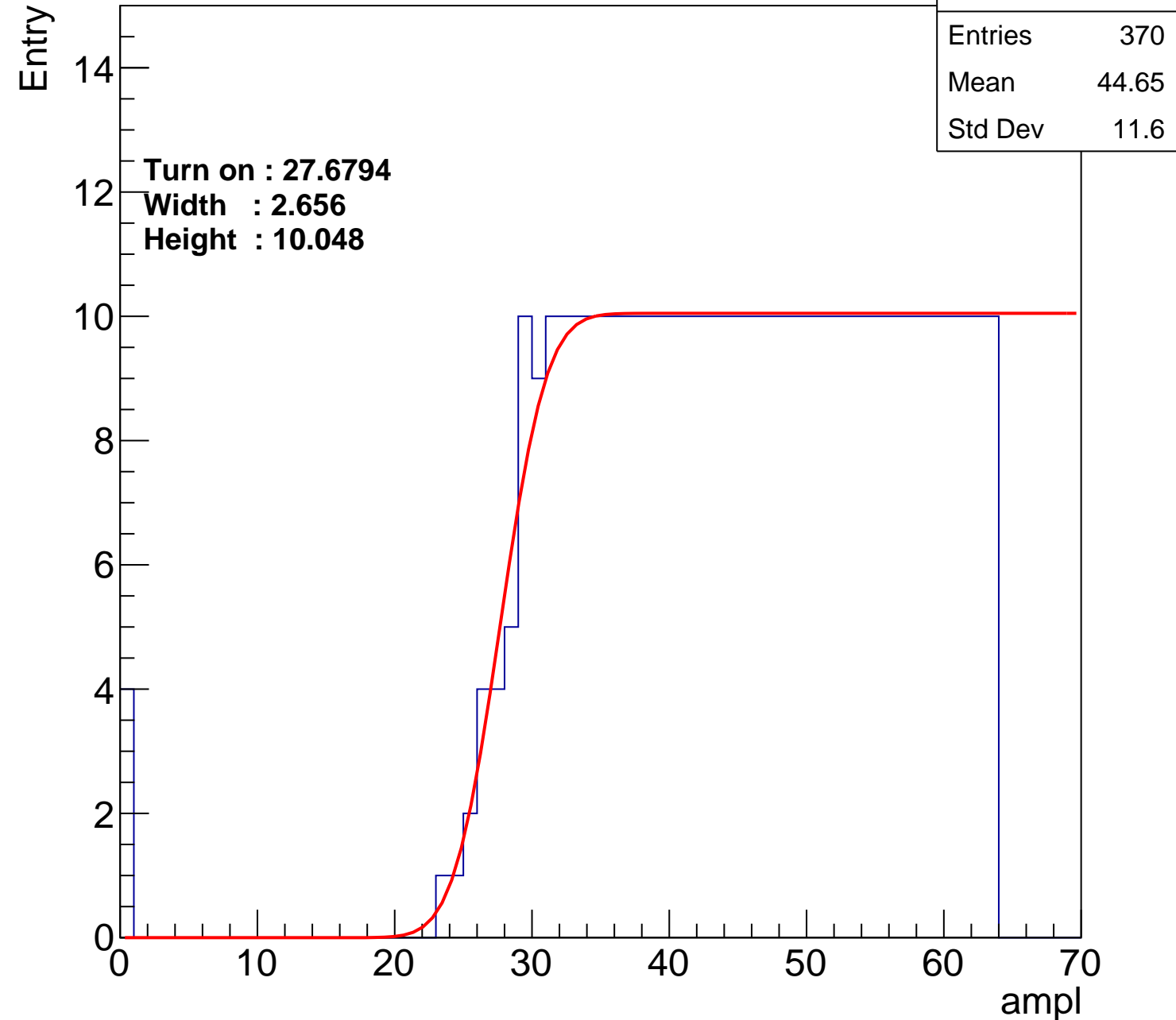
Width : 2.656

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.63
Std Dev	11.29

Turn on : 27.0164

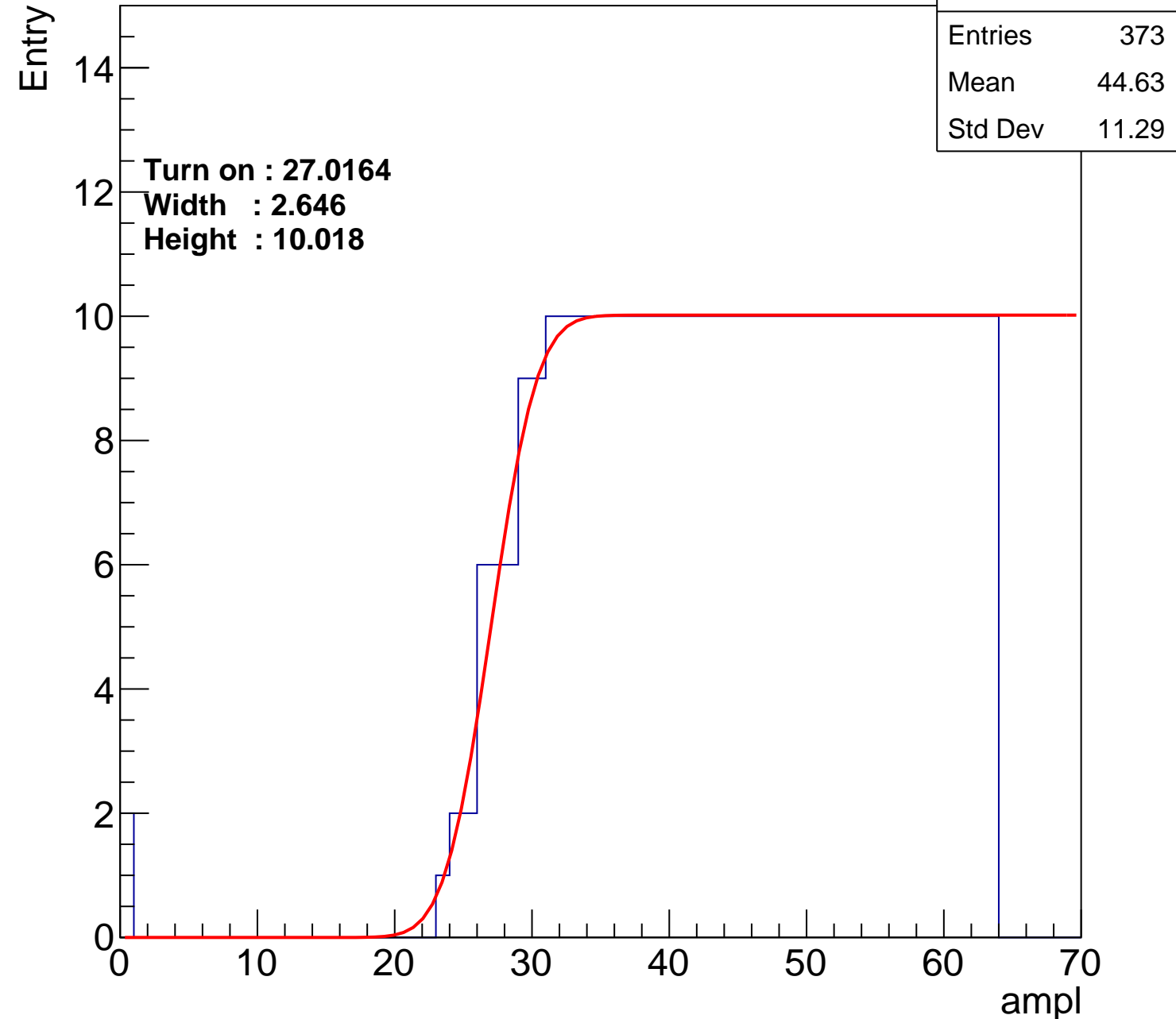
Width : 2.646

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.76
Std Dev	11.4

Turn on : 27.7004

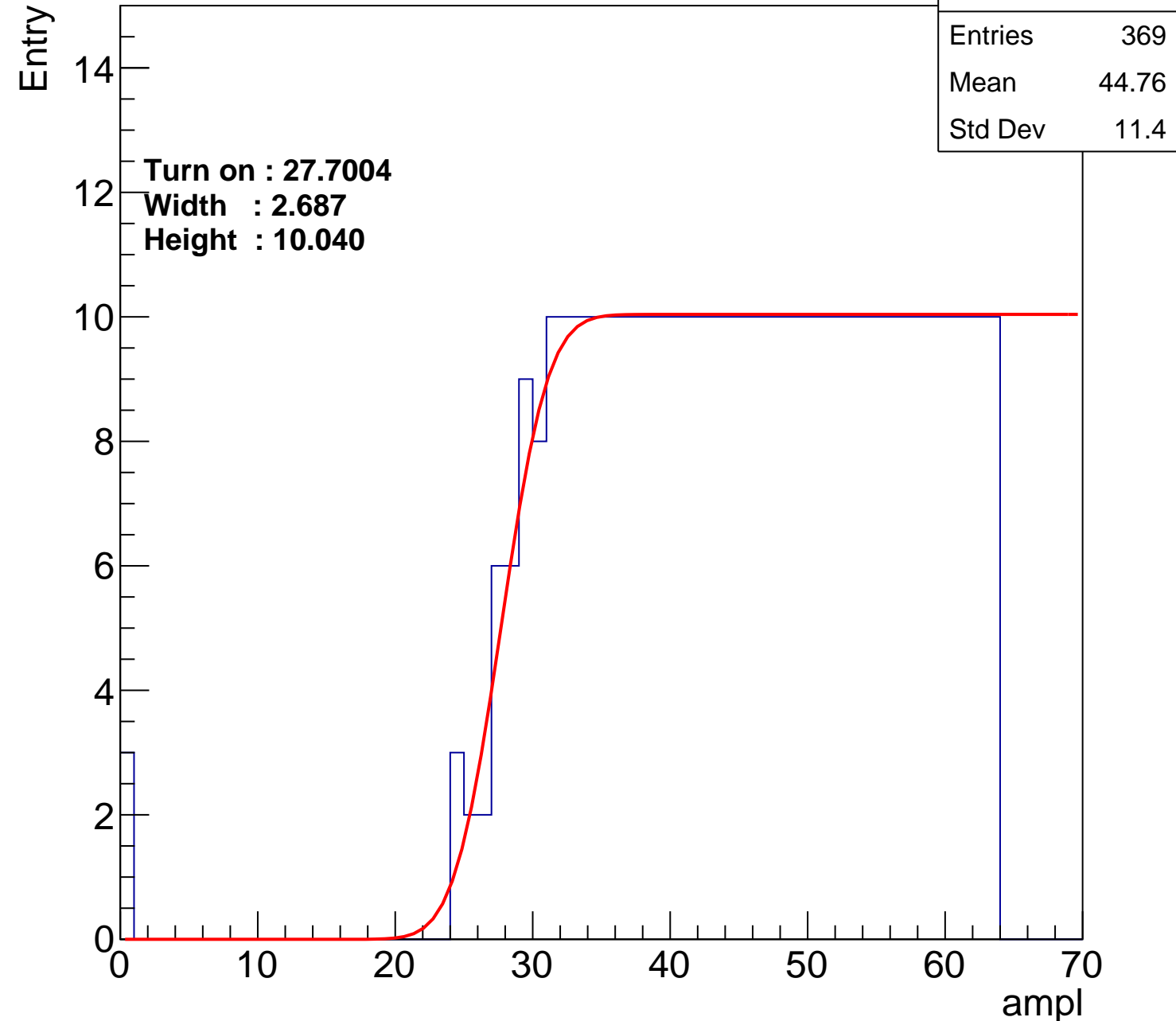
Width : 2.687

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.9091

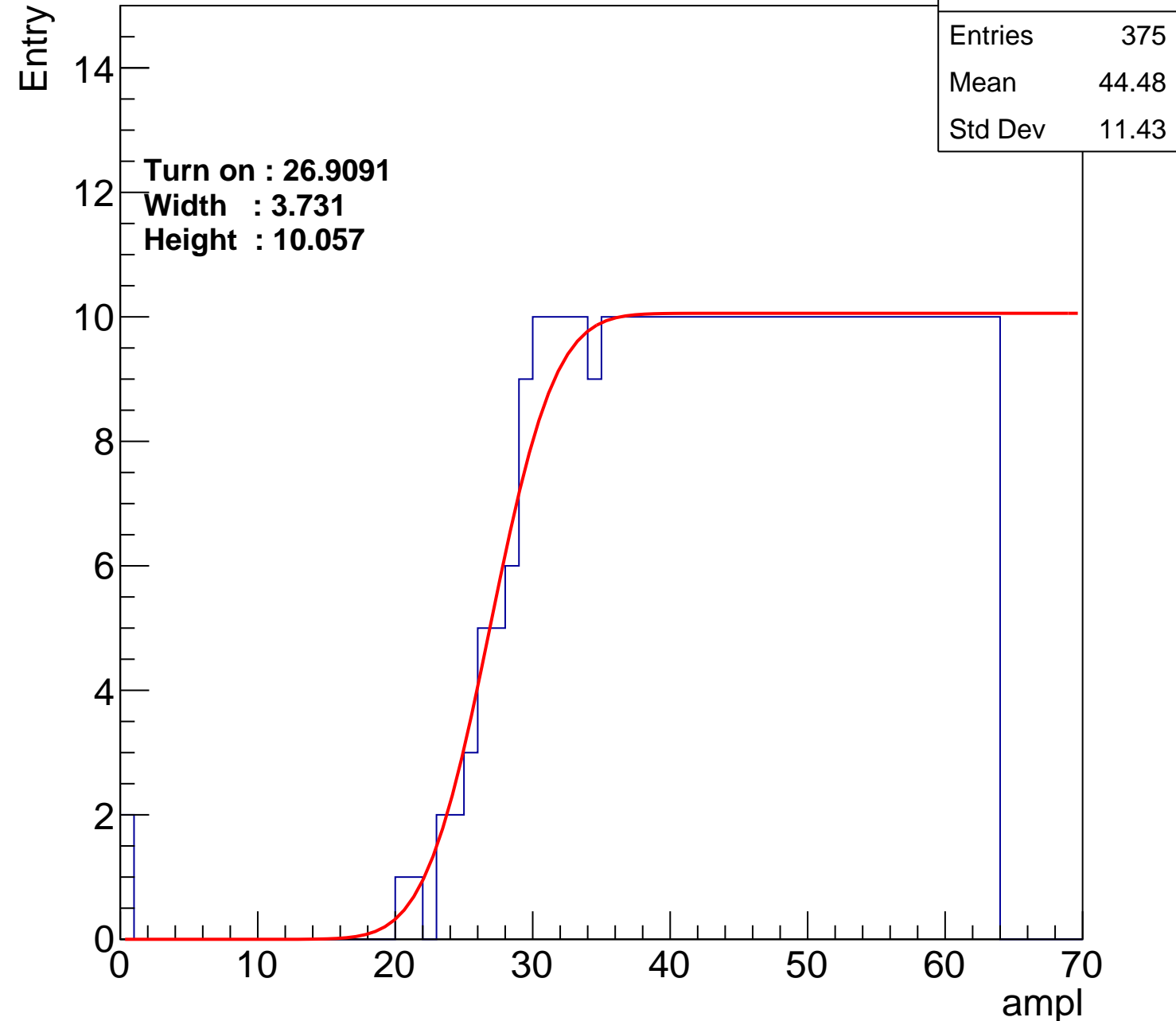
Width : 3.731

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.68
Std Dev	10.59

Turn on : 29.4618

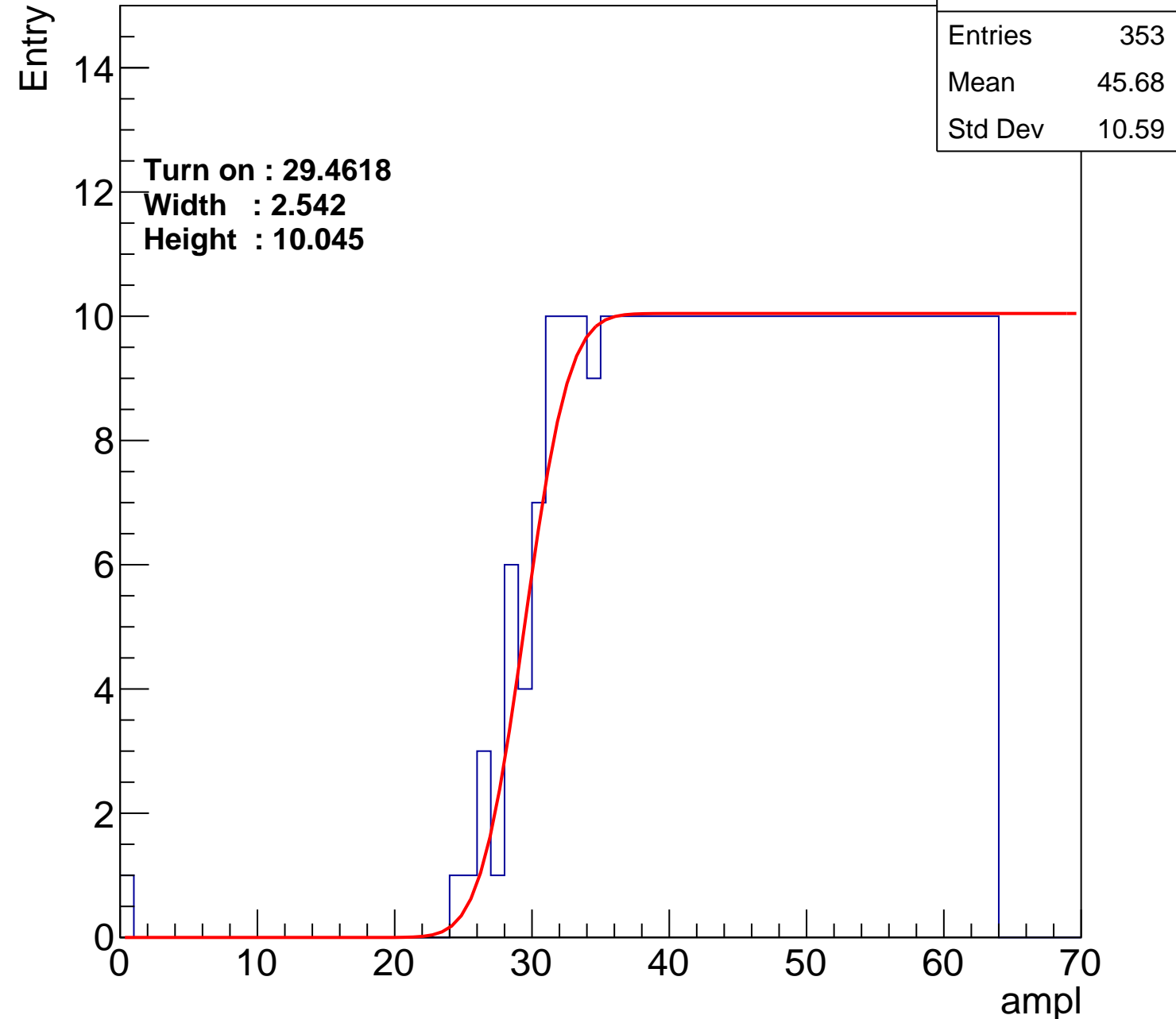
Width : 2.542

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.29
Std Dev	11.34

Turn on : 25.9963

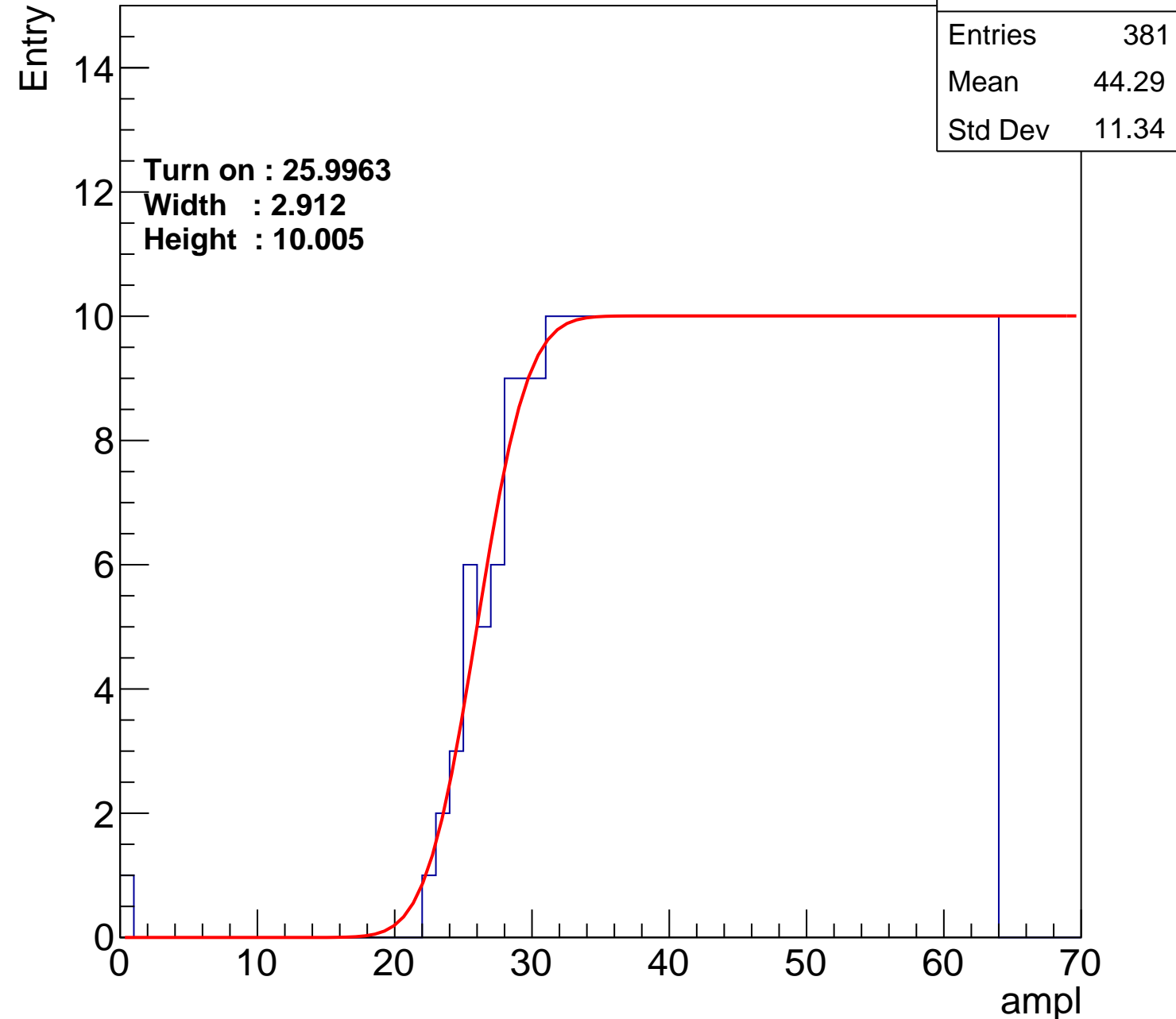
Width : 2.912

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch98

calib_packv5_042523_0143.root, FC#8, port C1

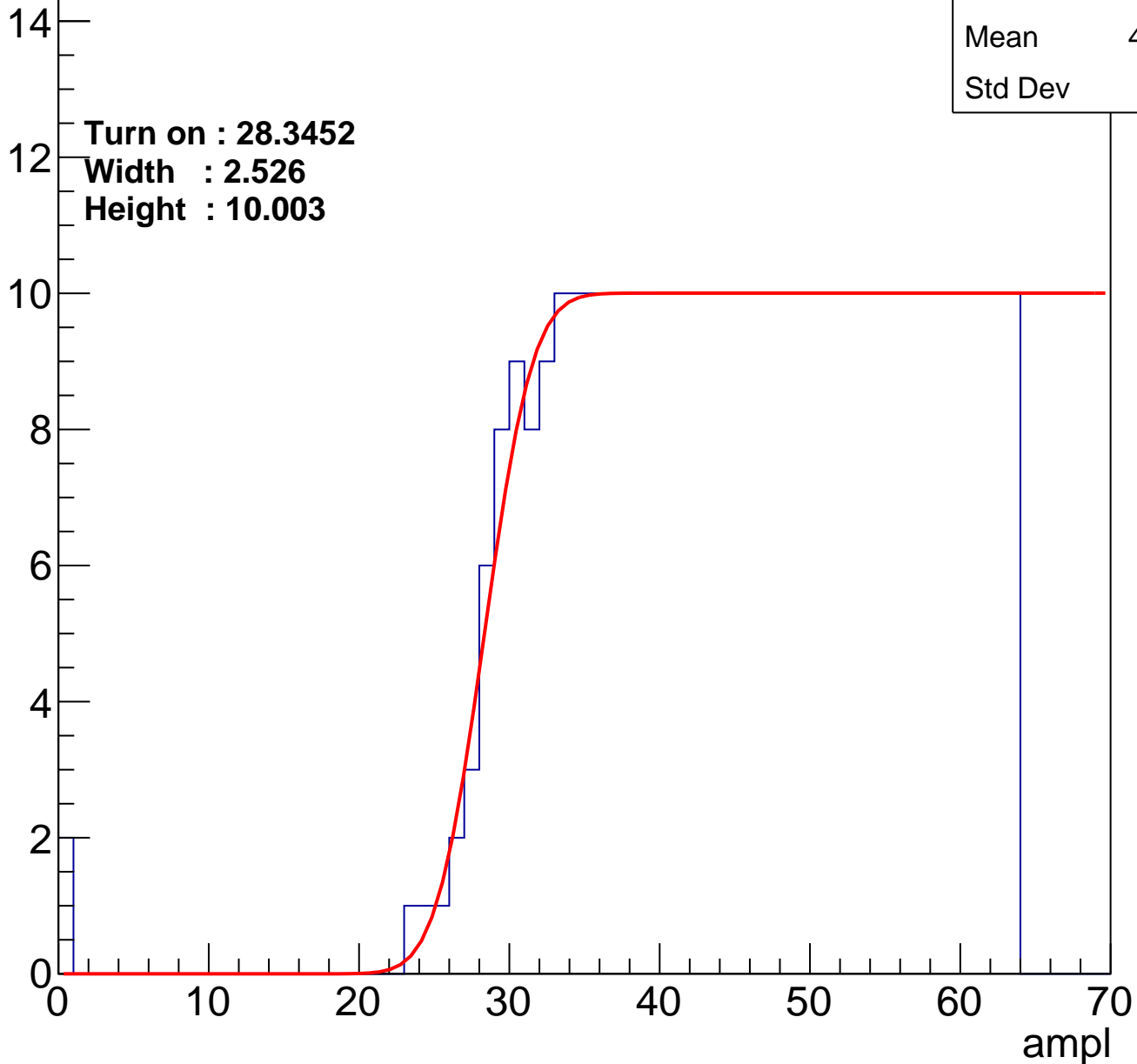
Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.3452

Width : 2.526

Height : 10.003

Entry



B0L002S, U22-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.61
Std Dev	10.63

Turn on : 28.7594

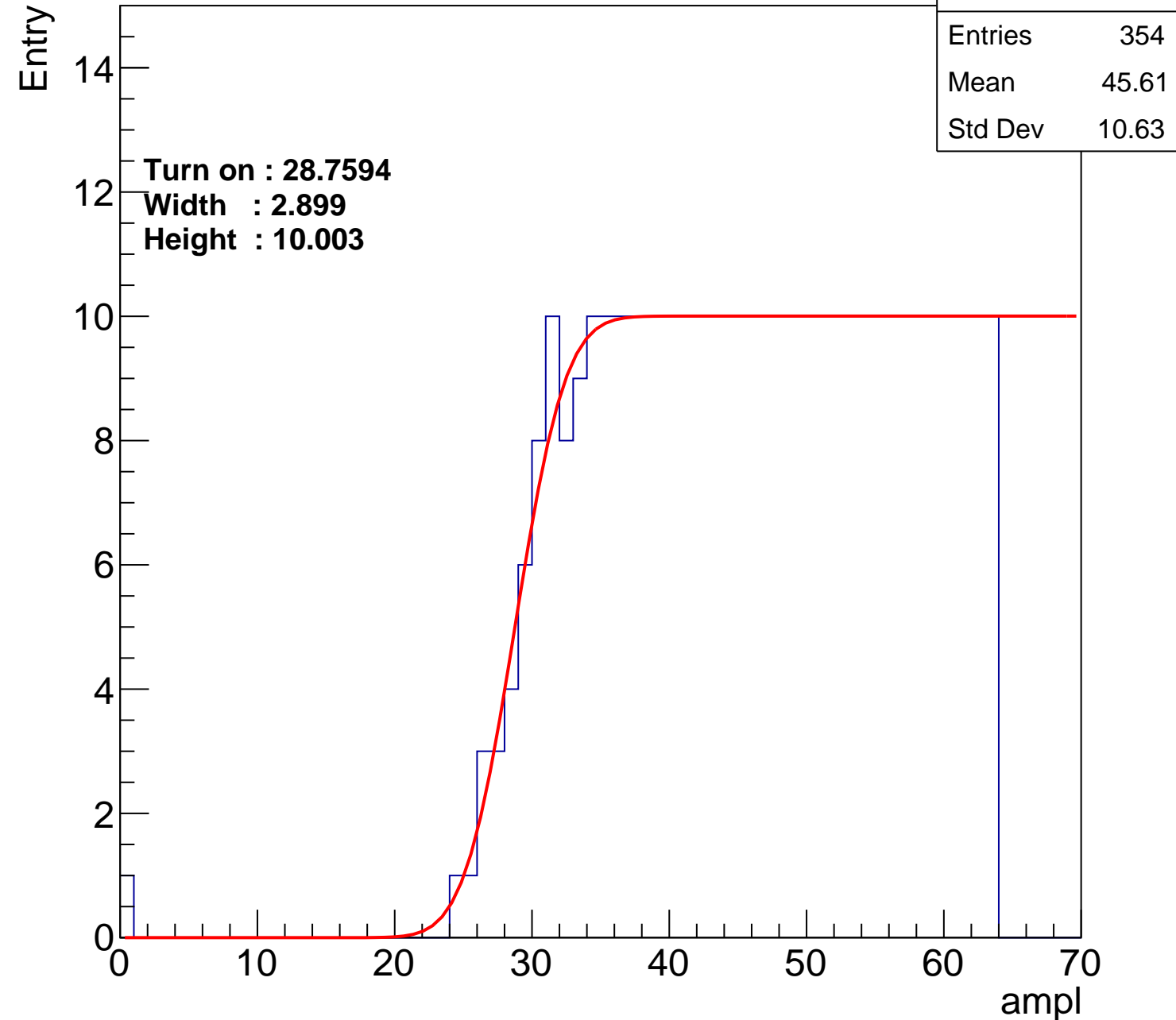
Width : 2.899

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch100

calib_packv5_042523_0143.root, FC#8, port C1

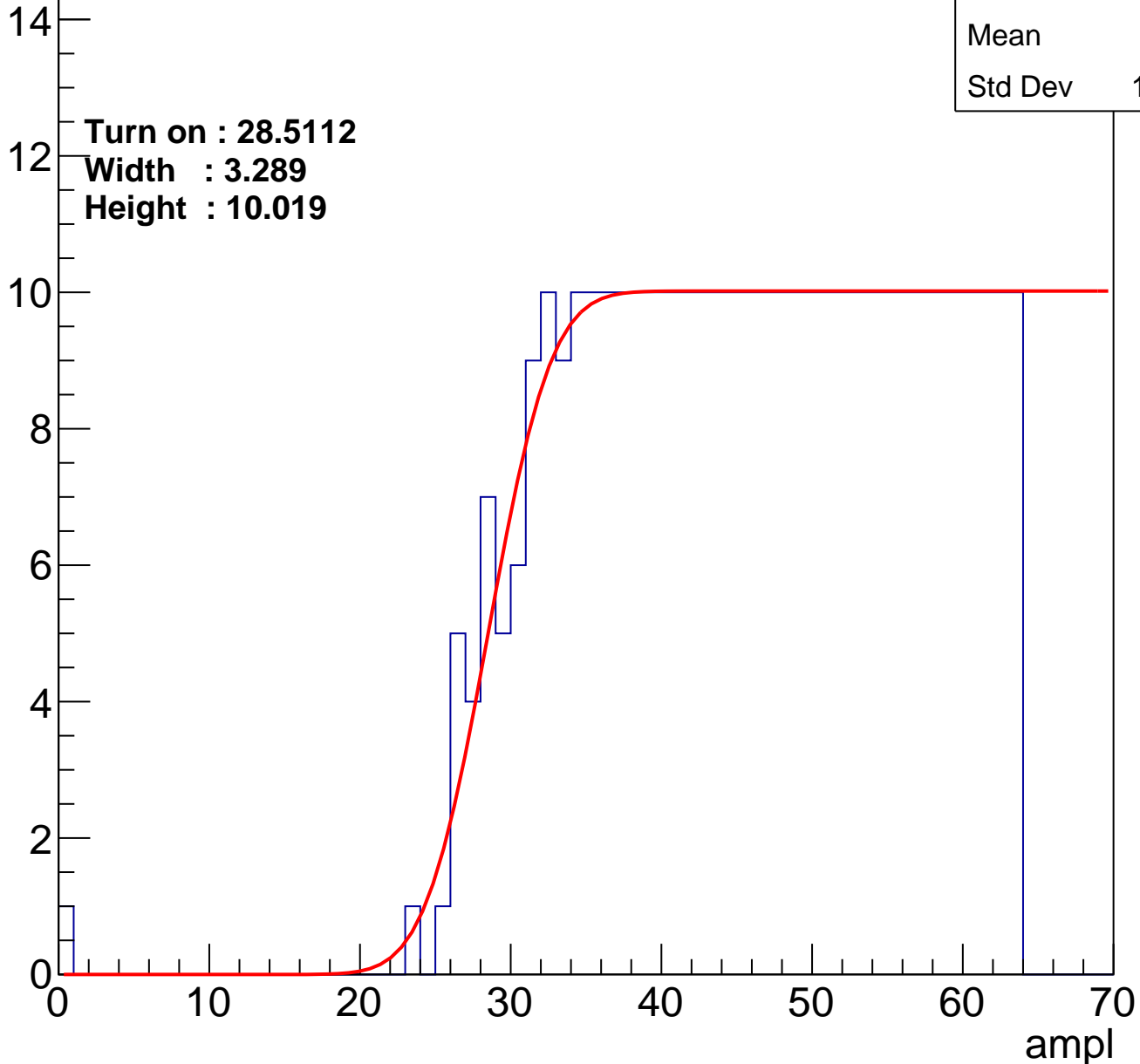
Entries	358
Mean	45.4
Std Dev	10.77

Turn on : 28.5112

Width : 3.289

Height : 10.019

Entry



B0L002S, U22-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.89
Std Dev	10.47

Turn on : 29.6846

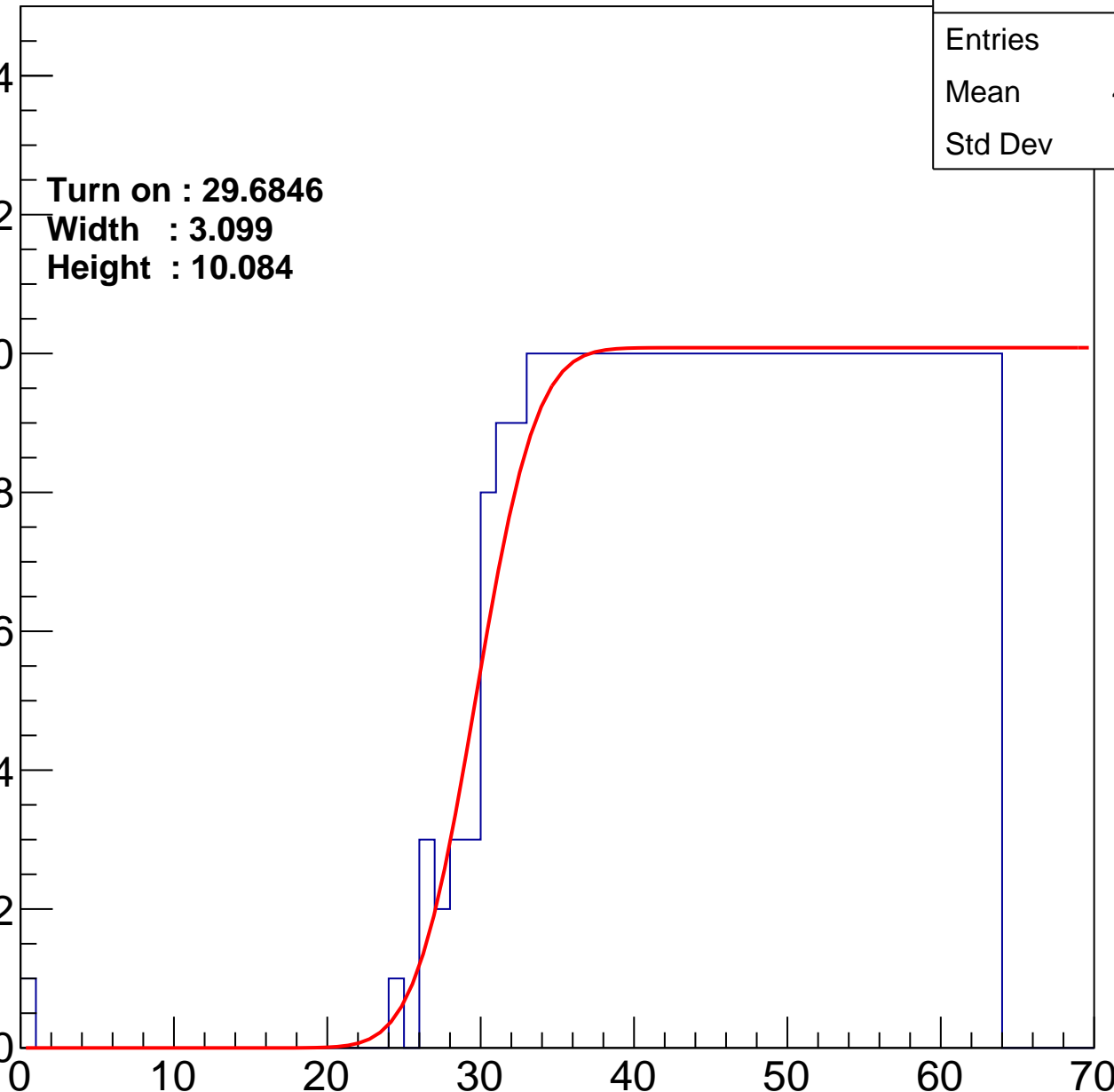
Width : 3.099

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 26.5131

Width : 3.162

Height : 10.032

Entry

14

12

10

8

6

4

2

0

0

10

20

30

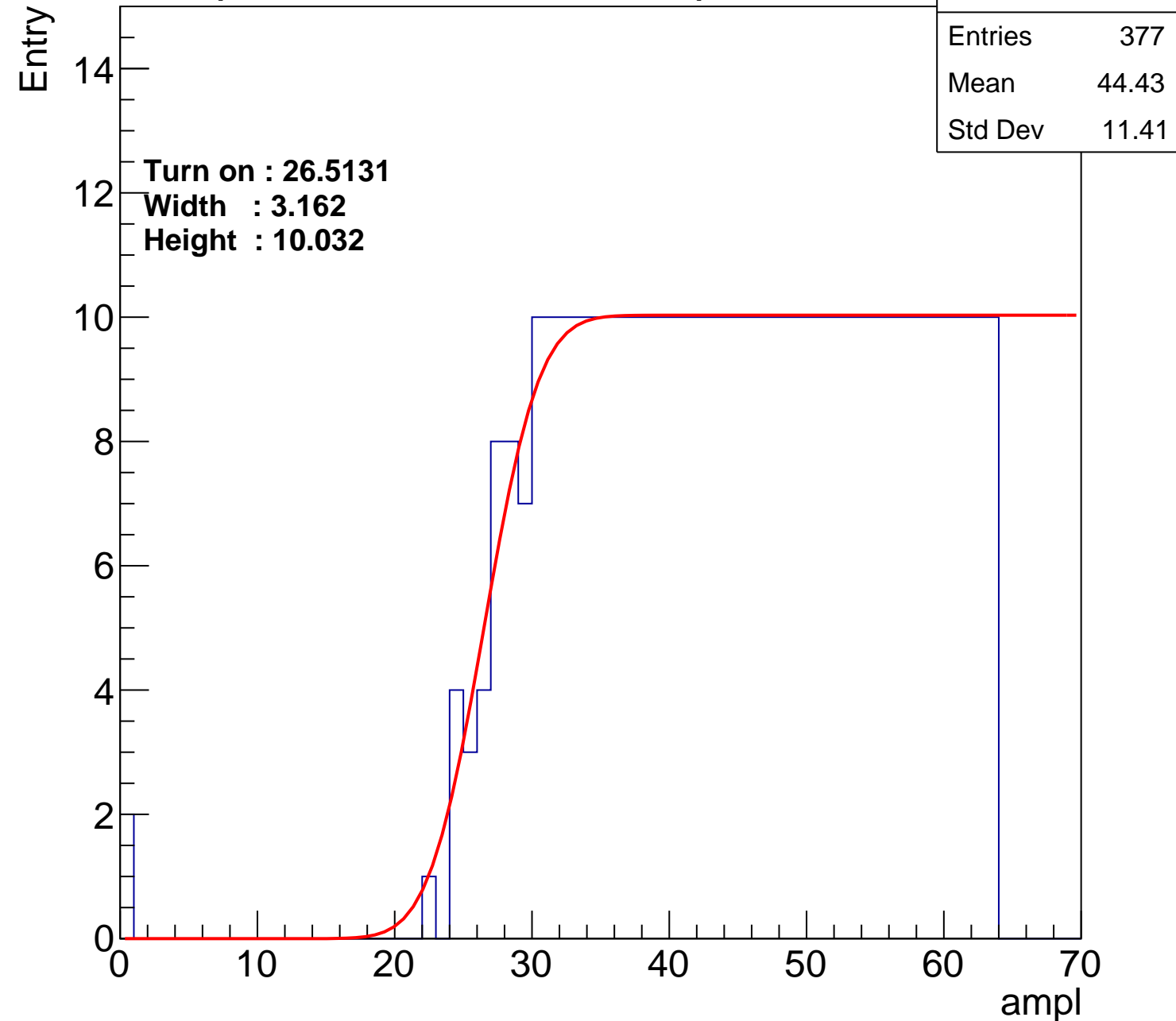
40

50

60

70

ampl



B0L002S, U22-ch103

calib_packv5_042523_0143.root, FC#8, port C1

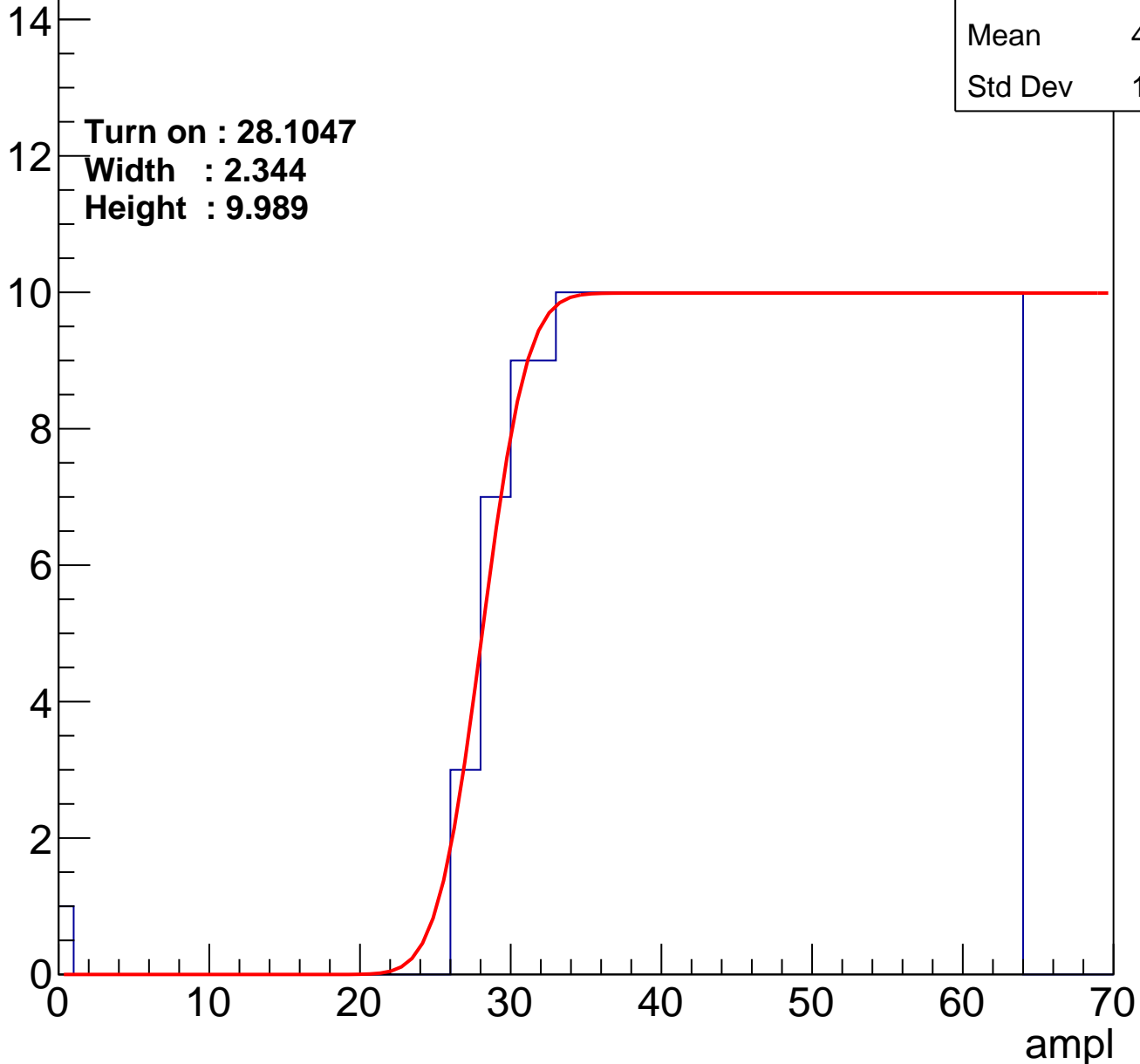
Entries	358
Mean	45.46
Std Dev	10.66

Turn on : 28.1047

Width : 2.344

Height : 9.989

Entry



B0L002S, U22-ch104

calib_packv5_042523_0143.root, FC#8, port C1

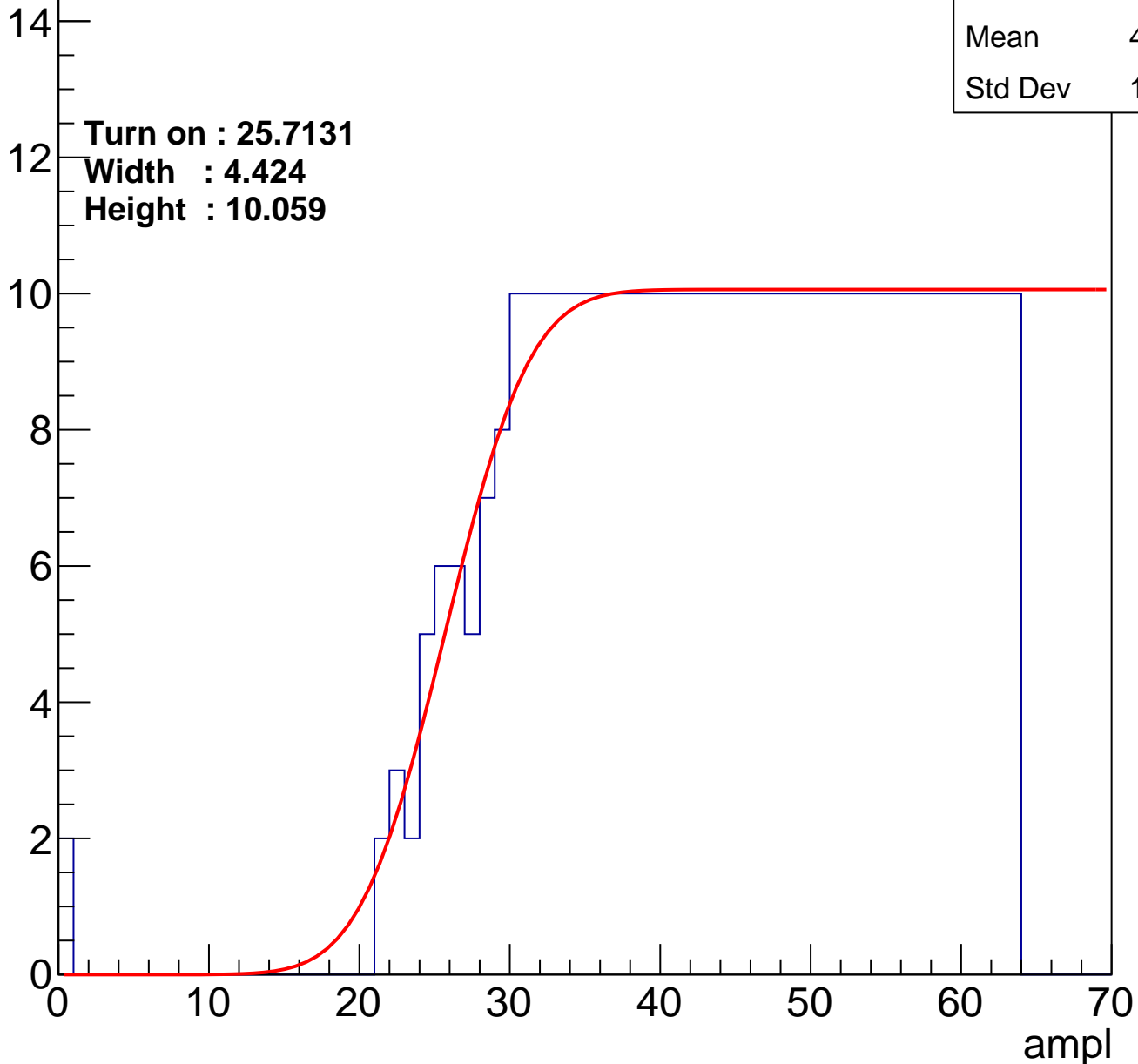
Entries	386
Mean	43.92
Std Dev	11.75

Turn on : 25.7131

Width : 4.424

Height : 10.059

Entry



B0L002S, U22-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.32
Std Dev	10.99

Turn on : 28.4626

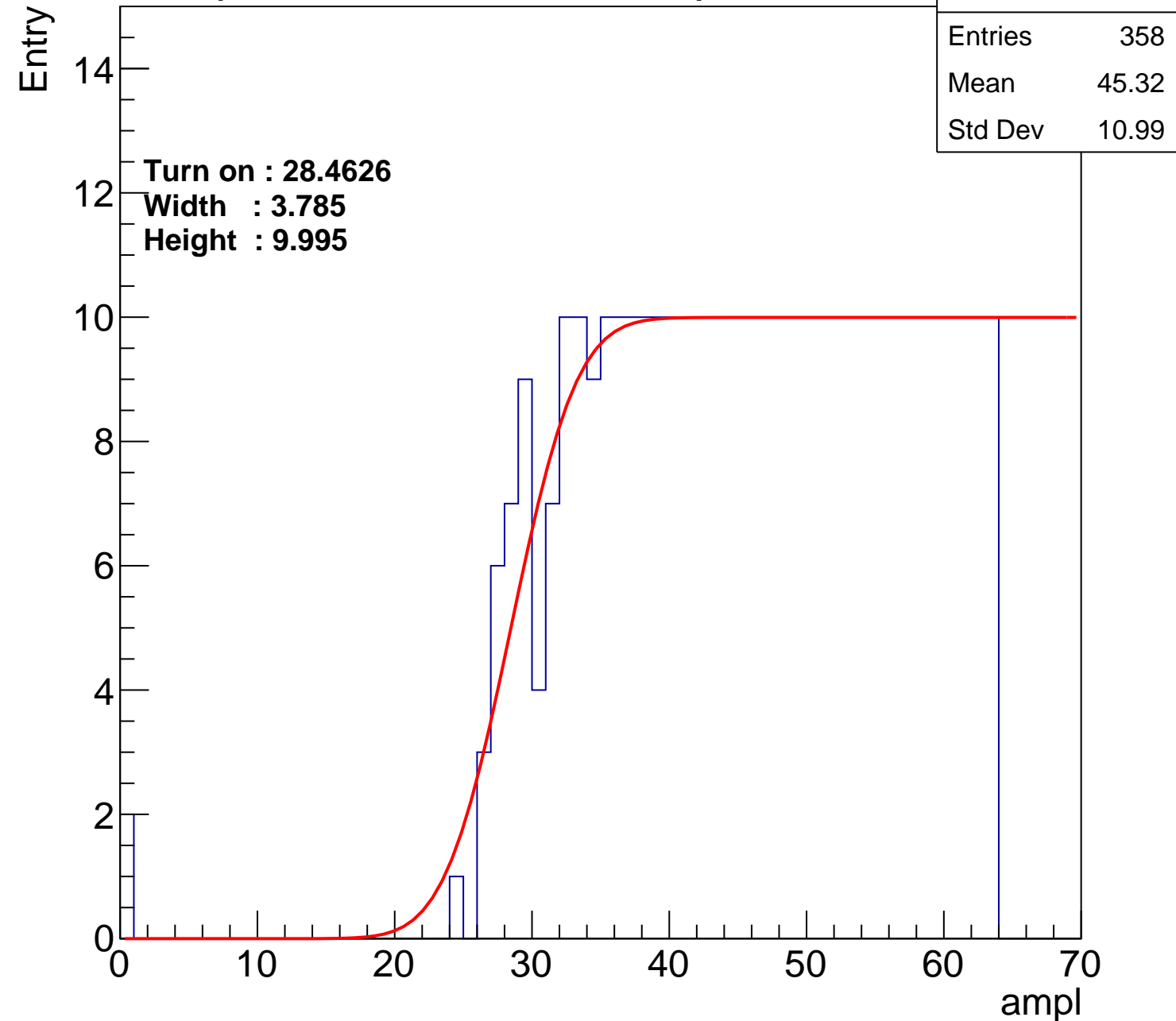
Width : 3.785

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch106

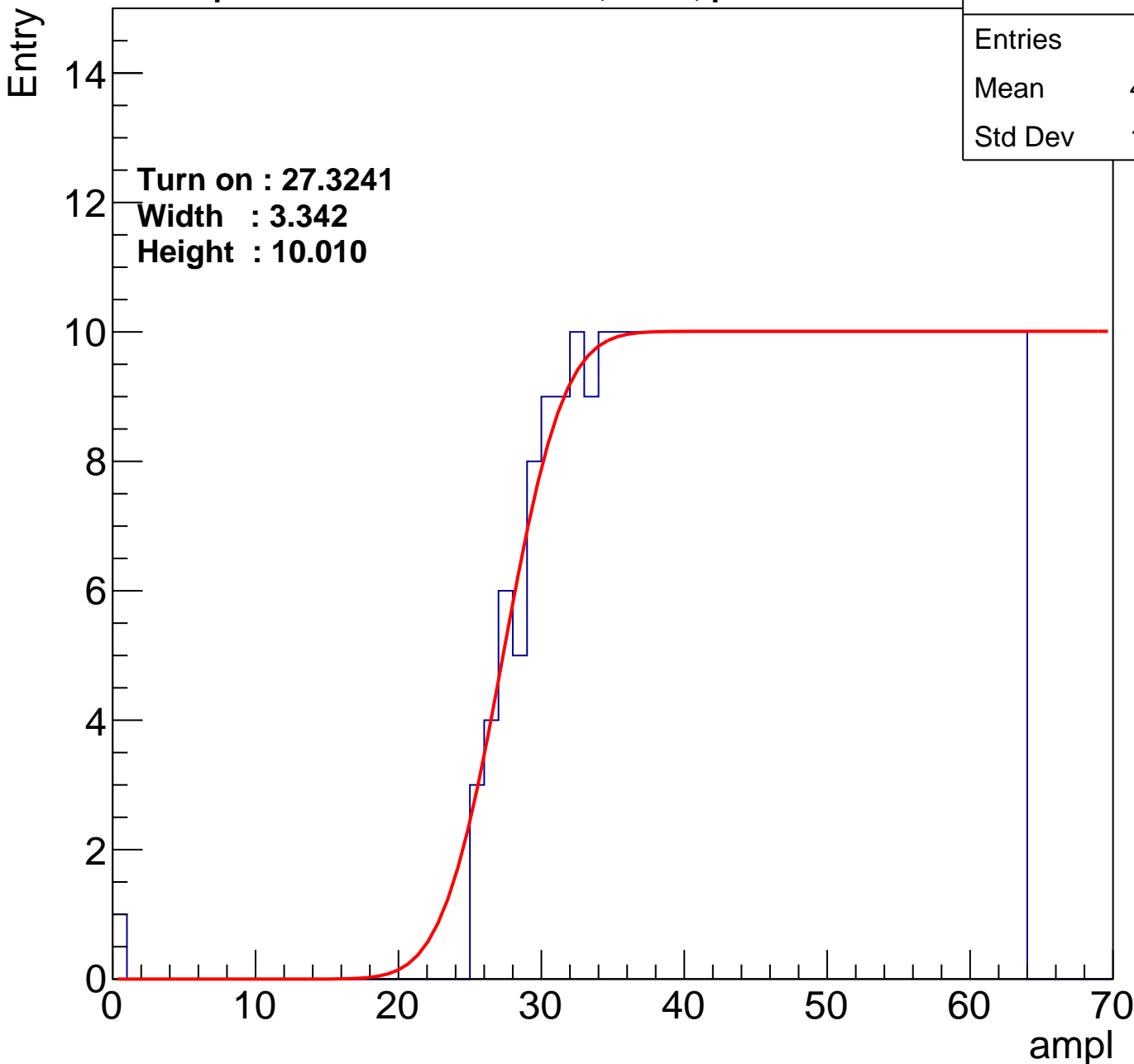
calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.13
Std Dev	10.87

Turn on : 27.3241

Width : 3.342

Height : 10.010



B0L002S, U22-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.55
Std Dev	11.88

Turn on : 27.5248

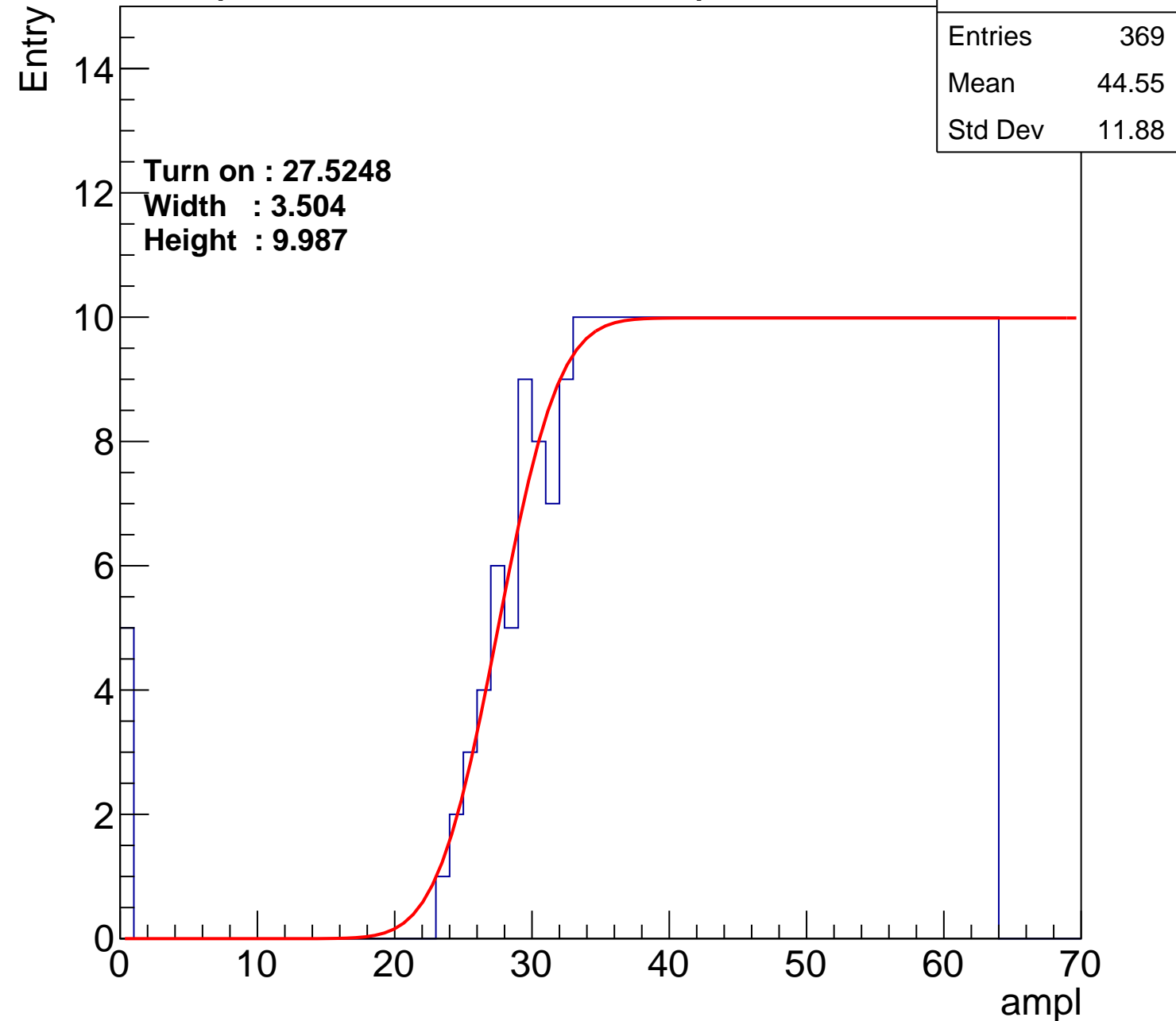
Width : 3.504

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.21
Std Dev	11.72

Turn on : 26.6260

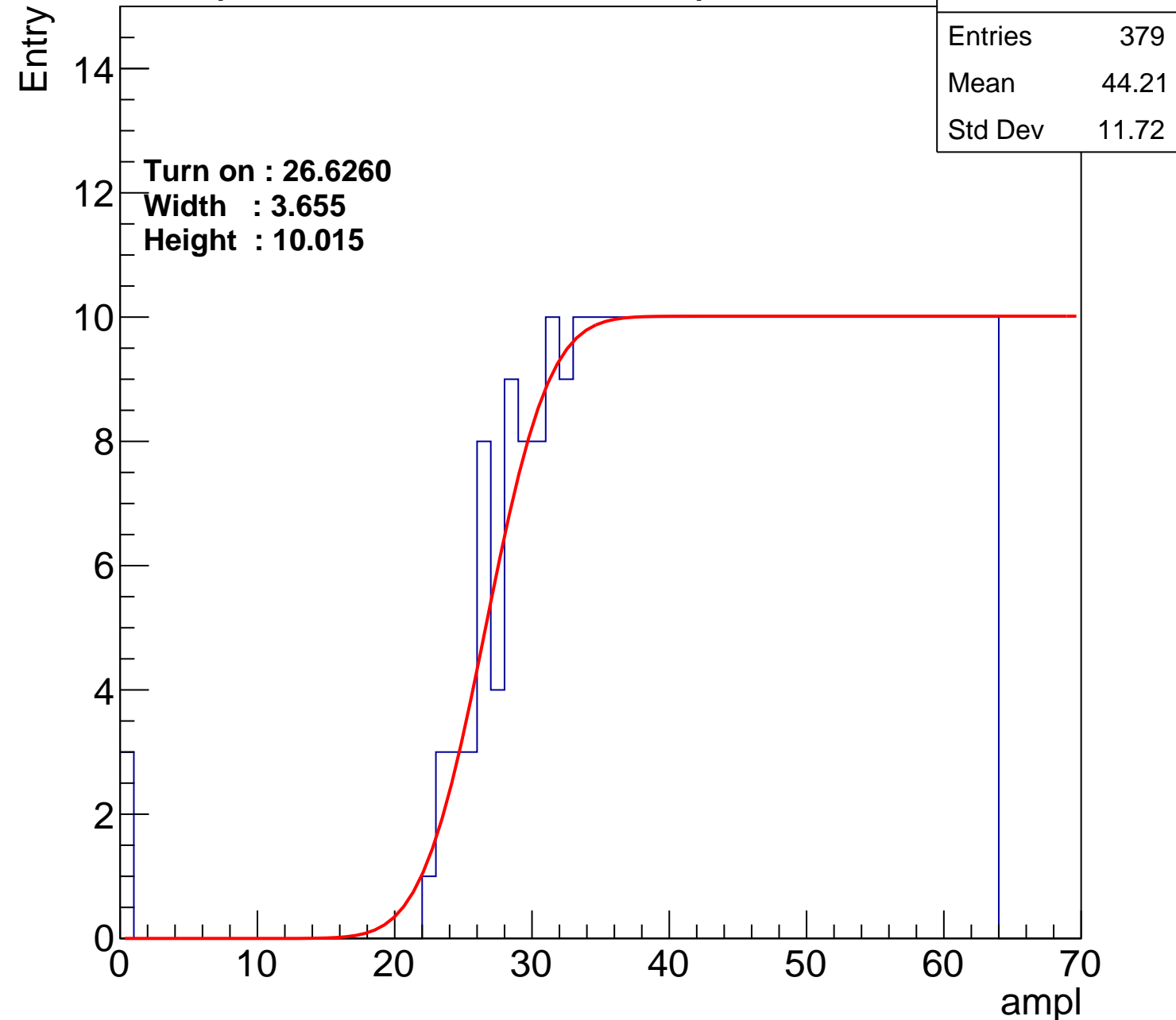
Width : 3.655

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch109

calib_packv5_042523_0143.root, FC#8, port C1

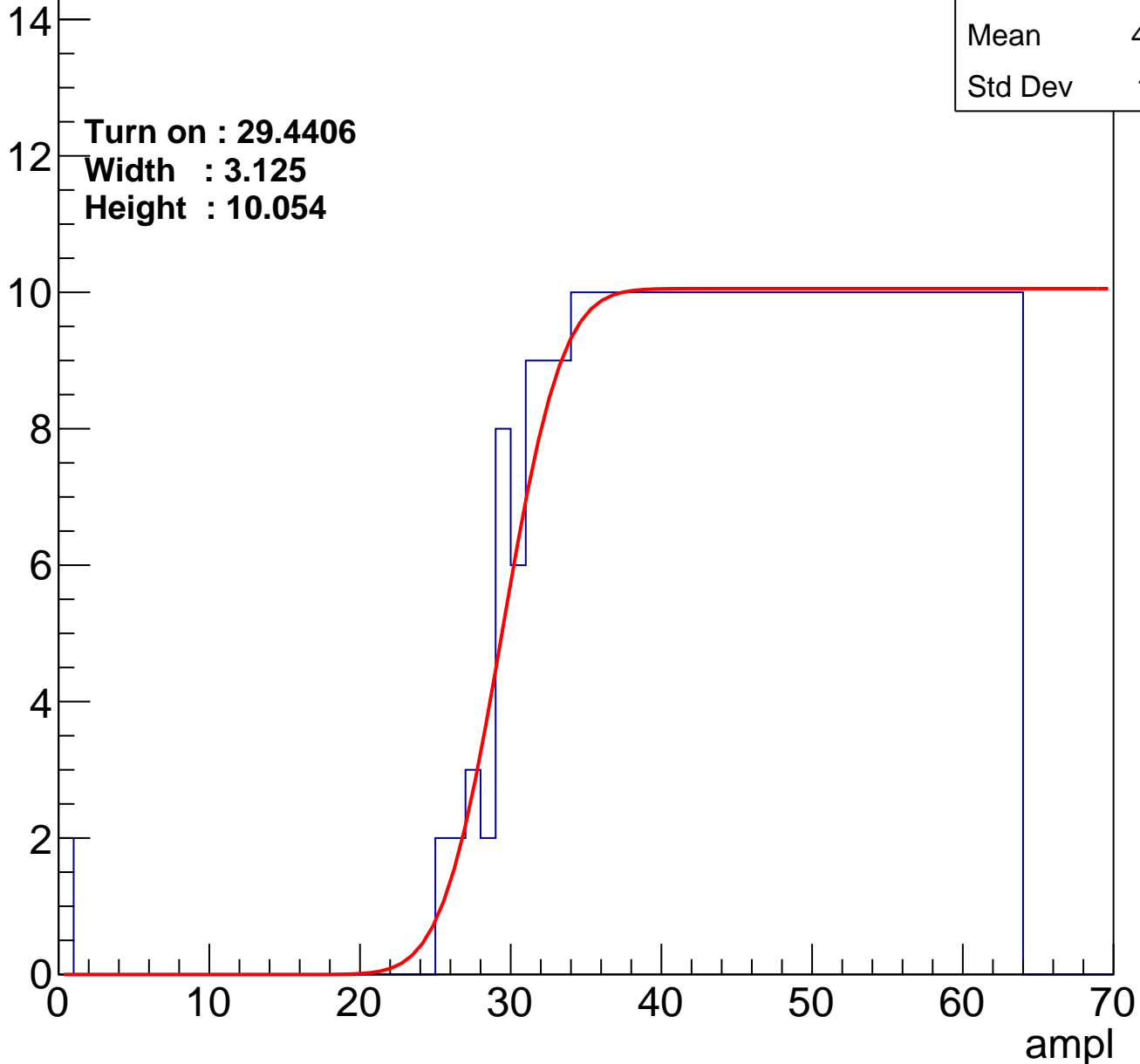
Entries	352
Mean	45.64
Std Dev	10.81

Turn on : 29.4406

Width : 3.125

Height : 10.054

Entry



B0L002S, U22-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.54
Std Dev	10.85

Turn on : 28.7659

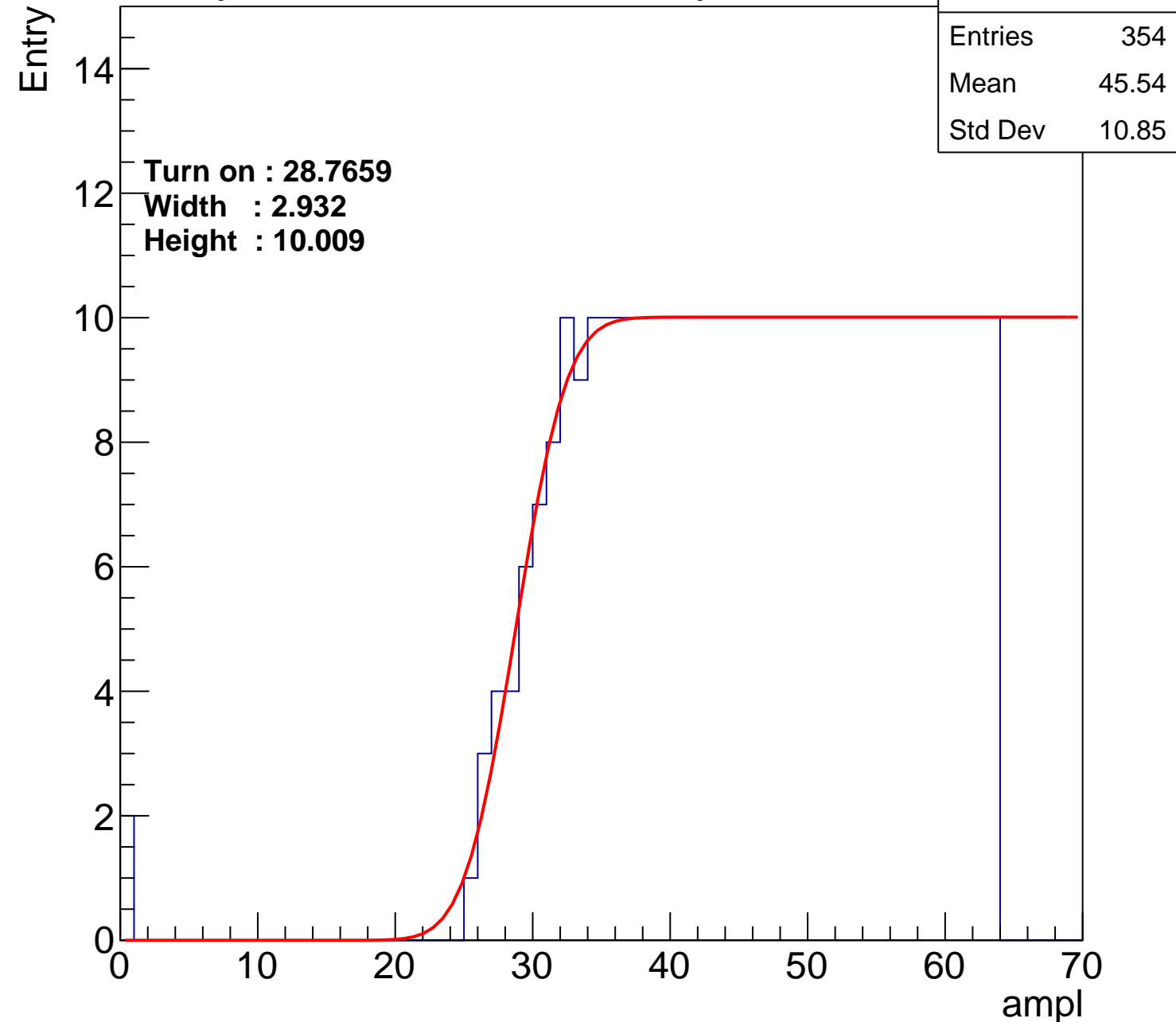
Width : 2.932

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.4
Std Dev	10.92

Turn on : 28.7292

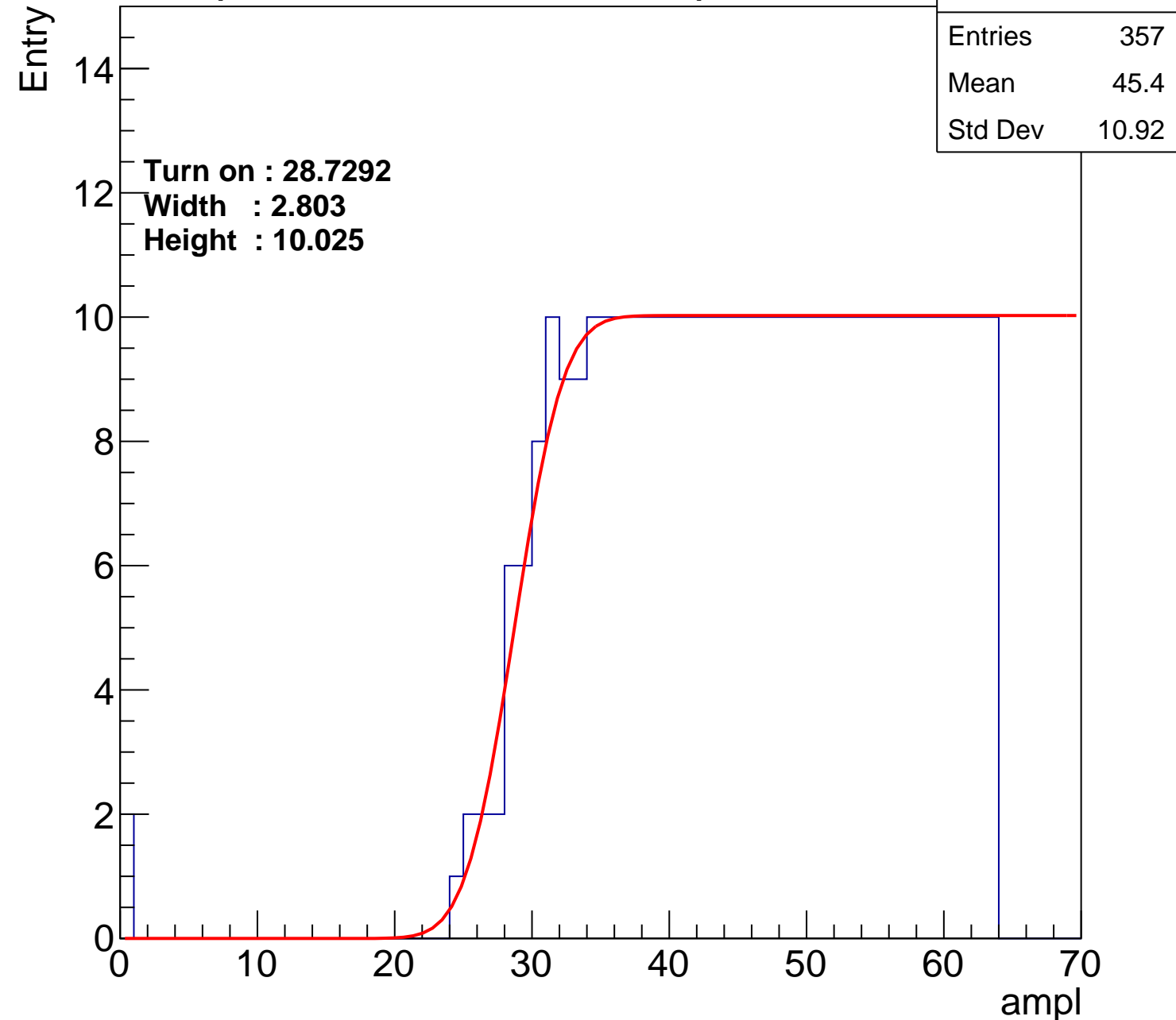
Width : 2.803

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.48
Std Dev	11.29

Turn on : 26.2676

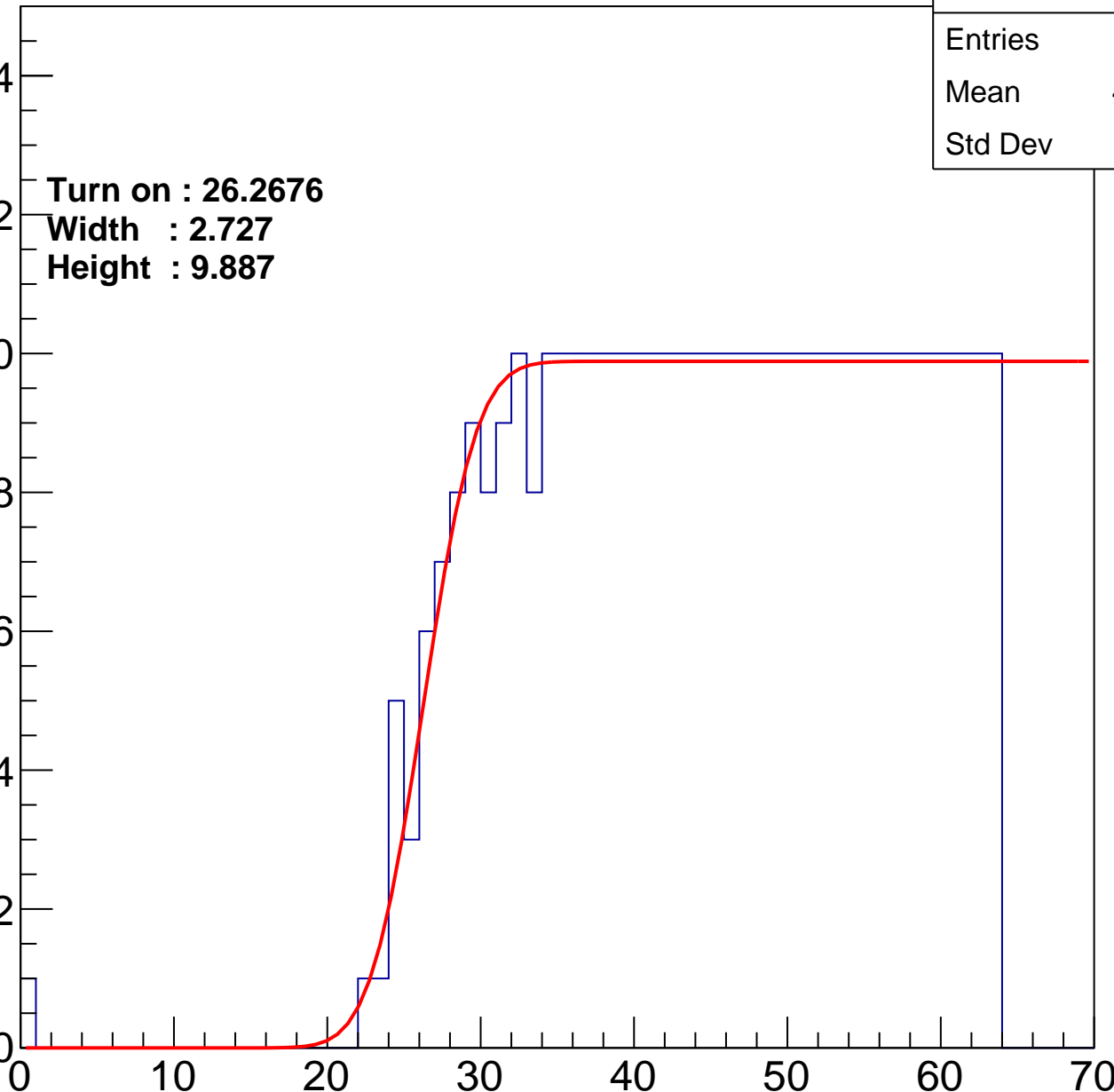
Width : 2.727

Height : 9.887

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.13
Std Dev	11.02

Turn on : 27.8980

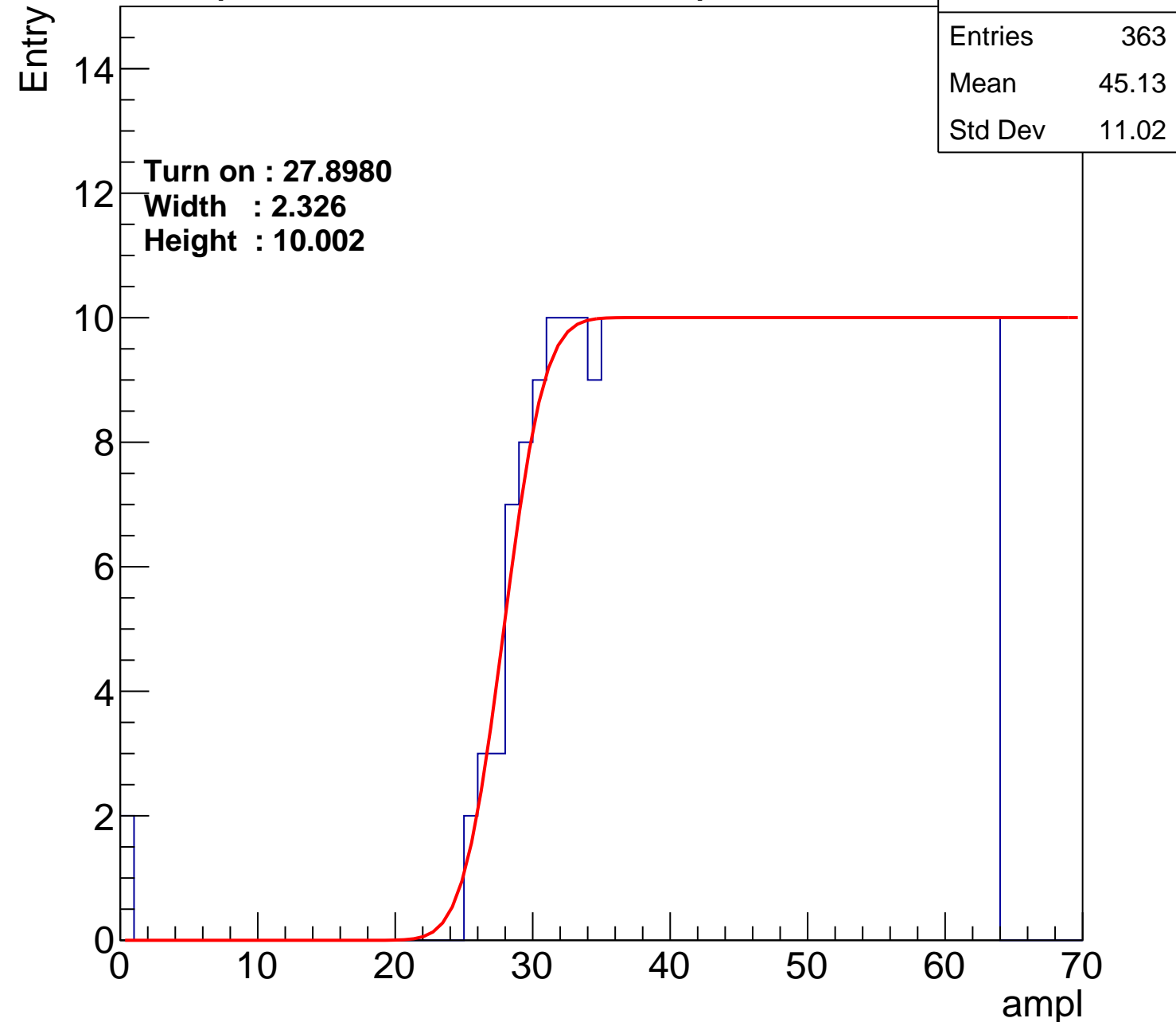
Width : 2.326

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.17
Std Dev	11.22

Turn on : 28.4282

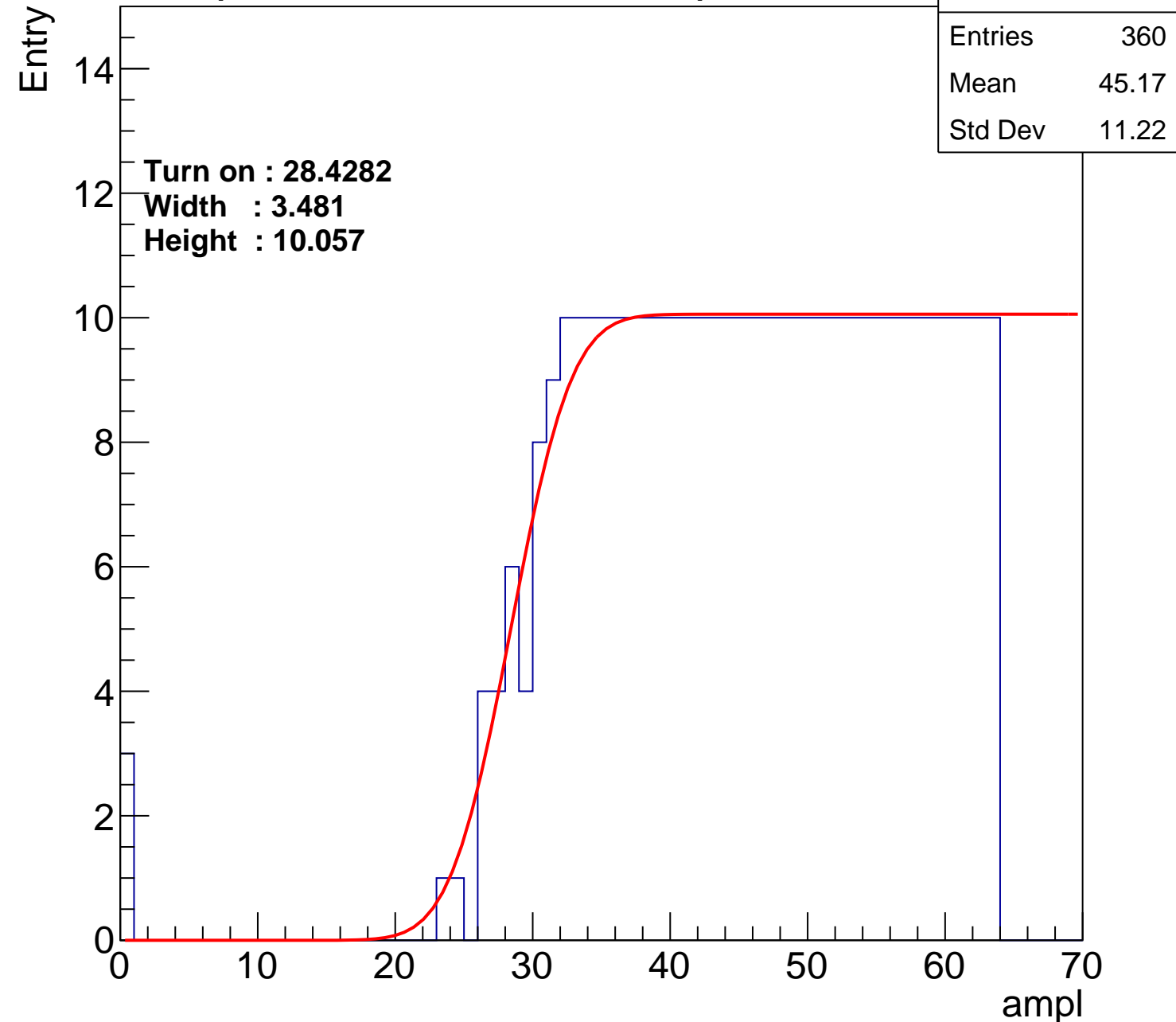
Width : 3.481

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch115

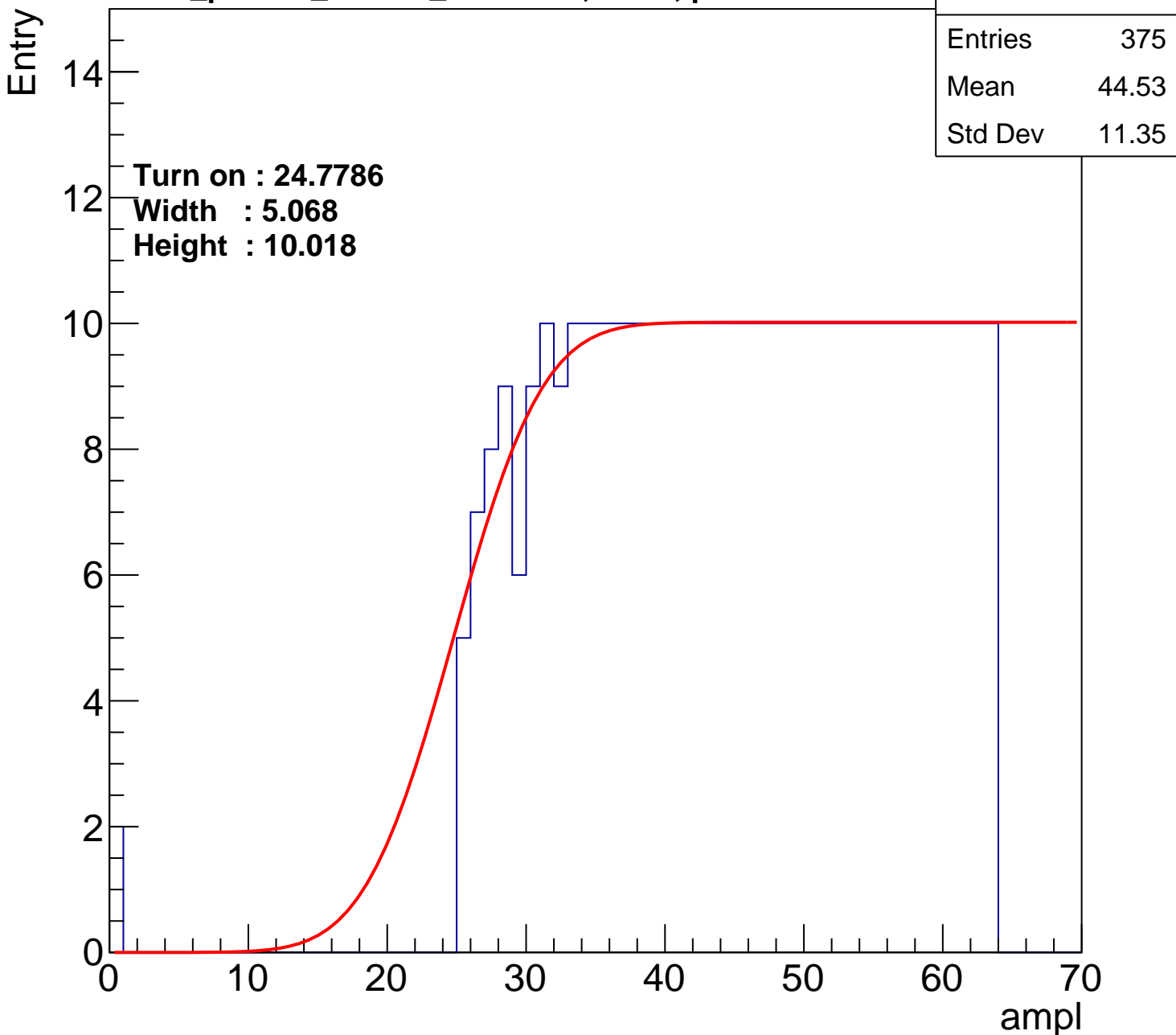
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 24.7786

Width : 5.068

Height : 10.018

Entries	375
Mean	44.53
Std Dev	11.35



B0L002S, U22-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.94
Std Dev	11.13

Turn on : 27.2153

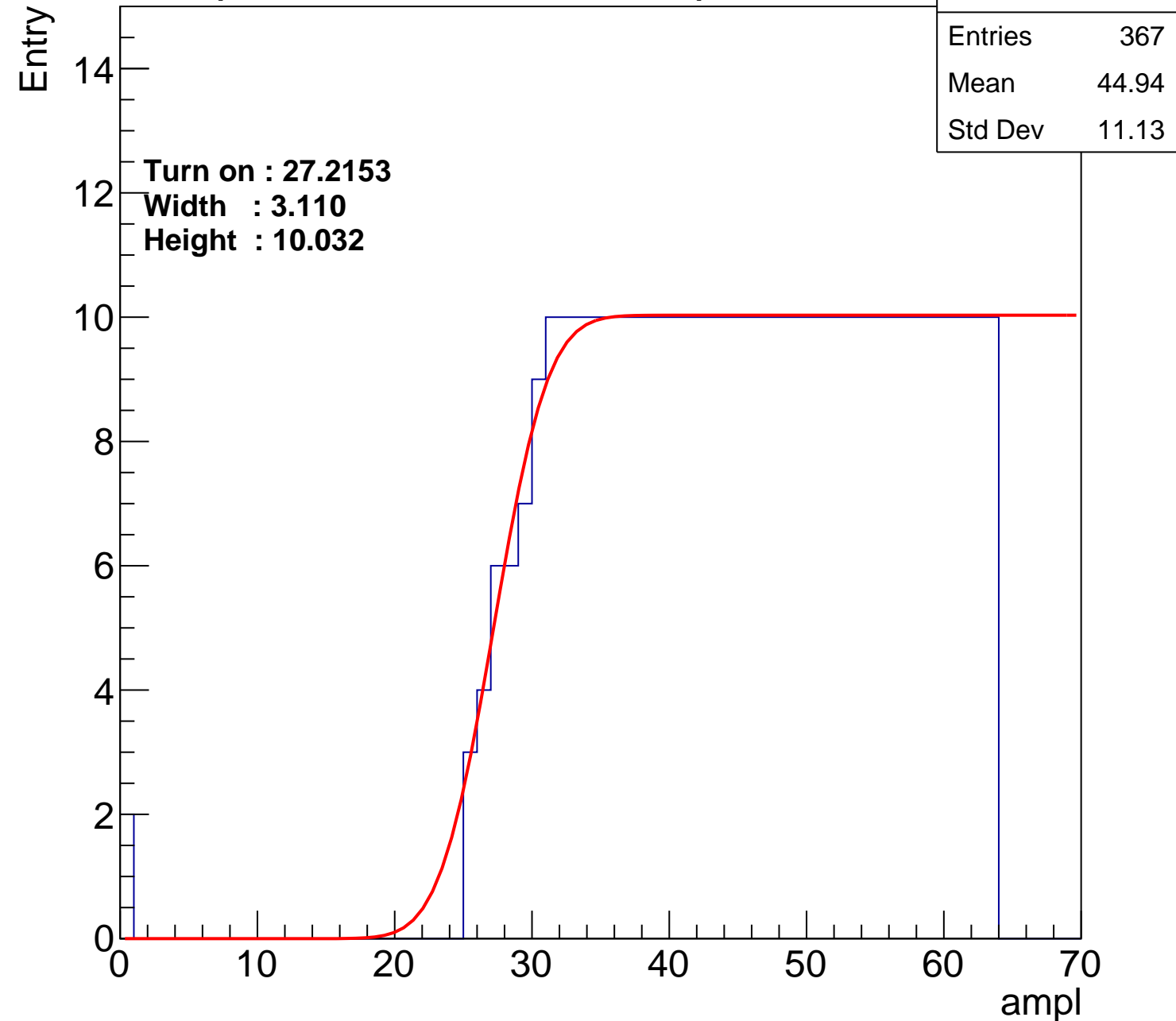
Width : 3.110

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.6
Std Dev	11.61

Turn on : 27.3344

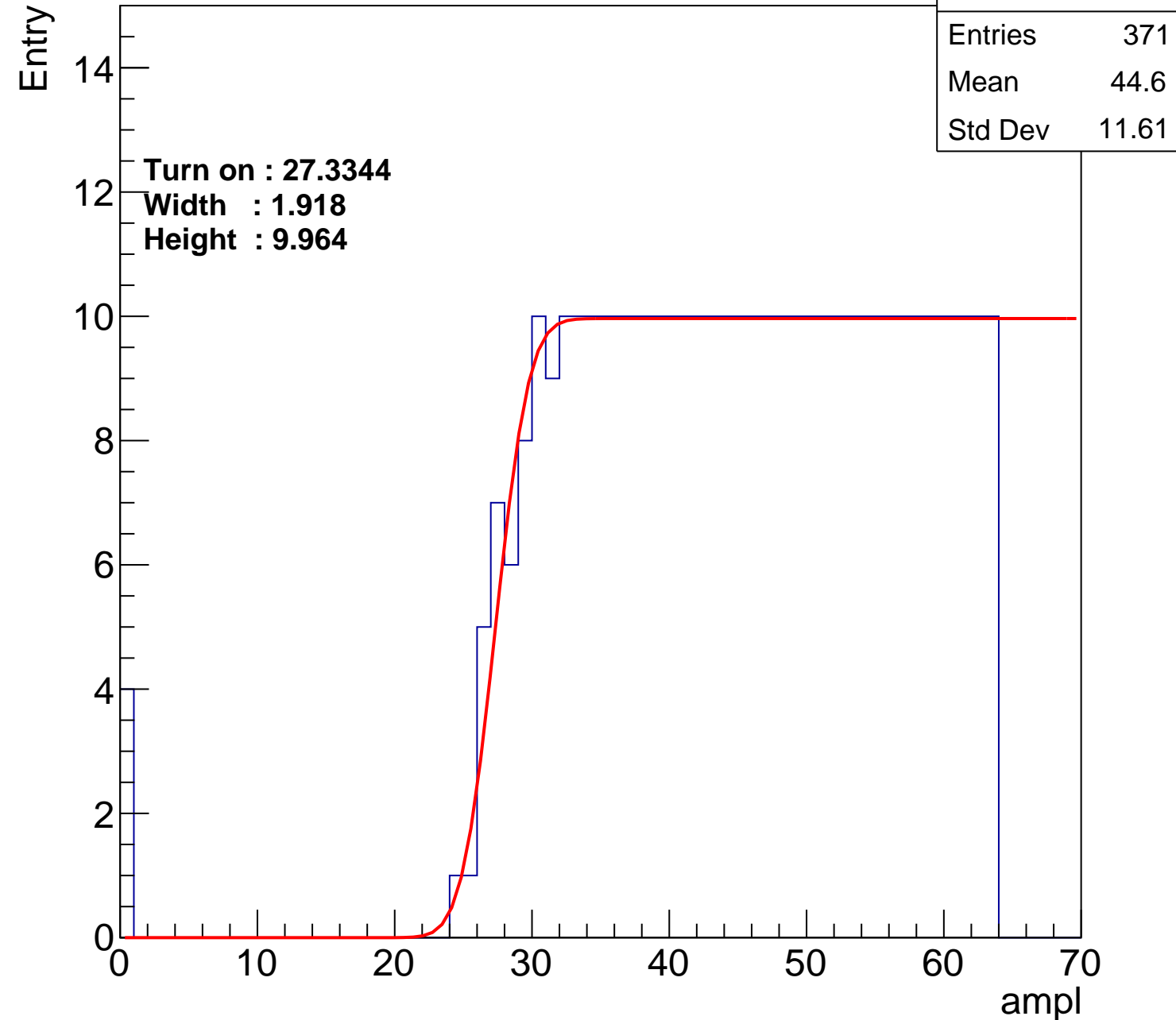
Width : 1.918

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.38
Std Dev	11.36

Turn on : 29.0666

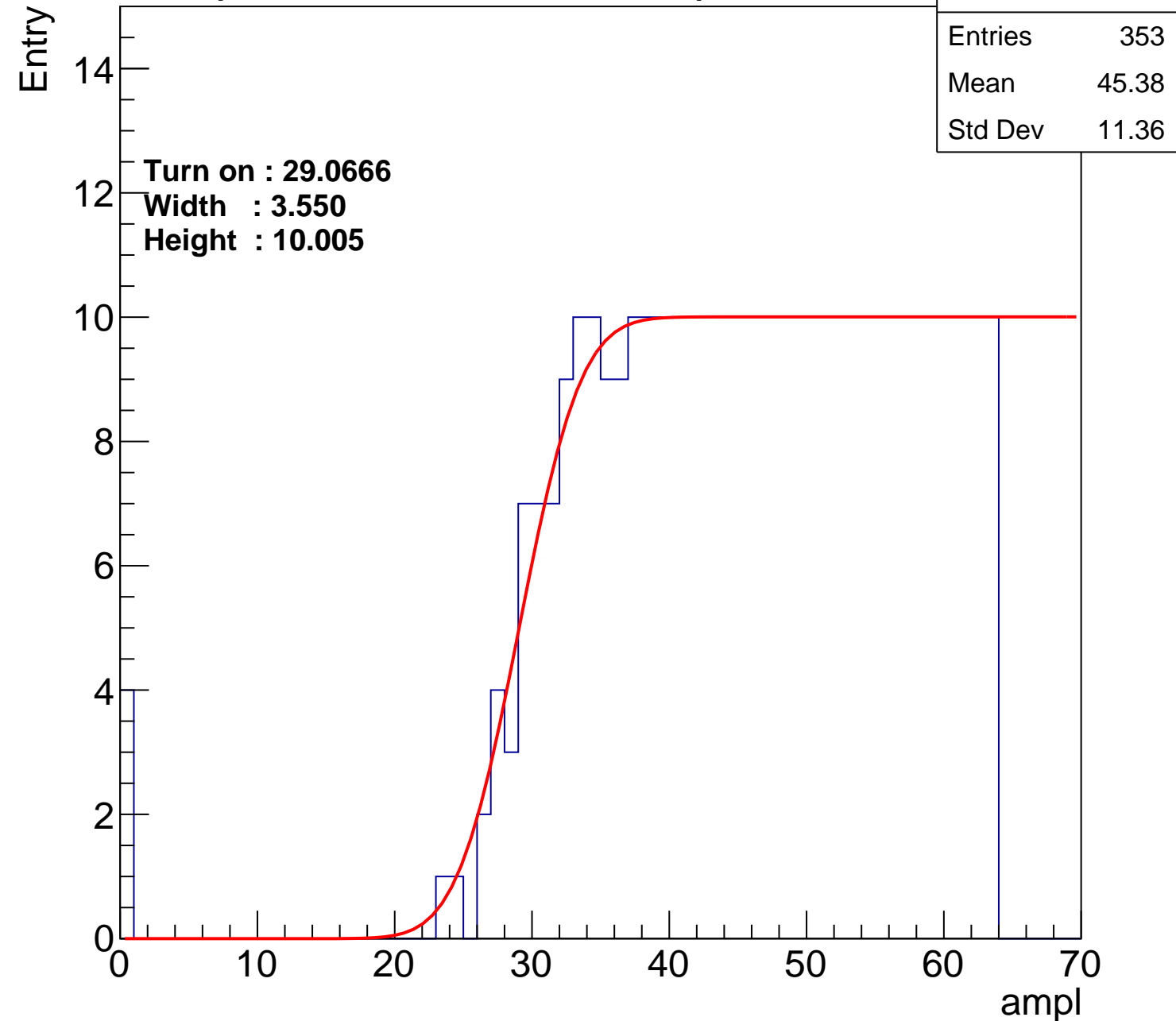
Width : 3.550

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.52
Std Dev	11.55

Turn on : 26.9350

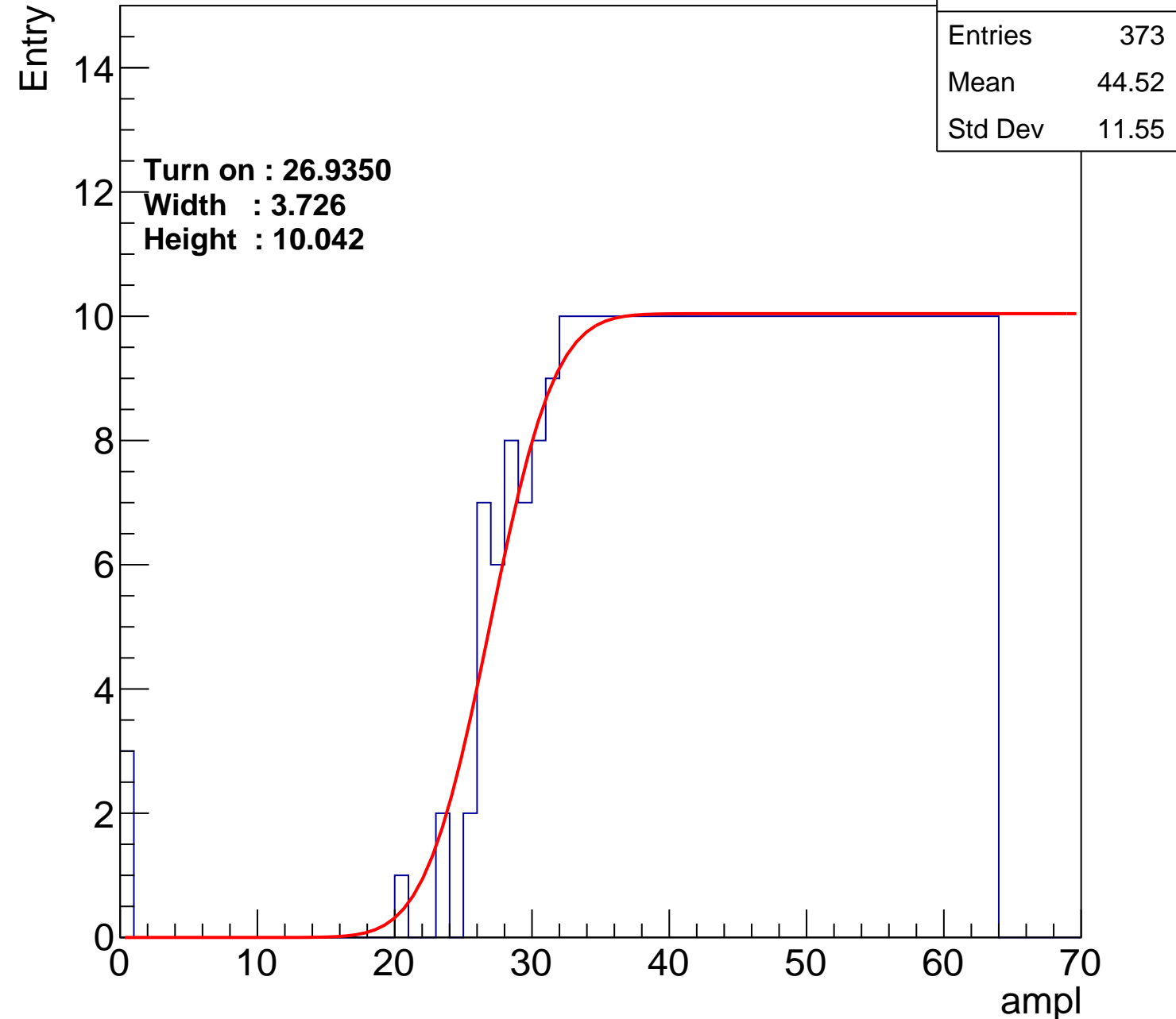
Width : 3.726

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.46
Std Dev	11.4

Turn on : 26.6055

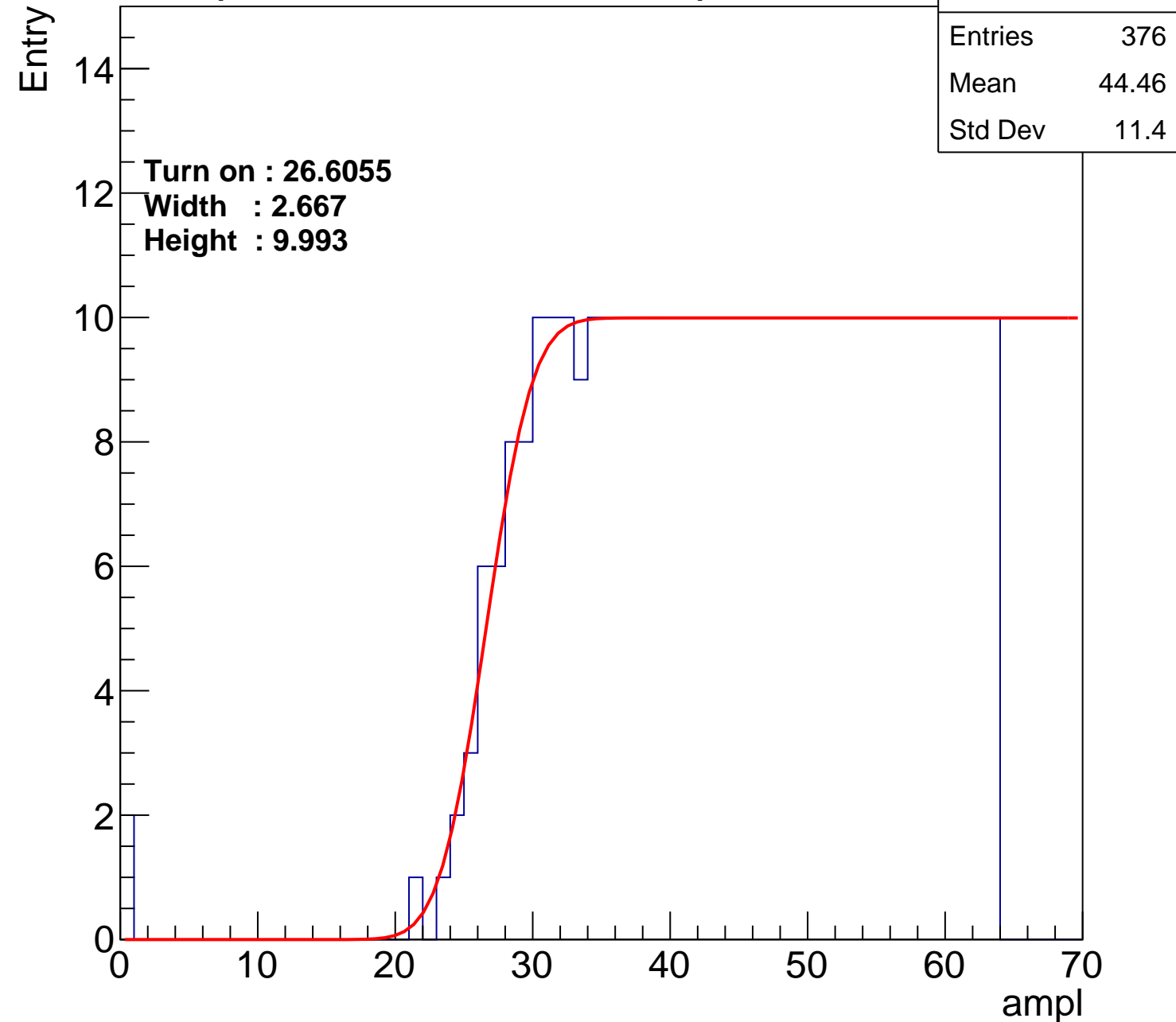
Width : 2.667

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.44
Std Dev	10.9

Turn on : 28.7160

Width : 2.691

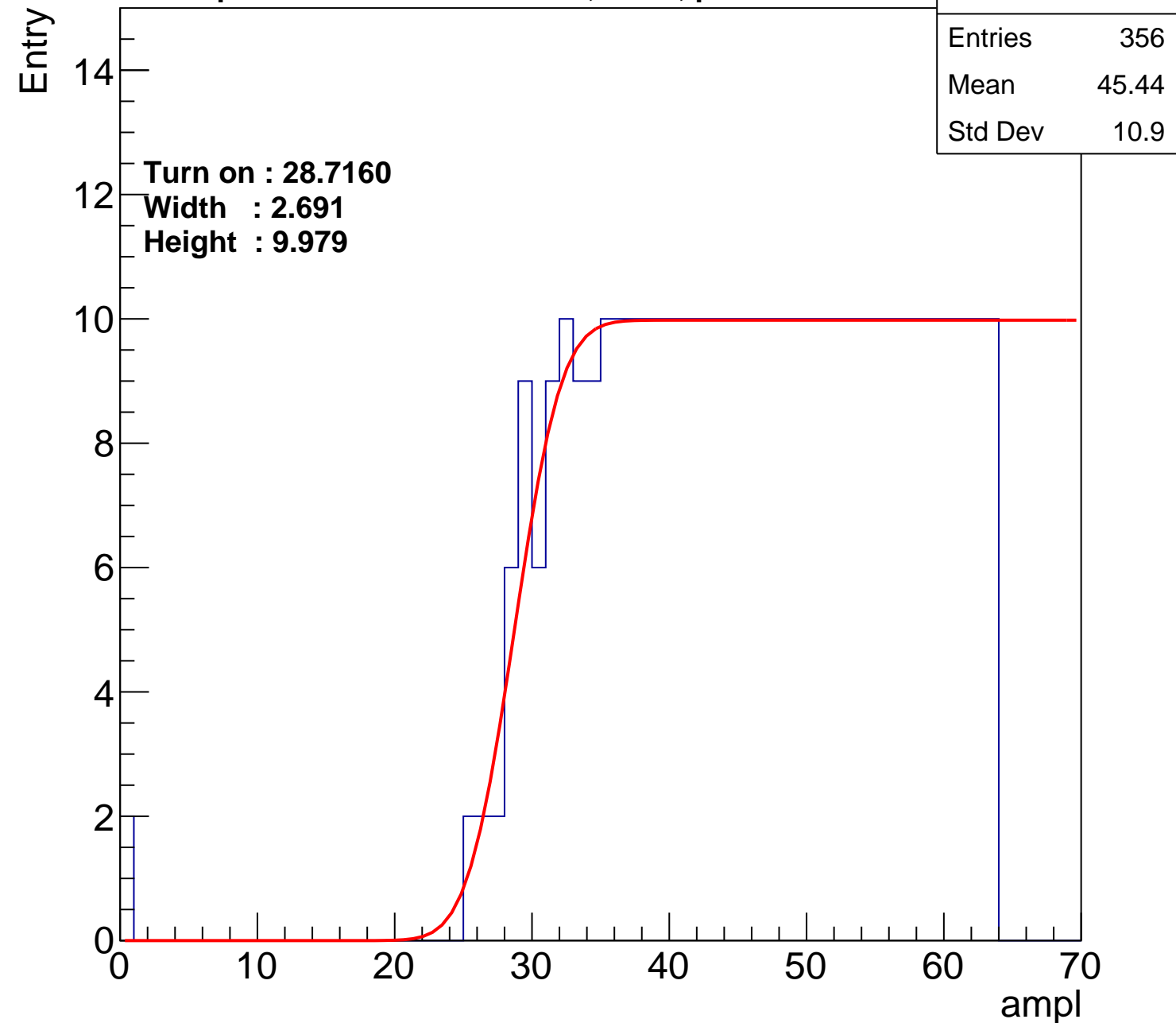
Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U22-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.34
Std Dev	10.98

Turn on : 28.9786

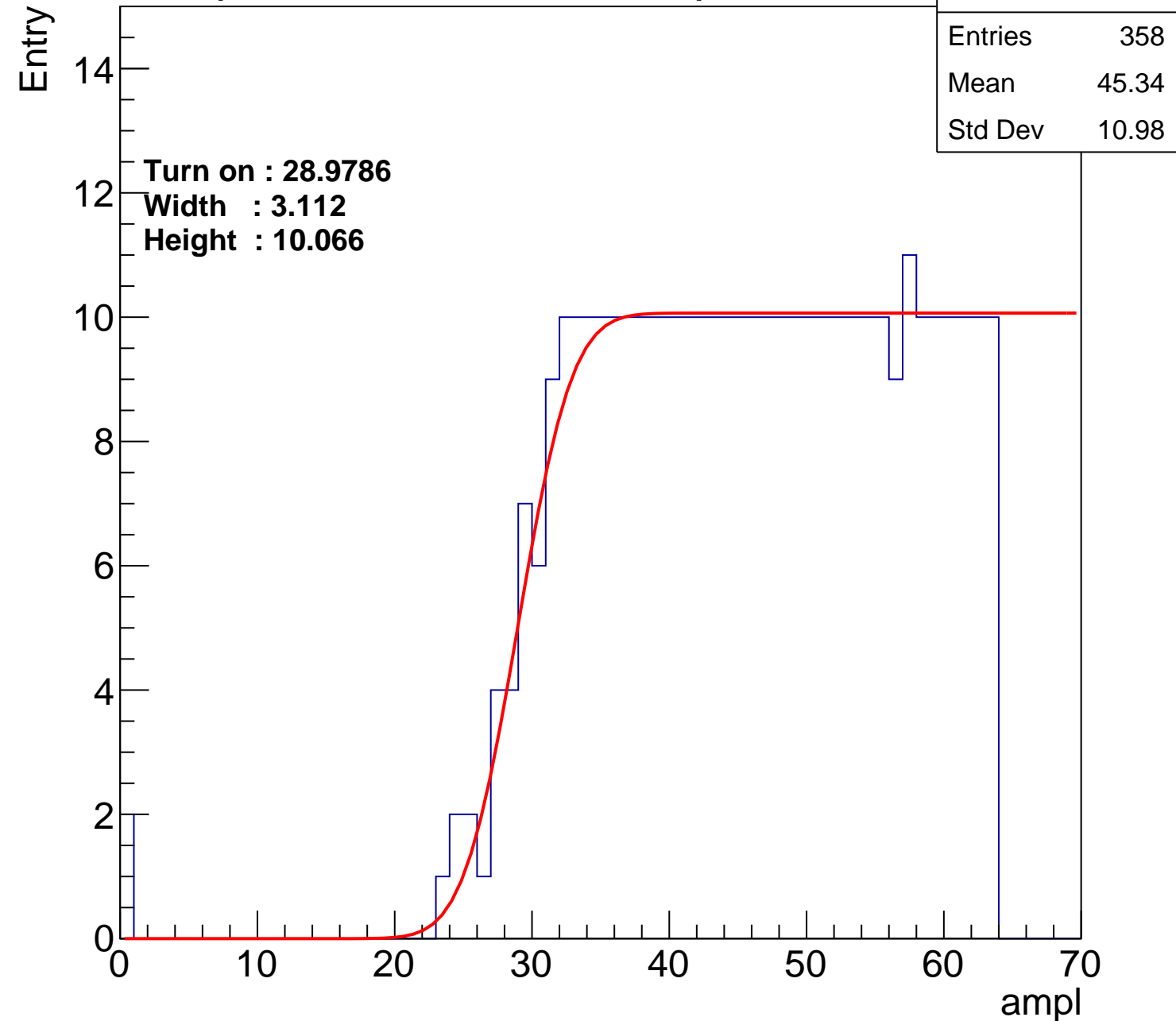
Width : 3.112

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.31
Std Dev	10.79

Turn on : 28.1058

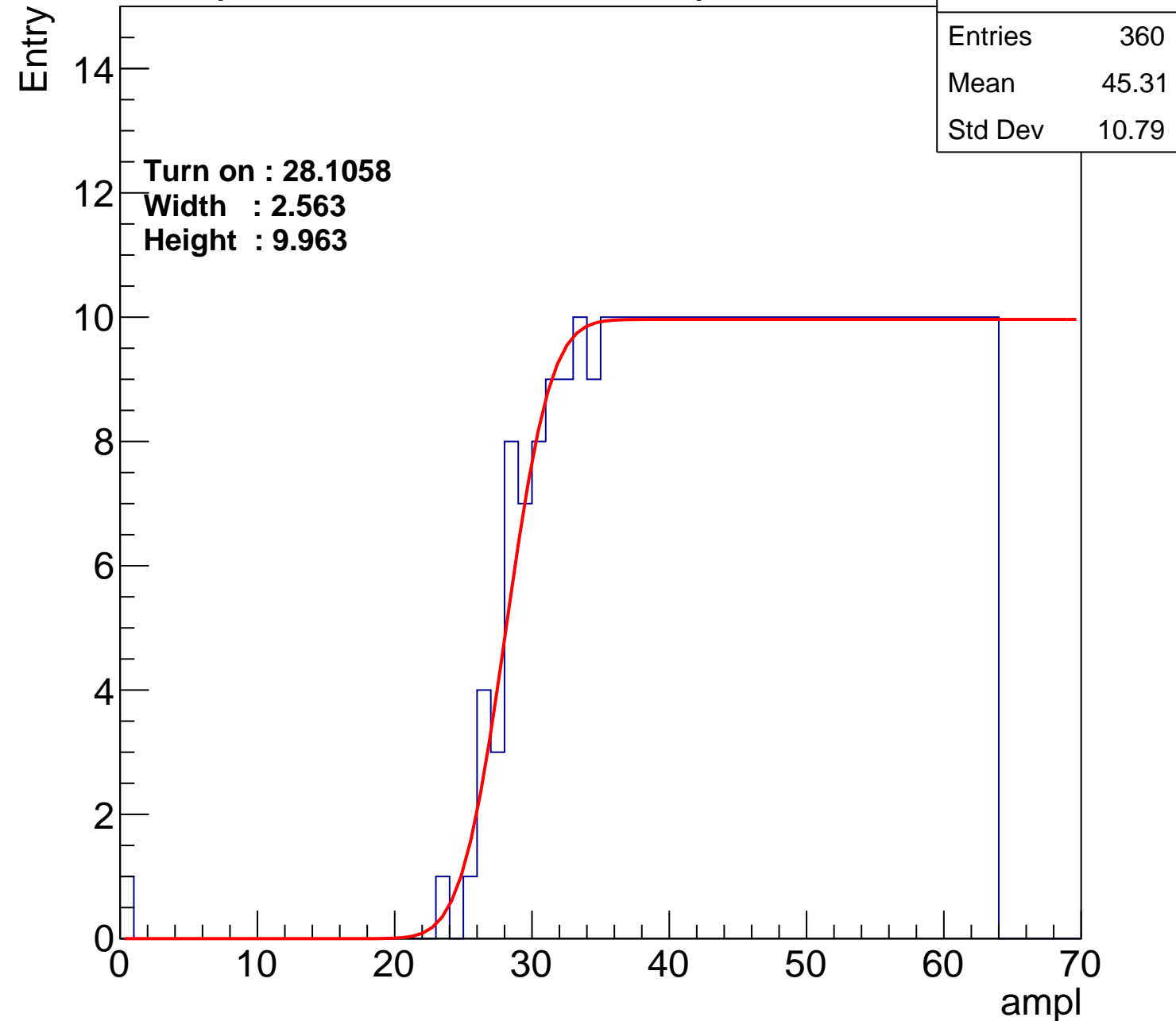
Width : 2.563

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.97
Std Dev	11.72

Turn on : 26.1672

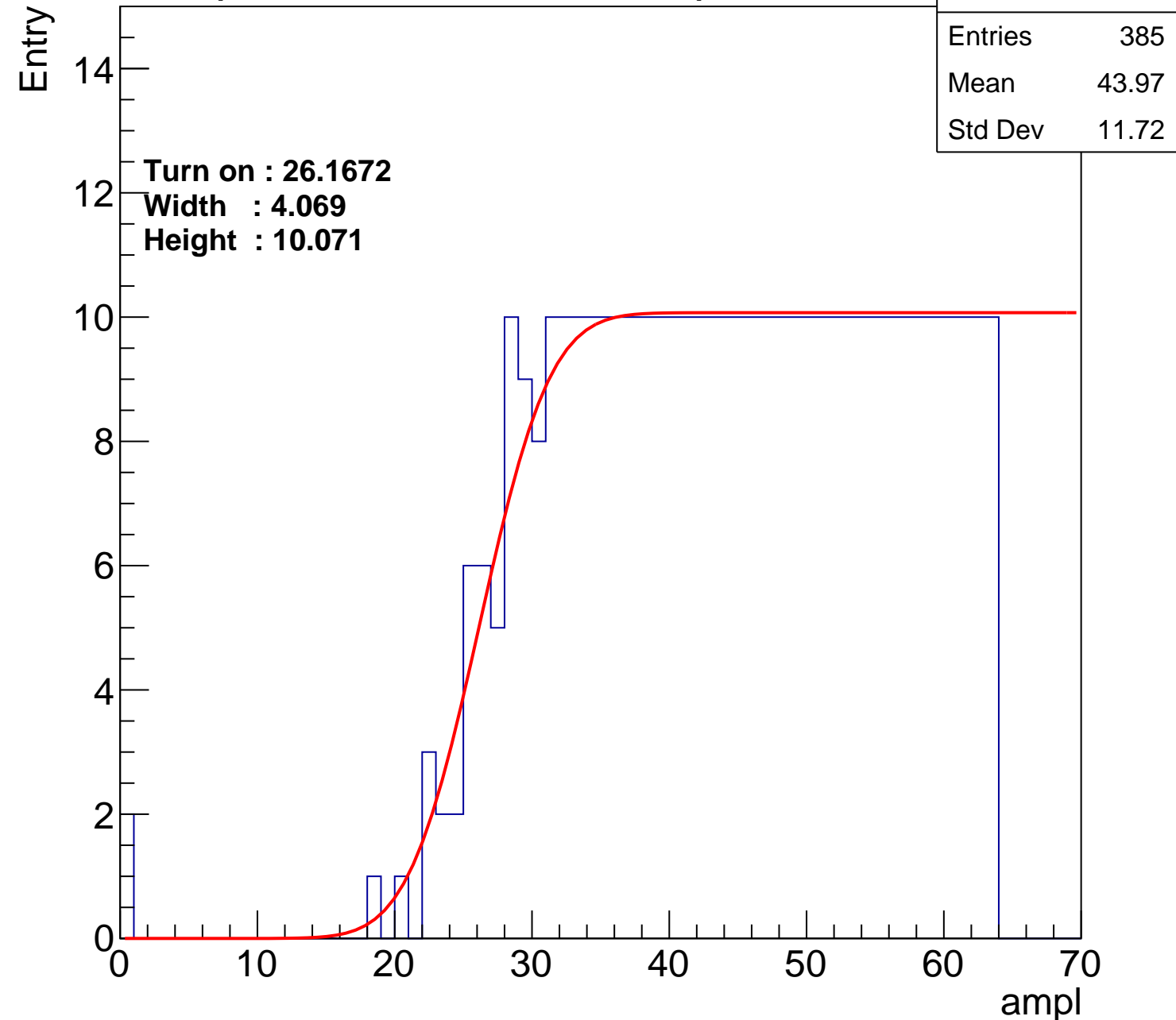
Width : 4.069

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.17
Std Dev	11.06

Turn on : 27.9299

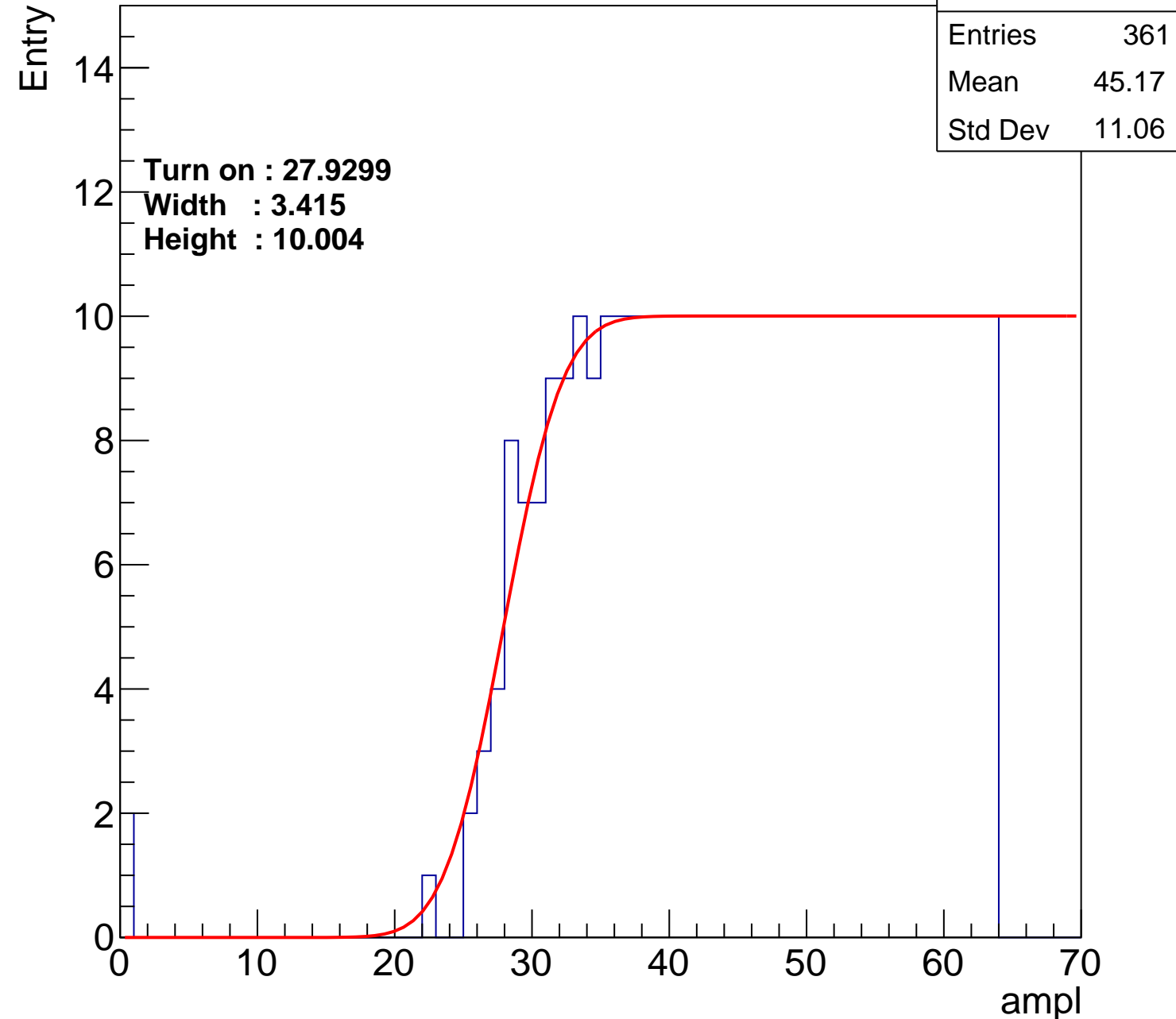
Width : 3.415

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.1783

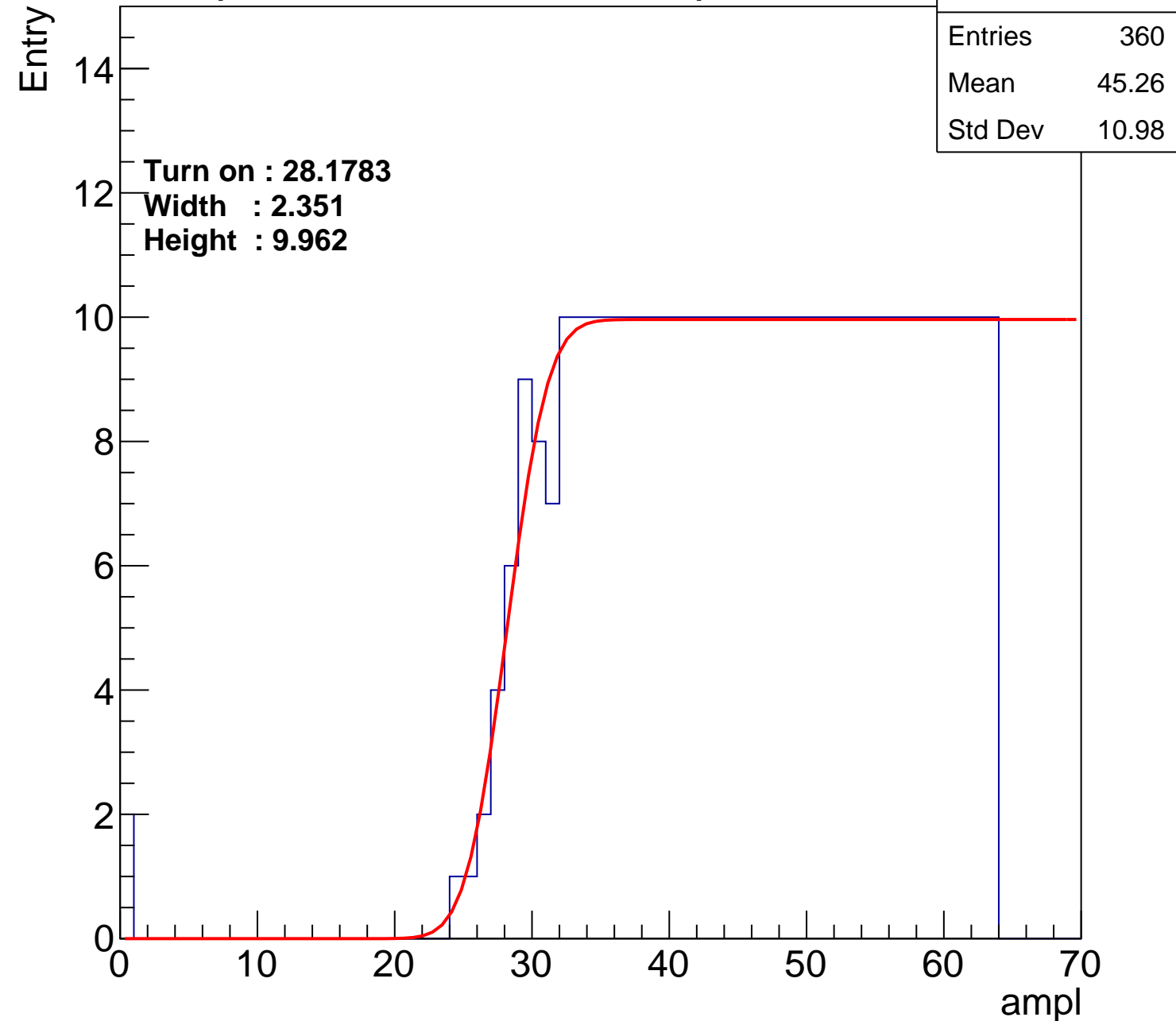
Width : 2.351

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U22-ch127

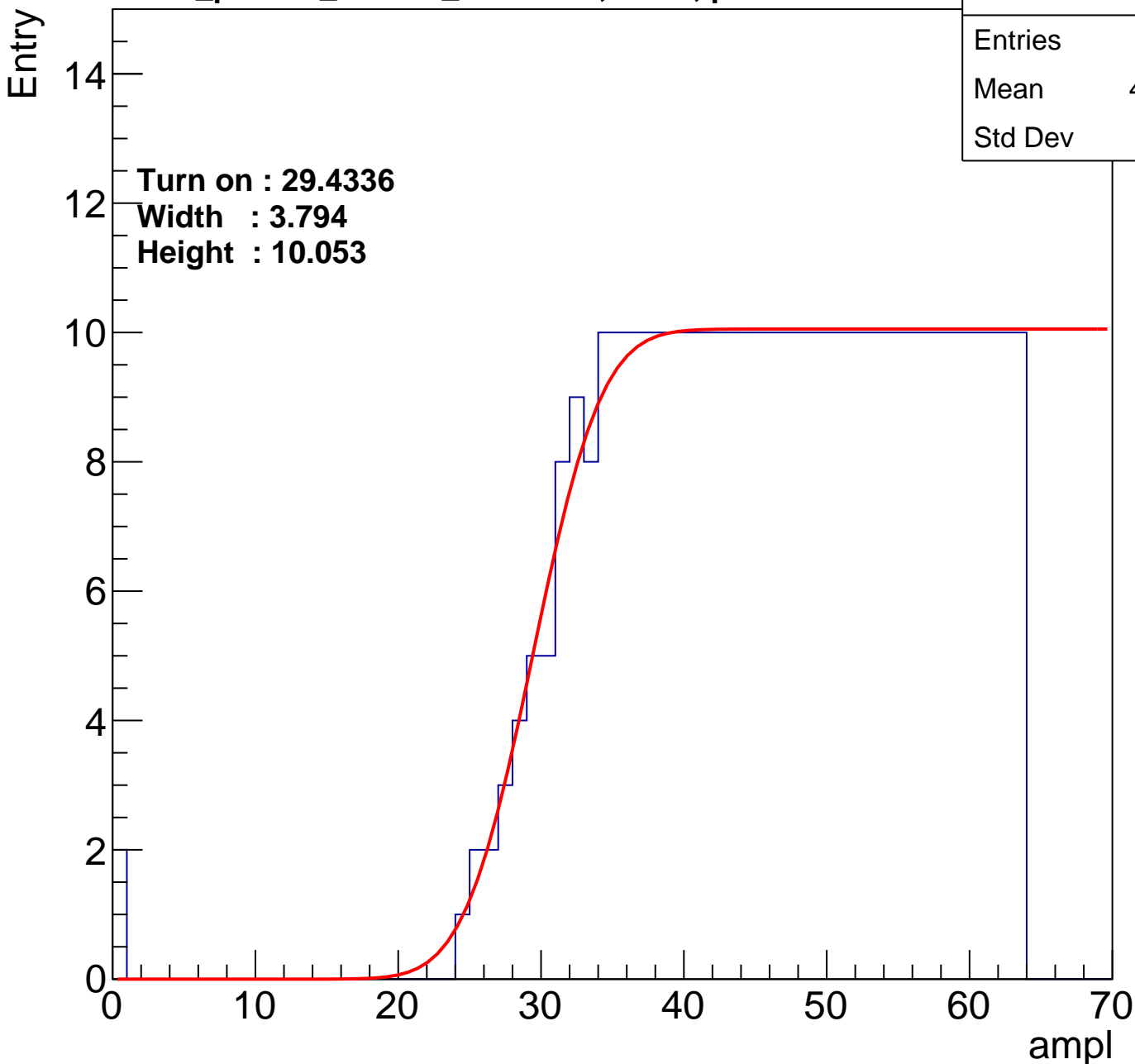
calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.74
Std Dev	10.8

Turn on : 29.4336

Width : 3.794

Height : 10.053



B0L002S, U22-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.74
Std Dev	10.8

Turn on : 29.4336

Width : 3.794

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

