

B1L102S, U2-ch0, adc0

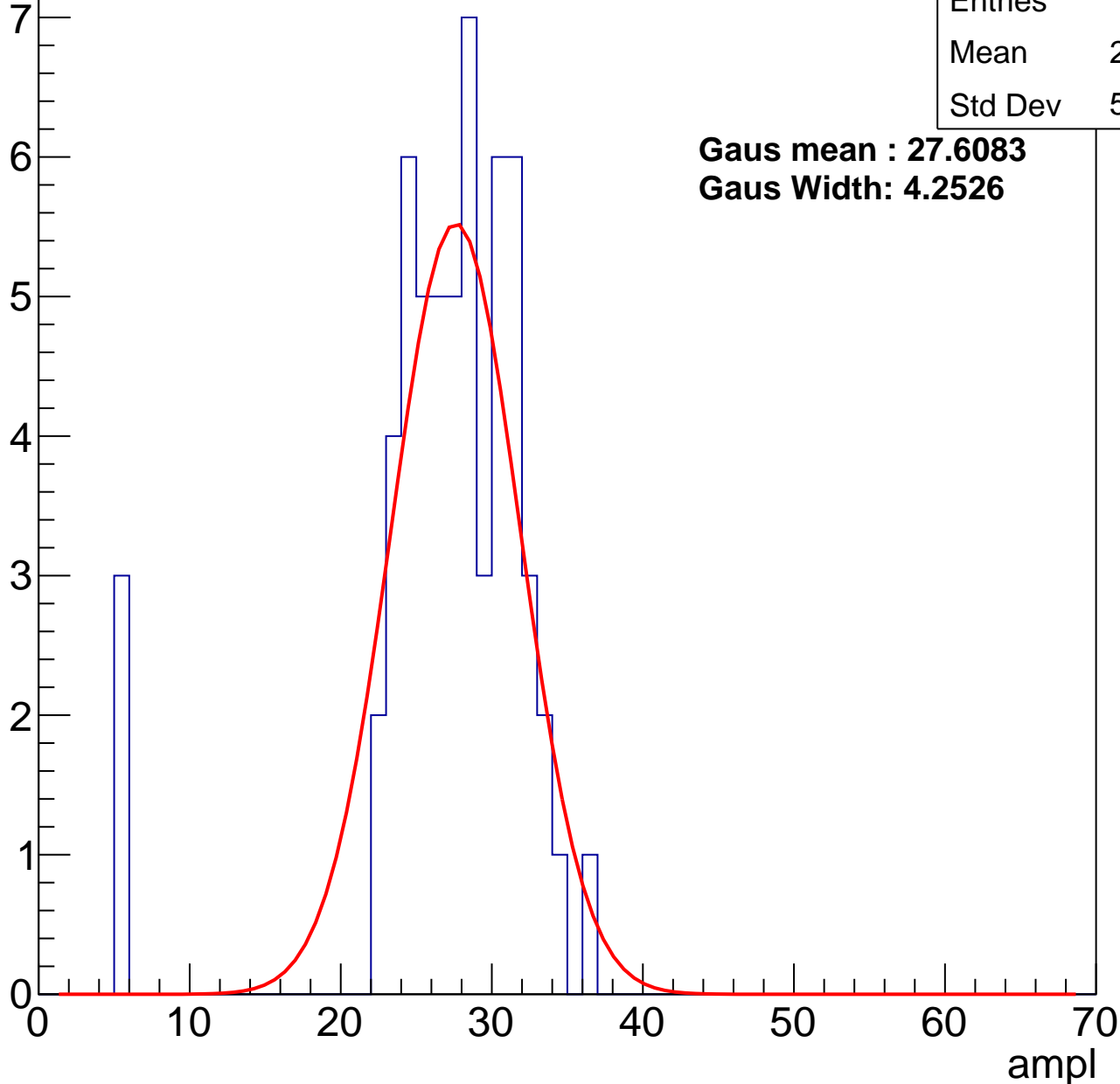
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	26.54
Std Dev	5.936

Gaus mean : 27.6083

Gaus Width: 4.2526



B1L102S, U2-ch0, adc1

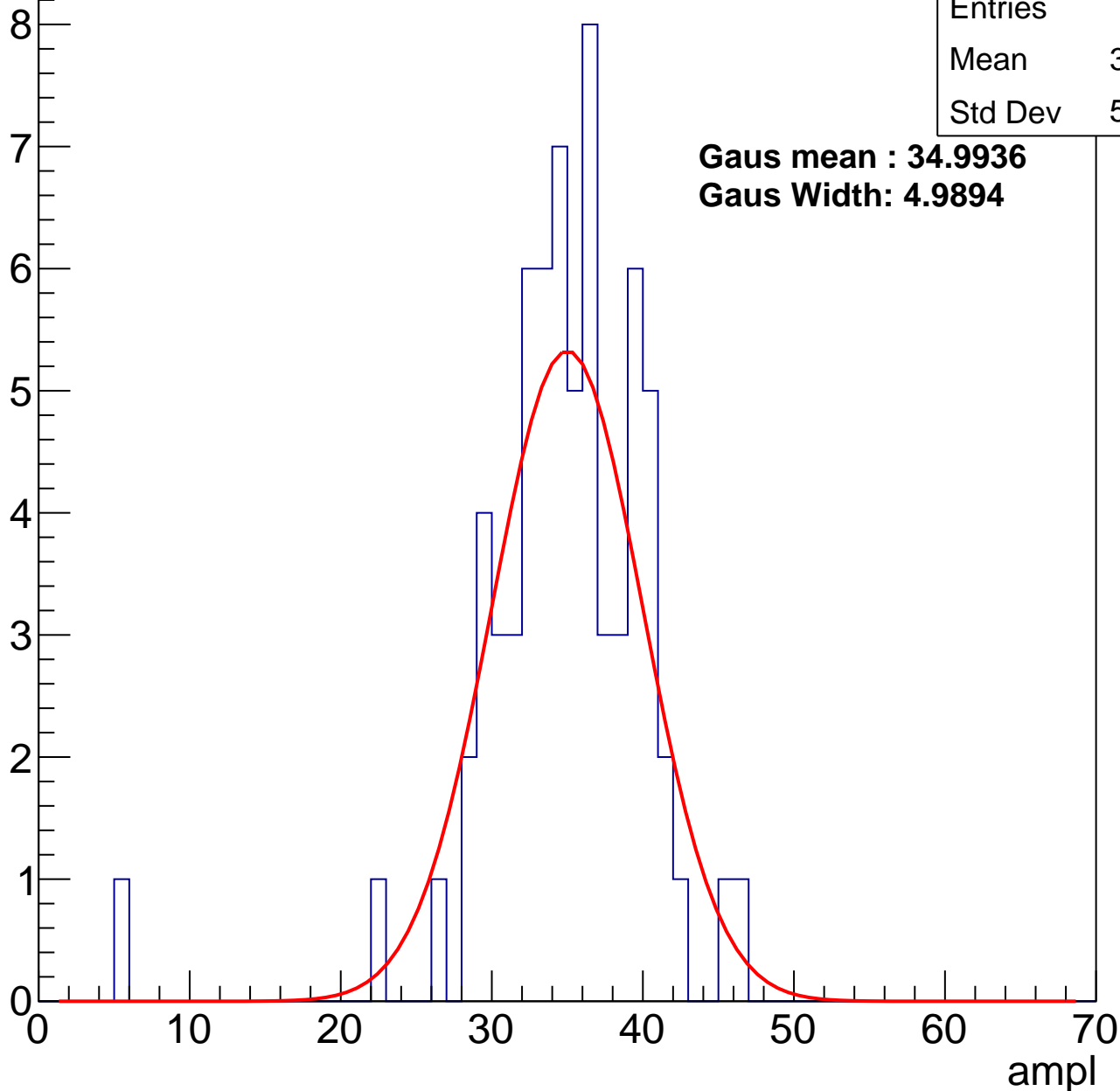
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.39
Std Dev	5.627

Gaus mean : 34.9936

Gaus Width: 4.9894



B1L102S, U2-ch0, adc2

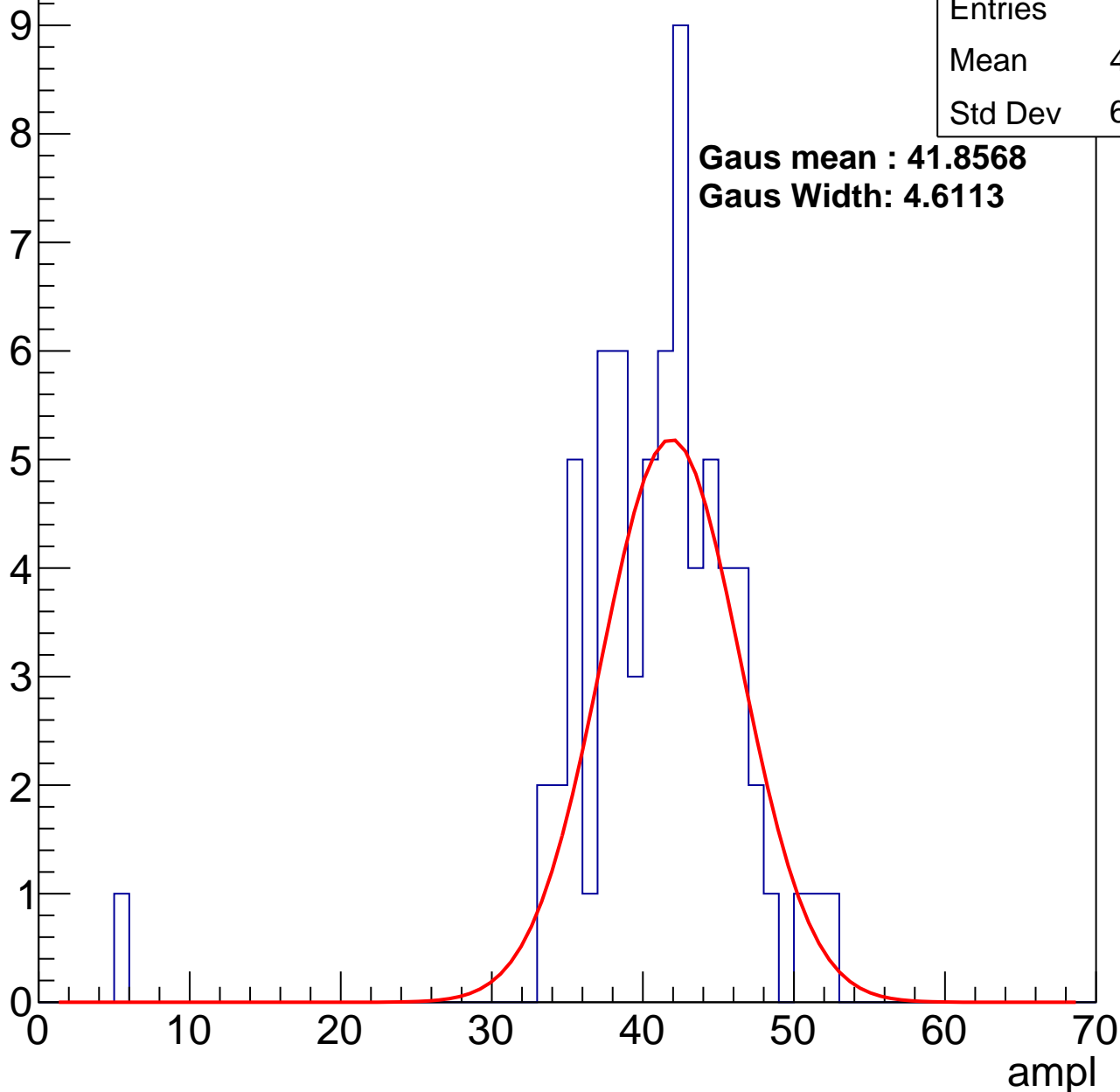
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.46
Std Dev	6.057

Gaus mean : 41.8568

Gaus Width: 4.6113

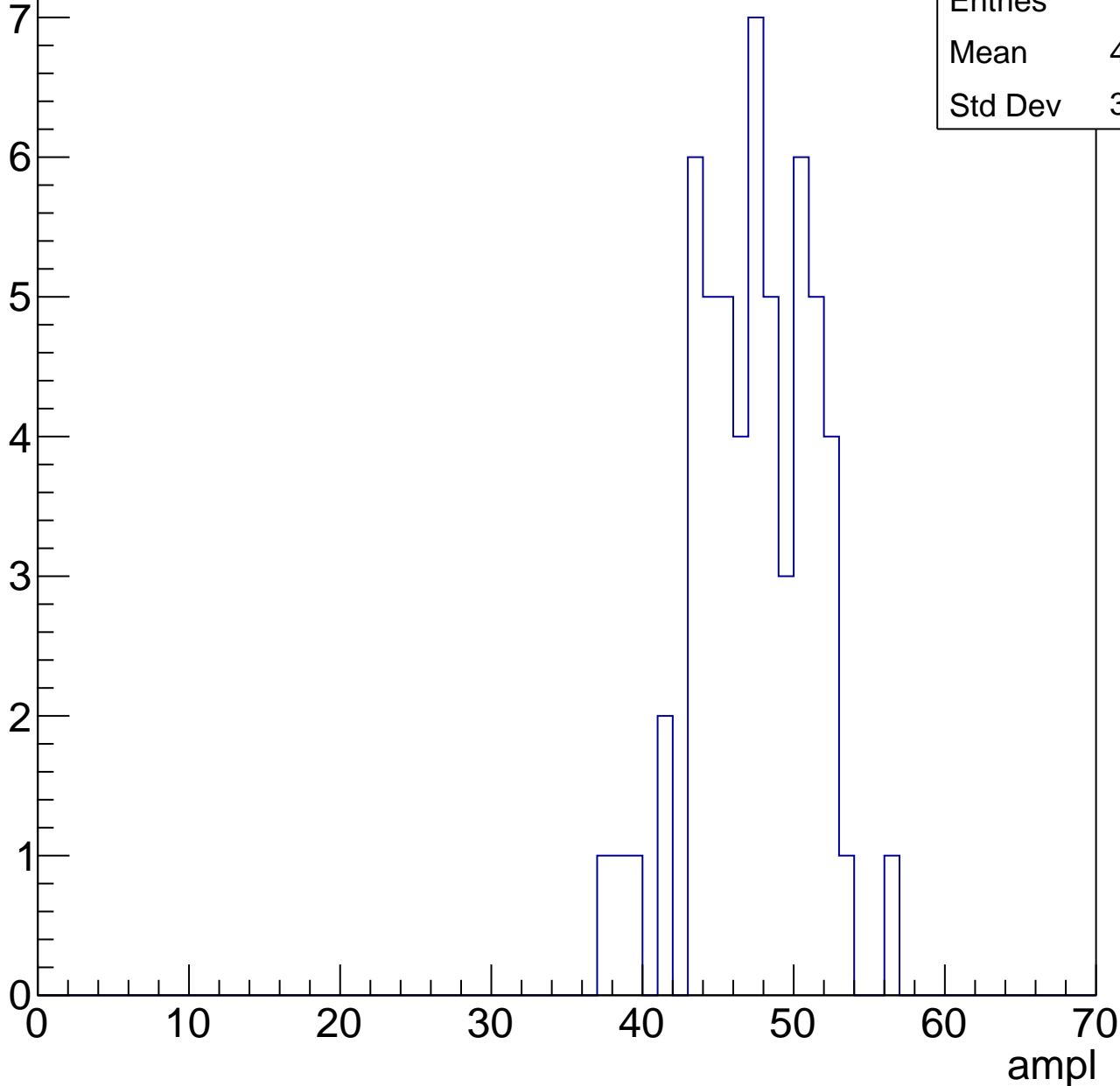


B1L102S, U2-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

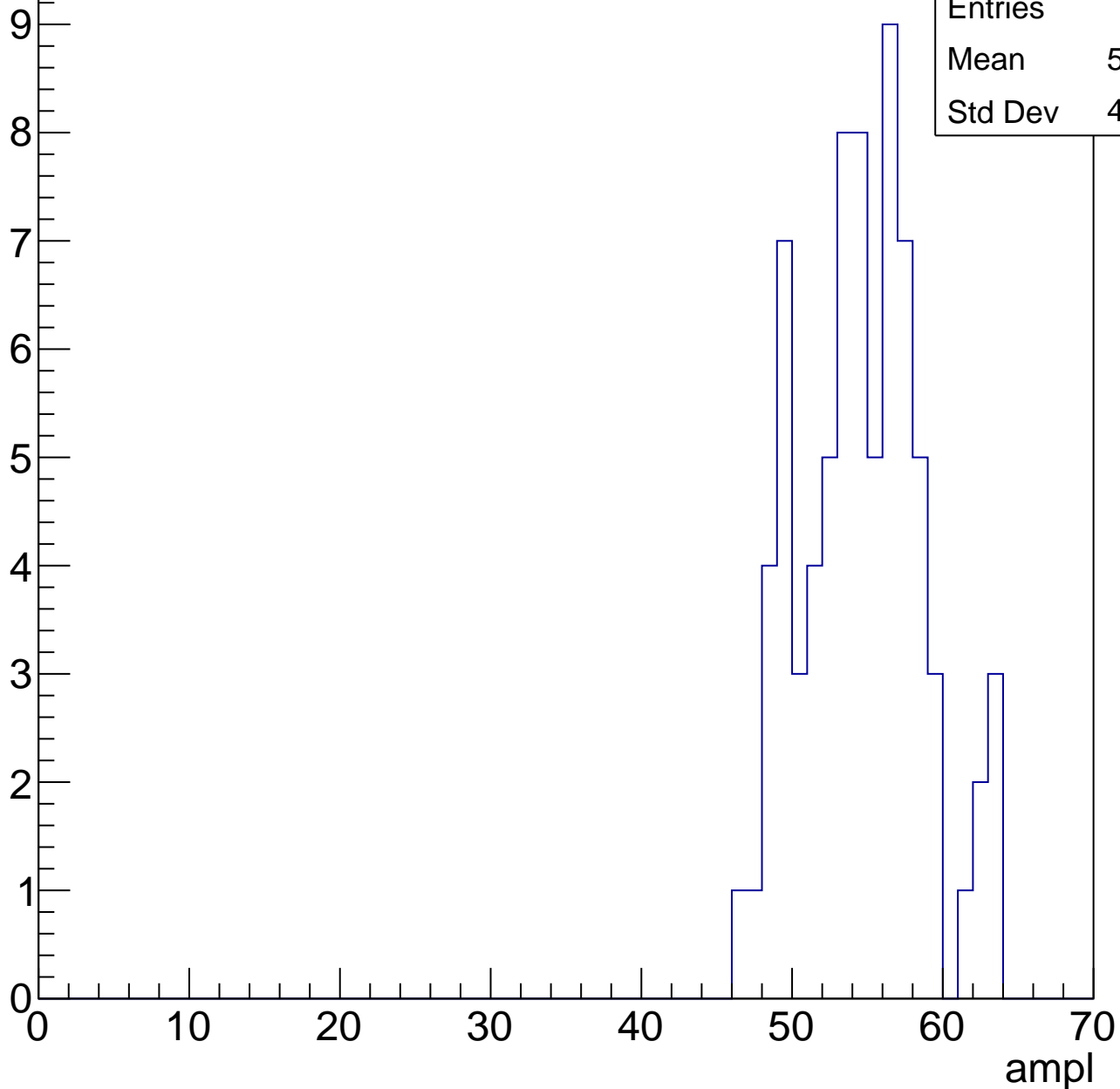
Entries	57
Mean	46.86
Std Dev	3.859



B1L102S, U2-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch0, adc5

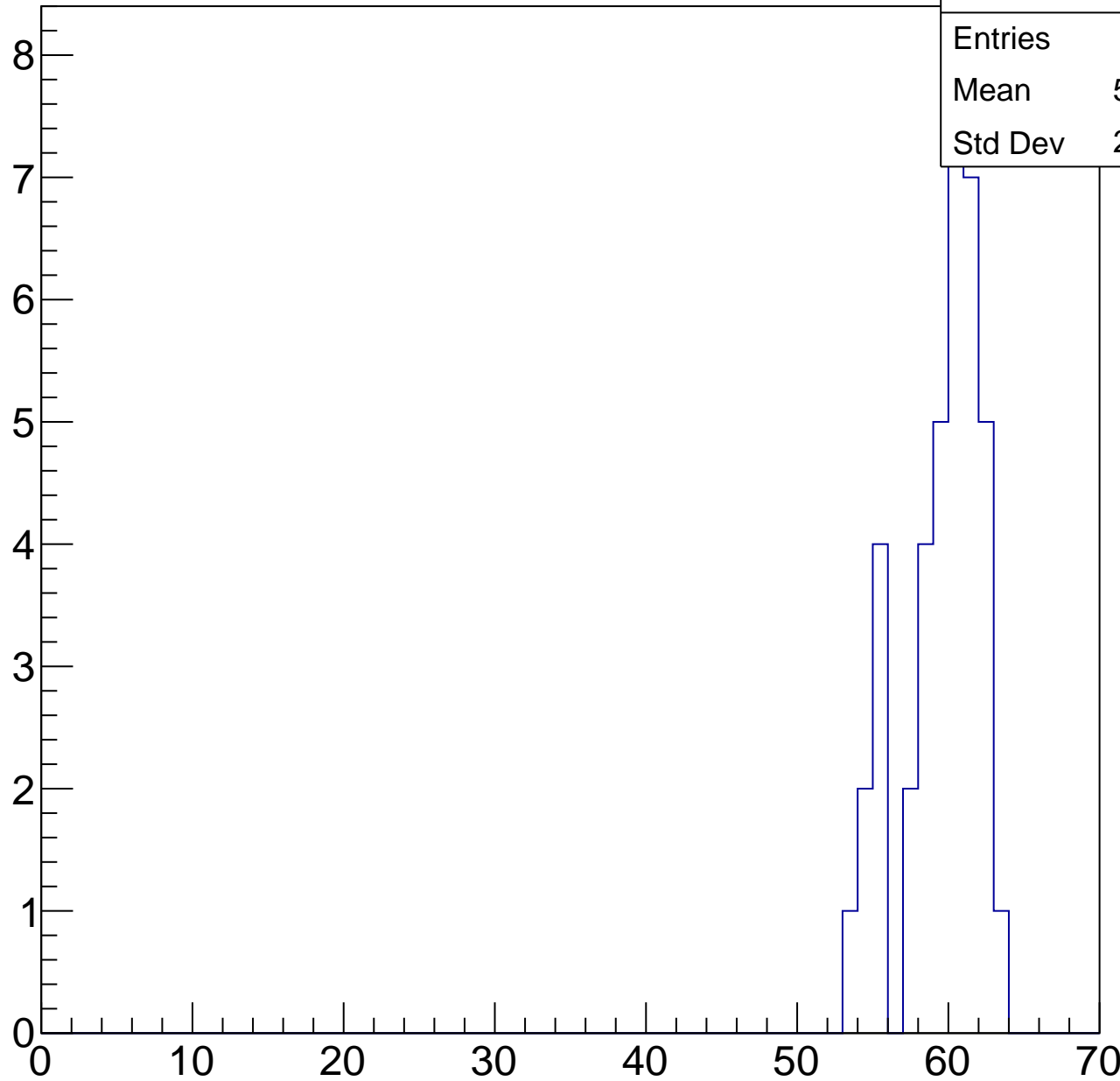
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.03
Std Dev	2.577

ampl

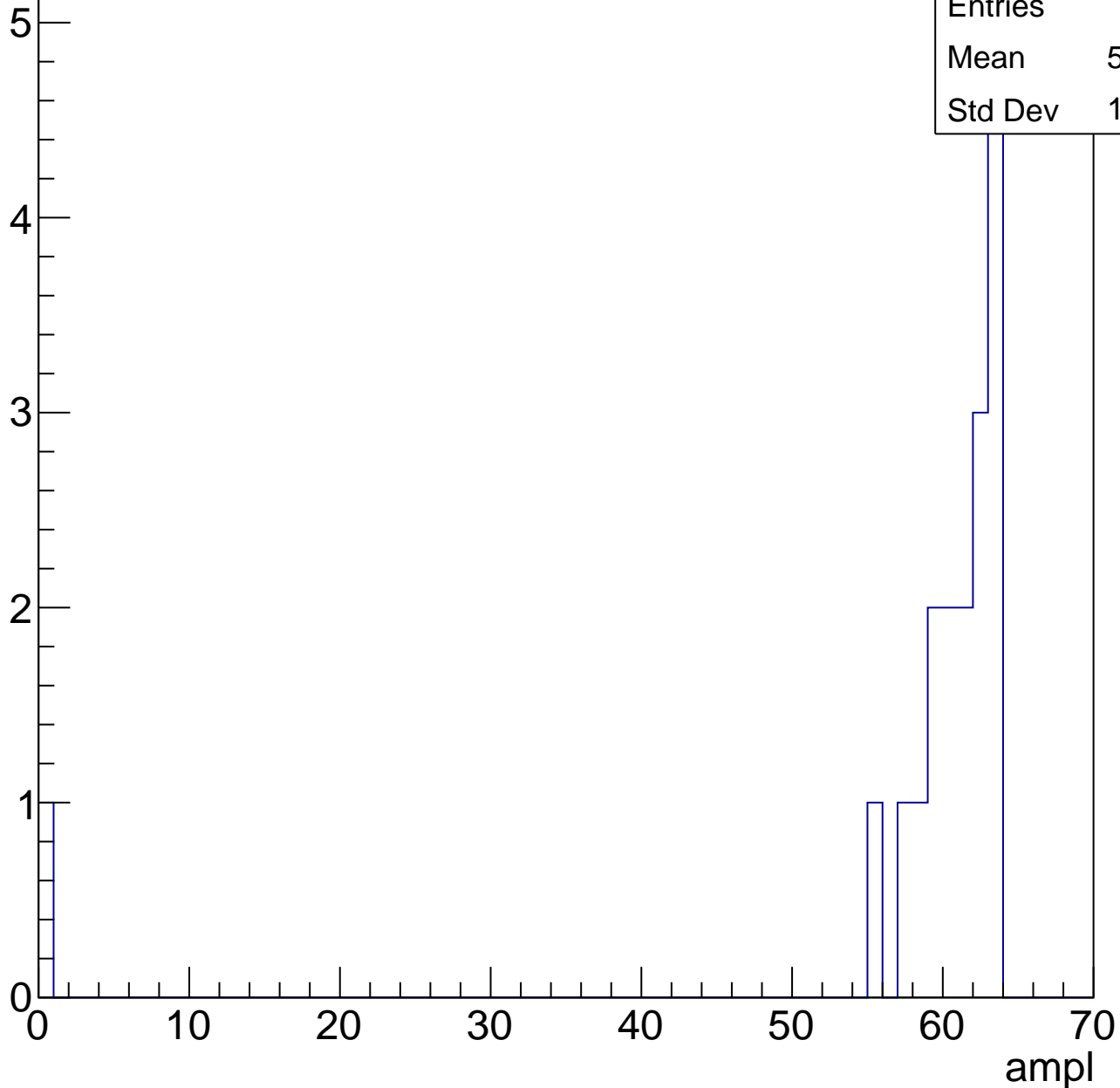


B1L102S, U2-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

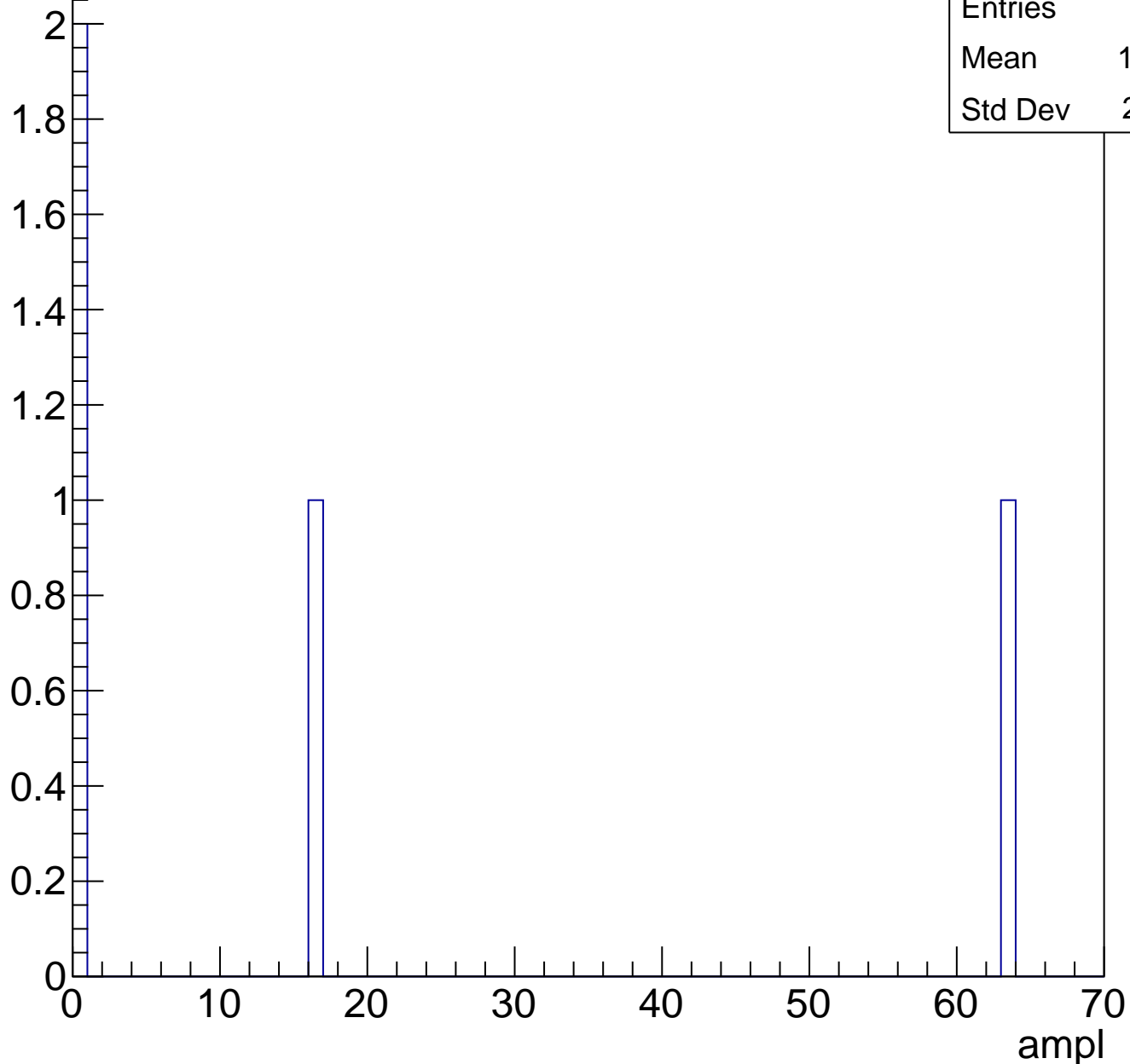
Entries	18
Mean	57.28
Std Dev	14.07



B1L102S, U2-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	19.75
Std Dev	25.81

B1L102S, U2-ch1, adc0

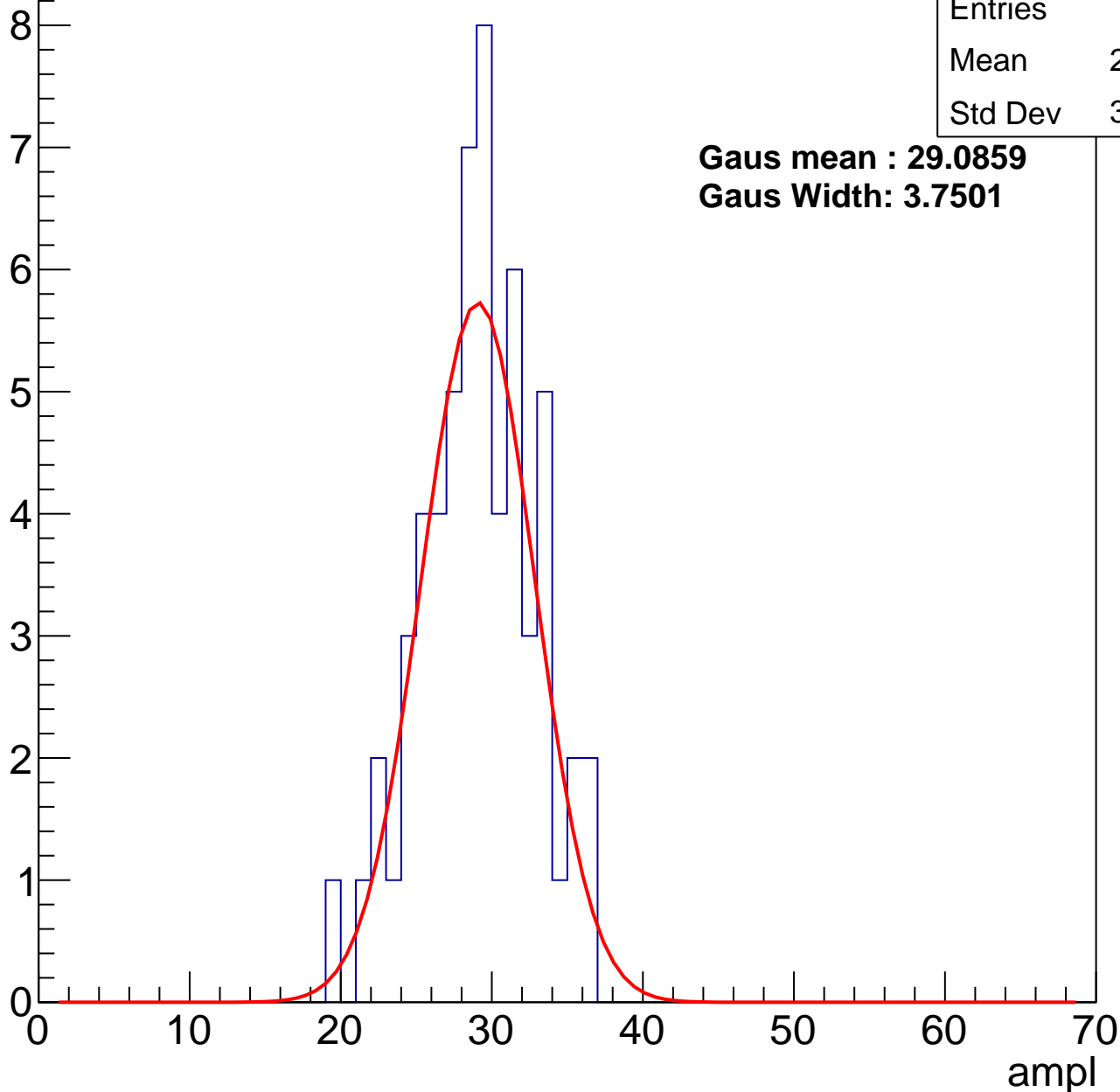
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.63
Std Dev	3.732

Gaus mean : 29.0859

Gaus Width: 3.7501



B1L102S, U2-ch1, adc1

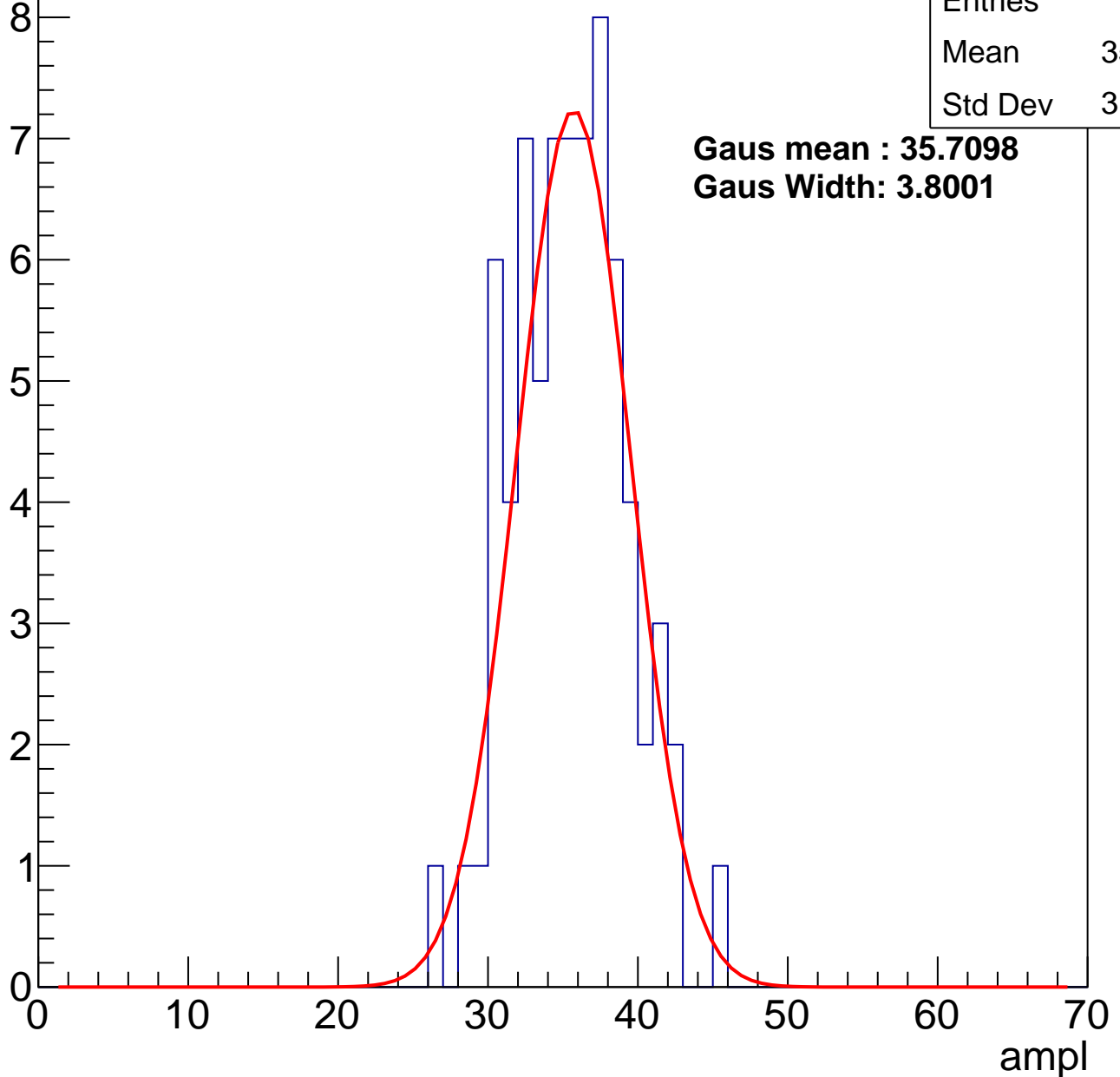
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.04
Std Dev	3.695

Gaus mean : 35.7098

Gaus Width: 3.8001



B1L102S, U2-ch1, adc2

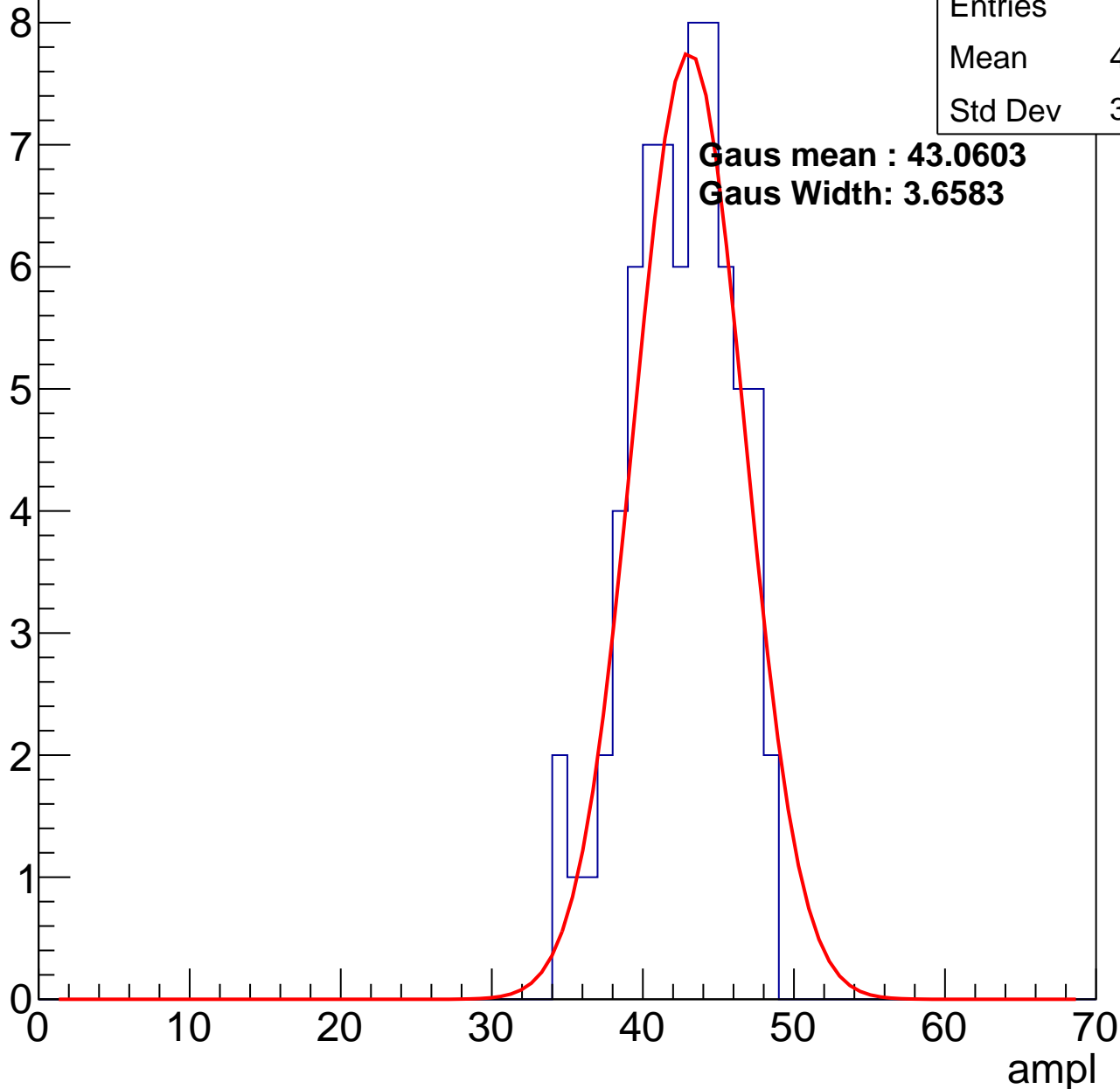
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.07
Std Dev	3.352

Gaus mean : 43.0603

Gaus Width: 3.6583



B1L102S, U2-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	48.93
Std Dev	3.356

Entry

10

8

6

4

2

0

0

10

20

30

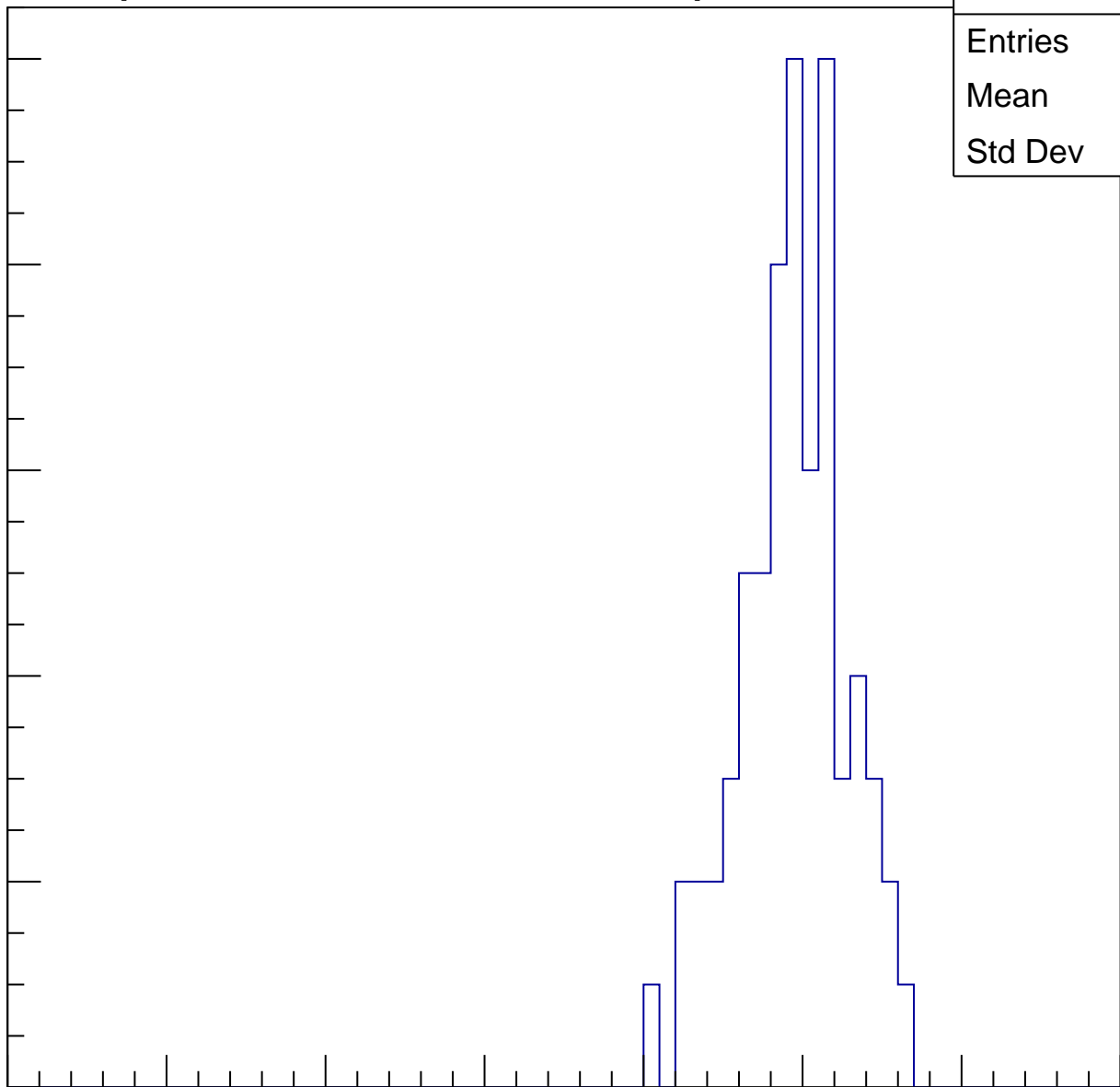
40

50

60

70

ampl

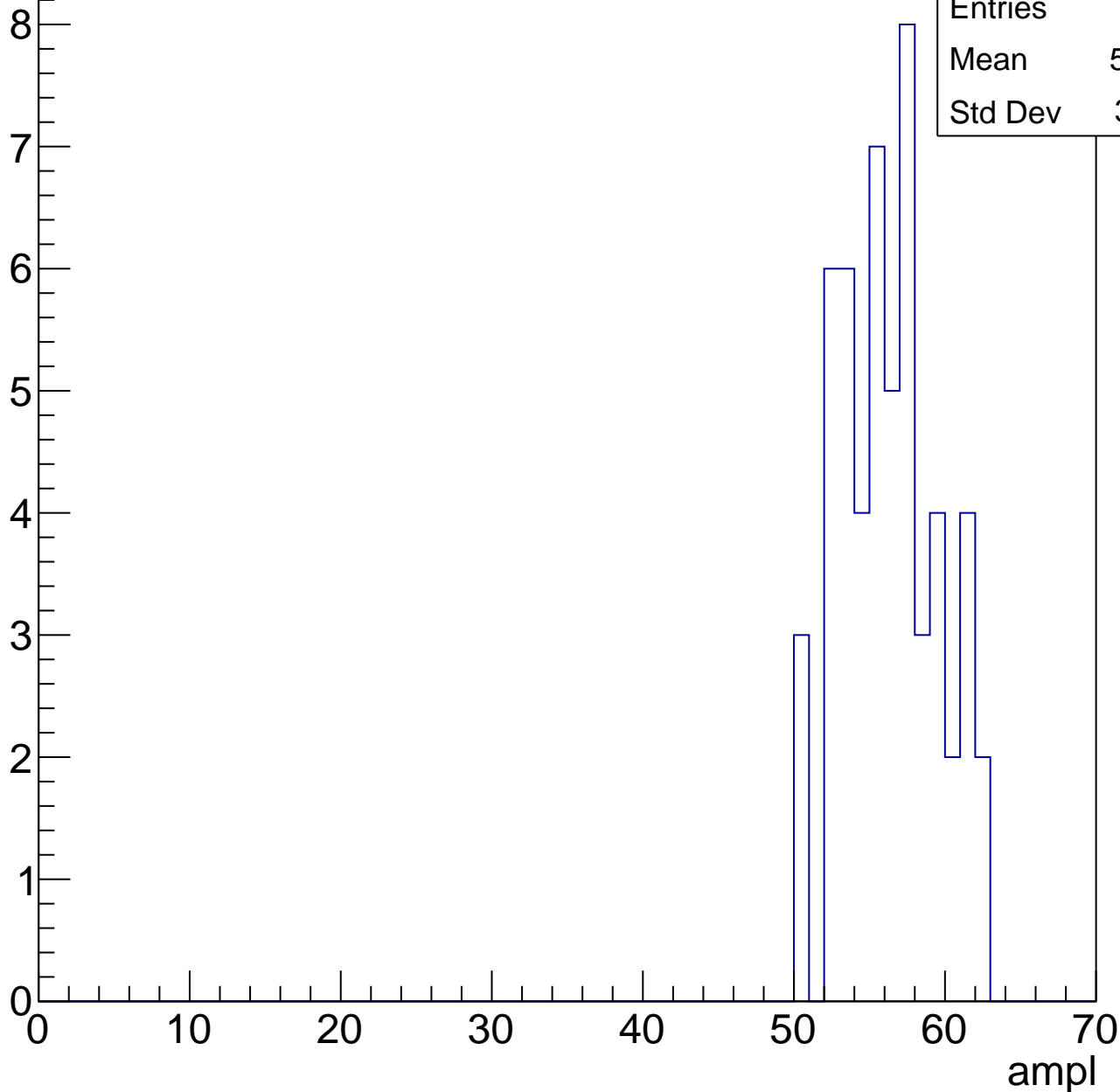


B1L102S, U2-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.83
Std Dev	3.161



B1L102S, U2-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 47

Mean 58.96

Std Dev 3.066

ampl

0

10

20

30

40

50

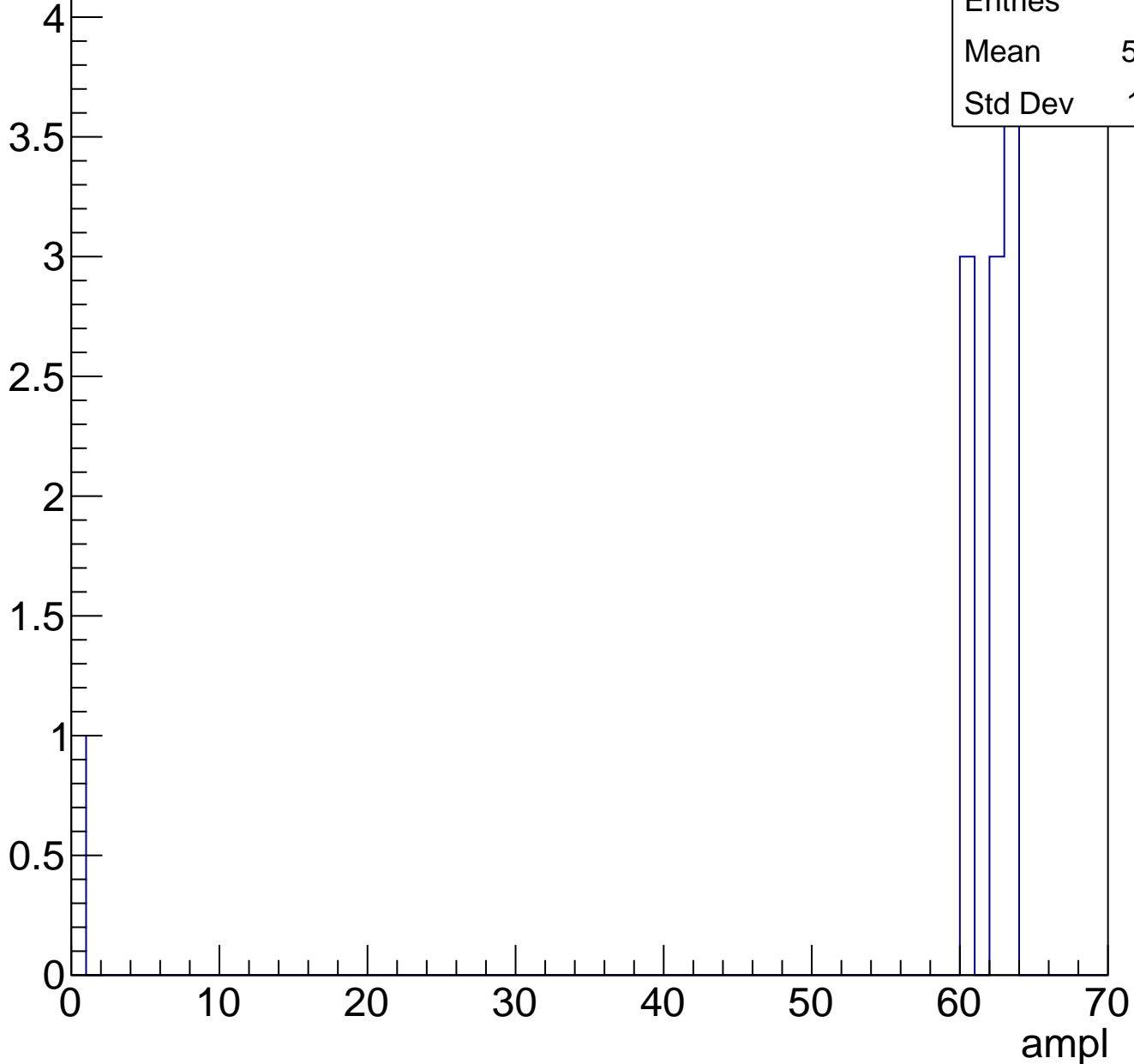
60

70

B1L102S, U2-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	11
Mean	56.18
Std Dev	17.81

B1L102S, U2-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch2, adc0

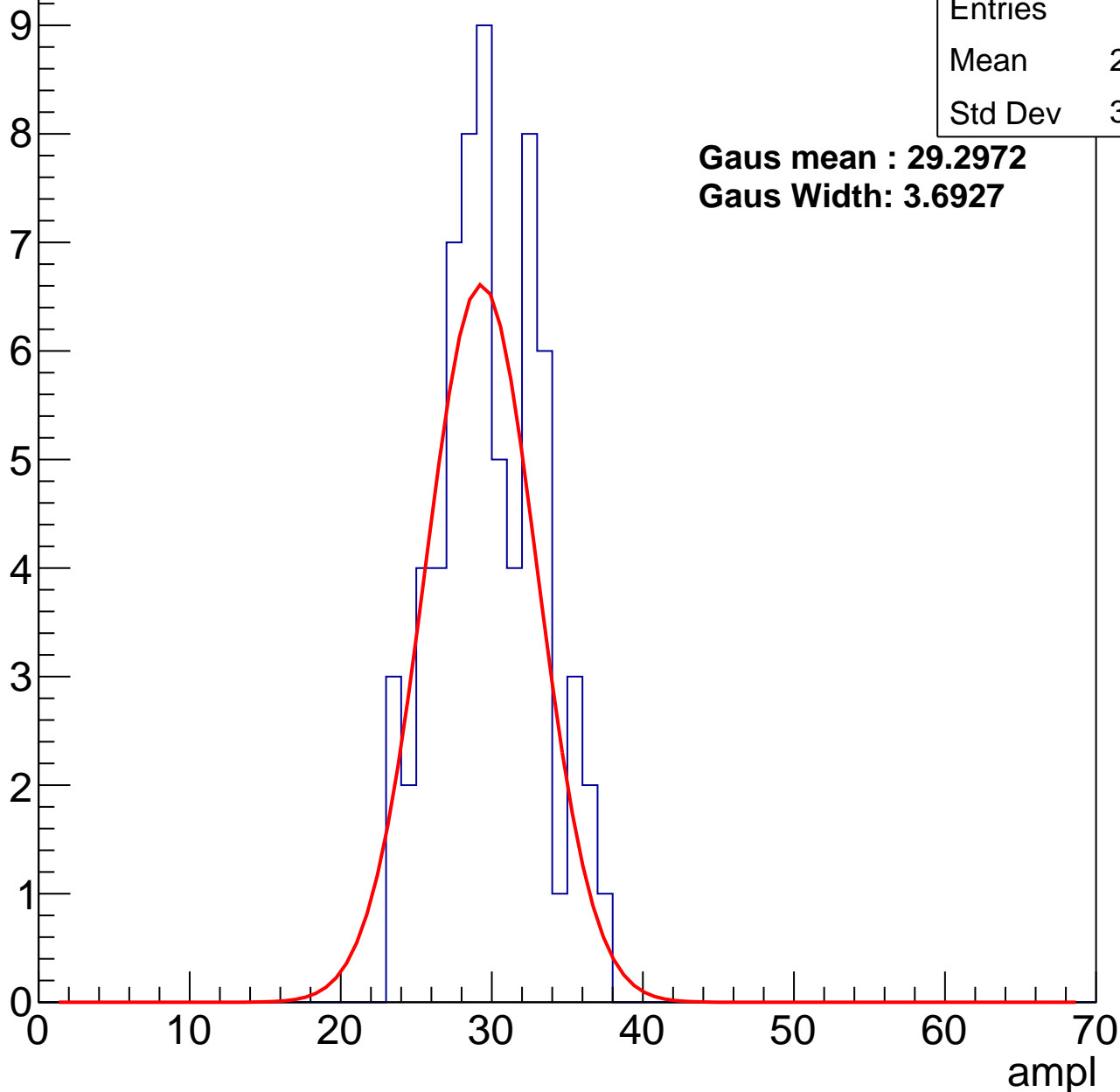
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	29.42
Std Dev	3.382

Gaus mean : 29.2972

Gaus Width: 3.6927



B1L102S, U2-ch2, adc1

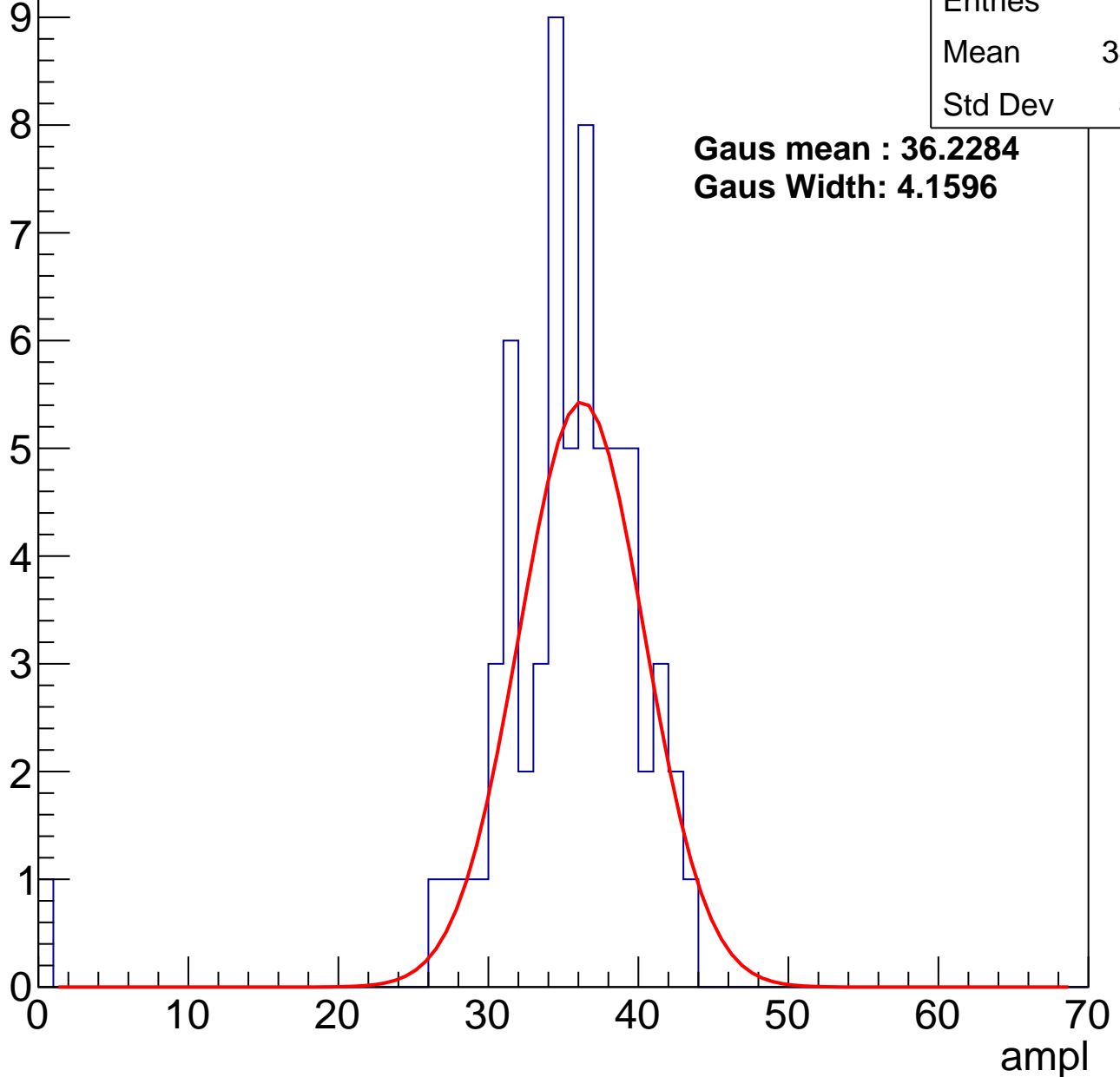
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.66
Std Dev	5.77

Gaus mean : 36.2284

Gaus Width: 4.1596



B1L102S, U2-ch2, adc2

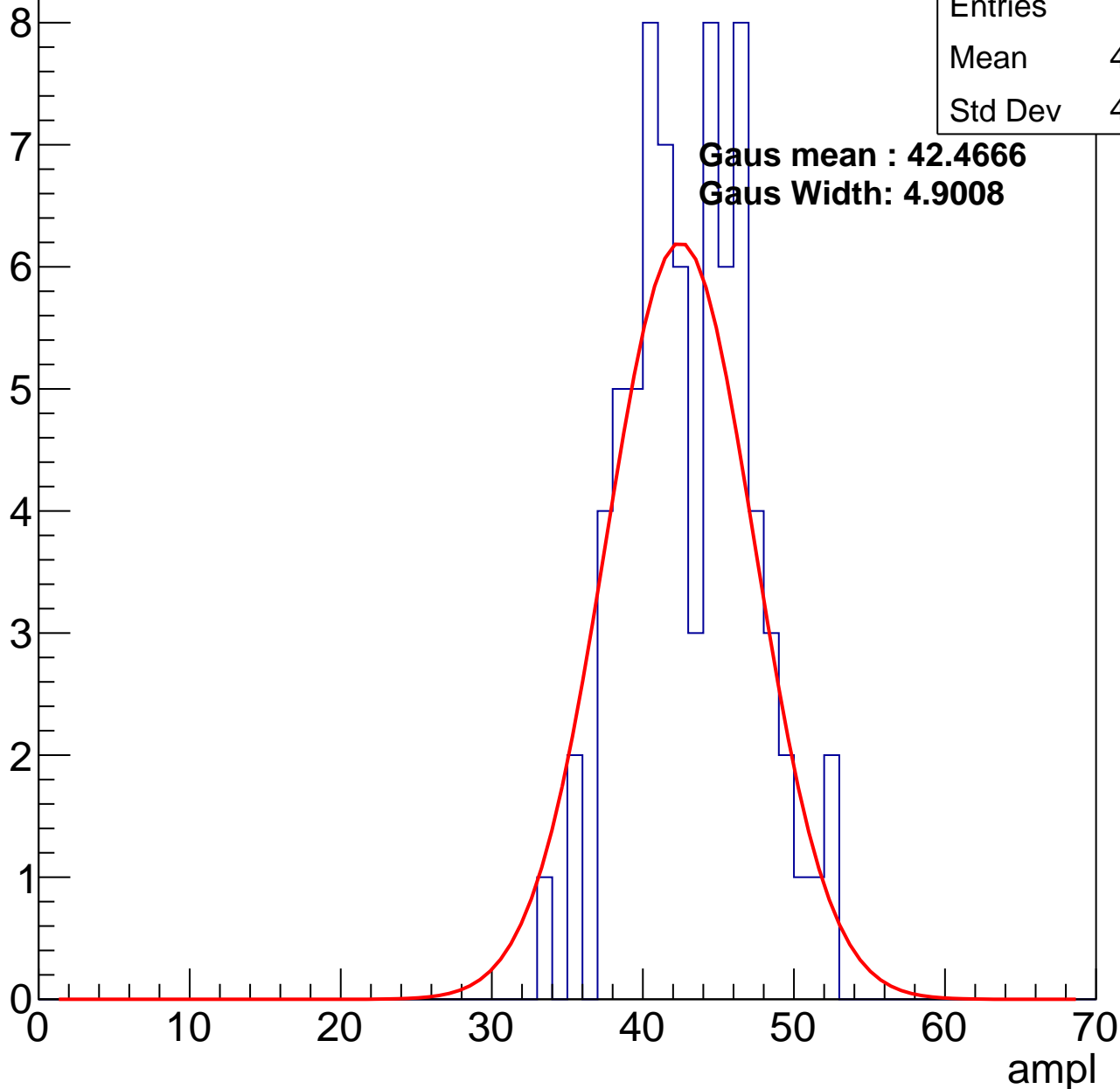
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.75
Std Dev	4.085

Gaus mean : 42.4666

Gaus Width: 4.9008

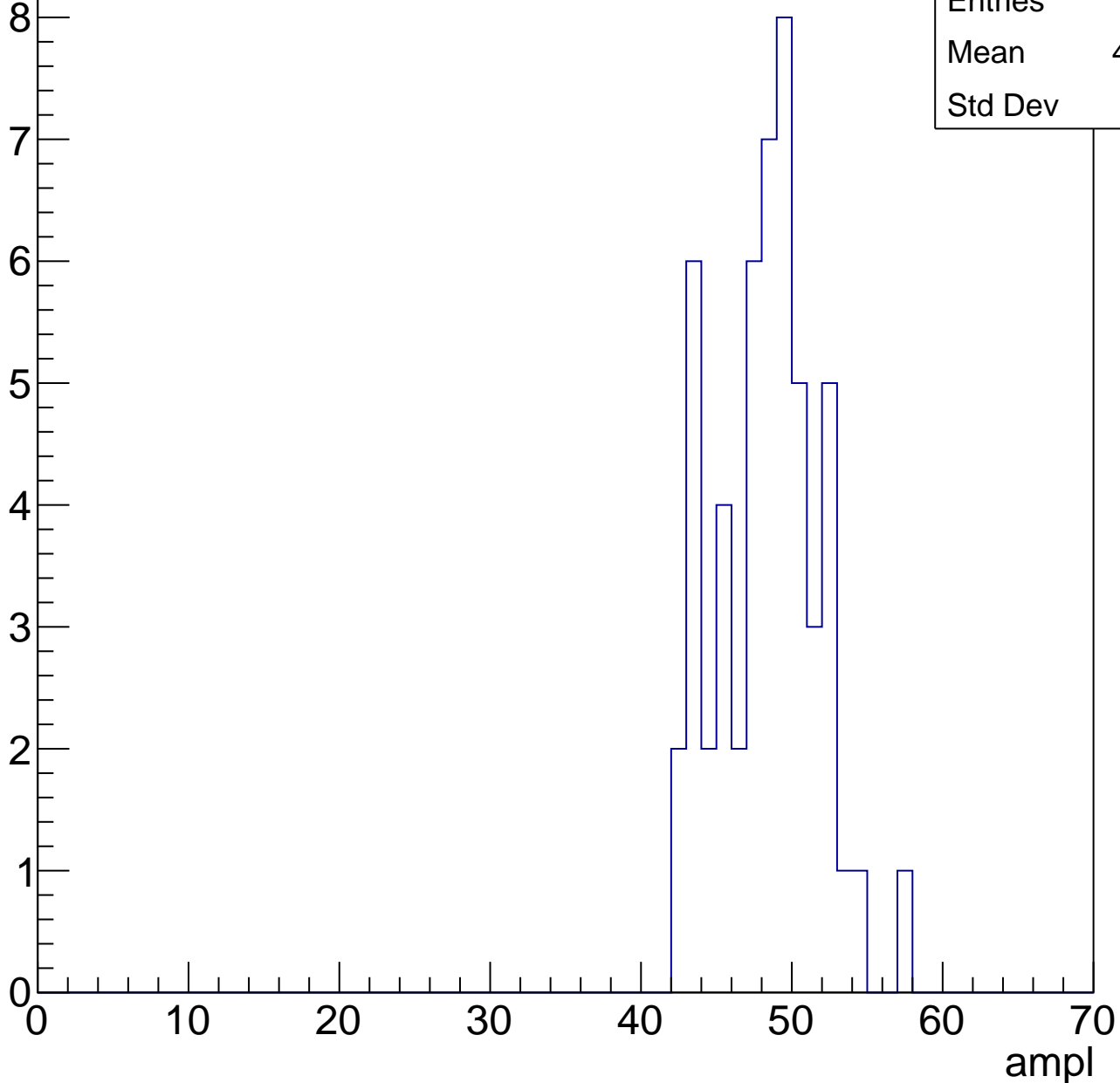


B1L102S, U2-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	47.91
Std Dev	3.31

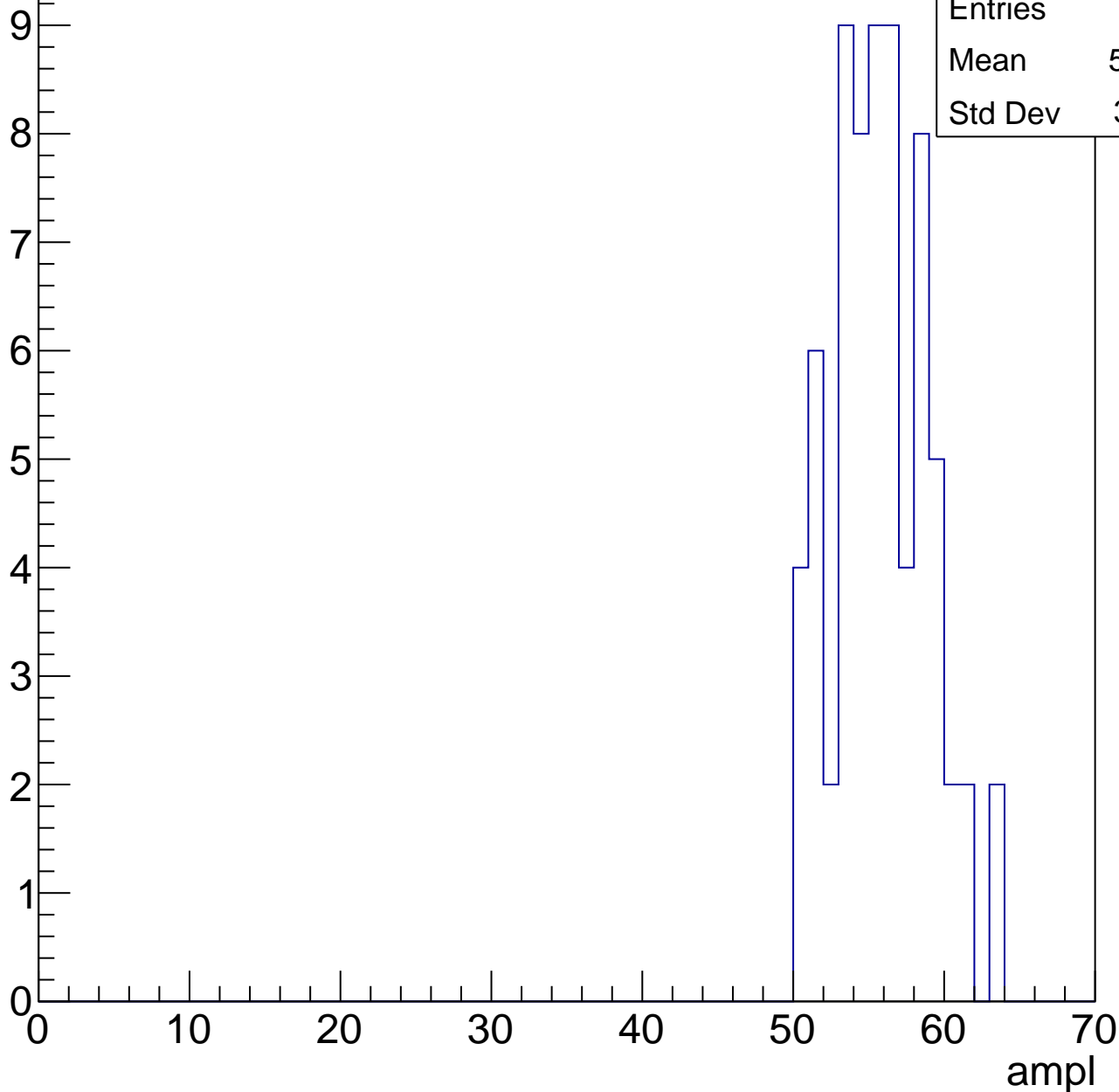


B1L102S, U2-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.33
Std Dev	3.111

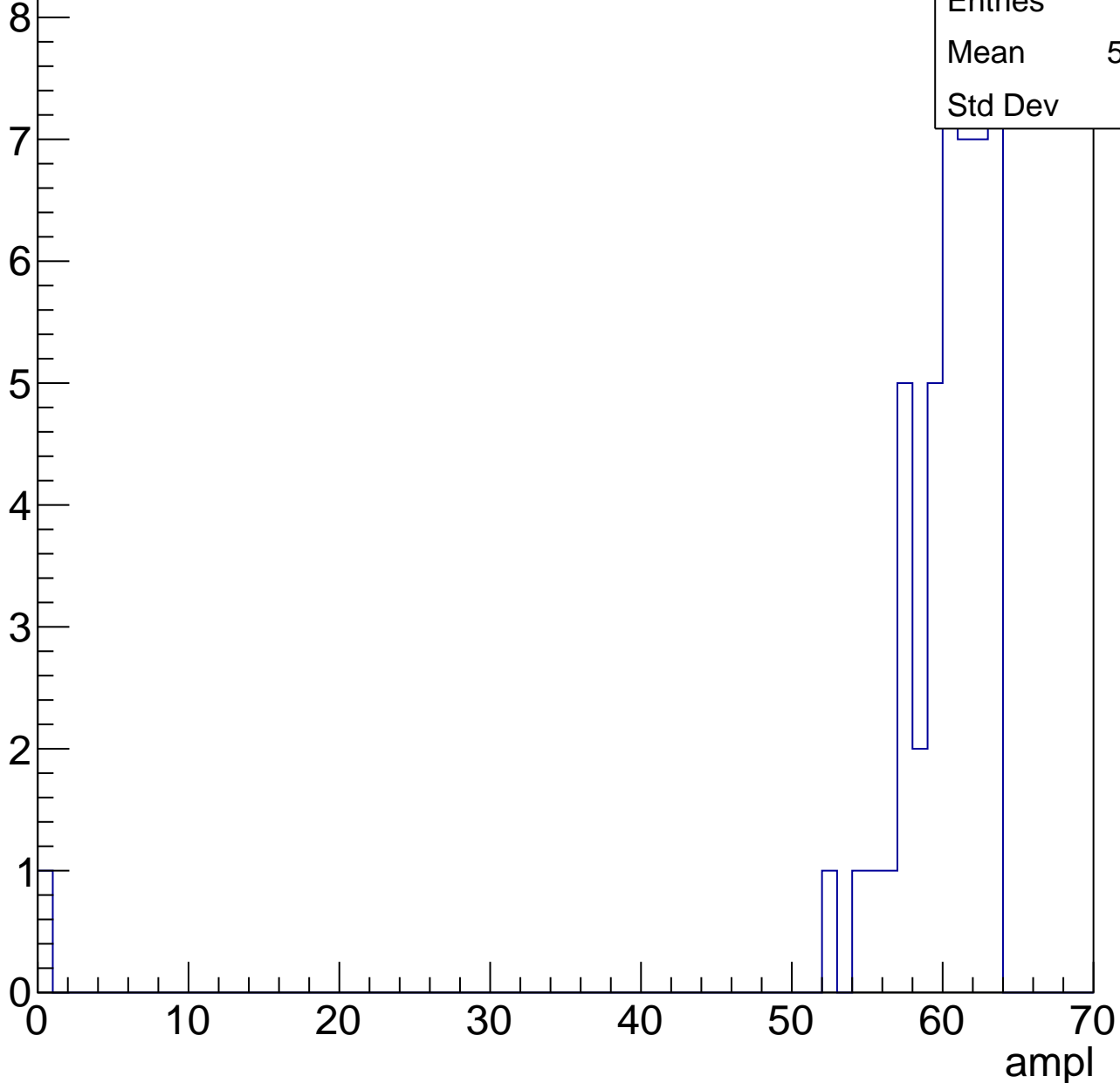


B1L102S, U2-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

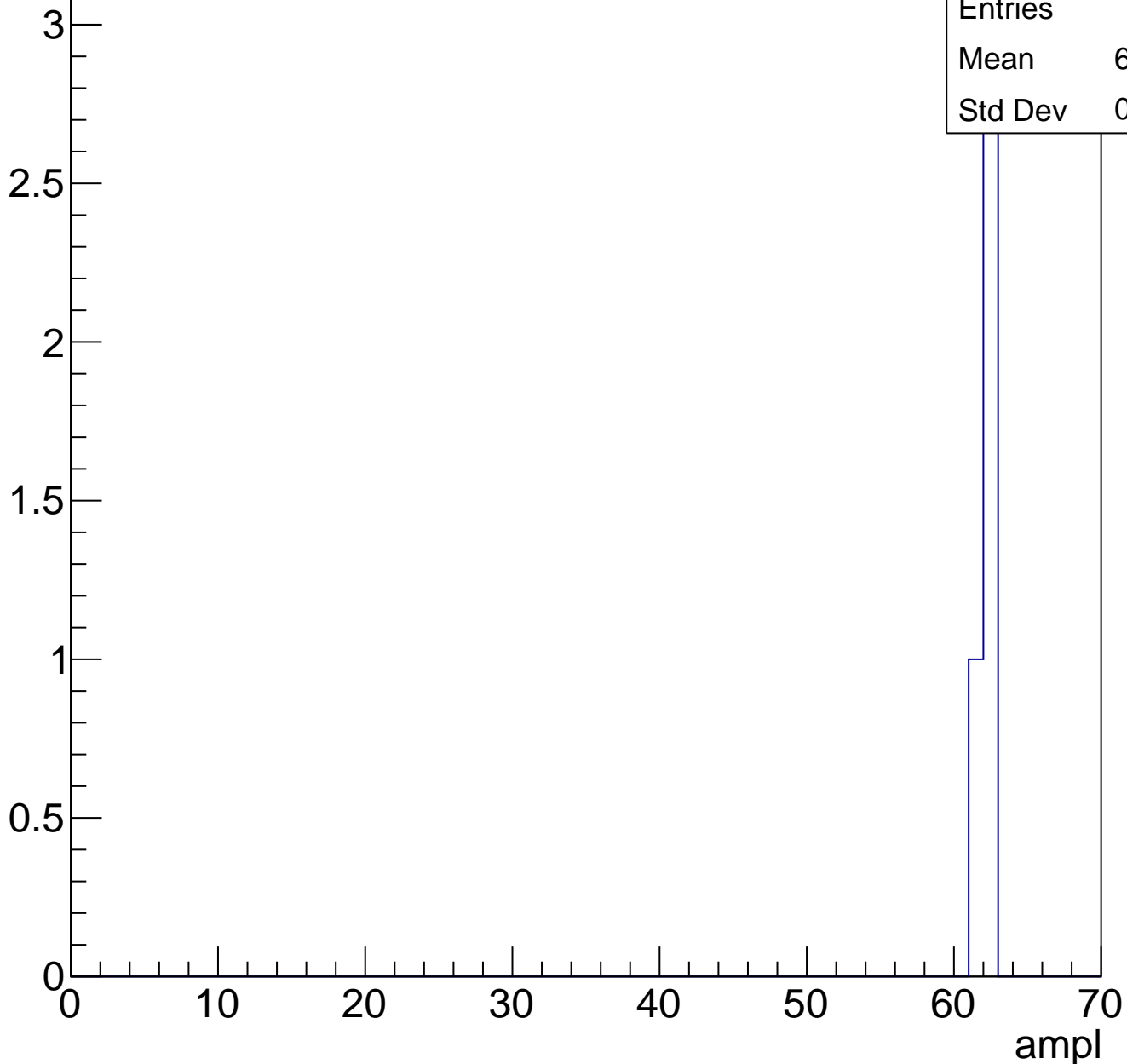
Entries	47
Mean	58.68
Std Dev	9.02



B1L102S, U2-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch3, adc0

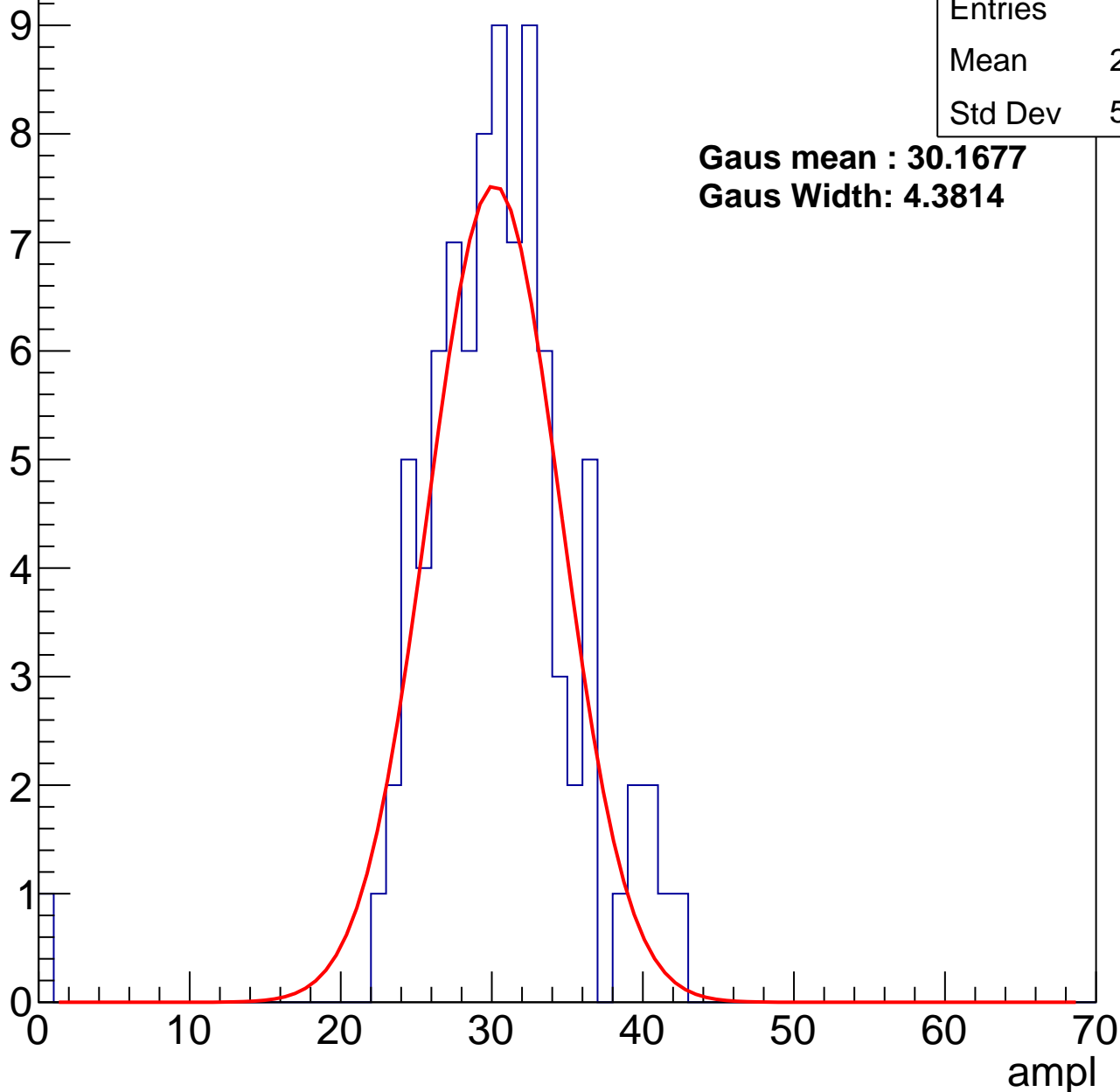
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	29.97
Std Dev	5.434

Gaus mean : 30.1677

Gaus Width: 4.3814



B1L102S, U2-ch3, adc1

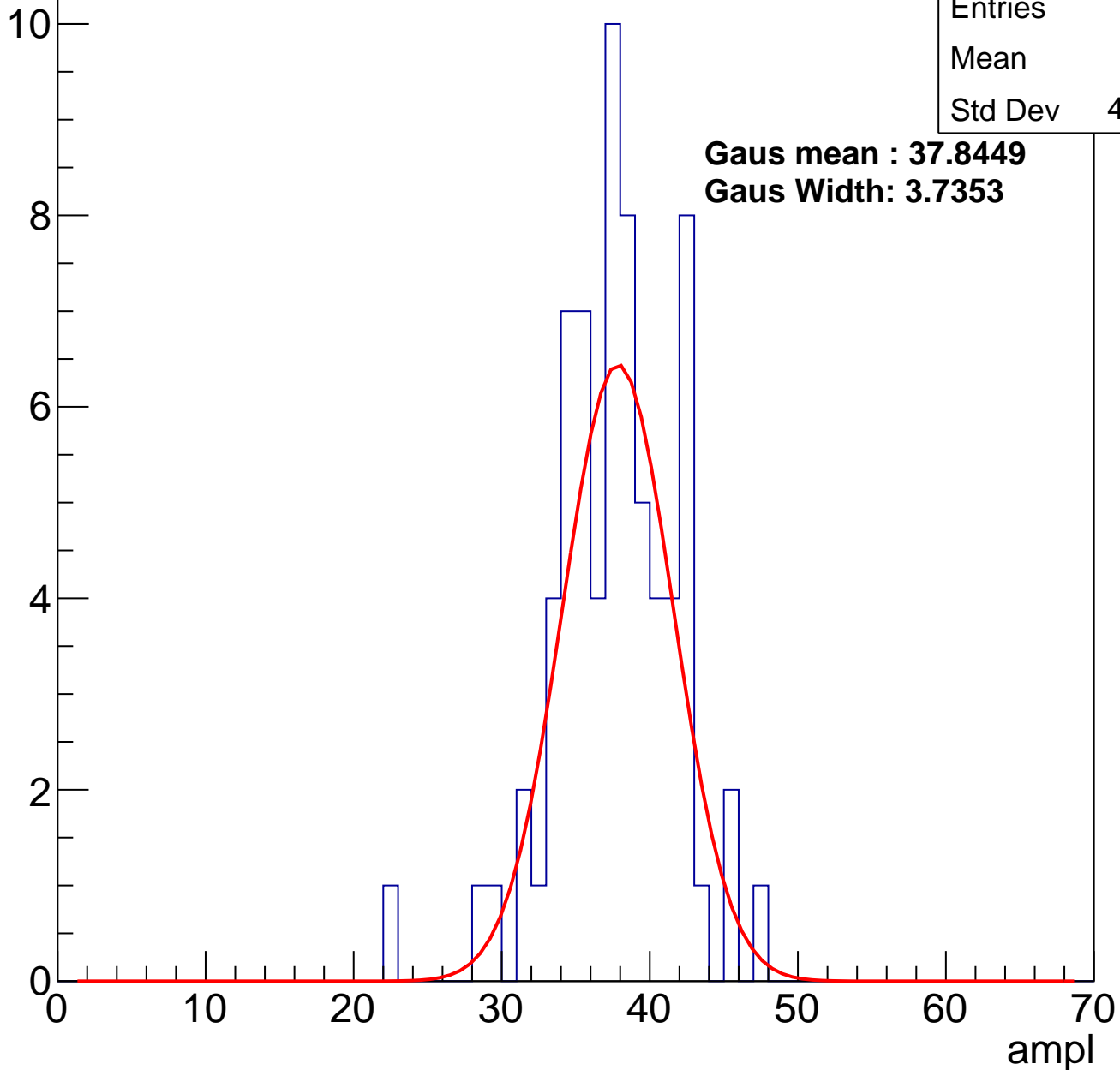
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	37.2
Std Dev	4.137

Gaus mean : 37.8449

Gaus Width: 3.7353

Entry



B1L102S, U2-ch3, adc2

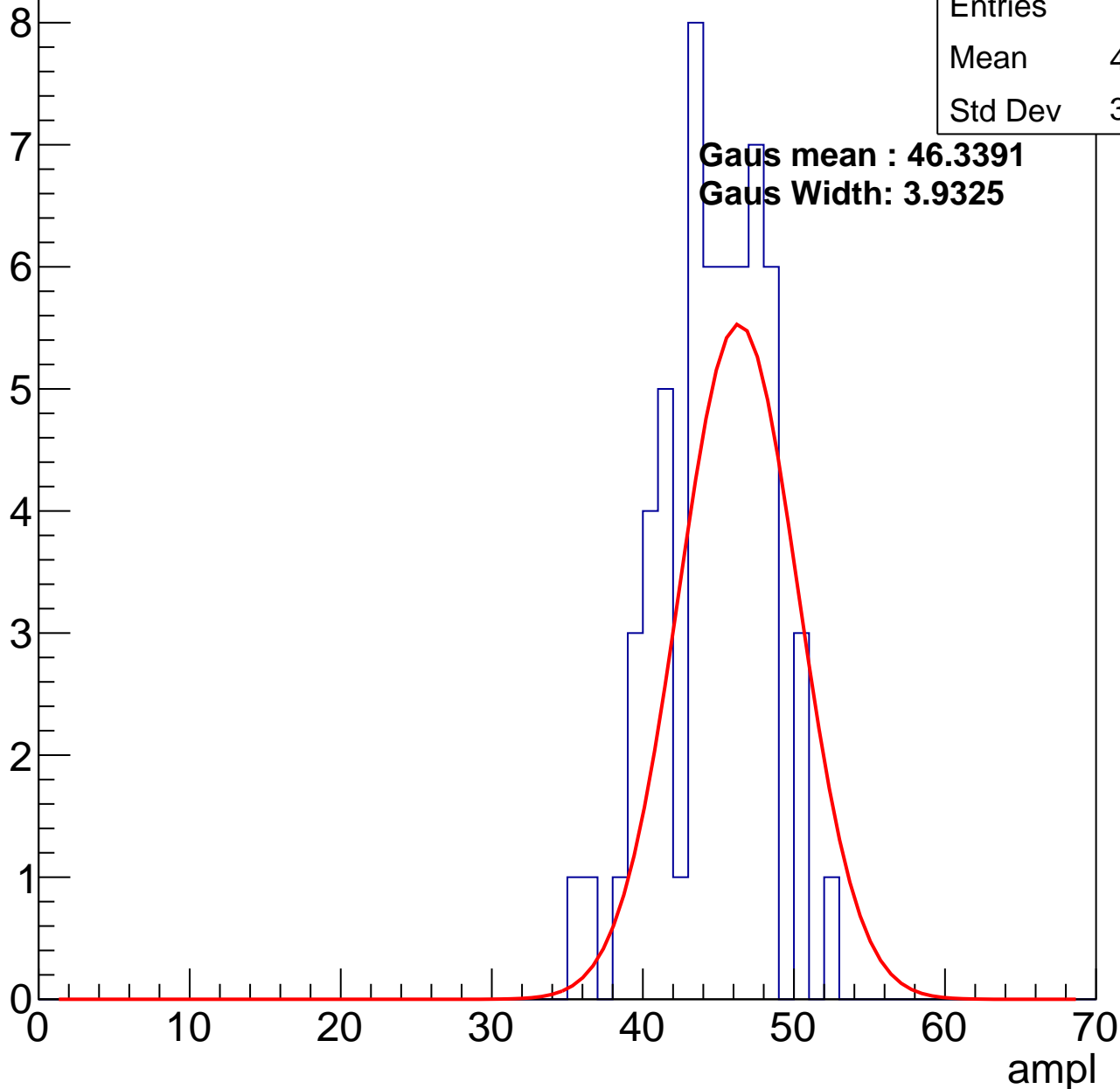
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.17
Std Dev	3.533

Gaus mean : 46.3391

Gaus Width: 3.9325



B1L102S, U2-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

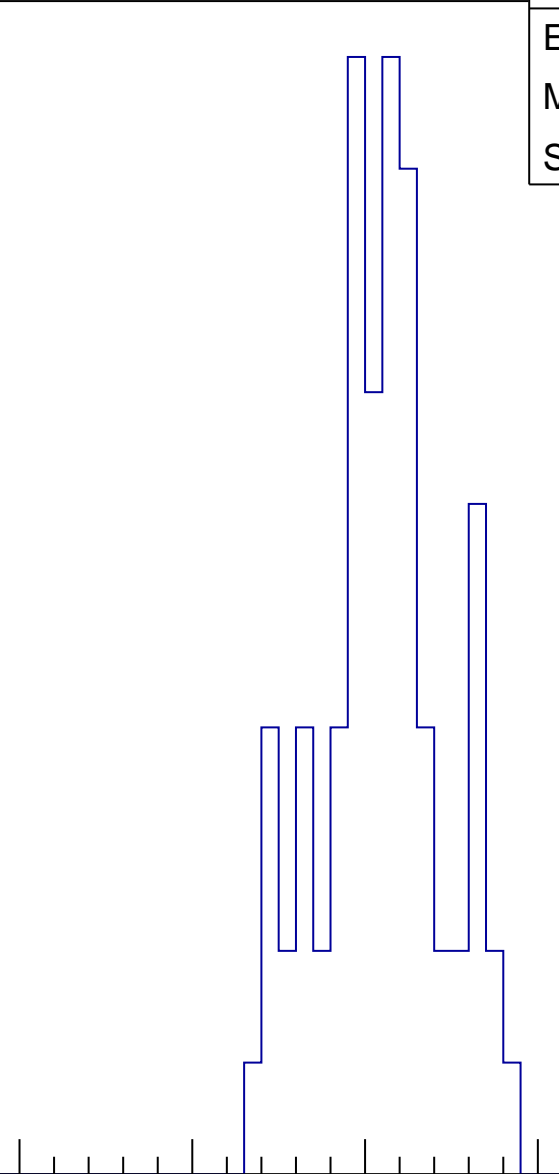
Entries	70
Mean	50.5
Std Dev	3.537

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

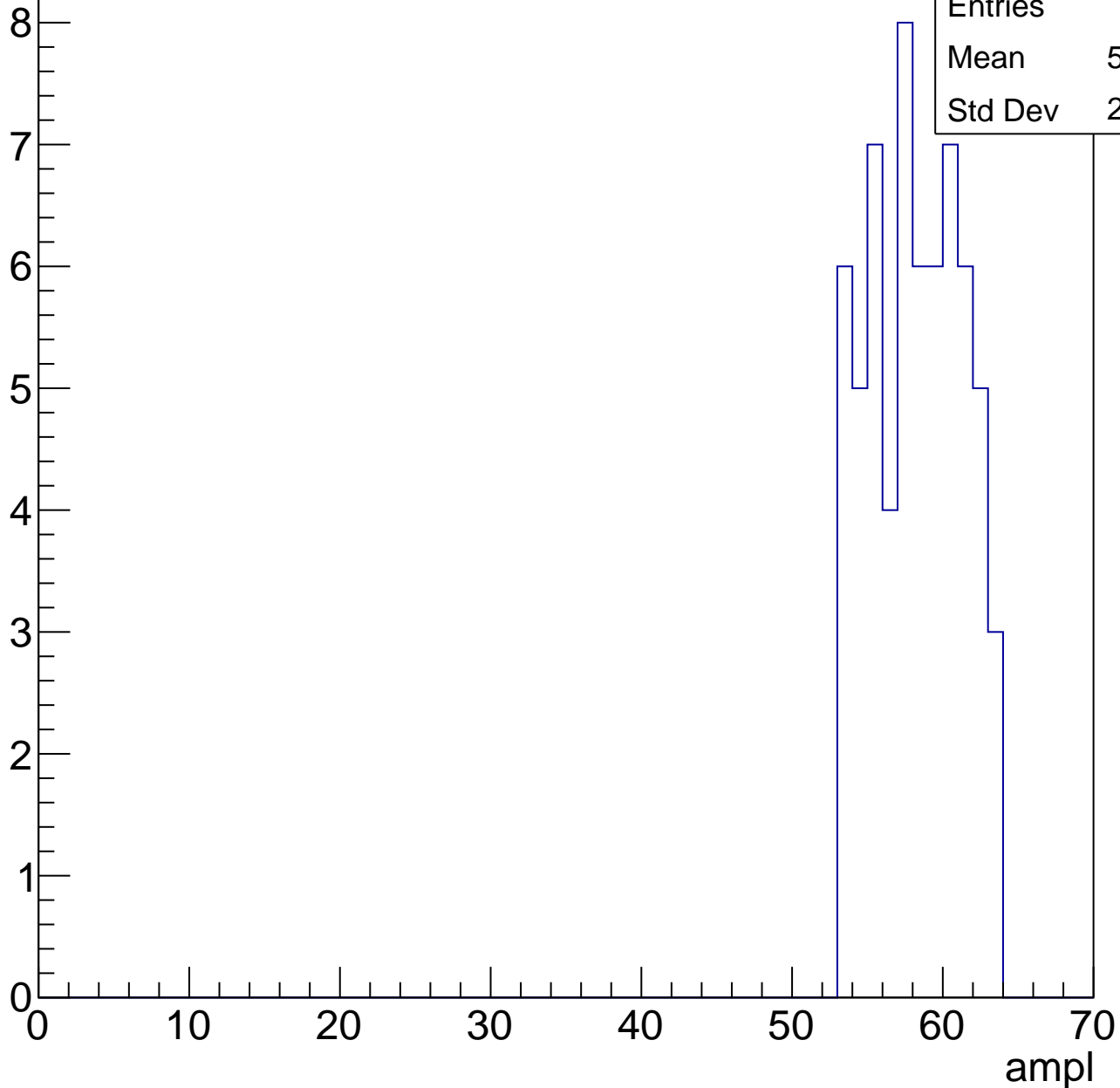
ampl



B1L102S, U2-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

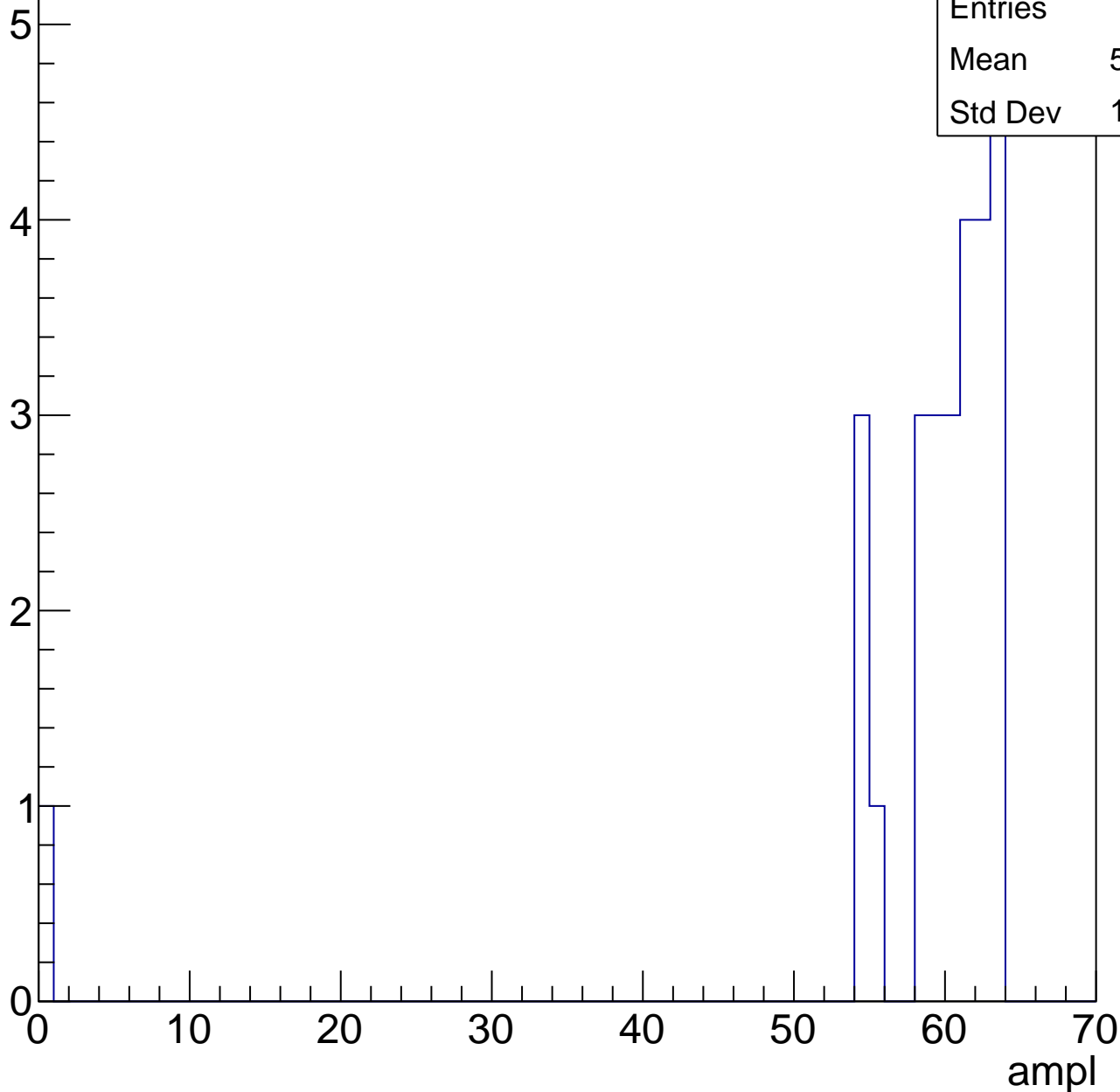


B1L102S, U2-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

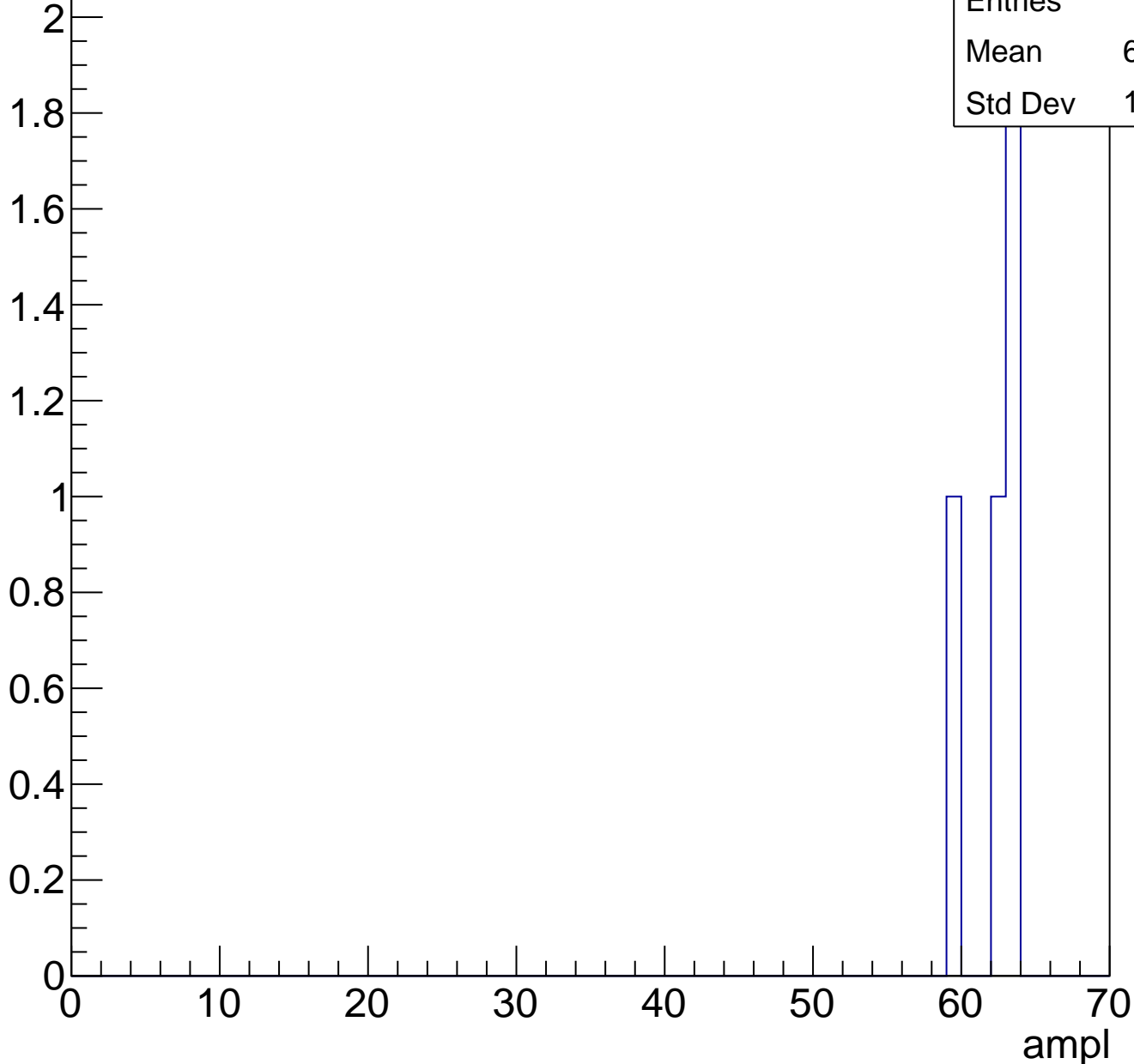
Entries	27
Mean	57.59
Std Dev	11.64



B1L102S, U2-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch4, adc0

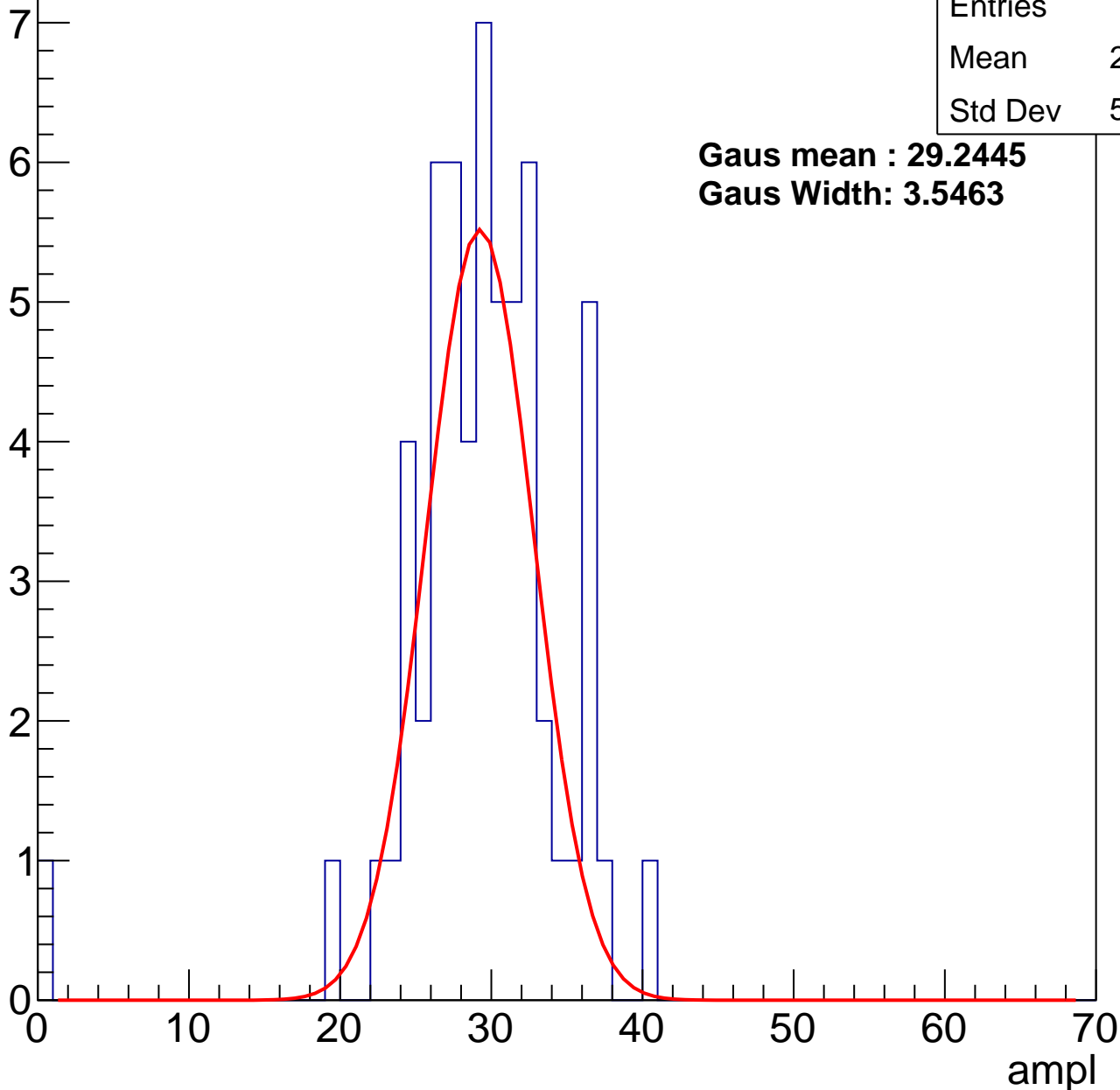
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.87
Std Dev	5.539

Gaus mean : 29.2445

Gaus Width: 3.5463



B1L102S, U2-ch4, adc1

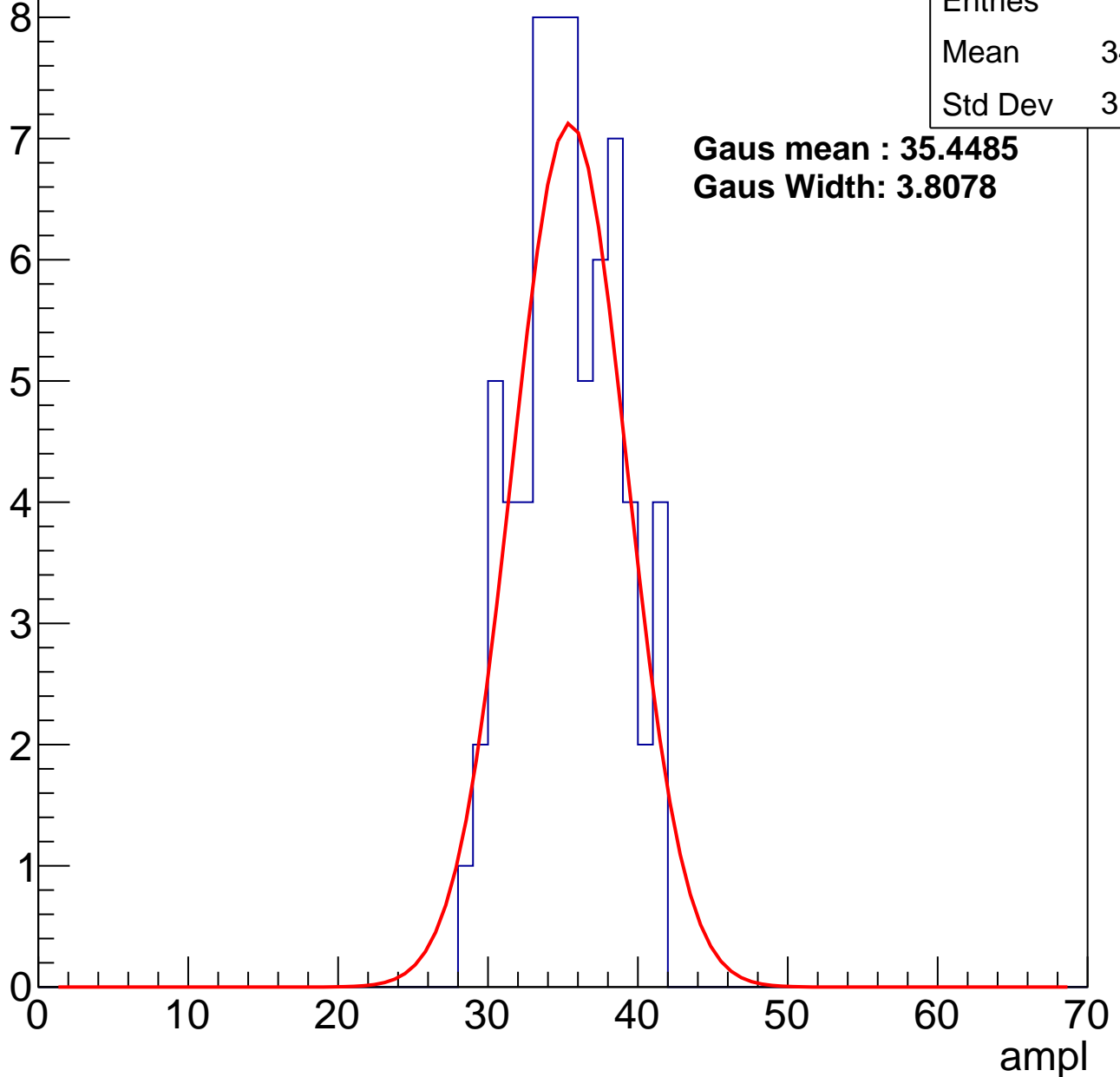
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.88
Std Dev	3.288

Gaus mean : 35.4485

Gaus Width: 3.8078



B1L102S, U2-ch4, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	42.96
Std Dev	3.666

Gaus mean : 43.0398

Gaus Width: 3.5862

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

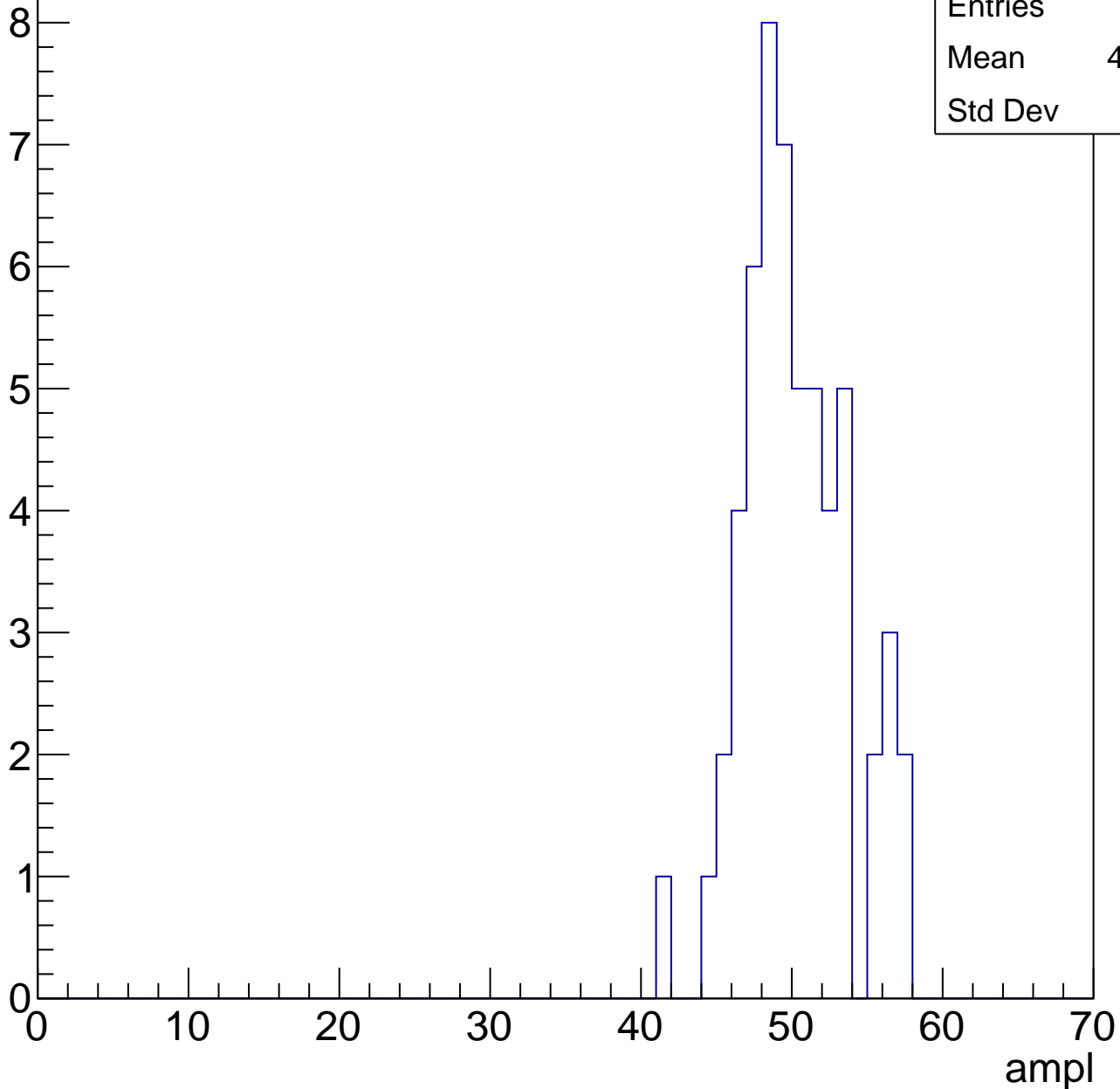
70

B1L102S, U2-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.78
Std Dev	3.42

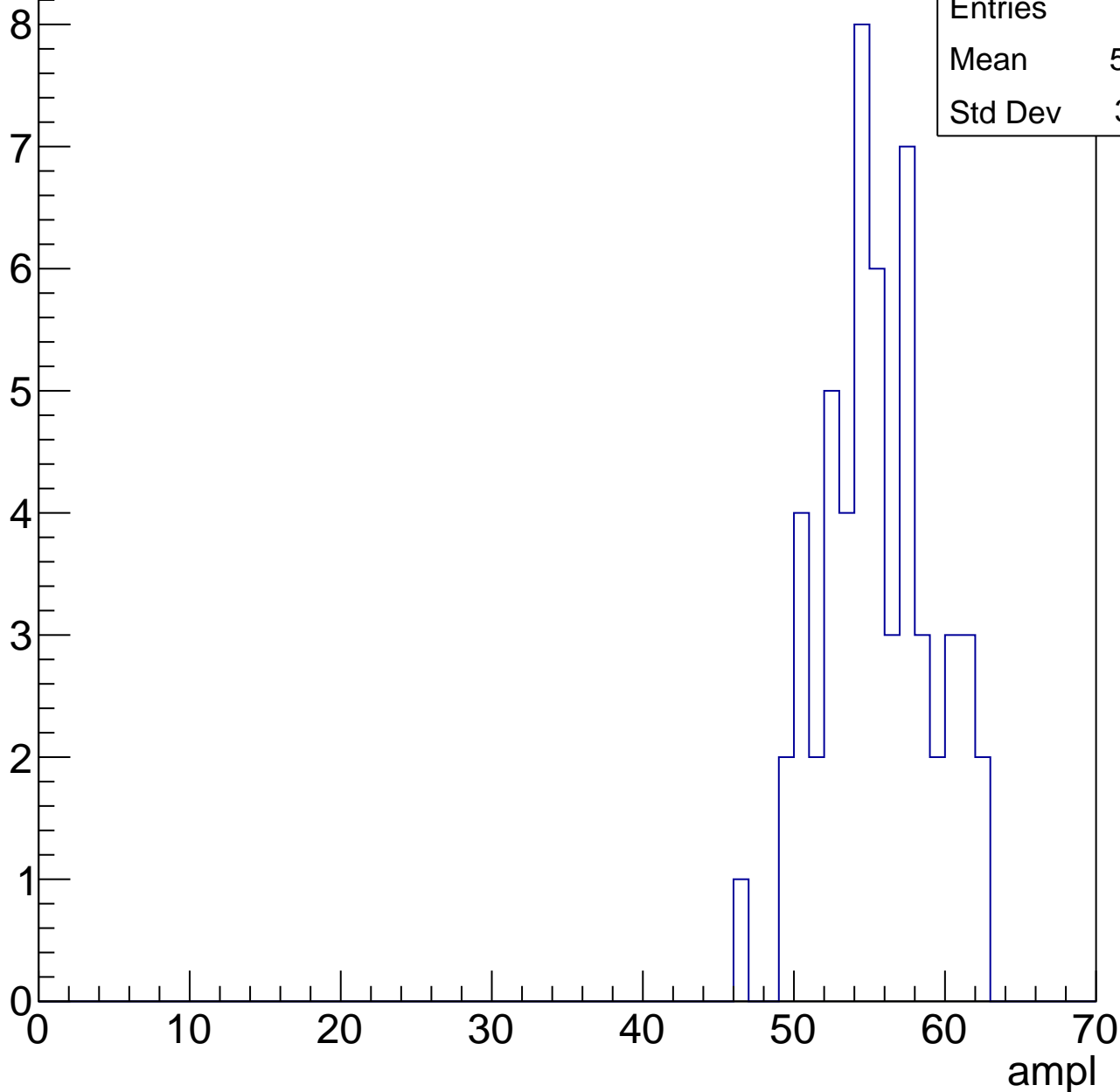


B1L102S, U2-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.02
Std Dev	3.631



B1L102S, U2-ch4, adc5

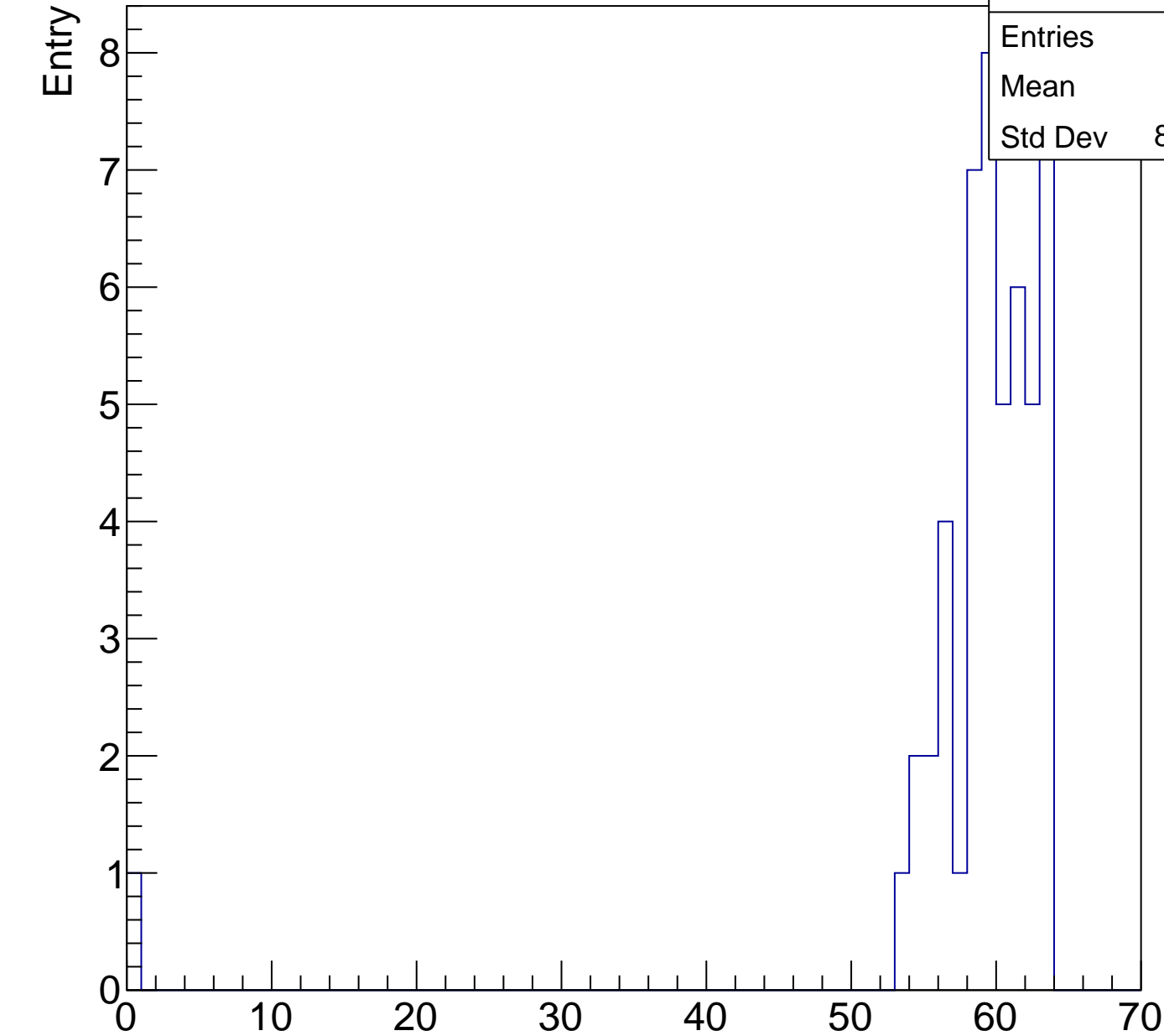
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.2
Std Dev	8.736

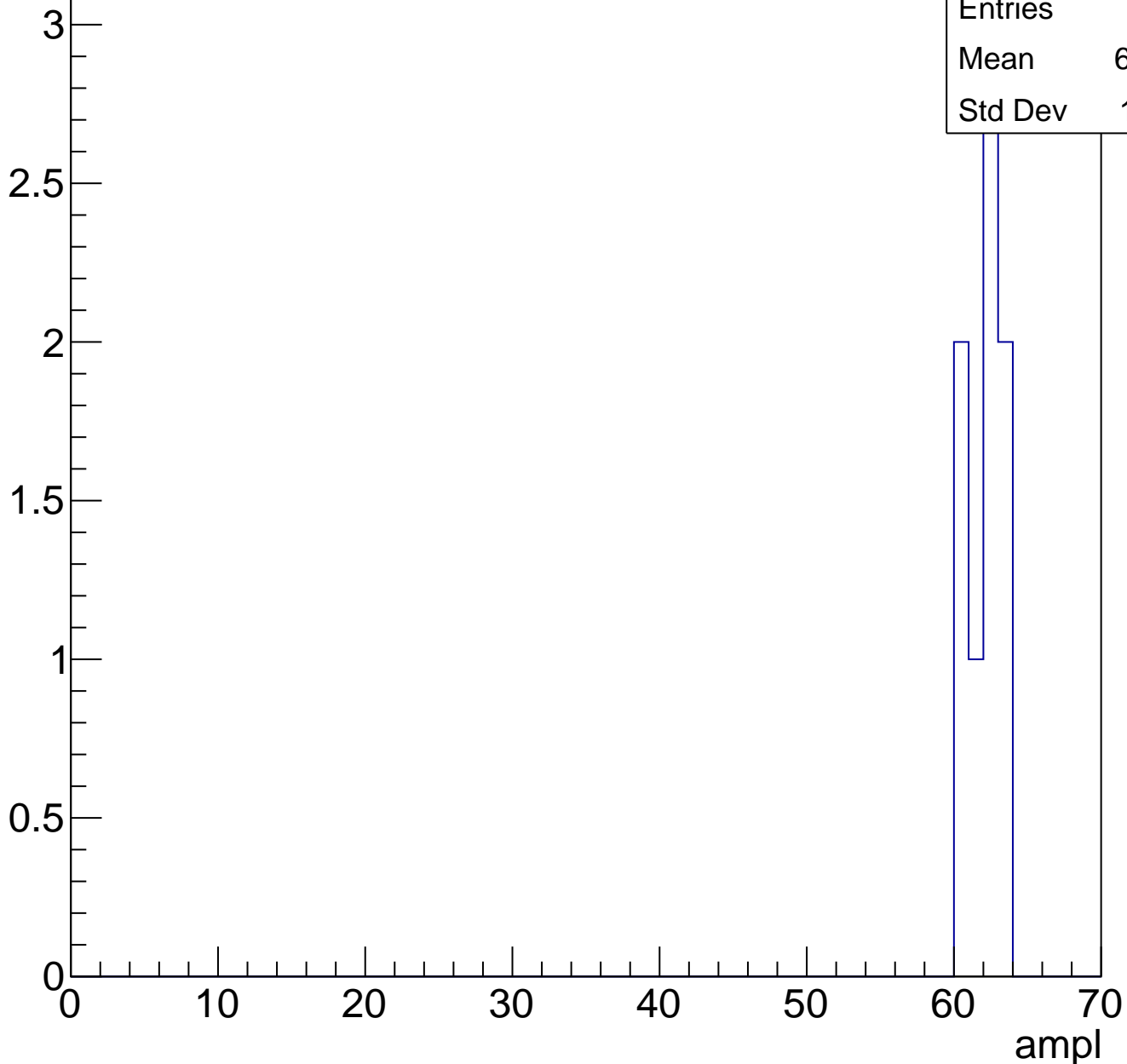
ampl



B1L102S, U2-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch5, adc0

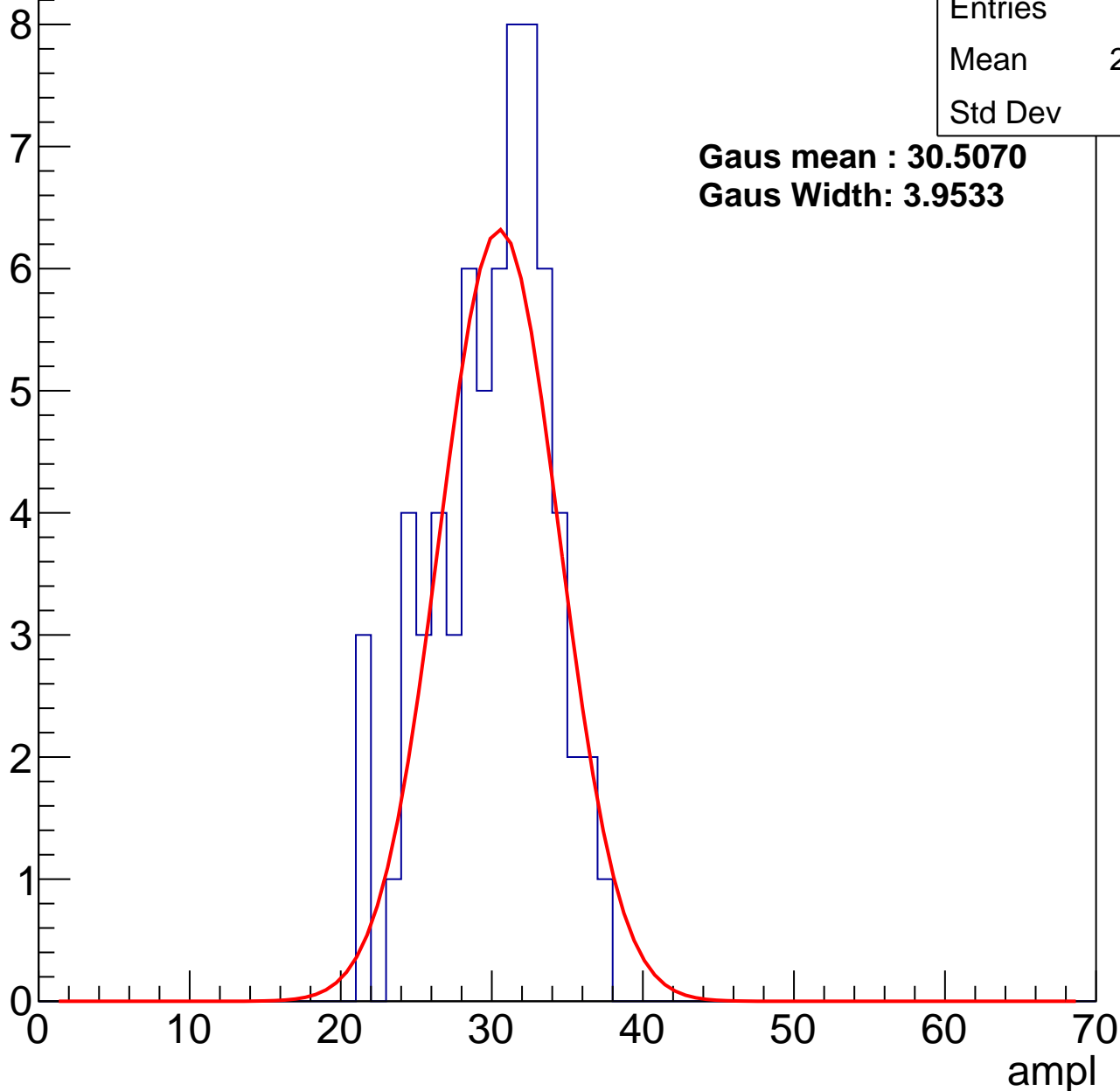
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.58
Std Dev	3.79

Gaus mean : 30.5070

Gaus Width: 3.9533



B1L102S, U2-ch5, adc1

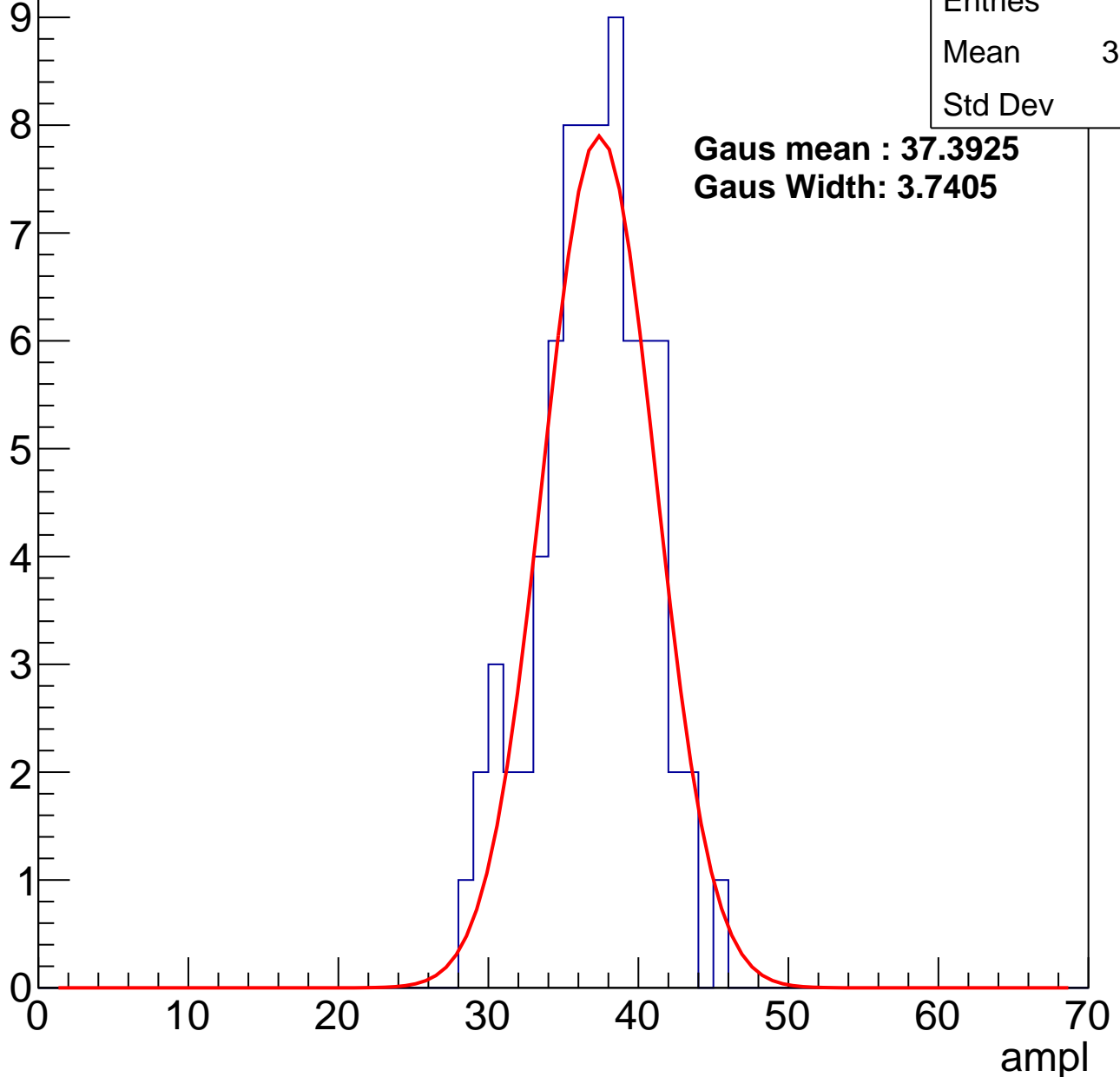
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	36.57
Std Dev	3.61

Gaus mean : 37.3925

Gaus Width: 3.7405



B1L102S, U2-ch5, adc2

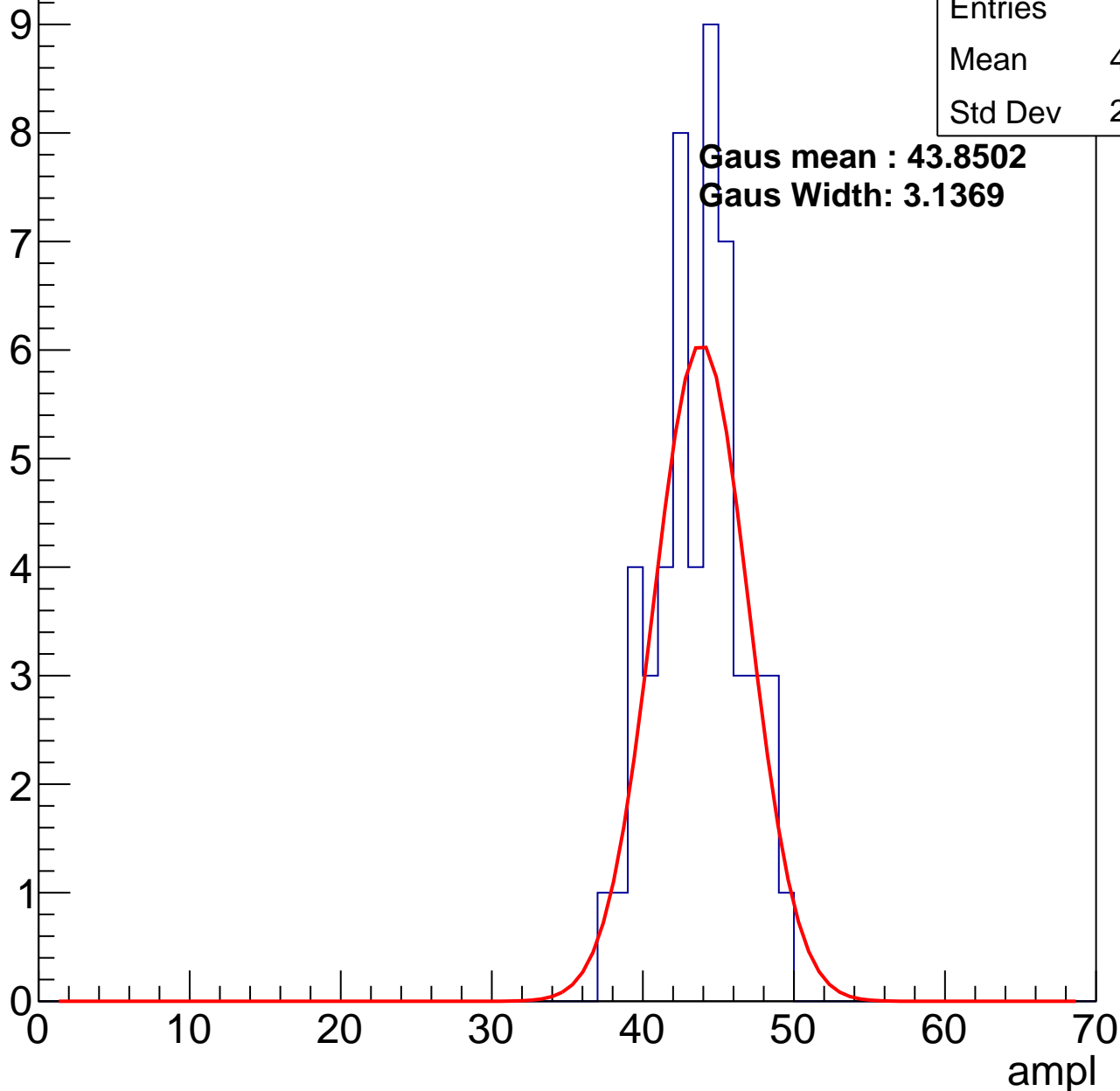
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	43.25
Std Dev	2.785

Gaus mean : 43.8502

Gaus Width: 3.1369

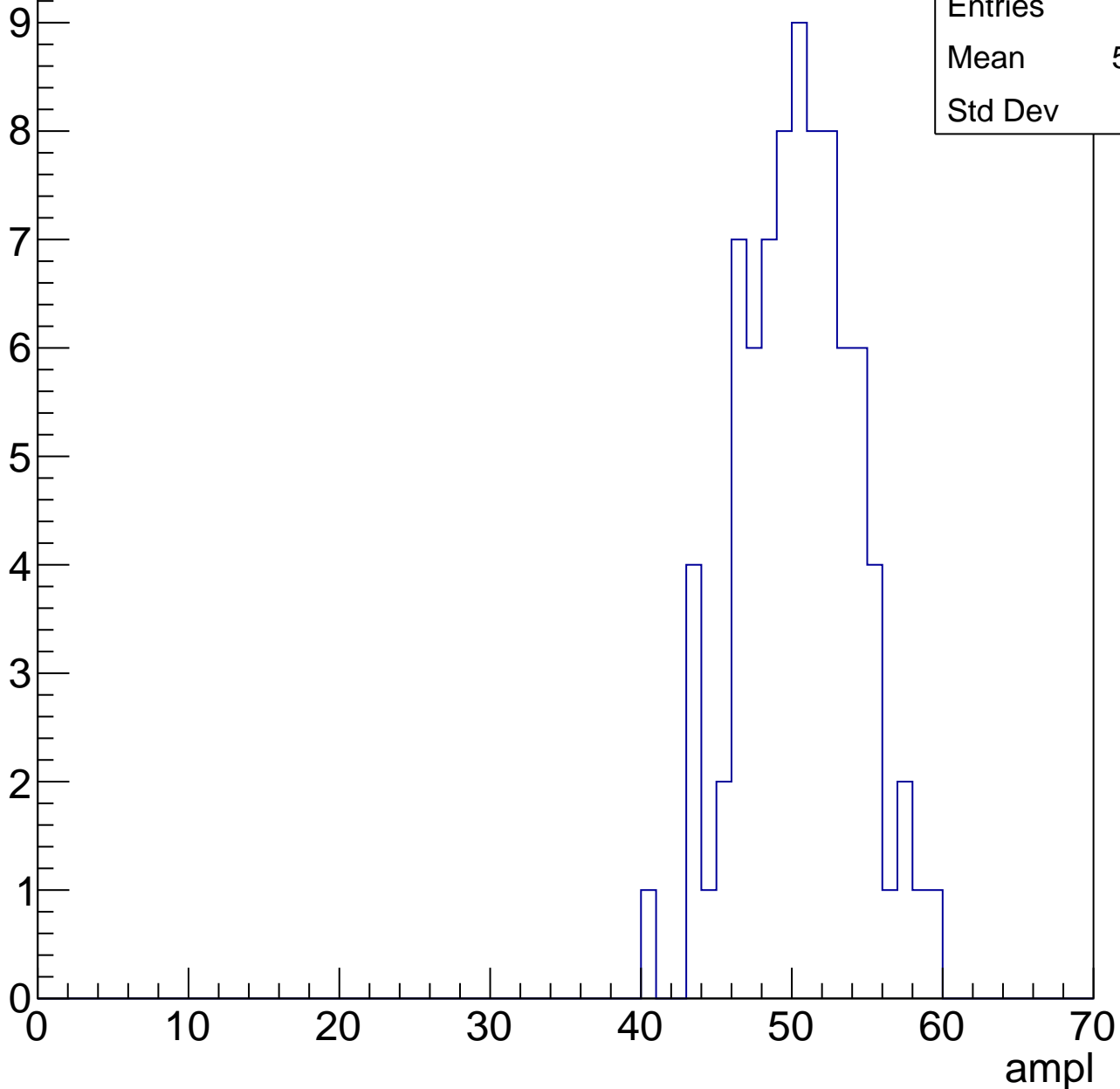


B1L102S, U2-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

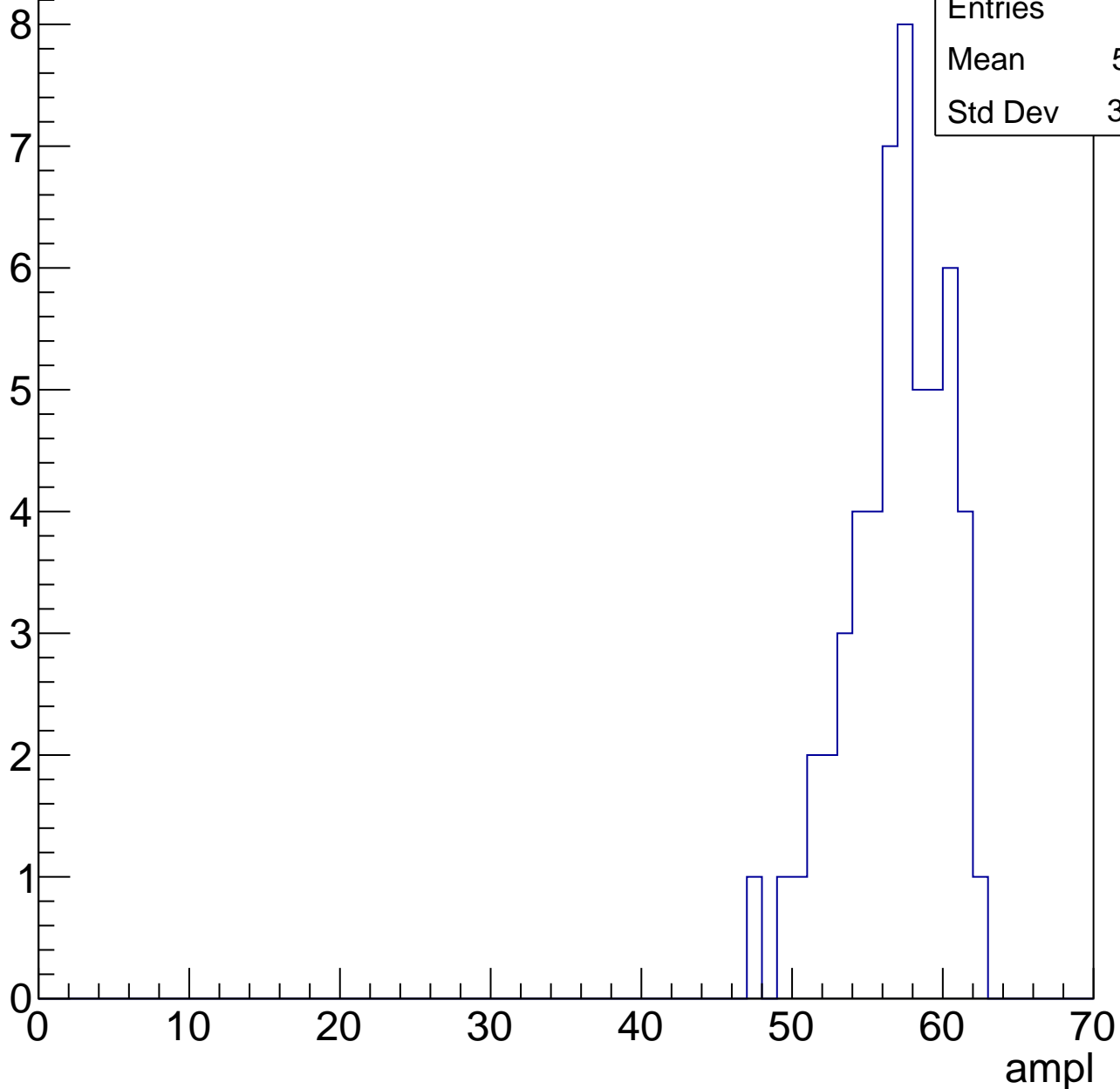
Entries	82
Mean	50.01
Std Dev	3.75



B1L102S, U2-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

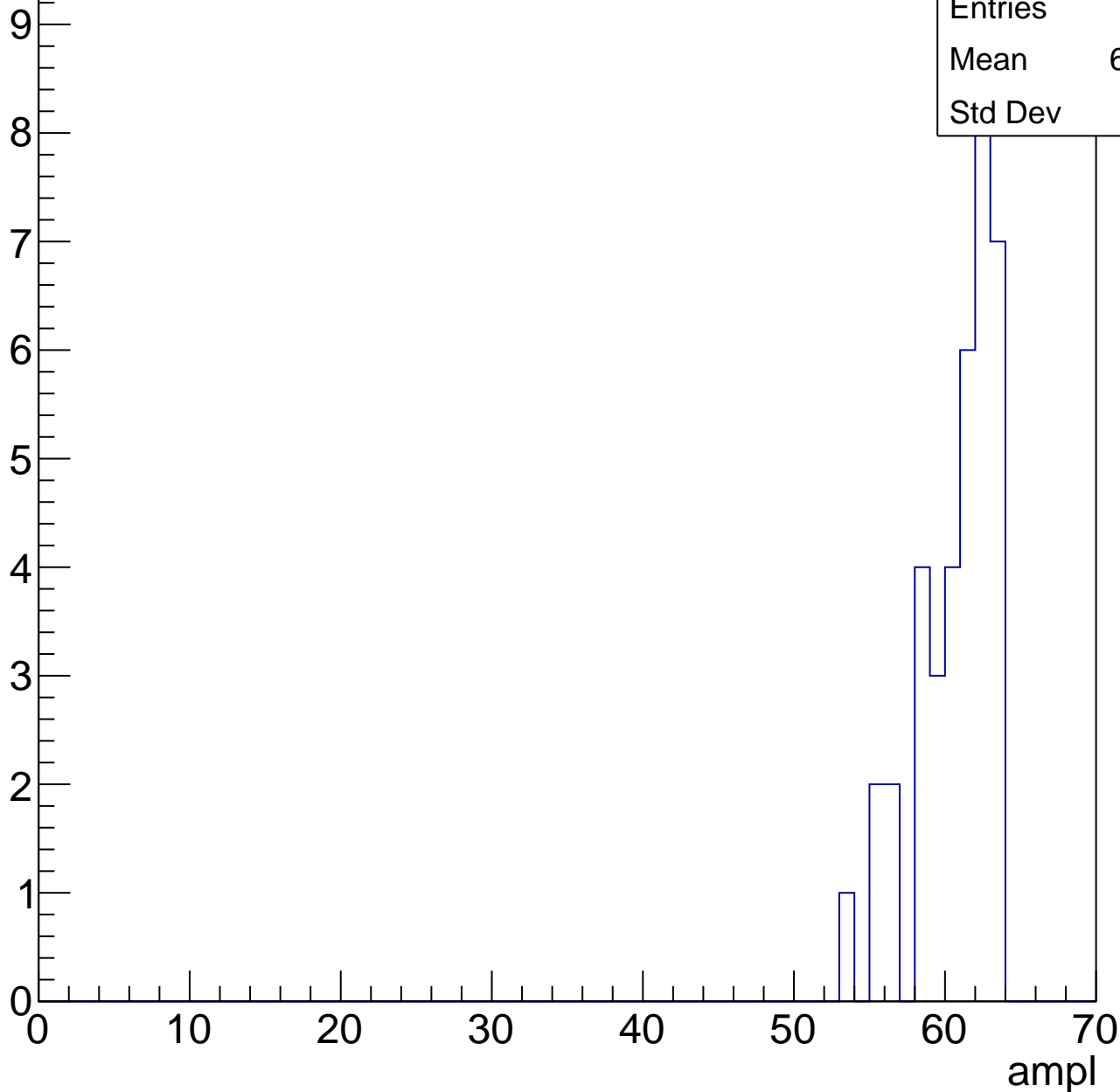


B1L102S, U2-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

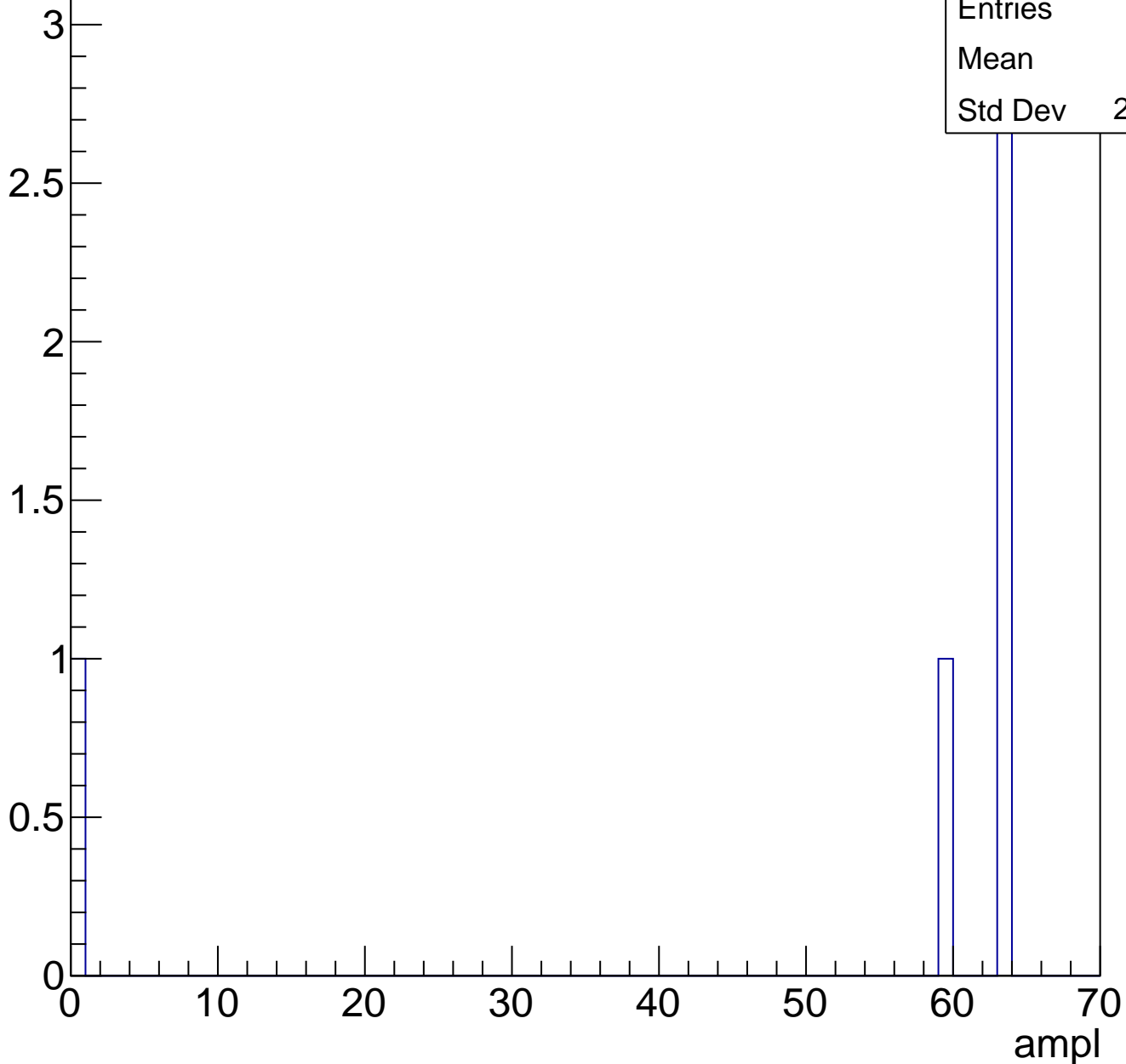
Entries	38
Mean	60.24
Std Dev	2.58



B1L102S, U2-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch6, adc0

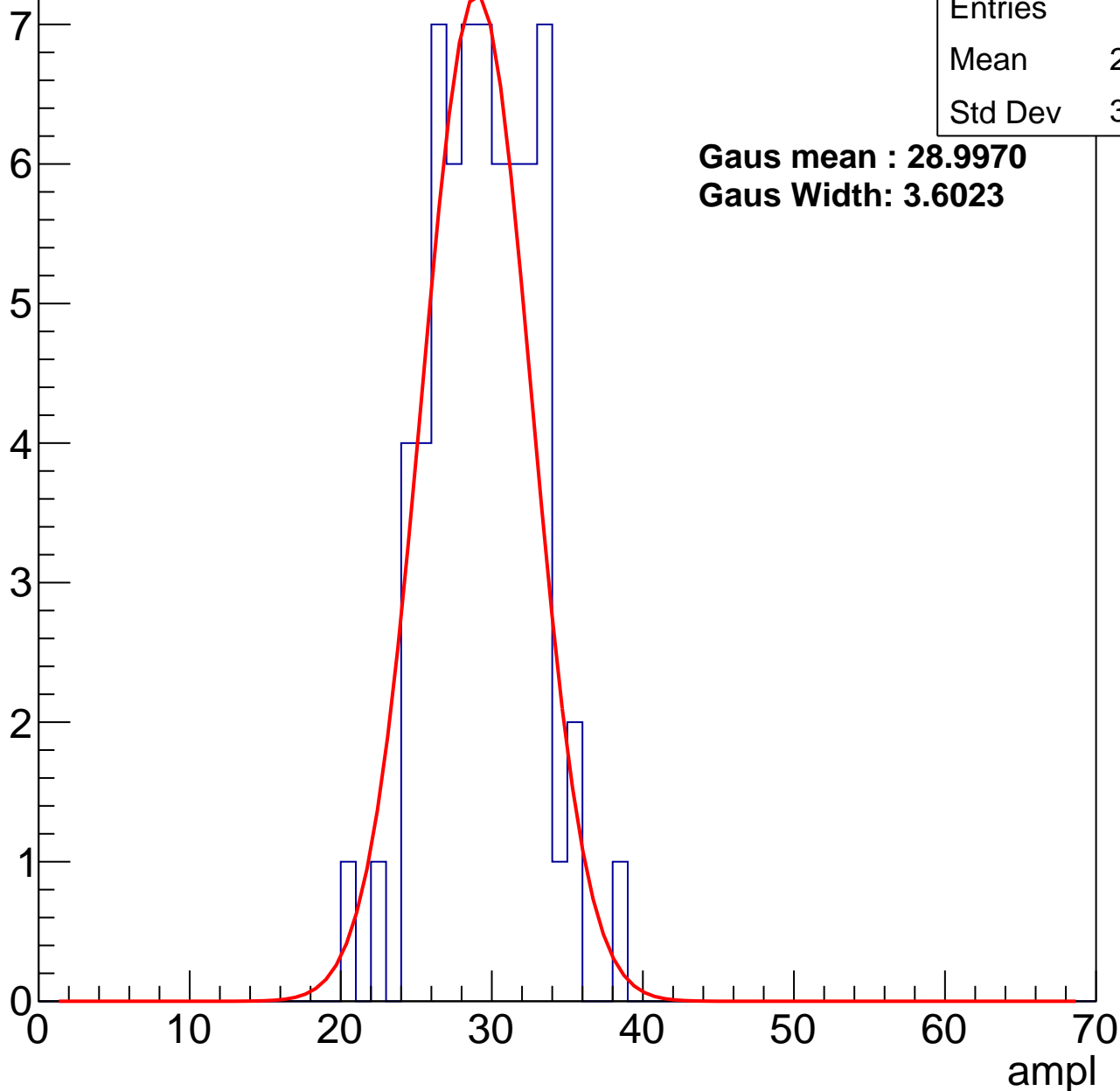
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.97
Std Dev	3.398

Gaus mean : 28.9970

Gaus Width: 3.6023



B1L102S, U2-ch6, adc1

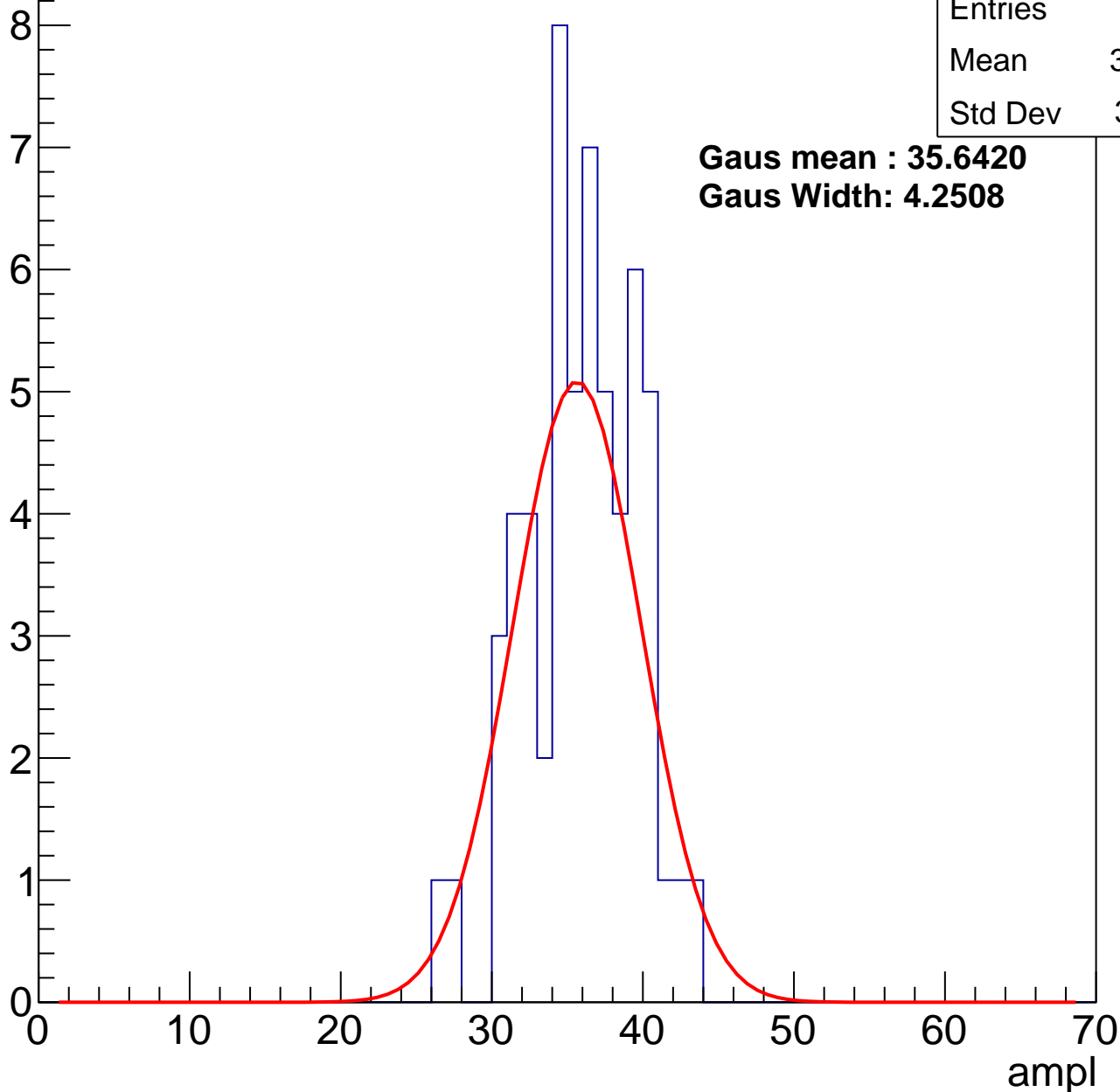
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.47
Std Dev	3.611

Gaus mean : 35.6420

Gaus Width: 4.2508



B1L102S, U2-ch6, adc2

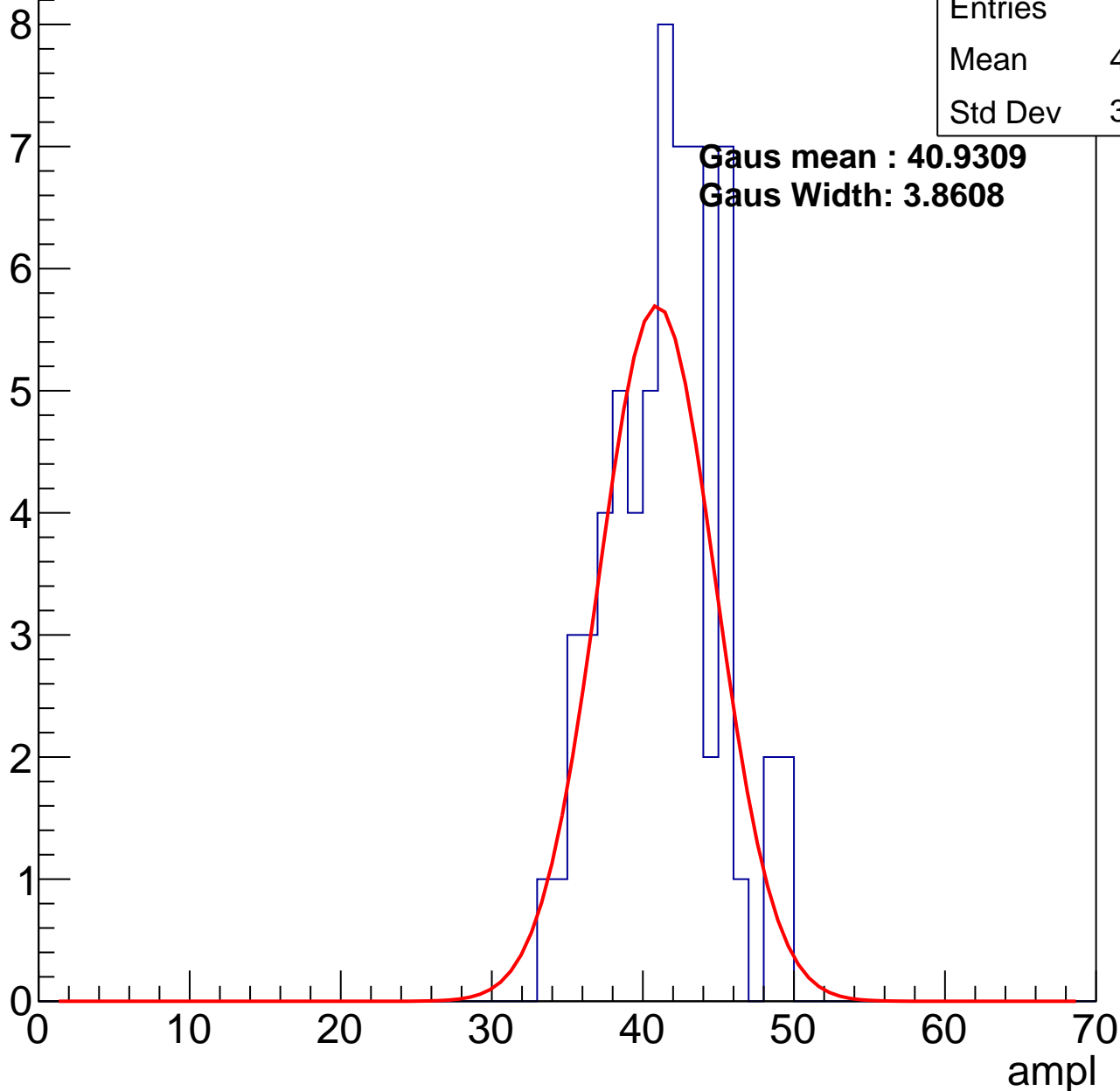
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.97
Std Dev	3.685

Gaus mean : 40.9309

Gaus Width: 3.8608



B1L102S, U2-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

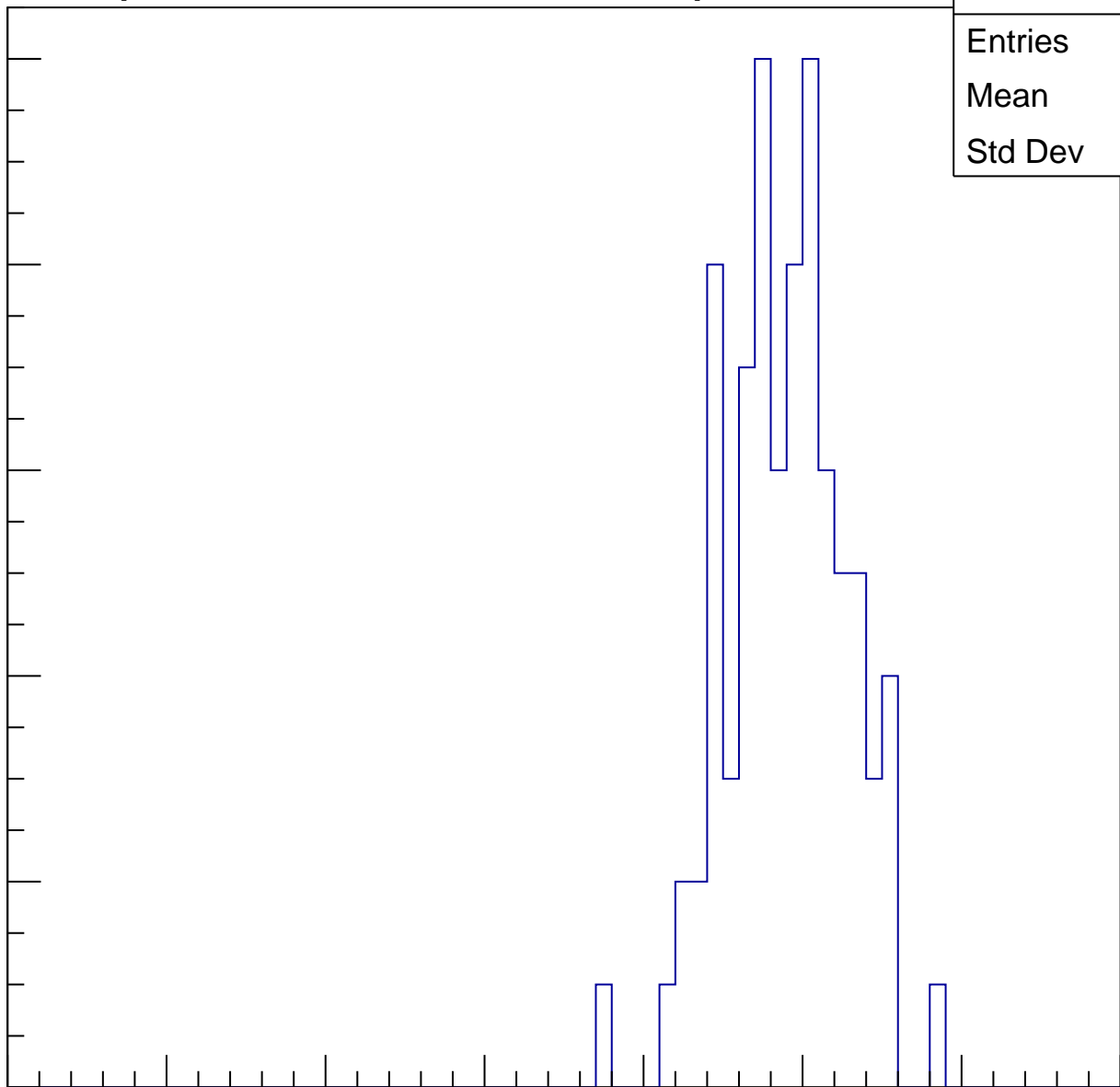
Entries	82
Mean	48.51
Std Dev	3.785

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

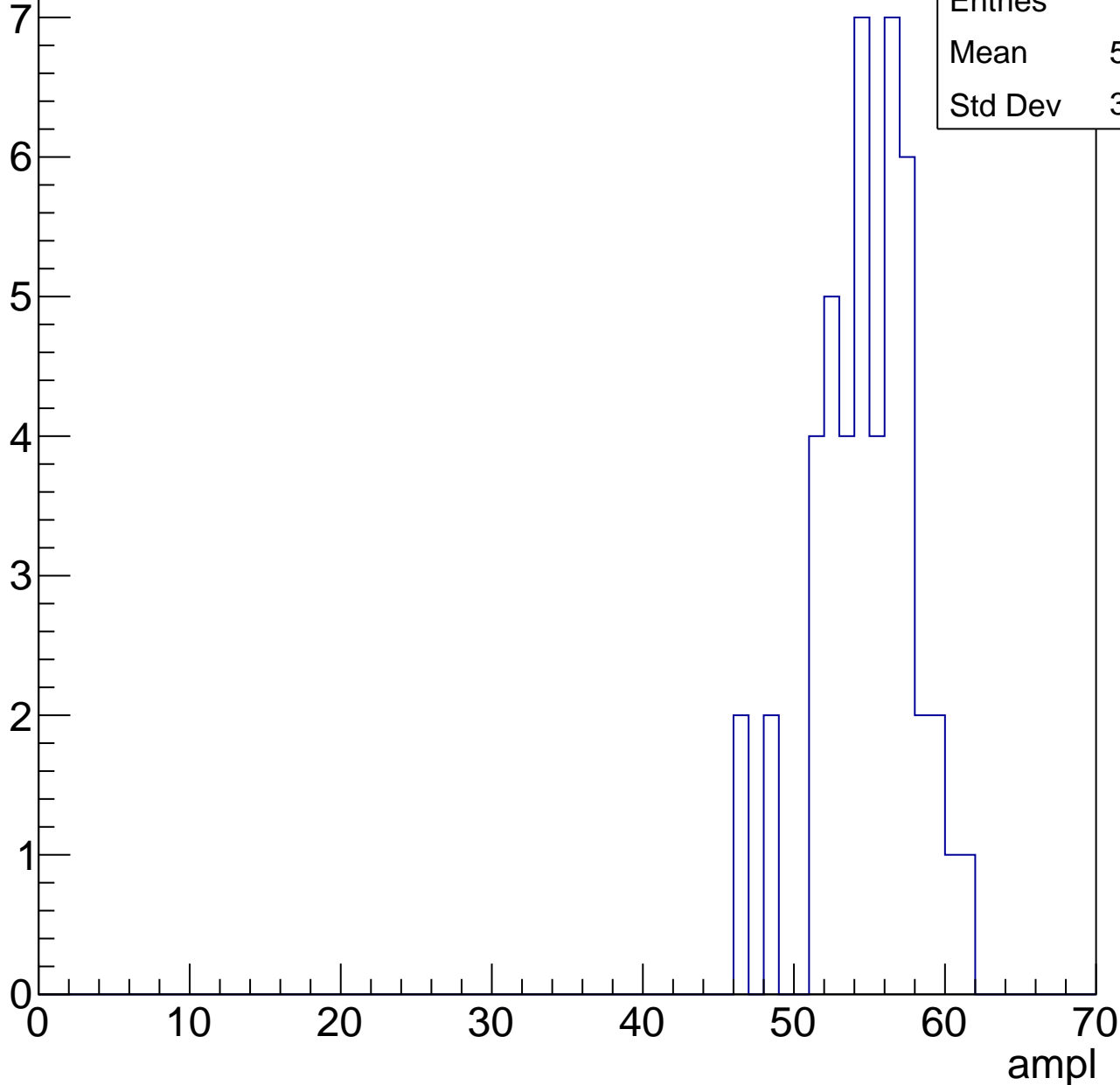
ampl



B1L102S, U2-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



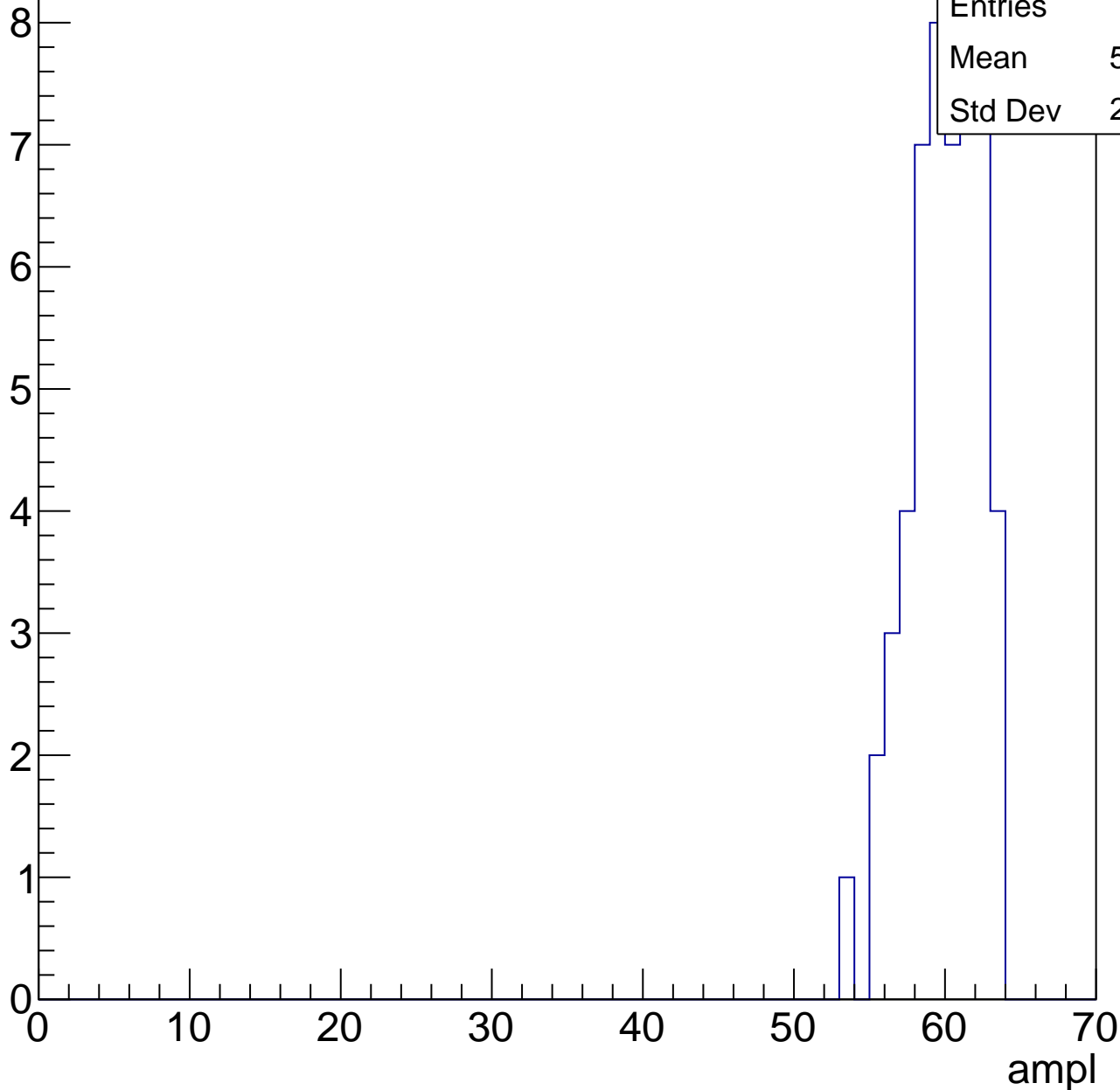
Entries	47
Mean	54.28
Std Dev	3.292

B1L102S, U2-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	59.48
Std Dev	2.325

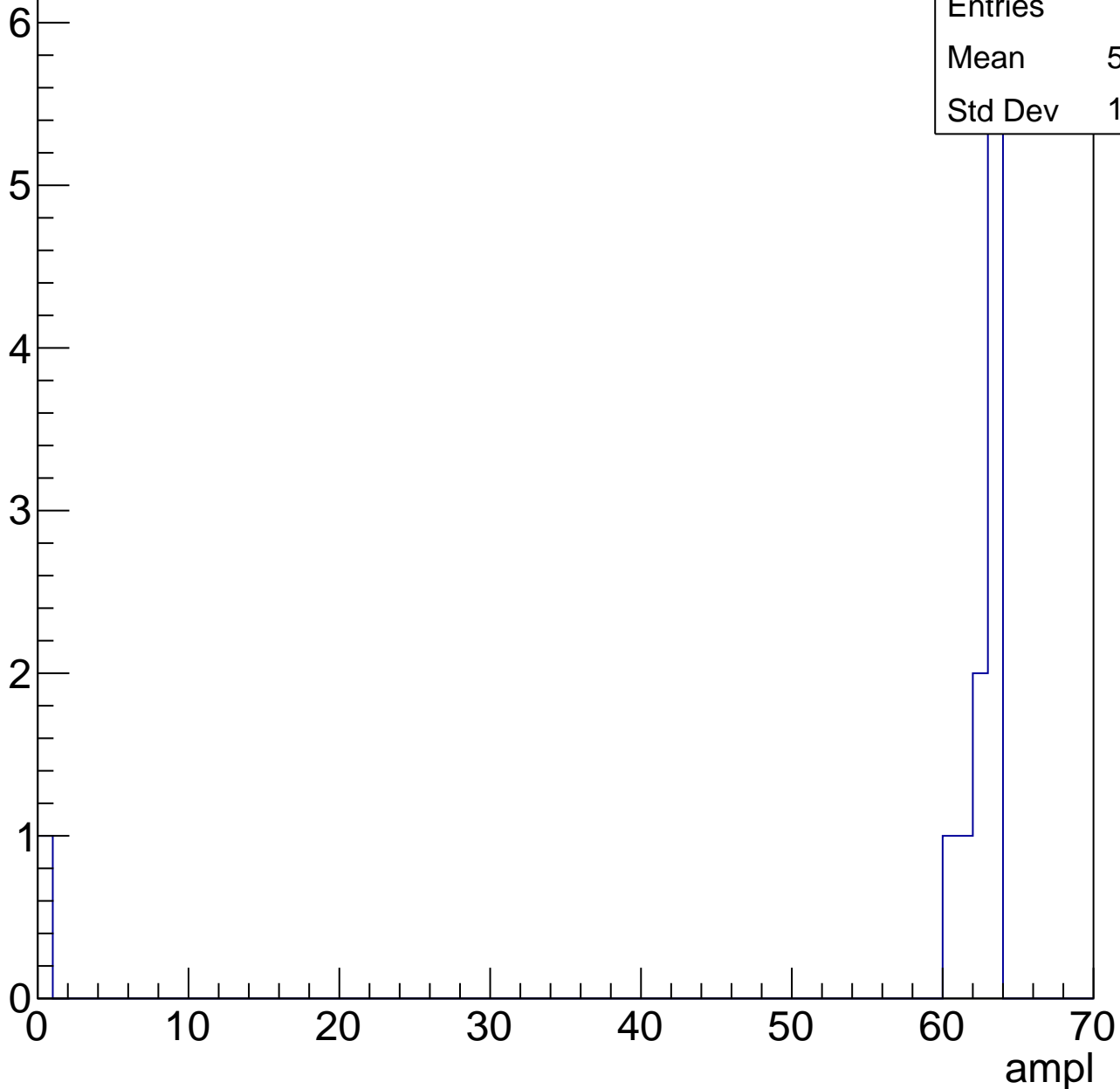


B1L102S, U2-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

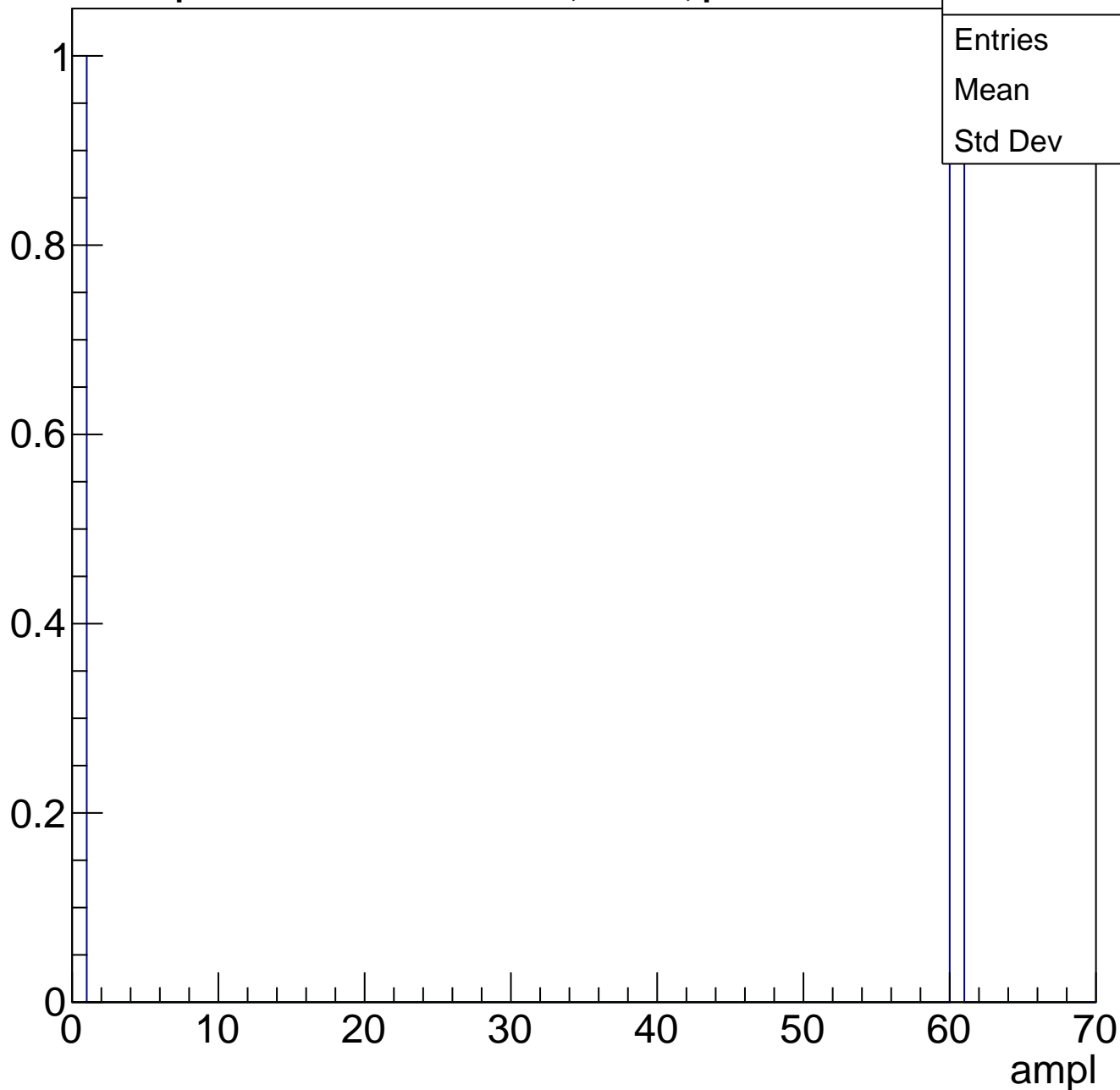
Entries	11
Mean	56.64
Std Dev	17.94



B1L102S, U2-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch7, adc0

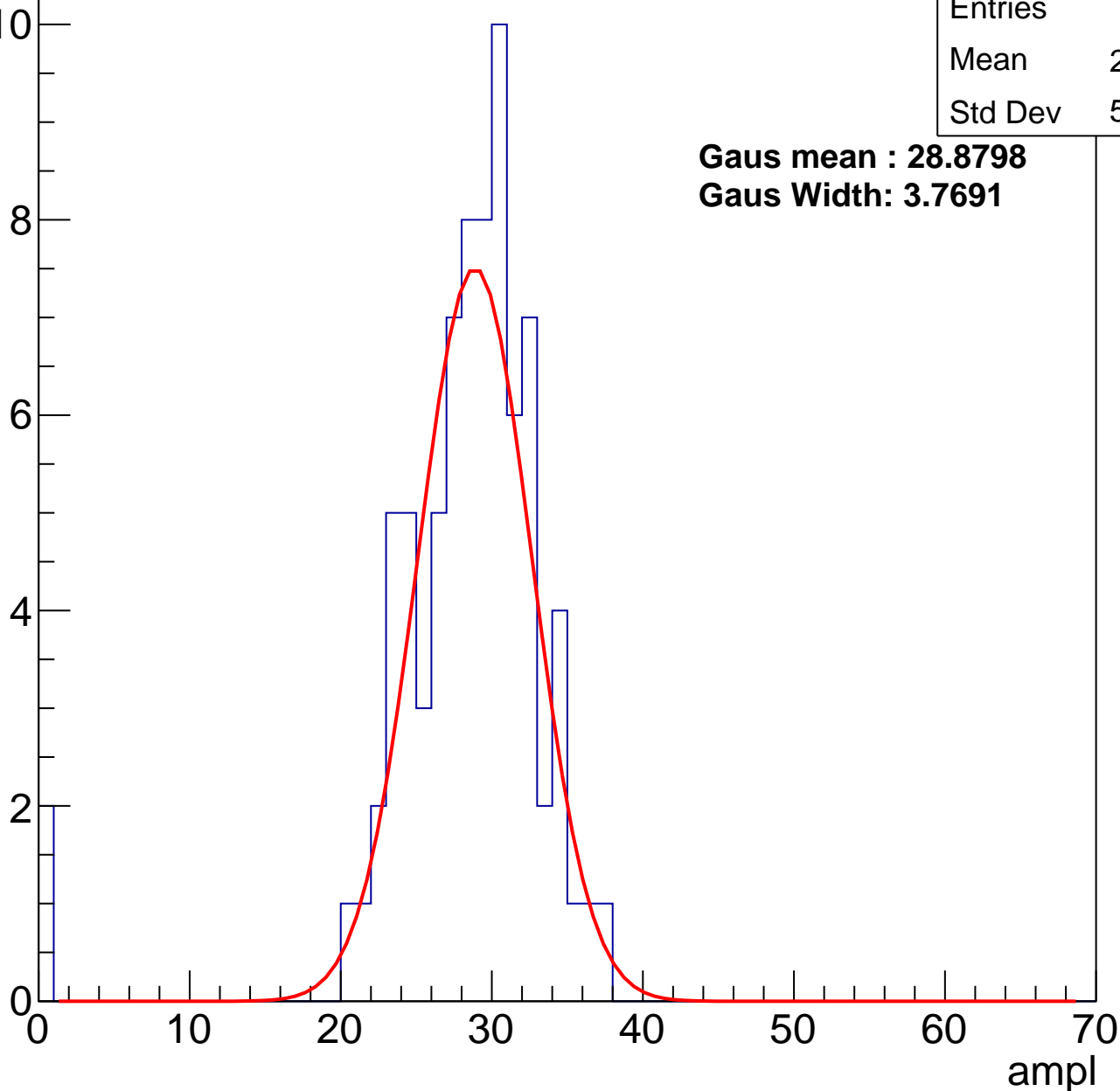
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.72
Std Dev	5.746

Gaus mean : 28.8798

Gaus Width: 3.7691



B1L102S, U2-ch7, adc1

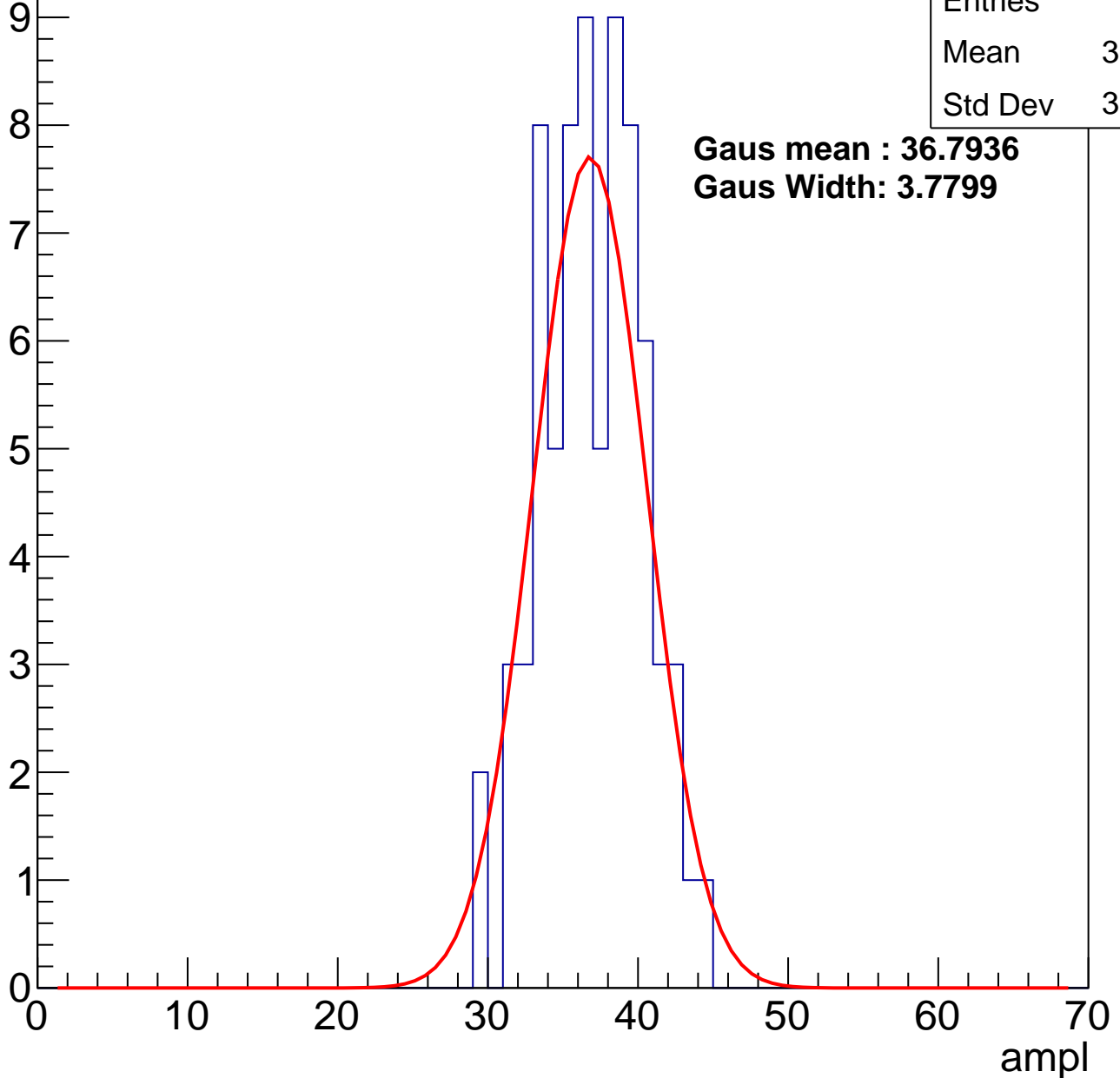
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.49
Std Dev	3.305

Gaus mean : 36.7936

Gaus Width: 3.7799



B1L102S, U2-ch7, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	43.64
Std Dev	3.677

Gaus mean : 44.3586

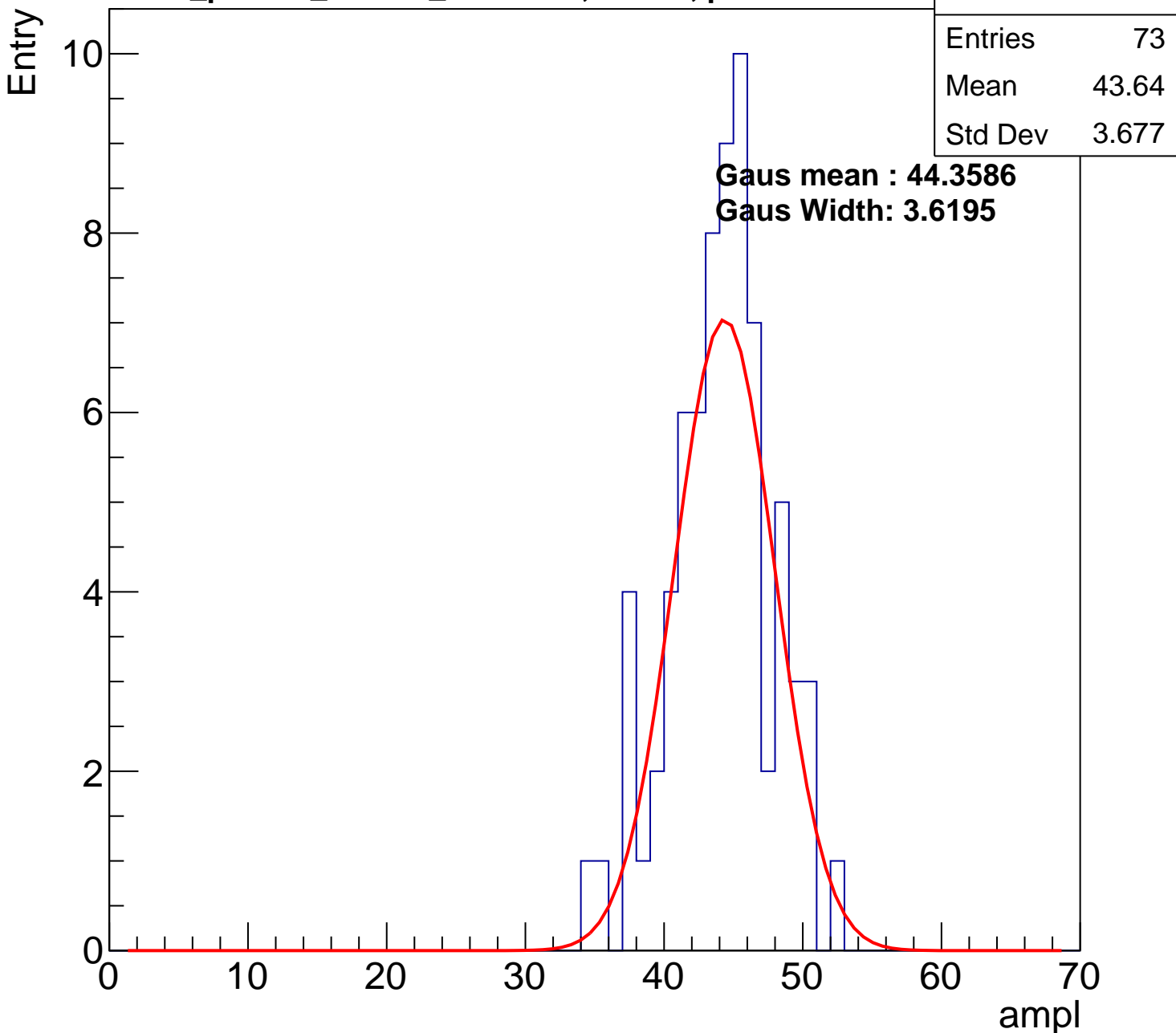
Gaus Width: 3.6195

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

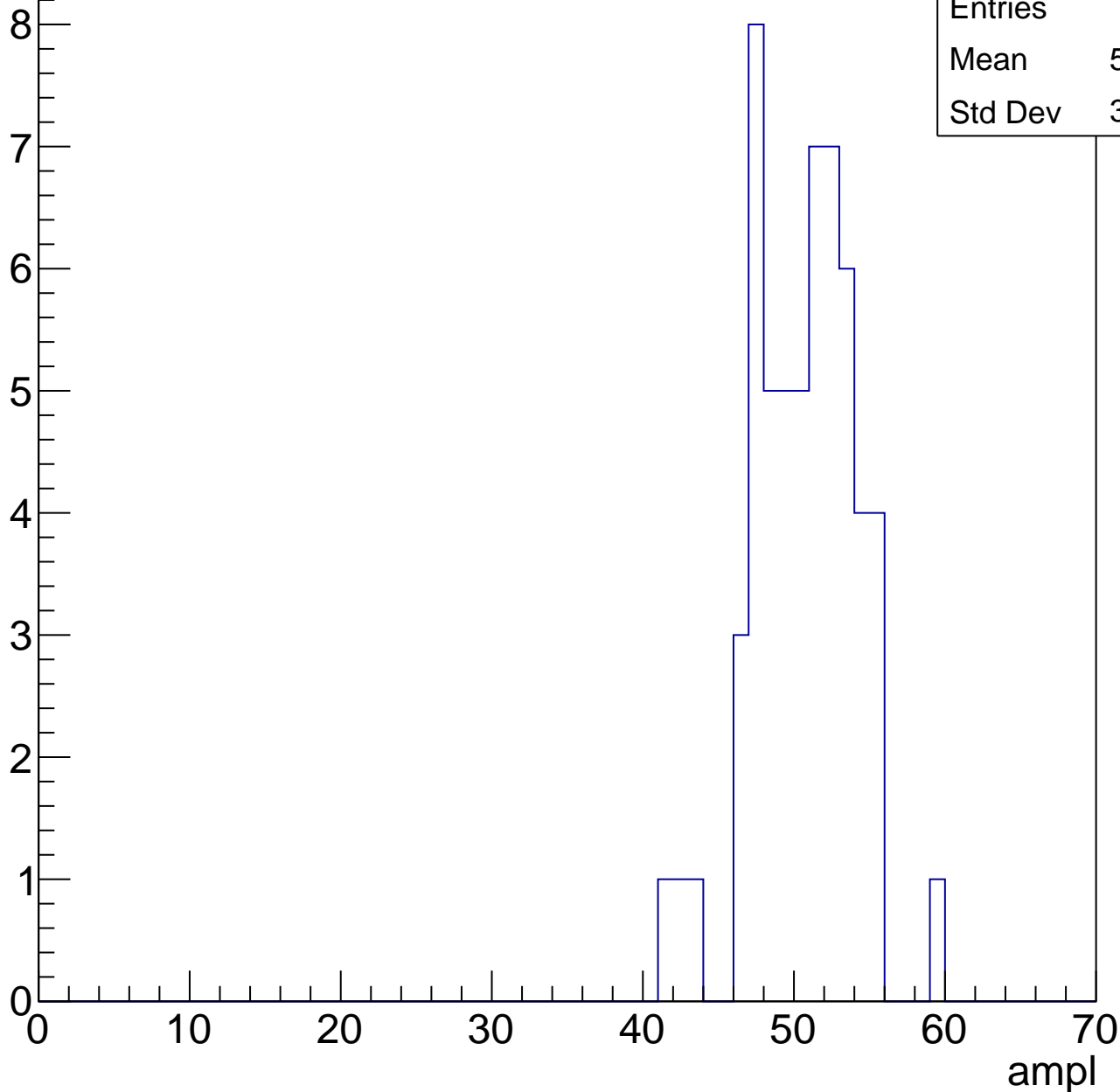


B1L102S, U2-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.16
Std Dev	3.403

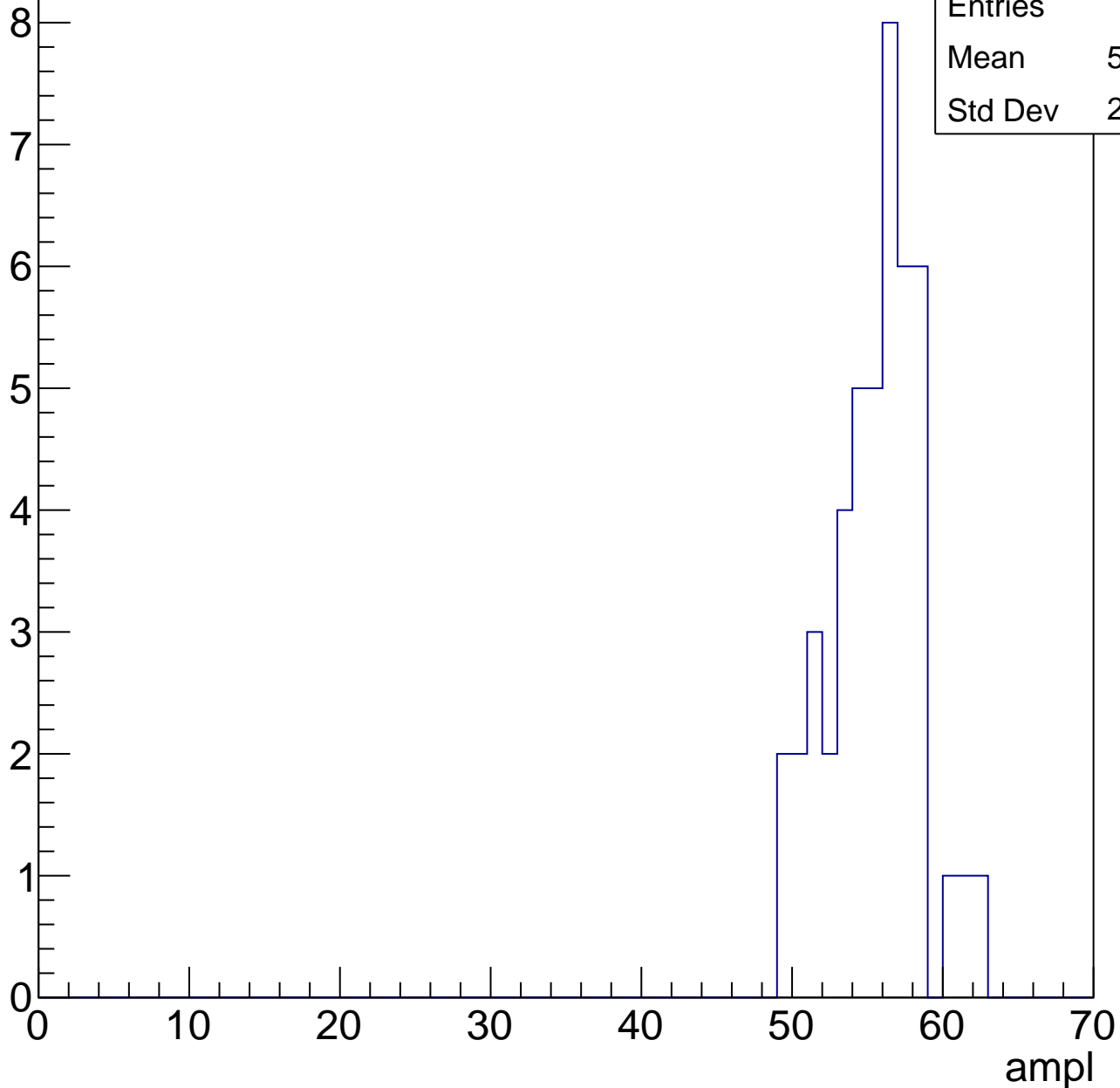


B1L102S, U2-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

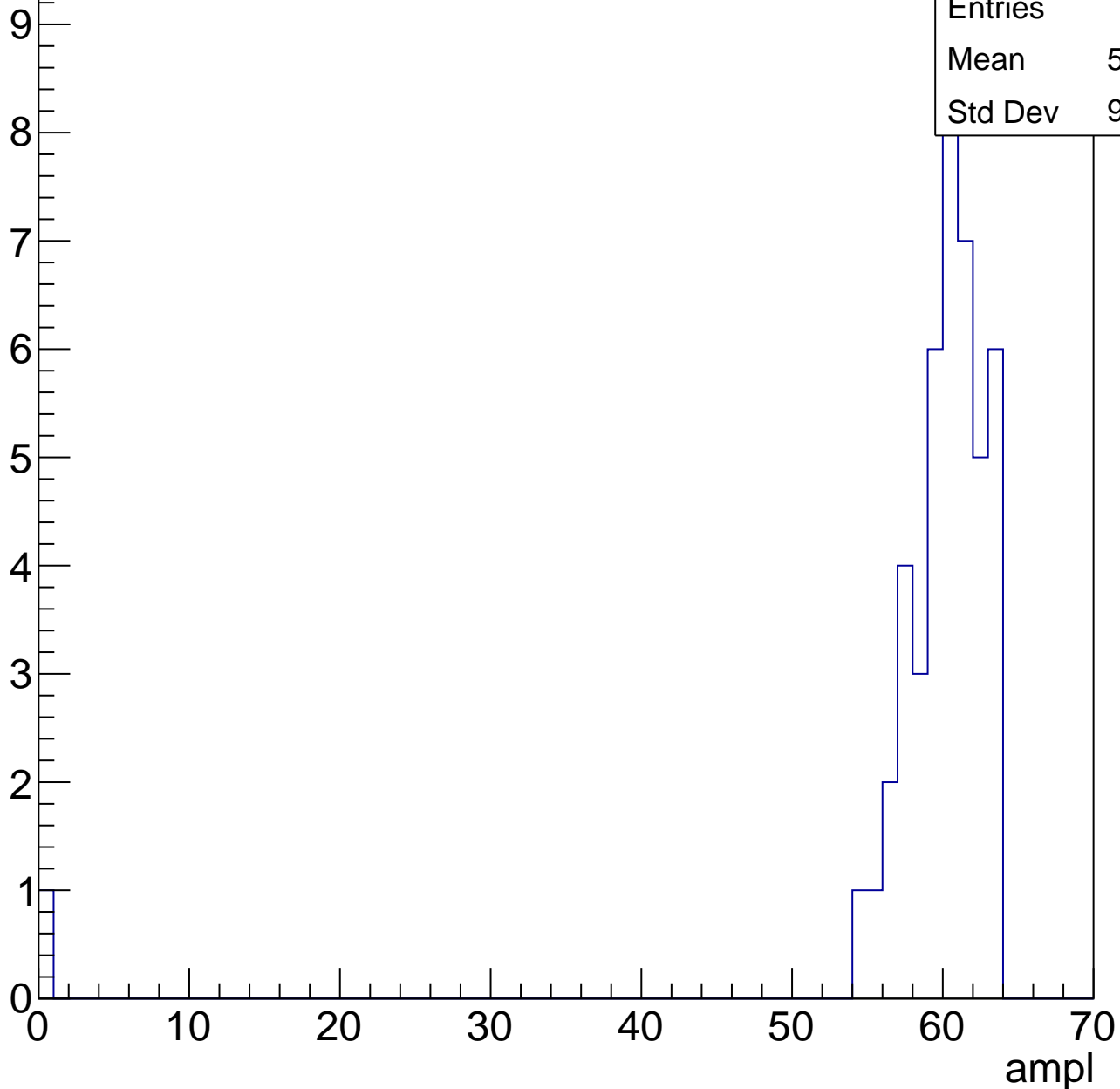
Entries	46
Mean	55.07
Std Dev	2.966



B1L102S, U2-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

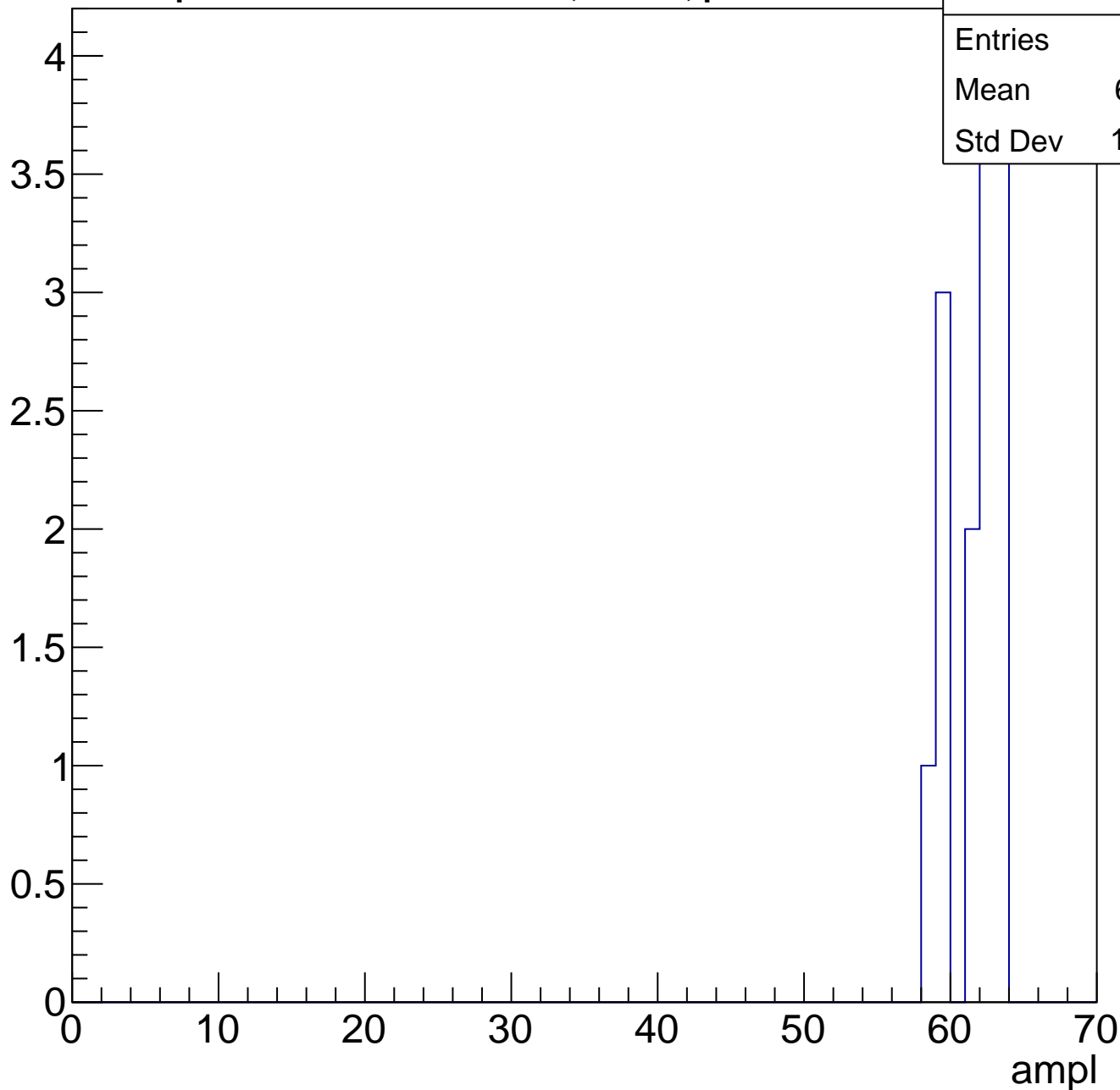
Entry



B1L102S, U2-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	14
Mean	61.21
Std Dev	1.698

B1L102S, U2-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U2-ch8, adc0

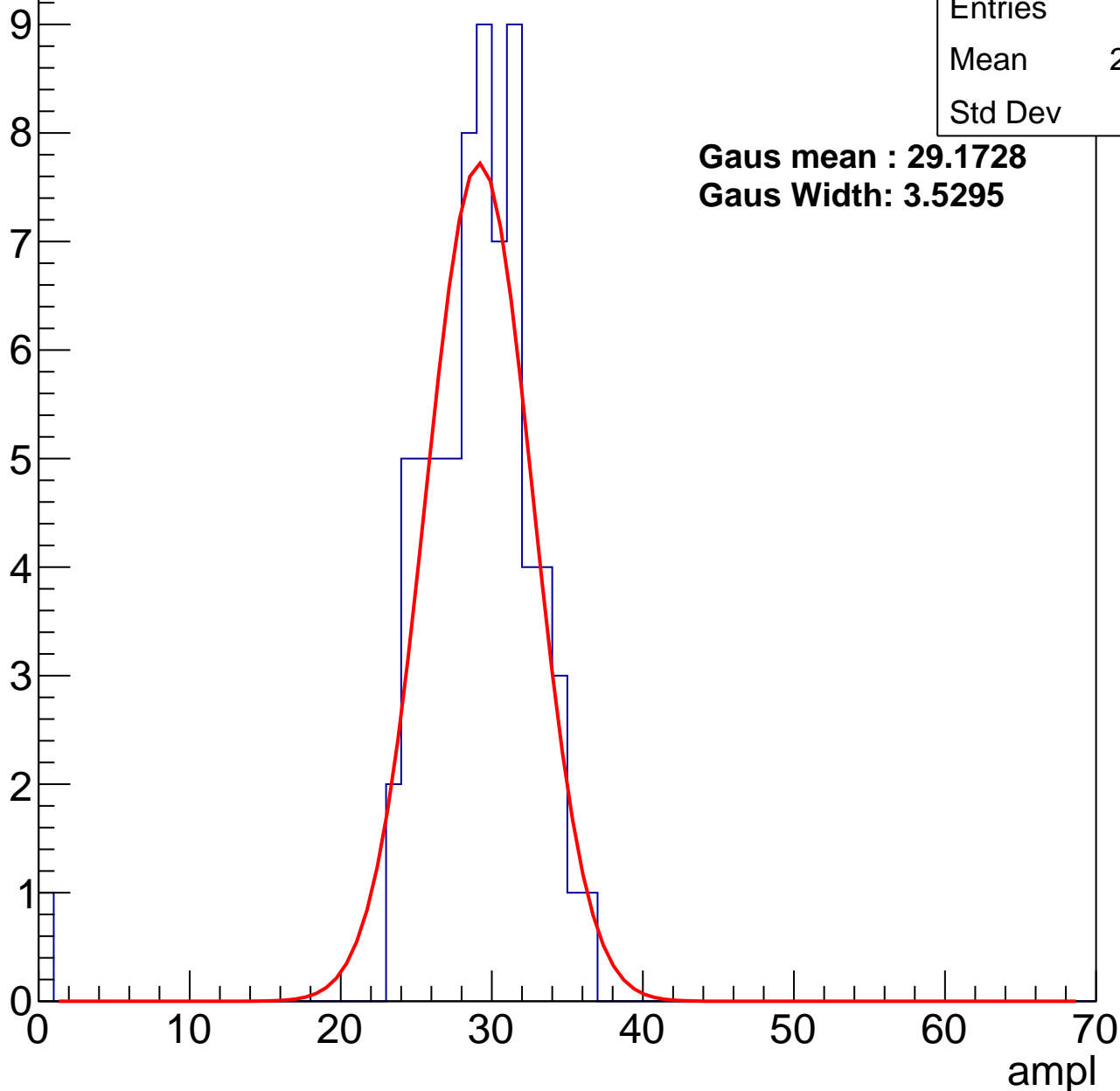
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.45
Std Dev	4.62

Gaus mean : 29.1728

Gaus Width: 3.5295



B1L102S, U2-ch8, adc1

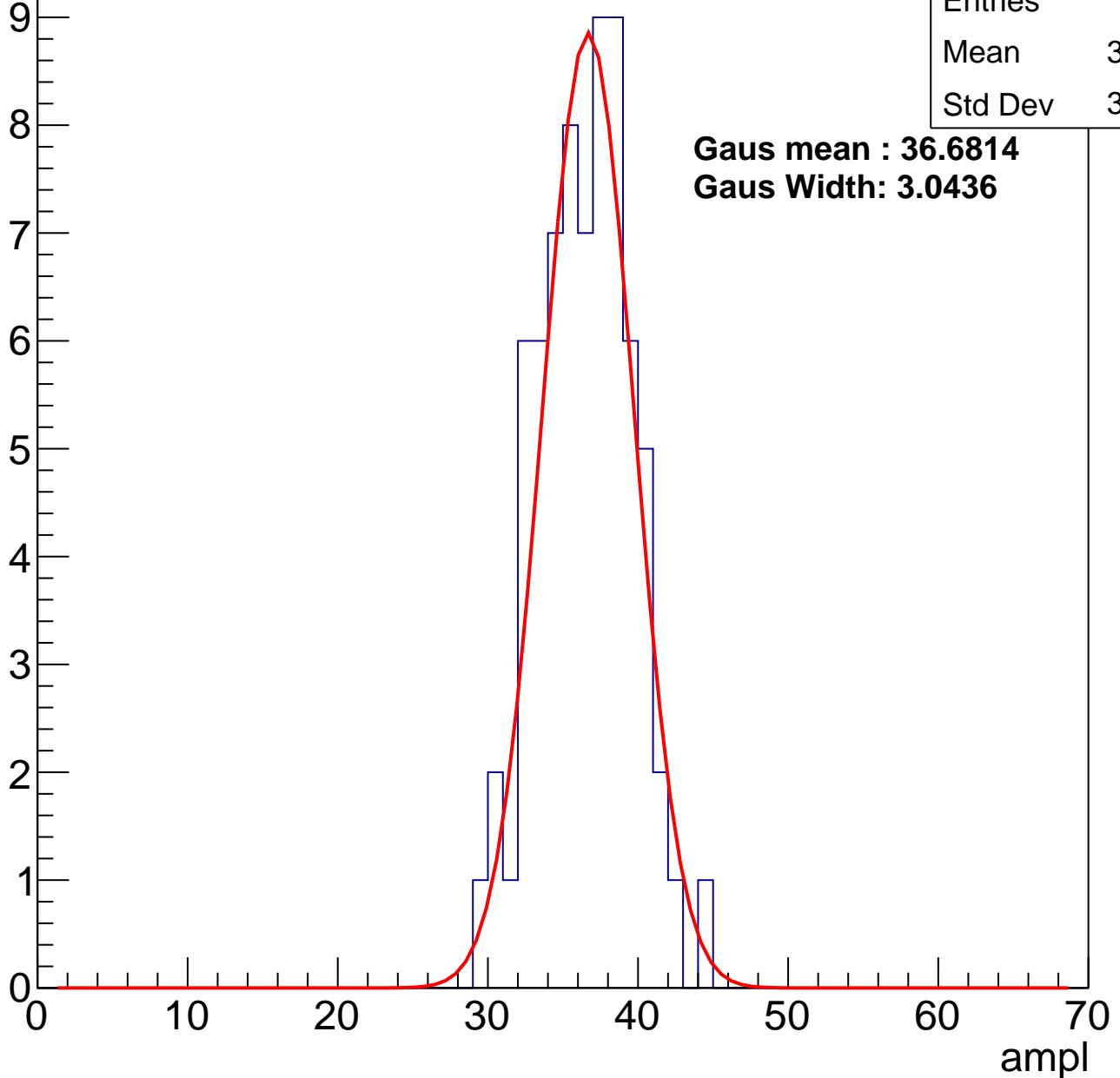
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.01
Std Dev	3.051

Gaus mean : 36.6814

Gaus Width: 3.0436



B1L102S, U2-ch8, adc2

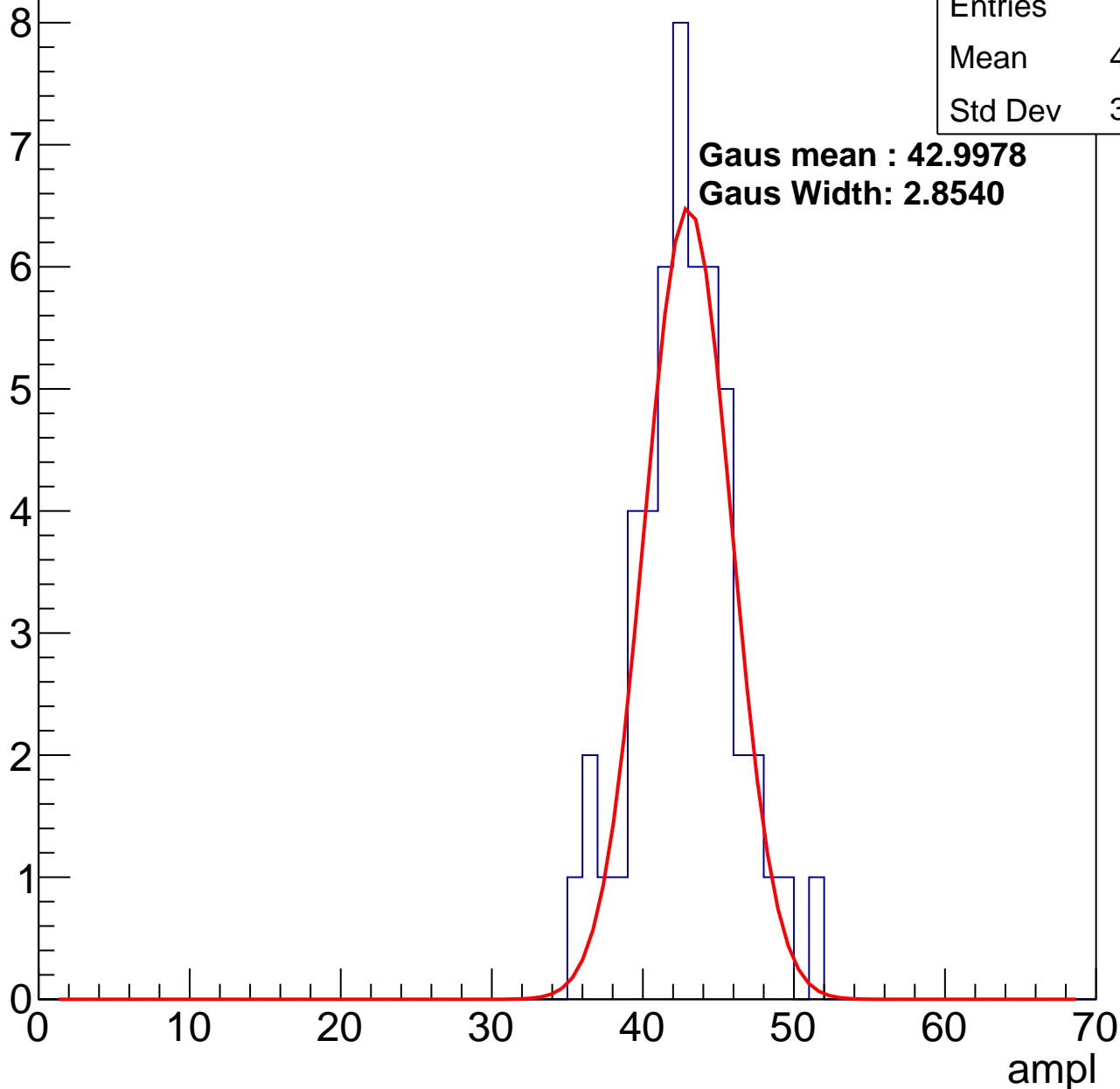
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.37
Std Dev	3.242

Gaus mean : 42.9978

Gaus Width: 2.8540

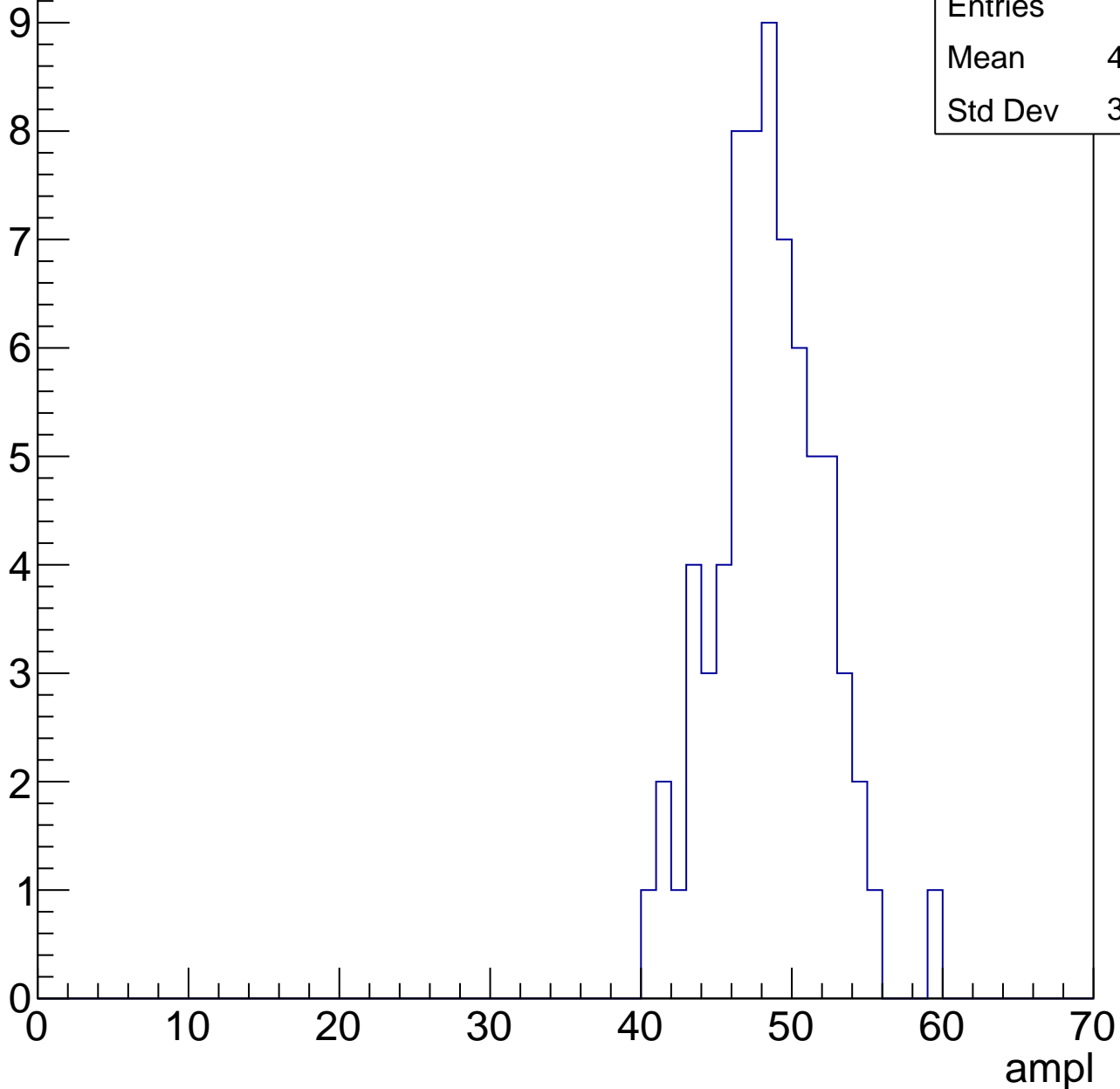


B1L102S, U2-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

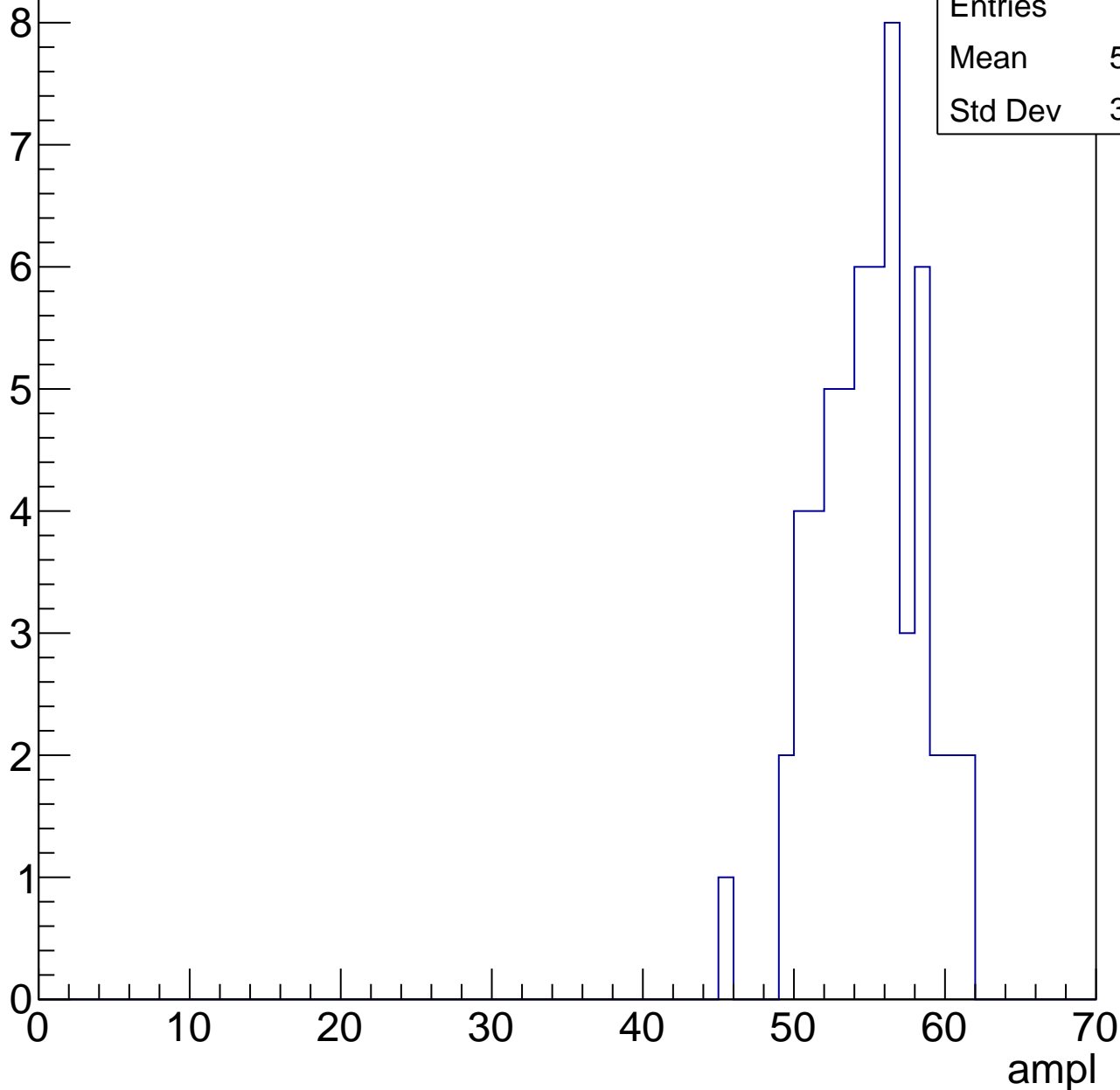
Entries	70
Mean	48.04
Std Dev	3.575



B1L102S, U2-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

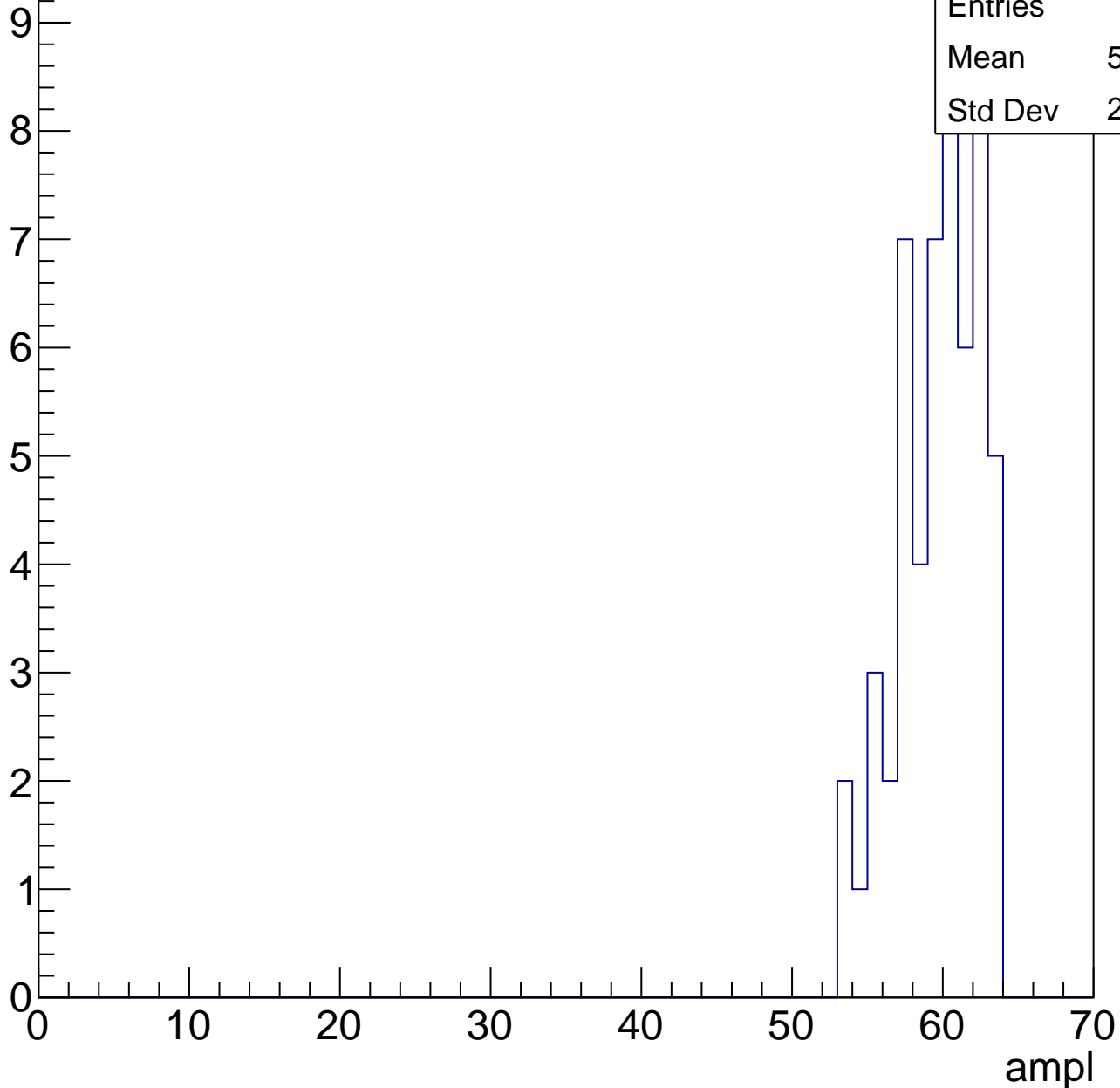


Entries	56
Mean	54.52
Std Dev	3.333

B1L102S, U2-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

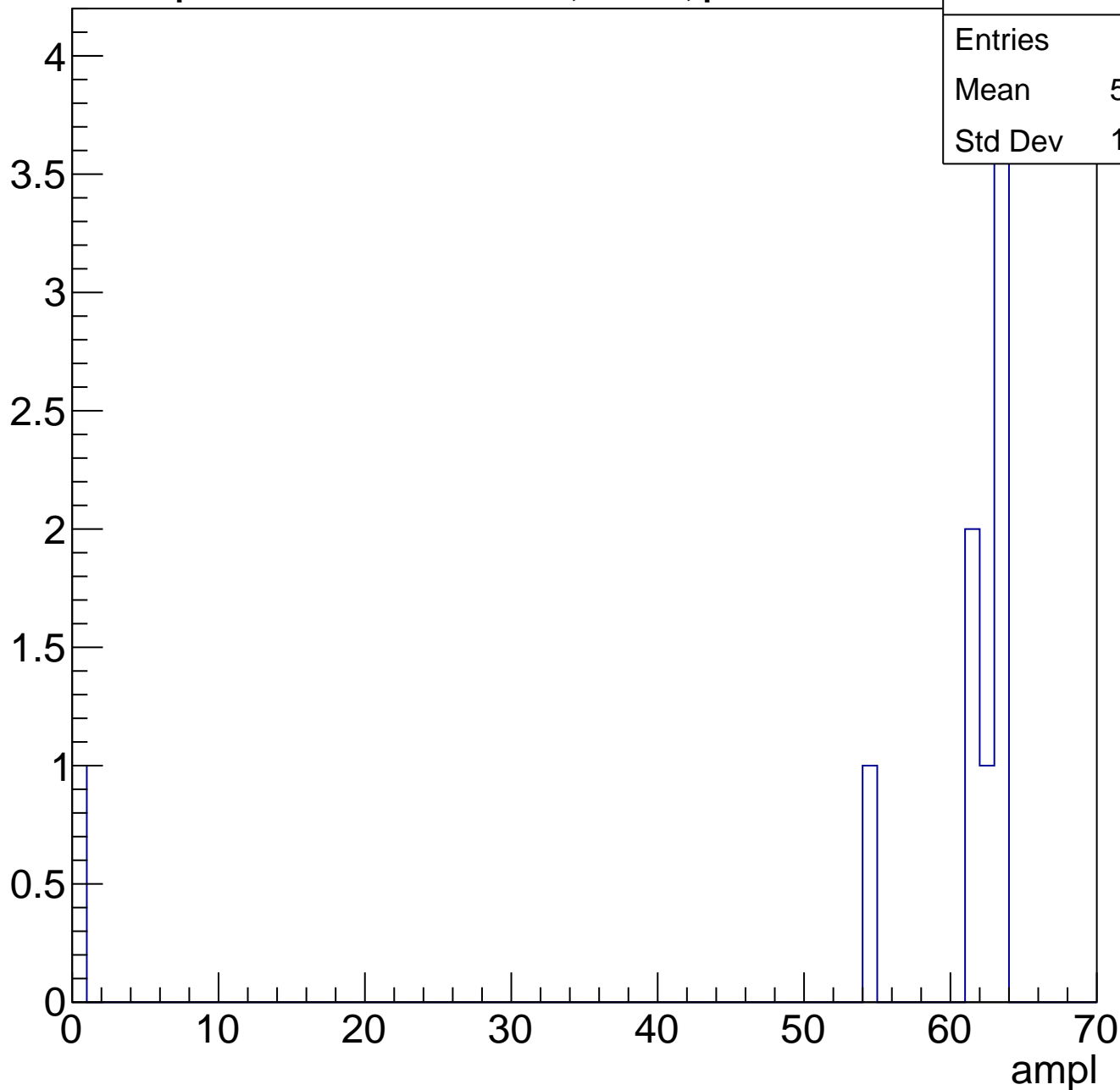
Entry



B1L102S, U2-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

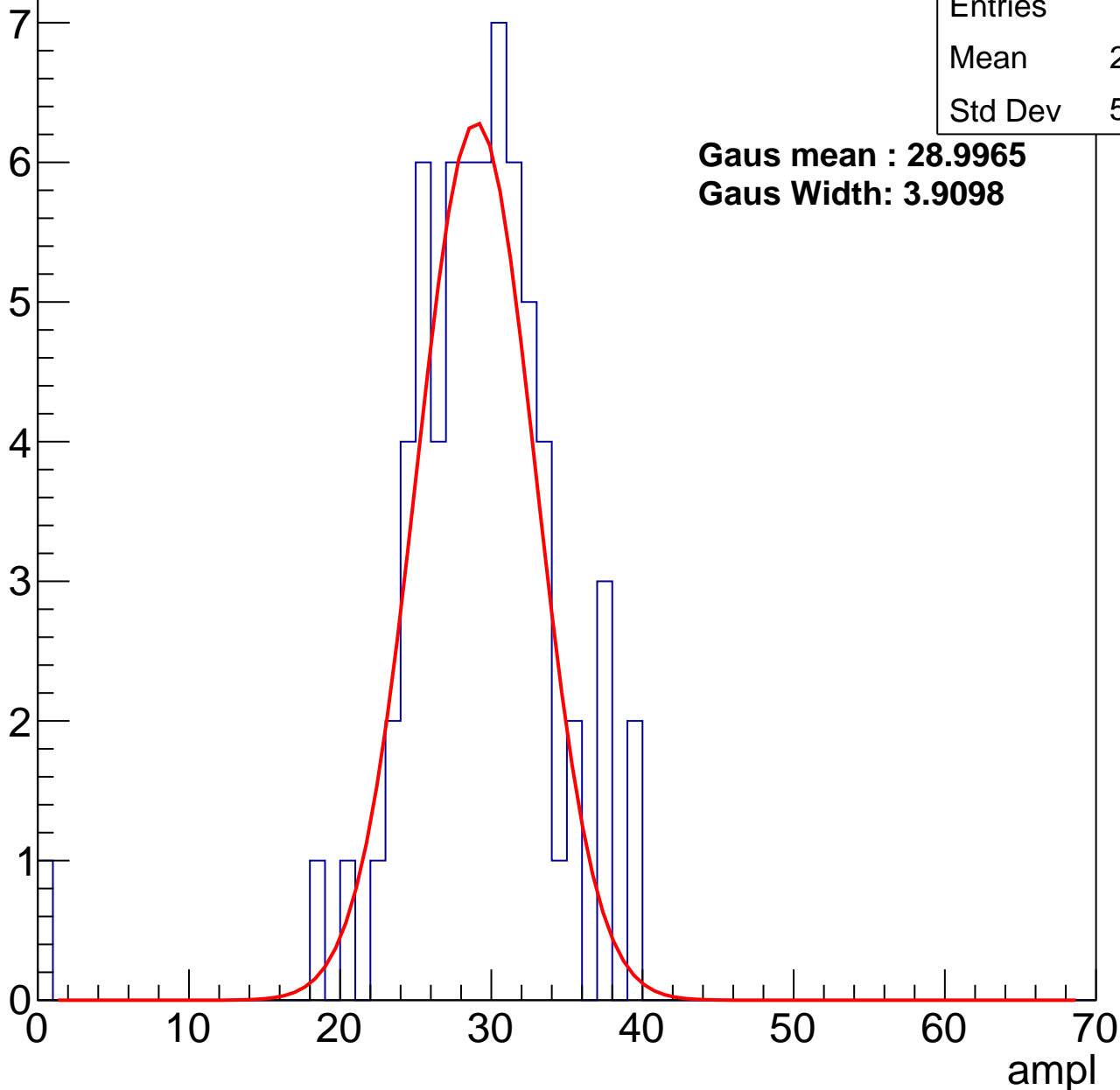
B1L102S, U2-ch9, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.54
Std Dev	5.478

Gaus mean : 28.9965
Gaus Width: 3.9098



B1L102S, U2-ch9, adc1

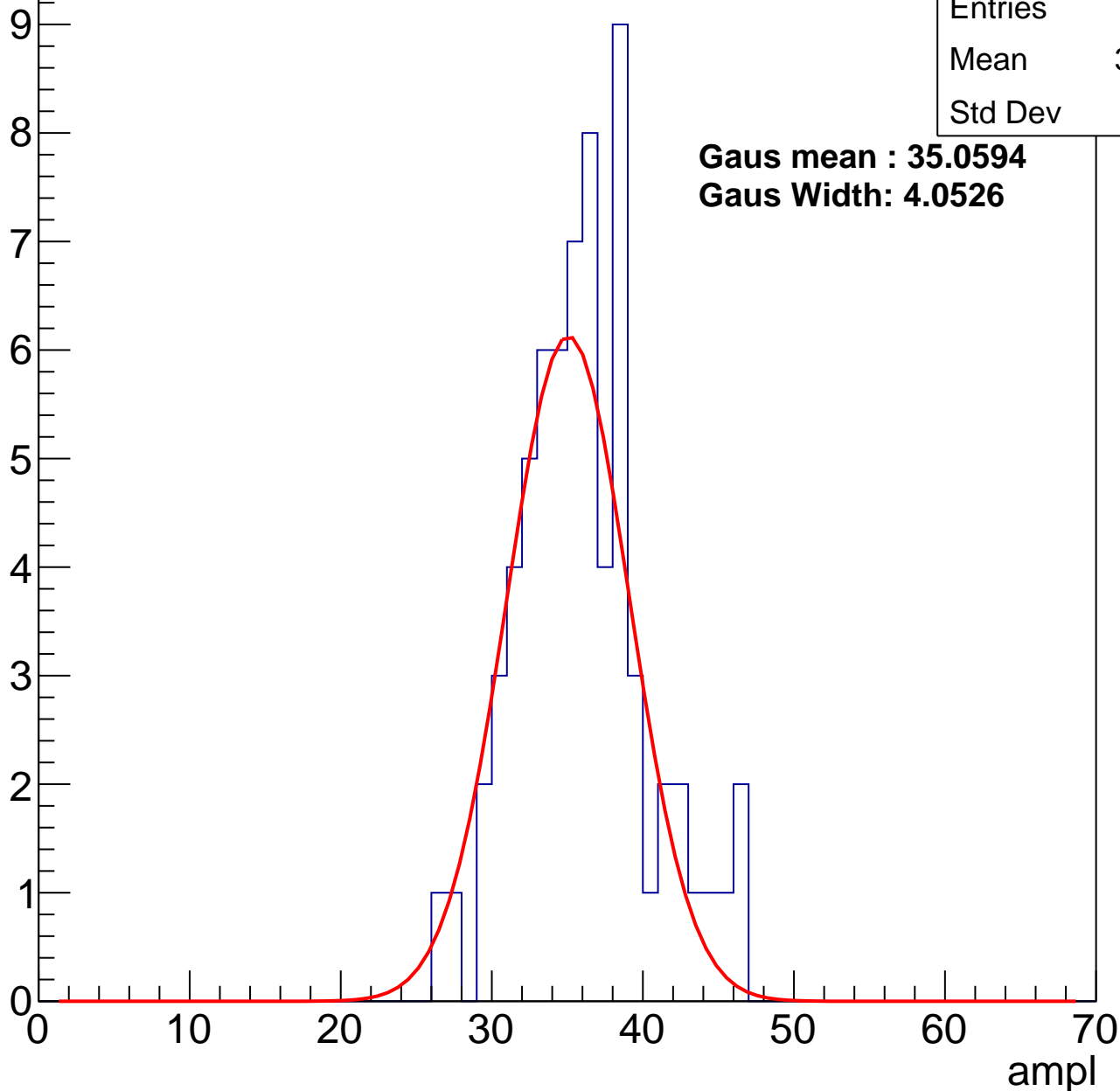
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.61
Std Dev	4.24

Gaus mean : 35.0594

Gaus Width: 4.0526



B1L102S, U2-ch9, adc2

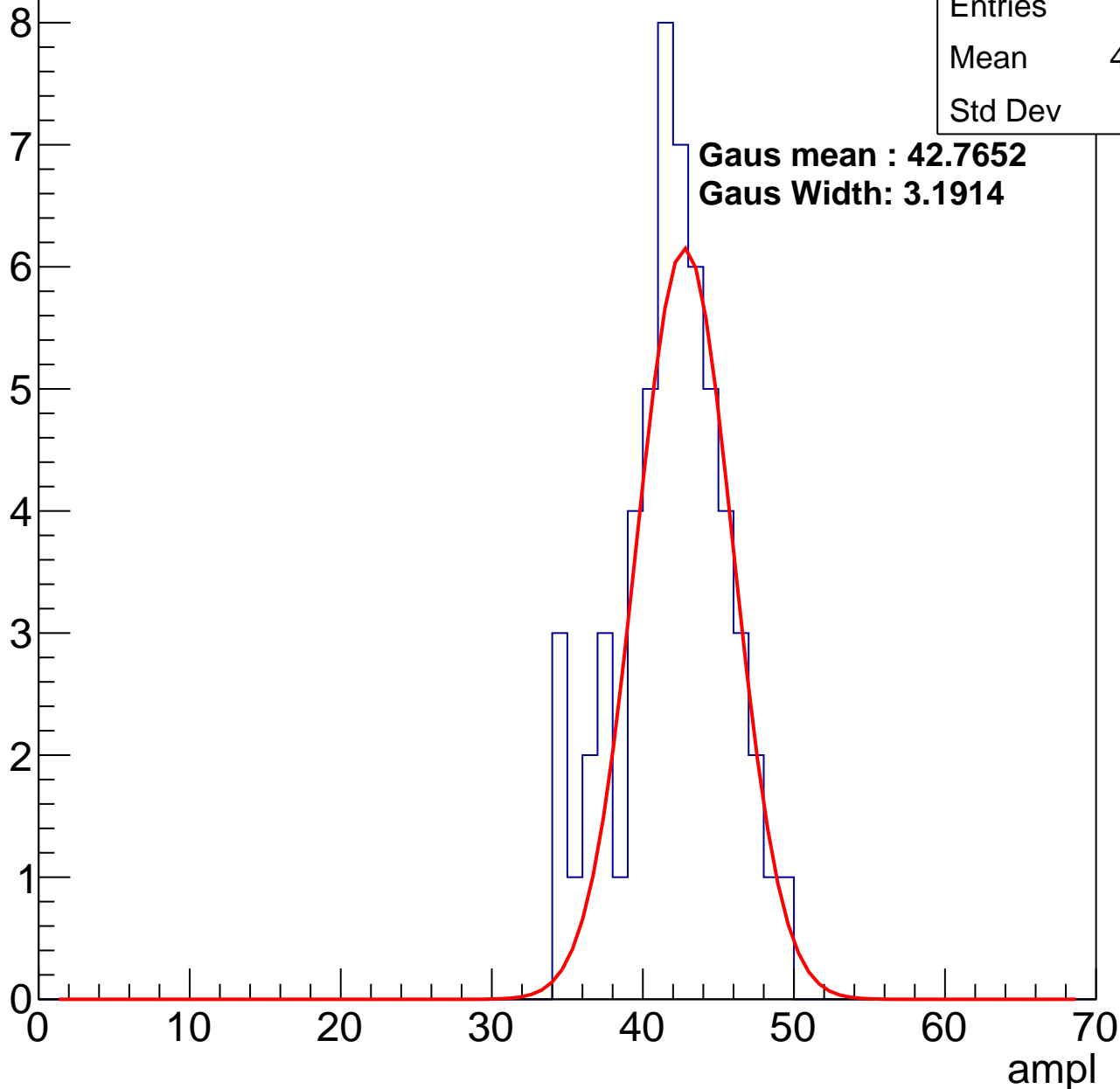
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	41.48
Std Dev	3.53

Gaus mean : 42.7652

Gaus Width: 3.1914

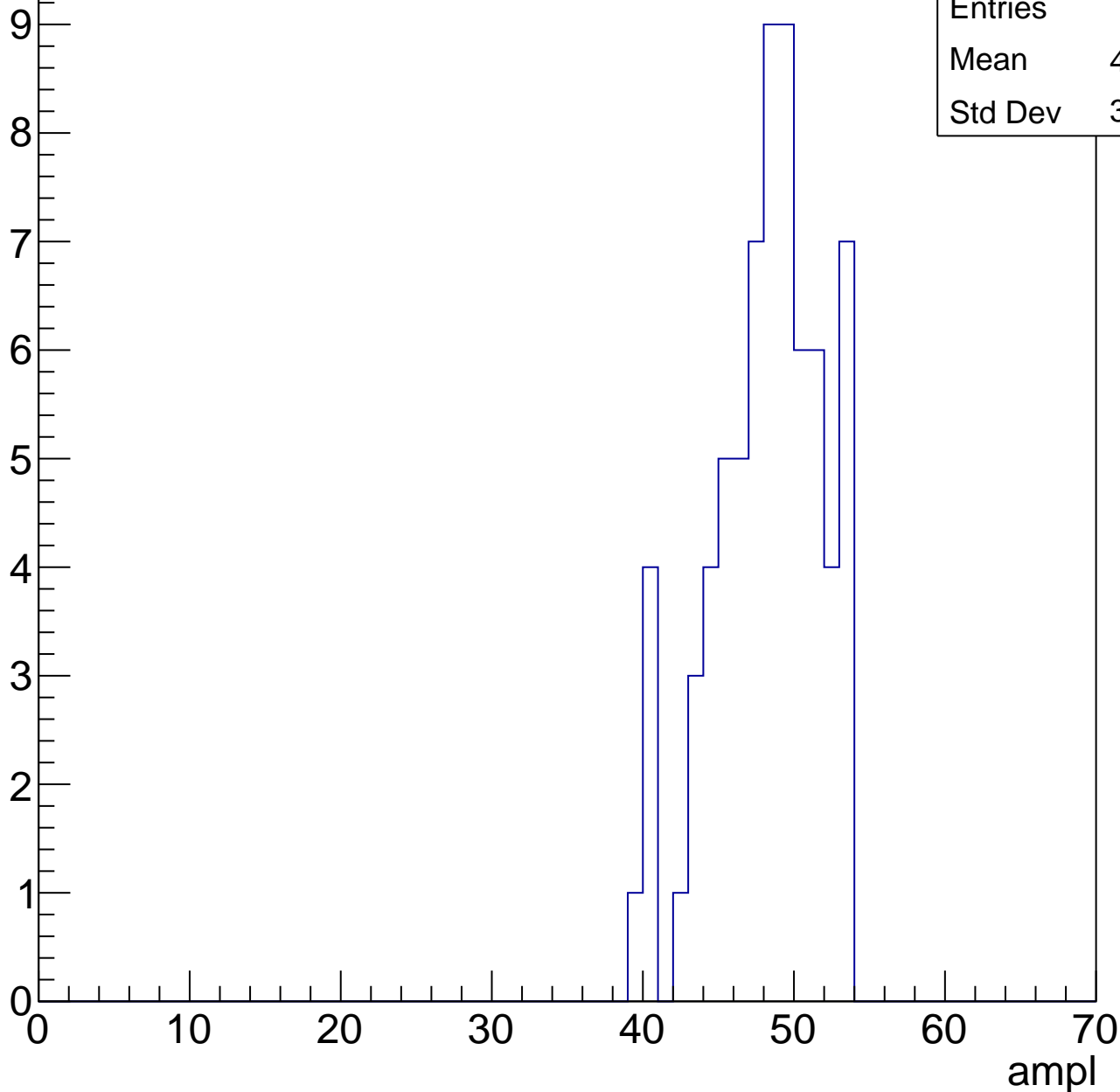


B1L102S, U2-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	47.72
Std Dev	3.569

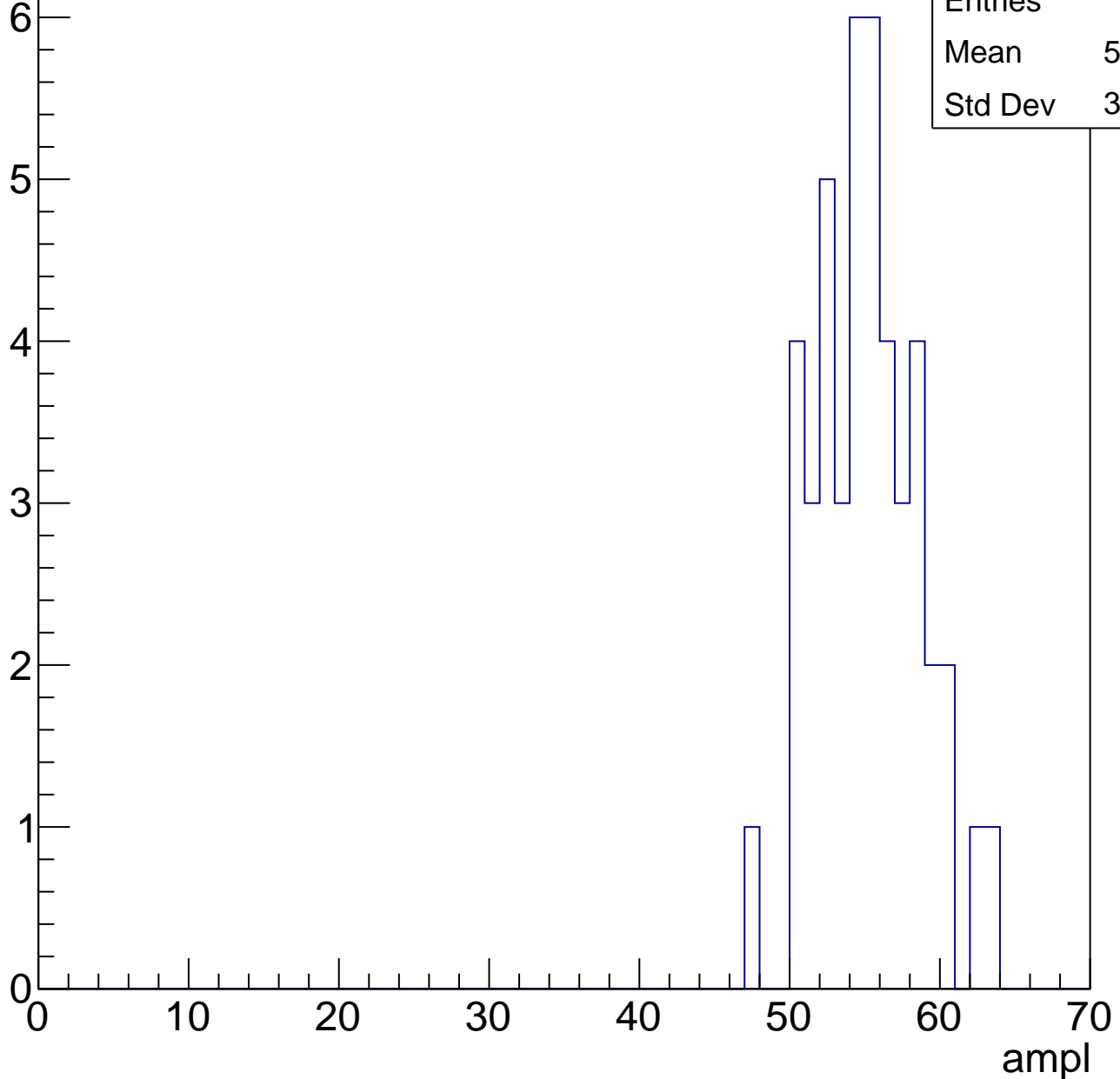


B1L102S, U2-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

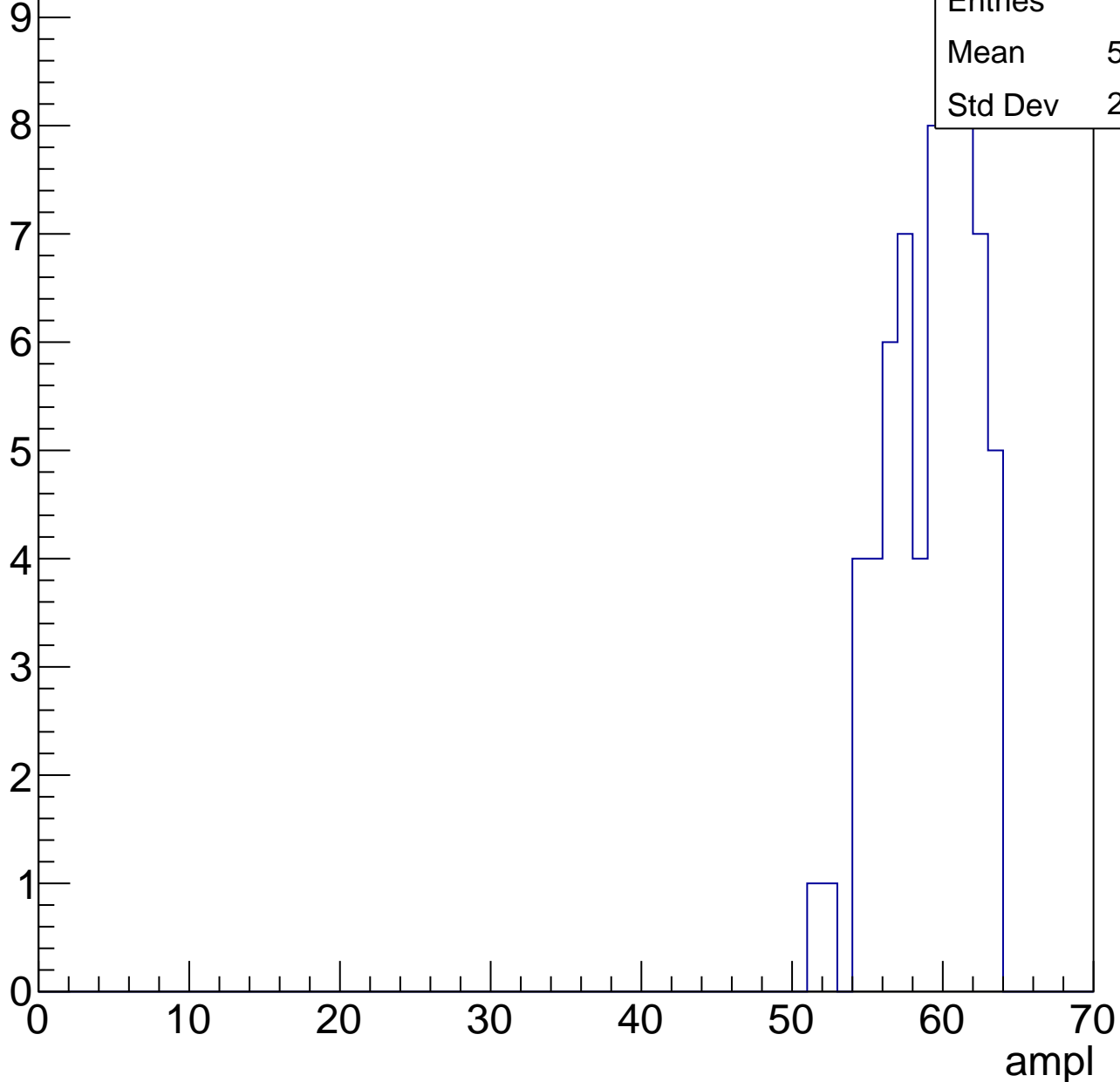
Entries	45
Mean	54.73
Std Dev	3.395



B1L102S, U2-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

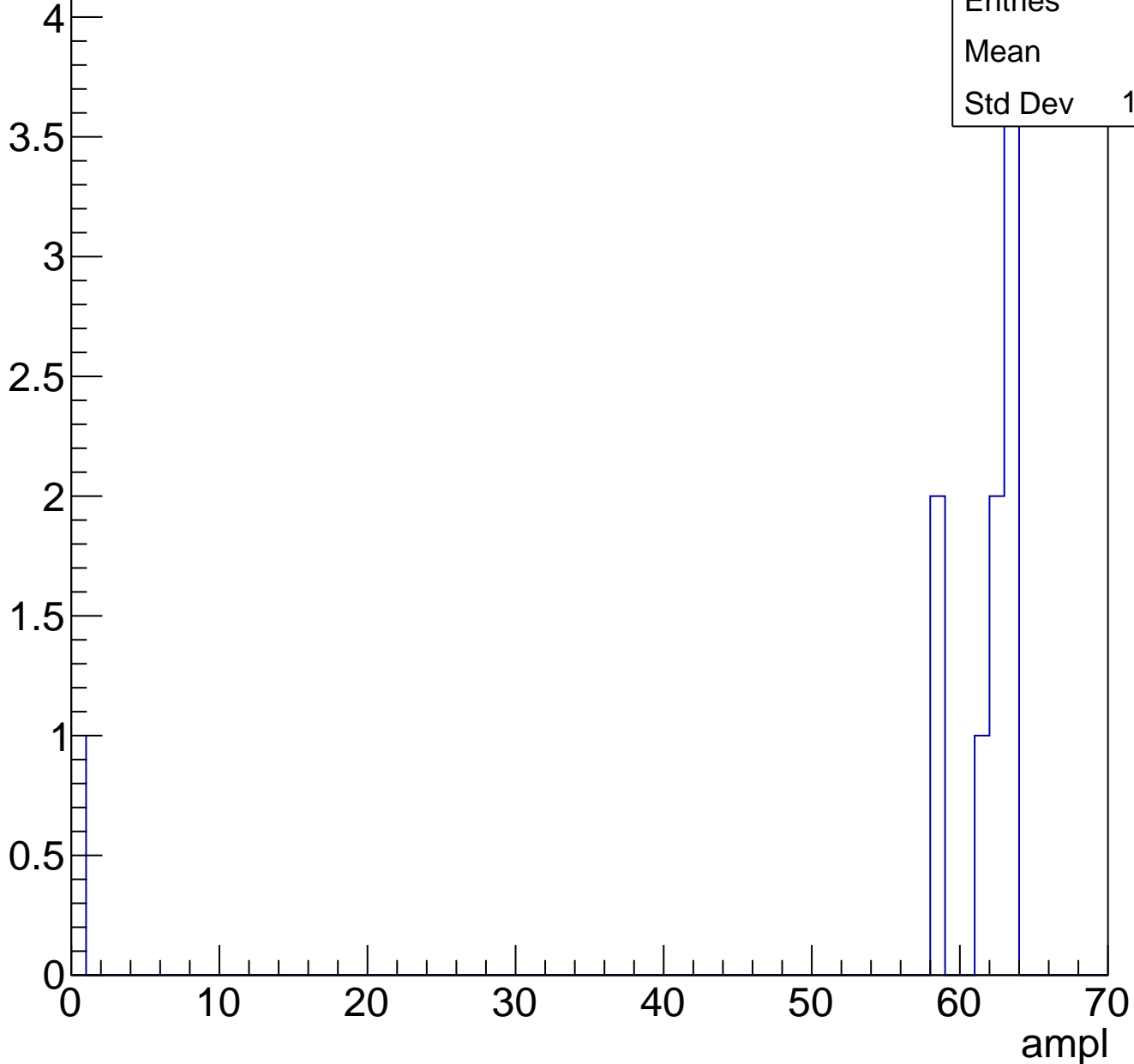
Entry



B1L102S, U2-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch10, adc0

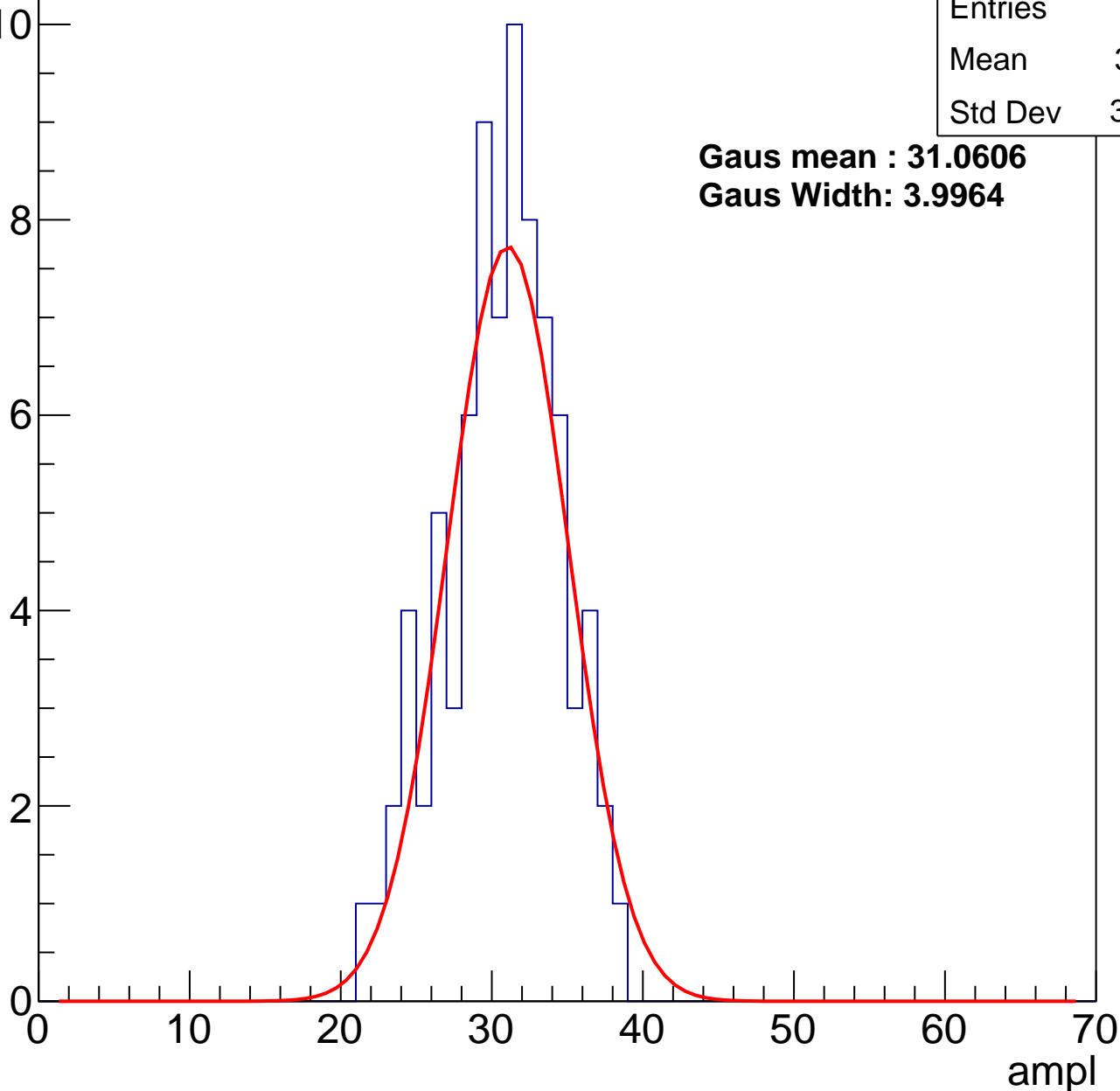
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	30.21
Std Dev	3.777

Gaus mean : 31.0606

Gaus Width: 3.9964



B1L102S, U2-ch10, adc1

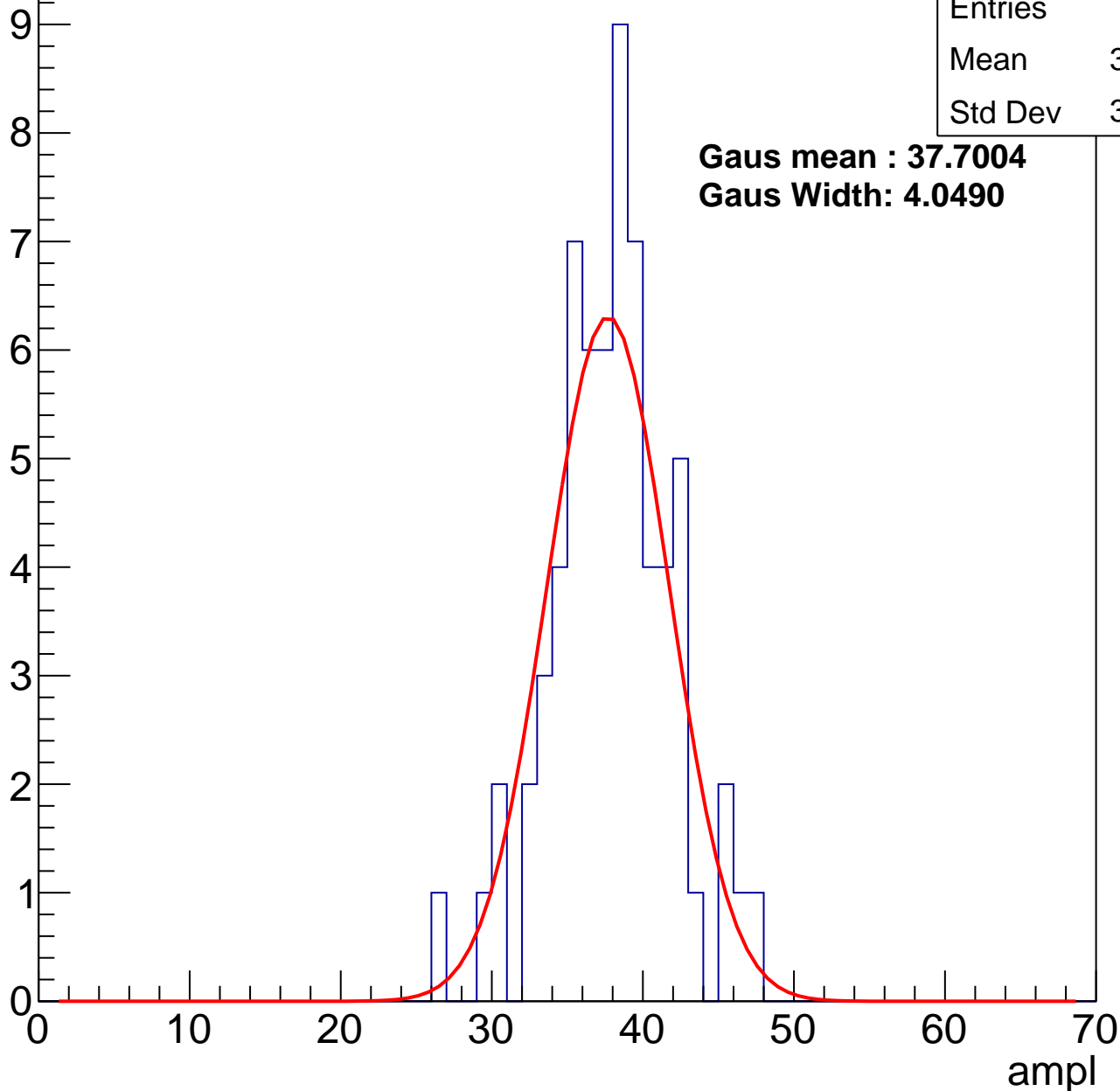
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	37.45
Std Dev	3.978

Gaus mean : 37.7004

Gaus Width: 4.0490



B1L102S, U2-ch10, adc2

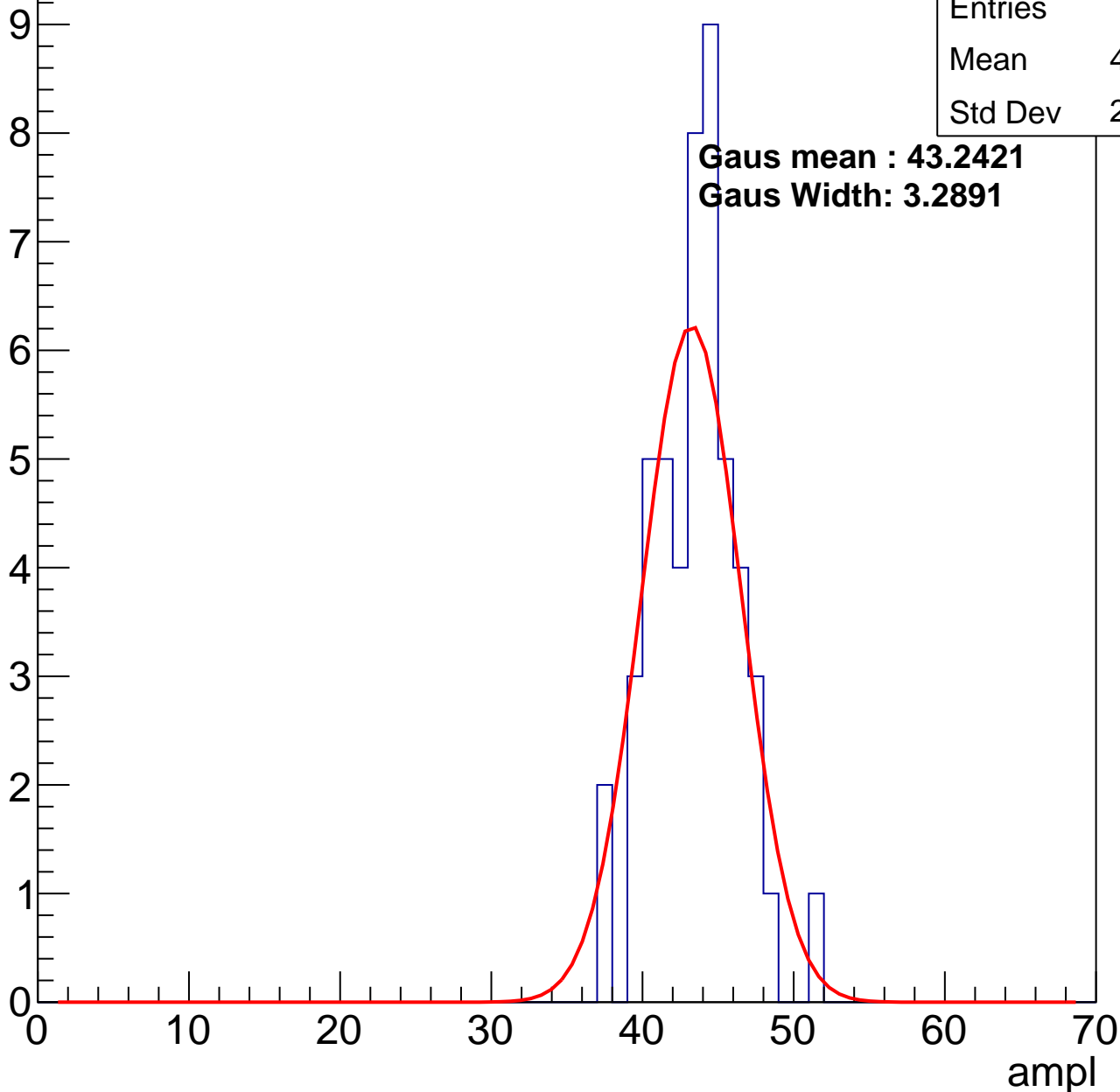
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.06
Std Dev	2.796

Gaus mean : 43.2421

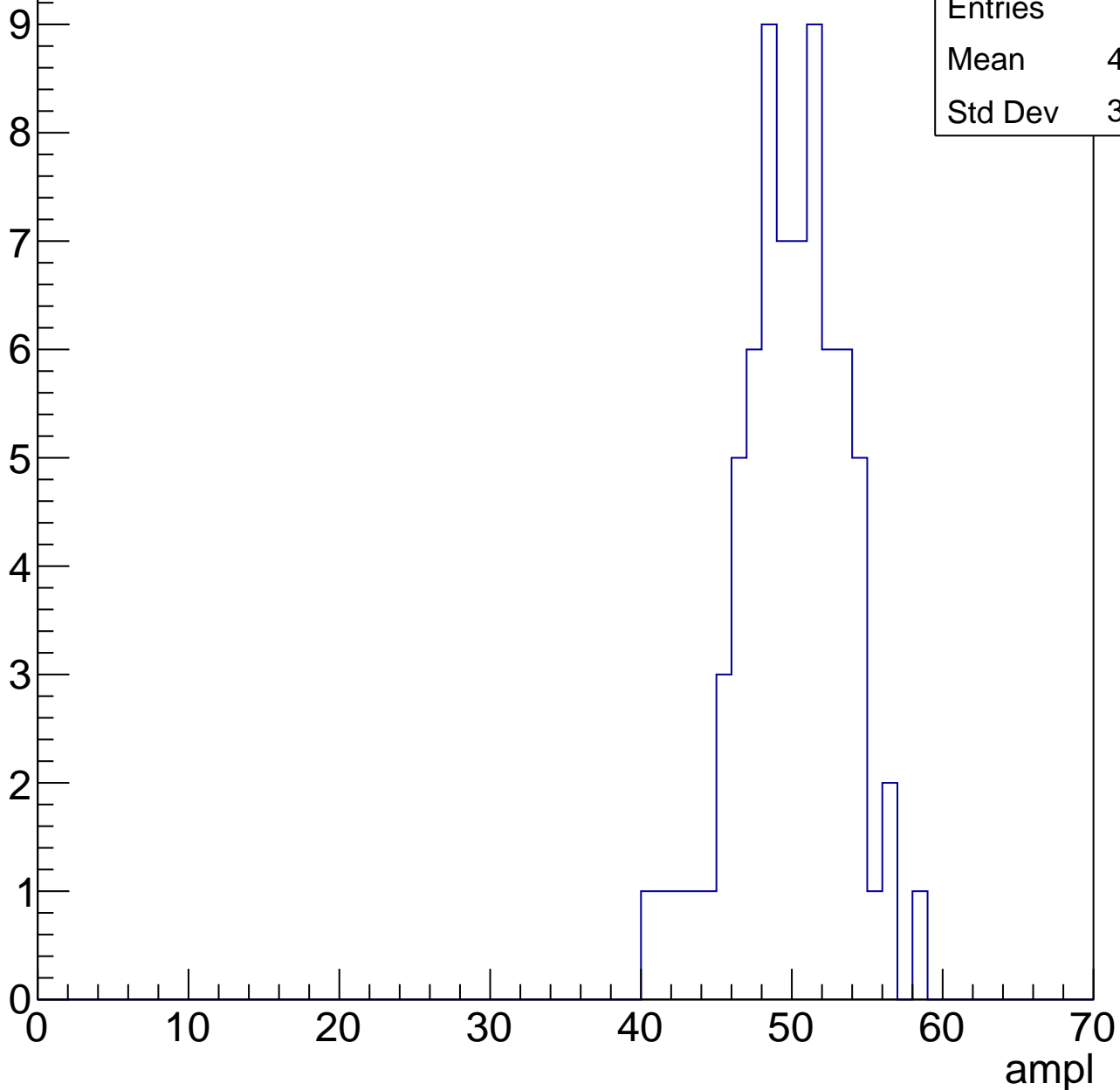
Gaus Width: 3.2891



B1L102S, U2-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

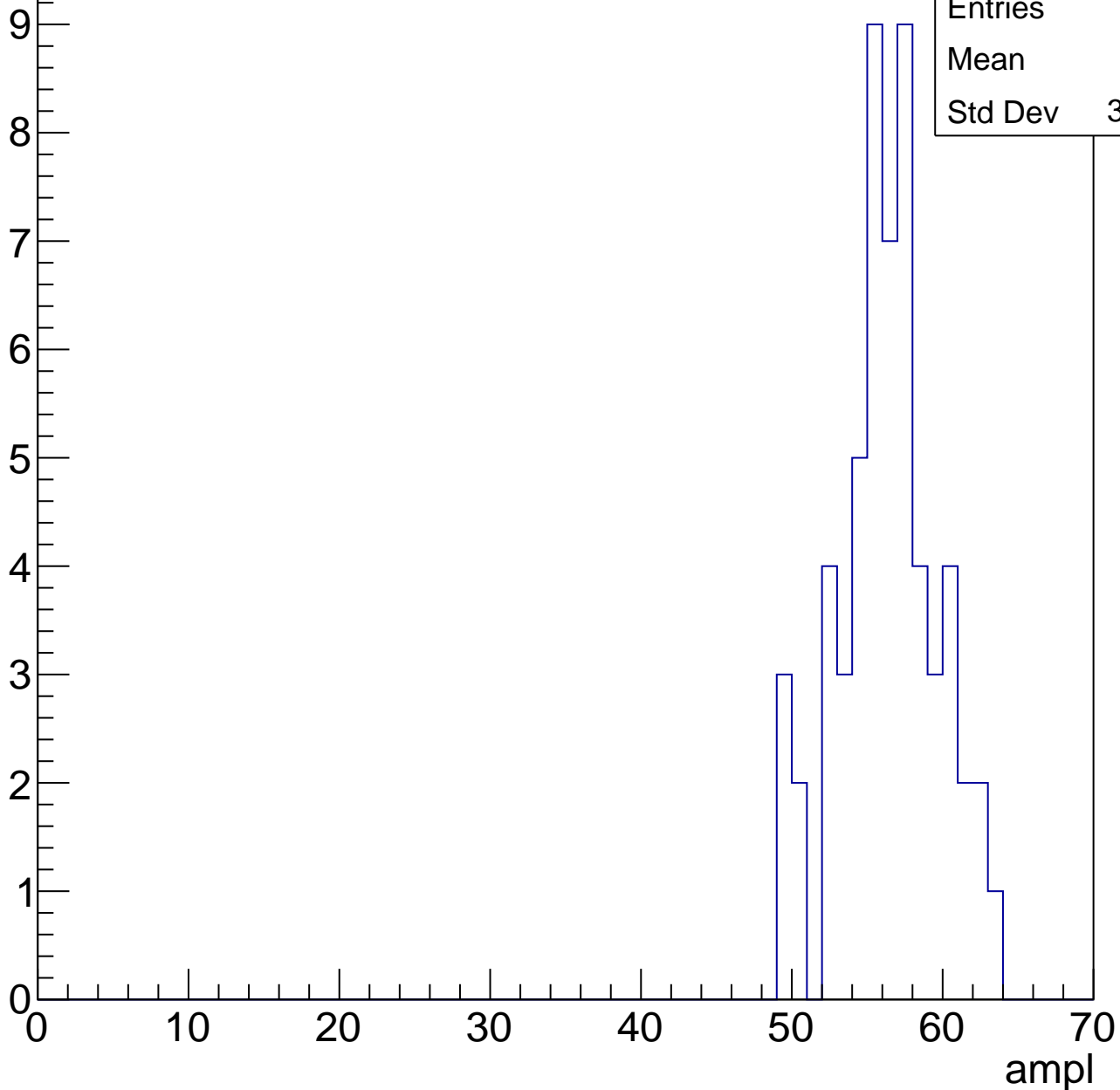


B1L102S, U2-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	55.9
Std Dev	3.284

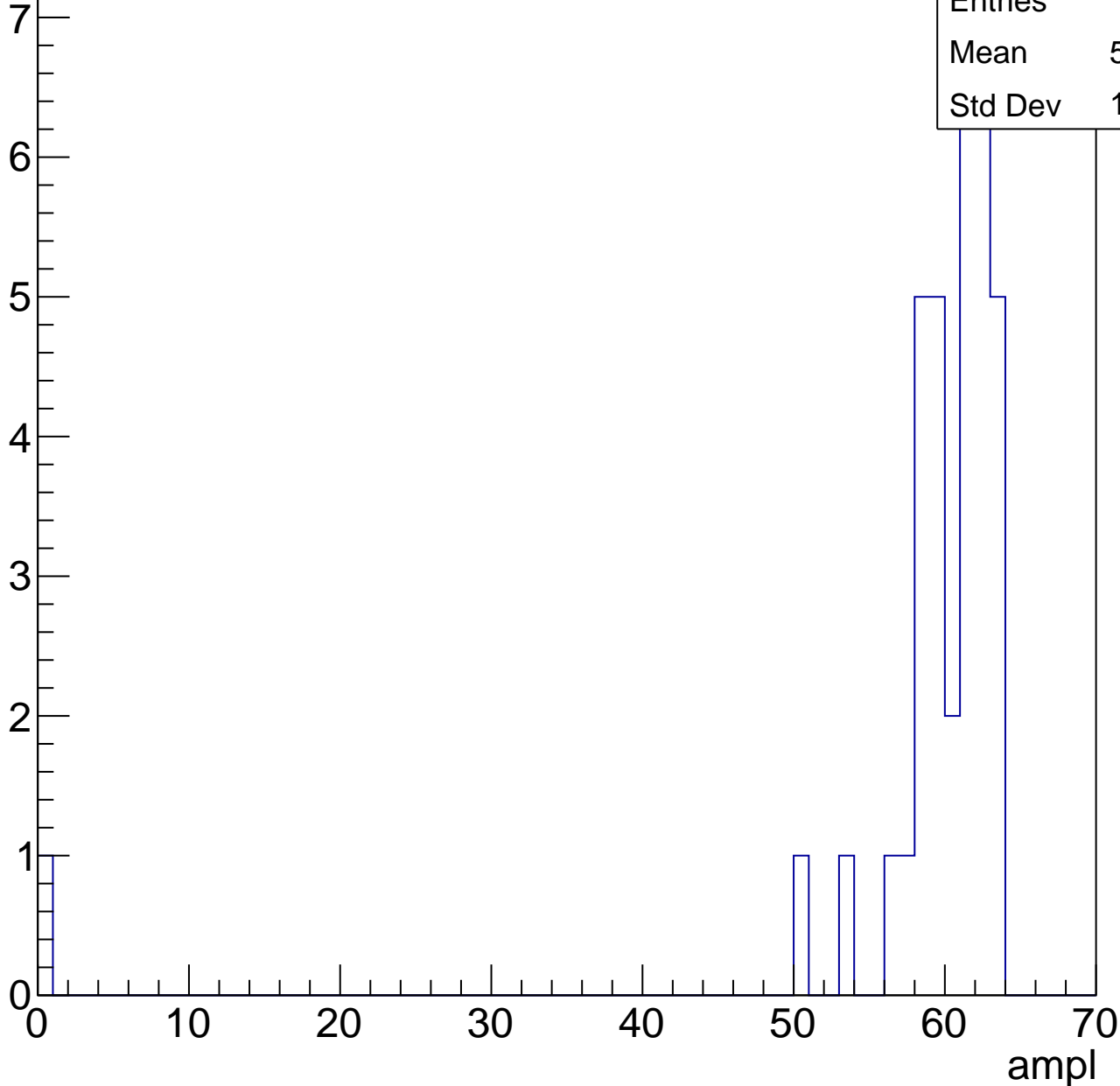


B1L102S, U2-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

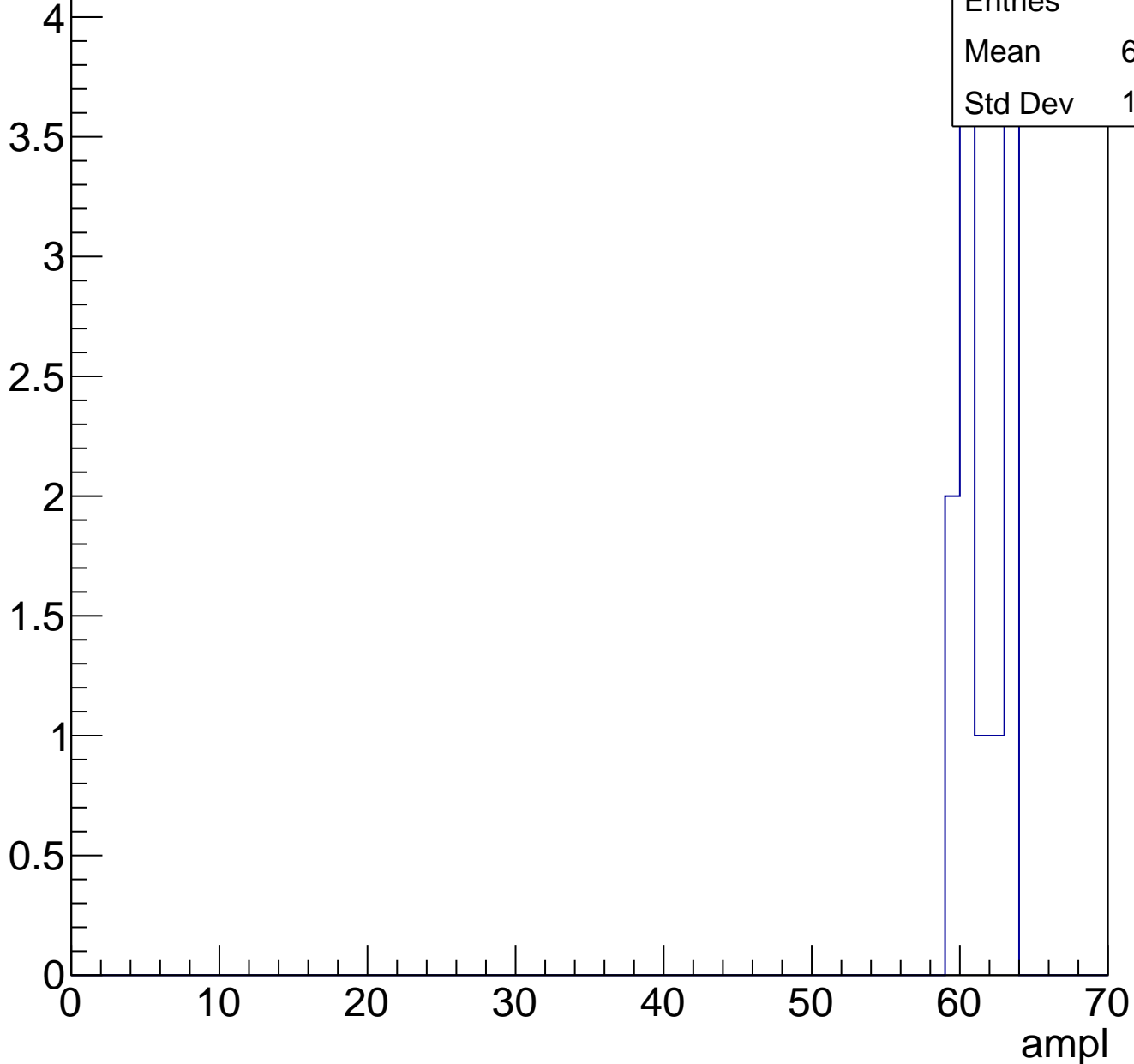
Entries	36
Mean	58.25
Std Dev	10.23



B1L102S, U2-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

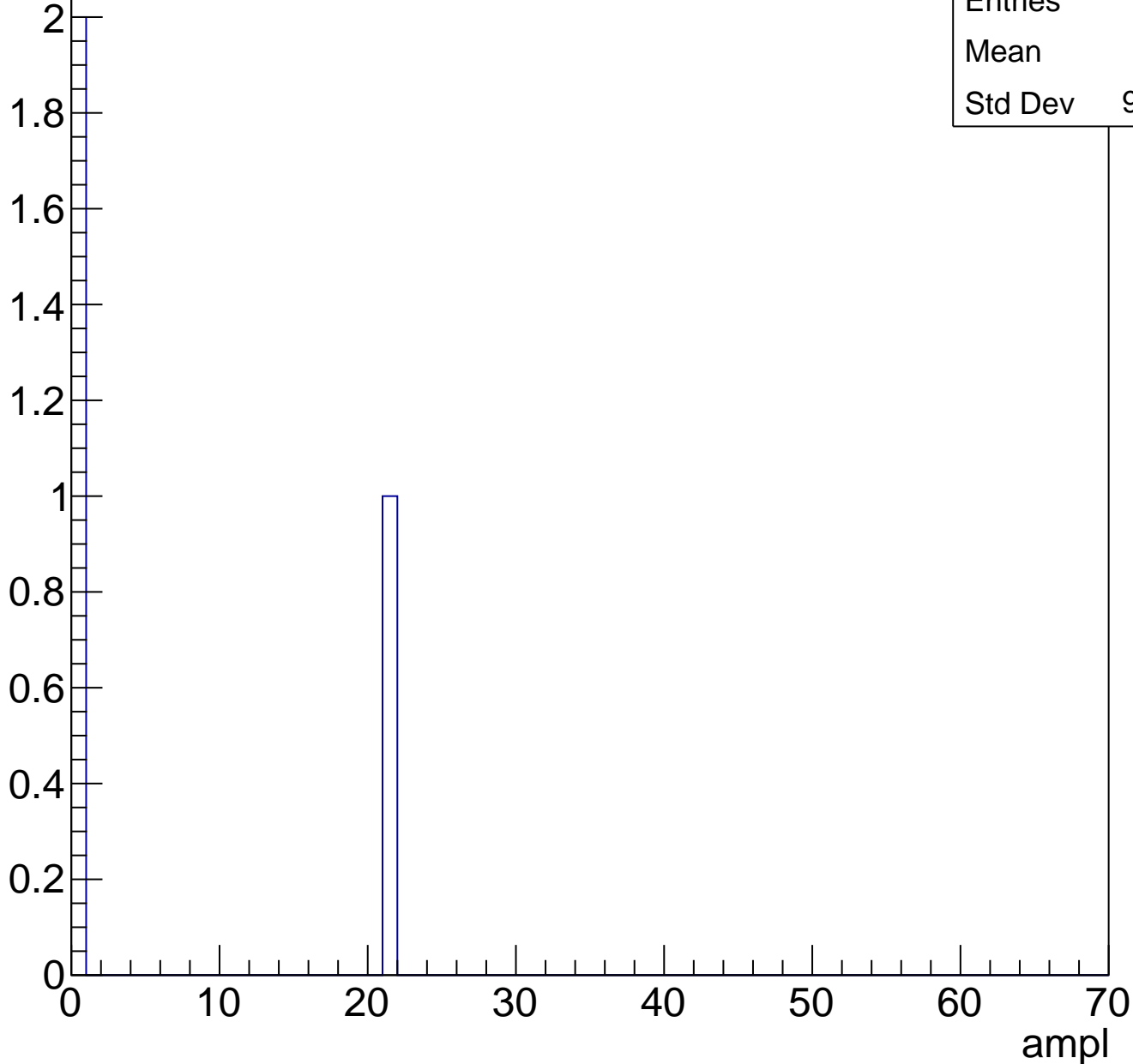
Entry



B1L102S, U2-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch11, adc0

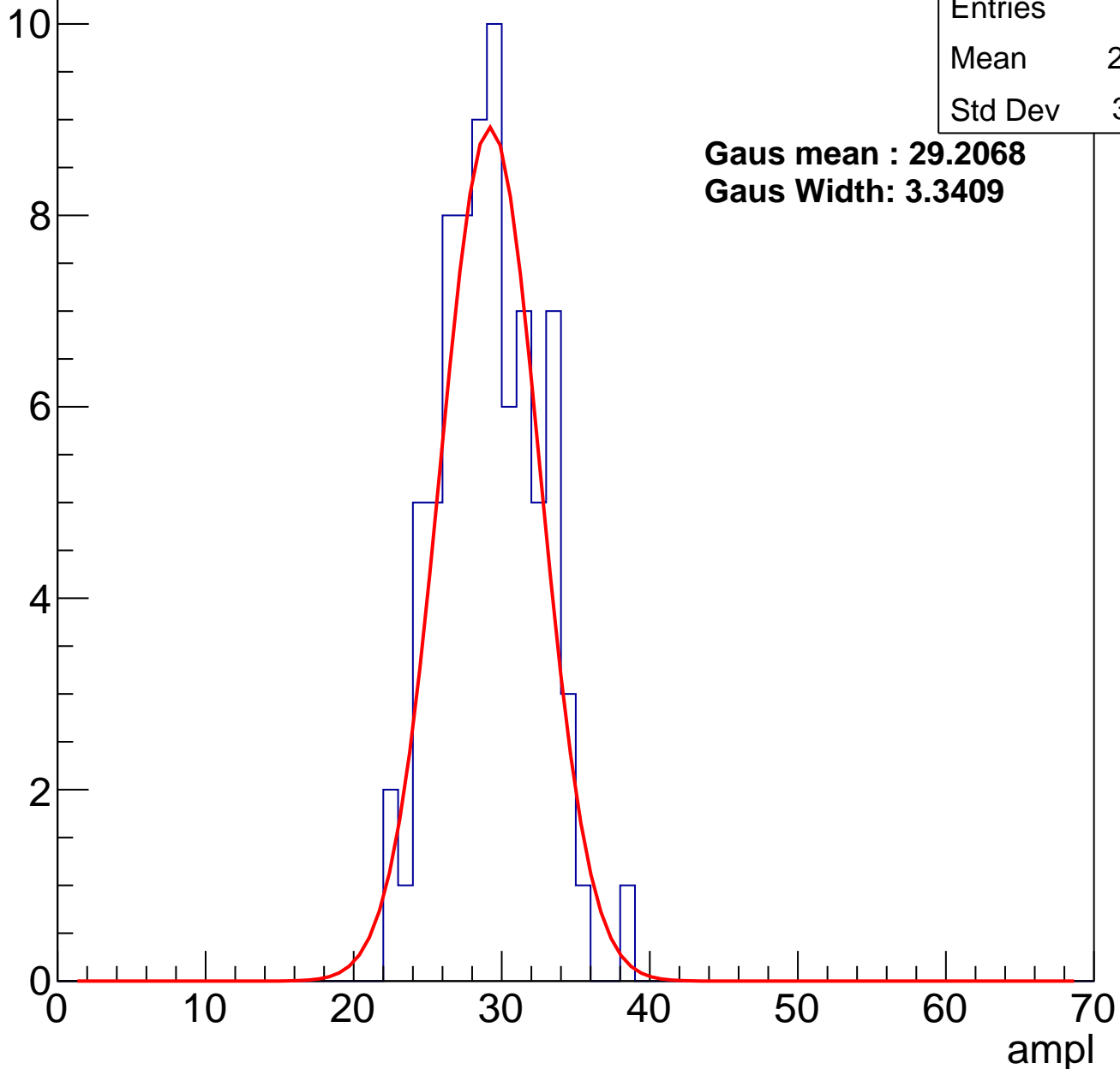
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.73
Std Dev	3.261

Gaus mean : 29.2068

Gaus Width: 3.3409

Entry



B1L102S, U2-ch11, adc1

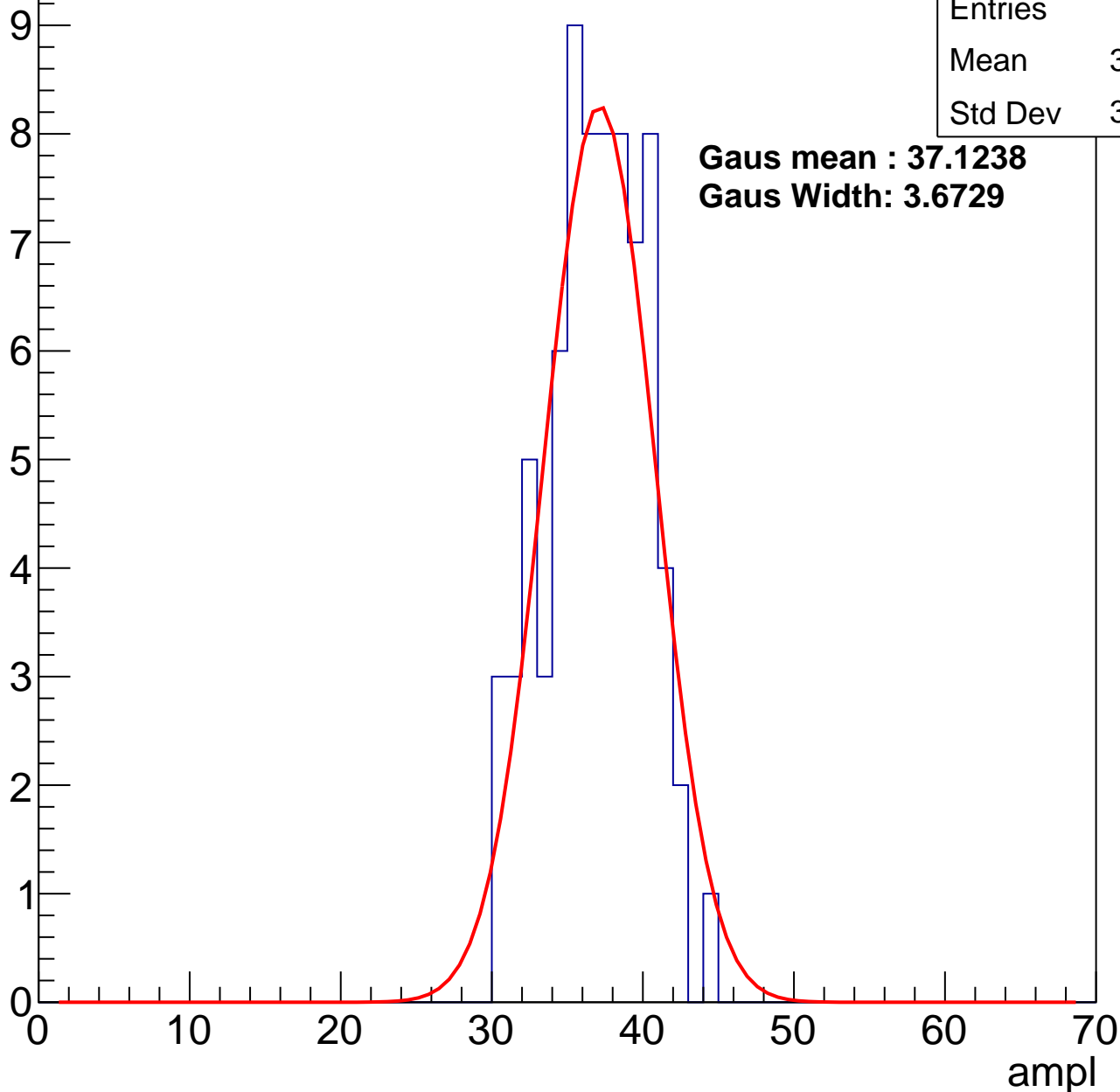
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.45
Std Dev	3.214

Gaus mean : 37.1238

Gaus Width: 3.6729



B1L102S, U2-ch11, adc2

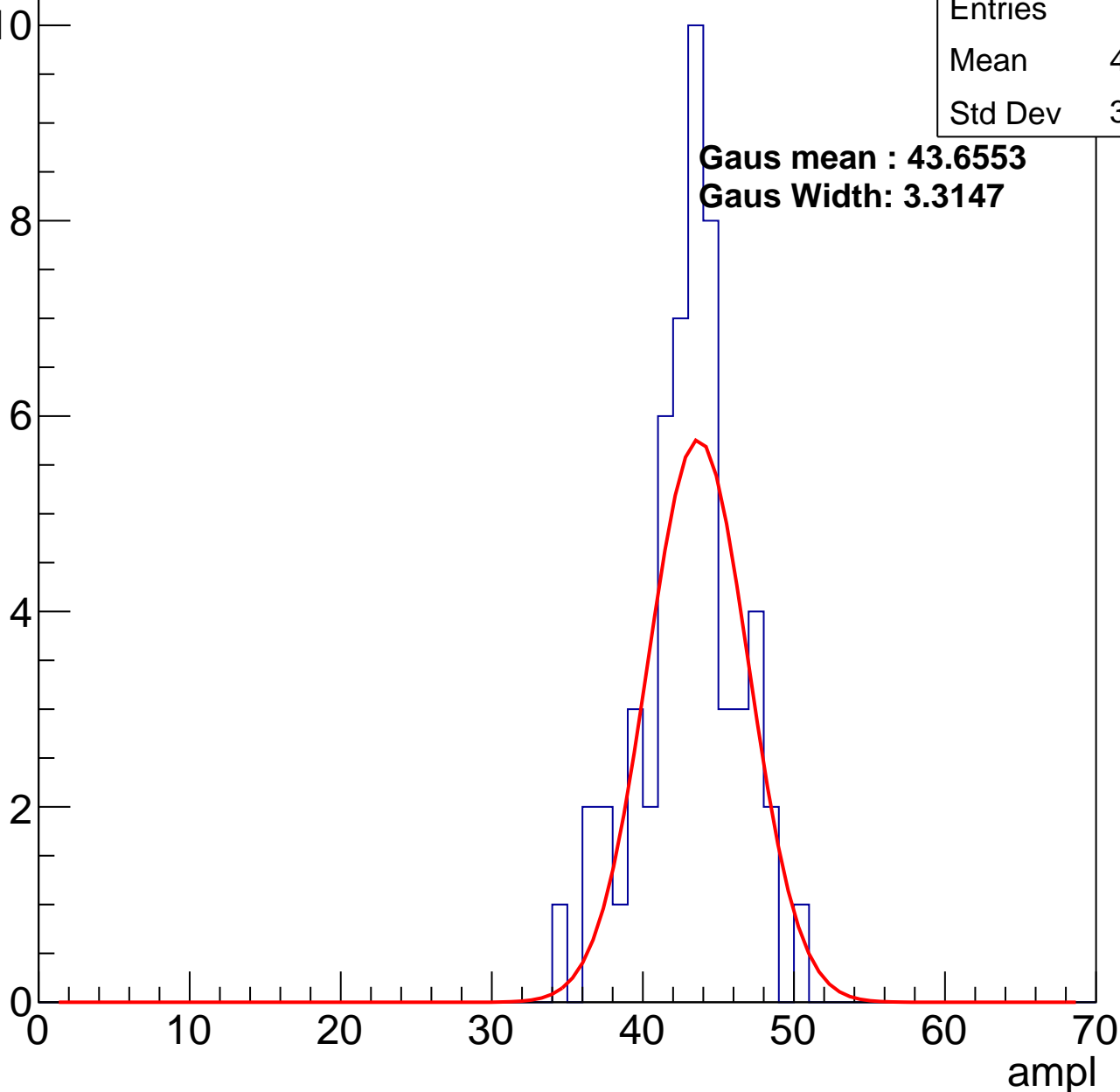
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.62
Std Dev	3.233

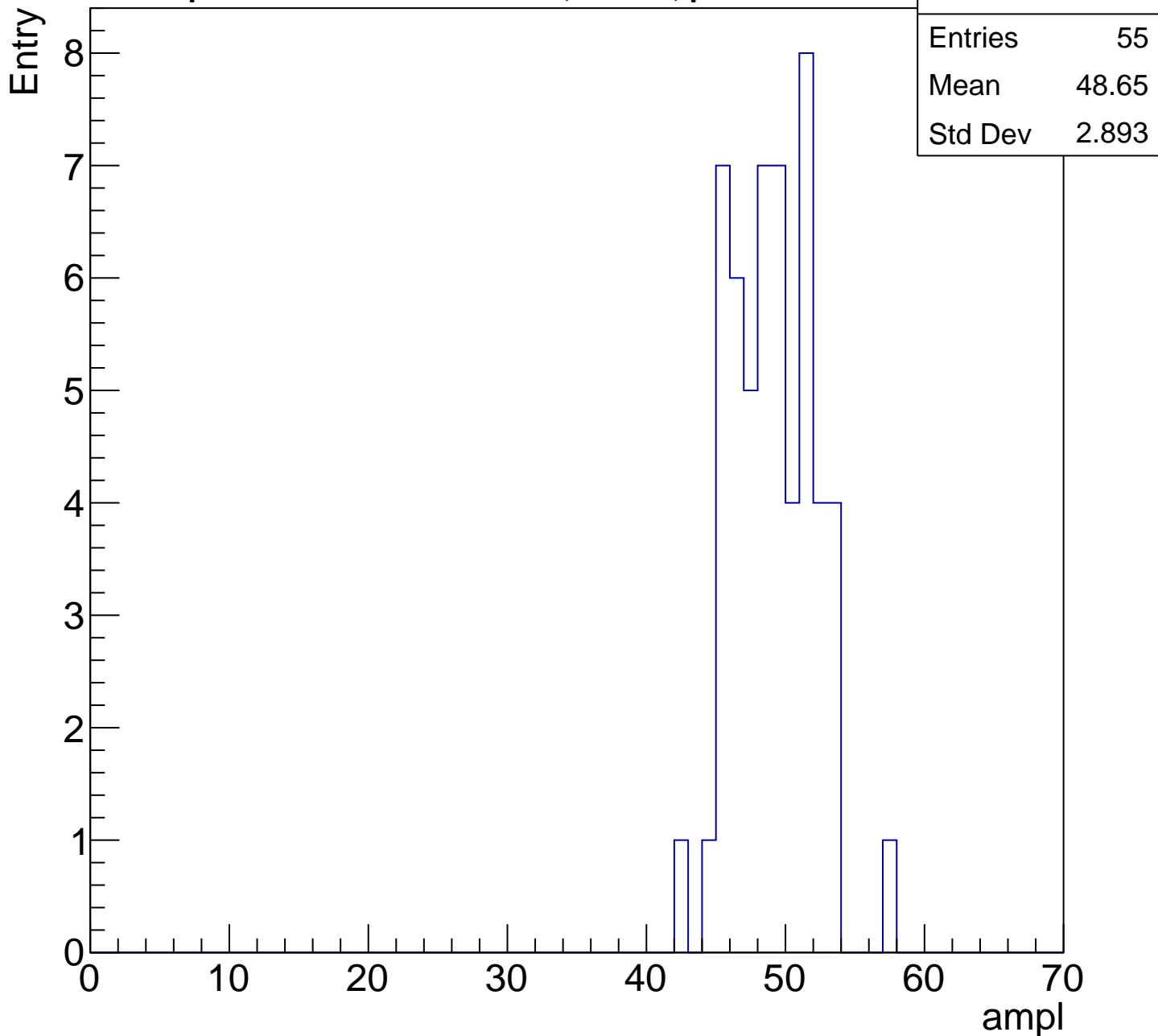
Gaus mean : 43.6553

Gaus Width: 3.3147



B1L102S, U2-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch11, adc4

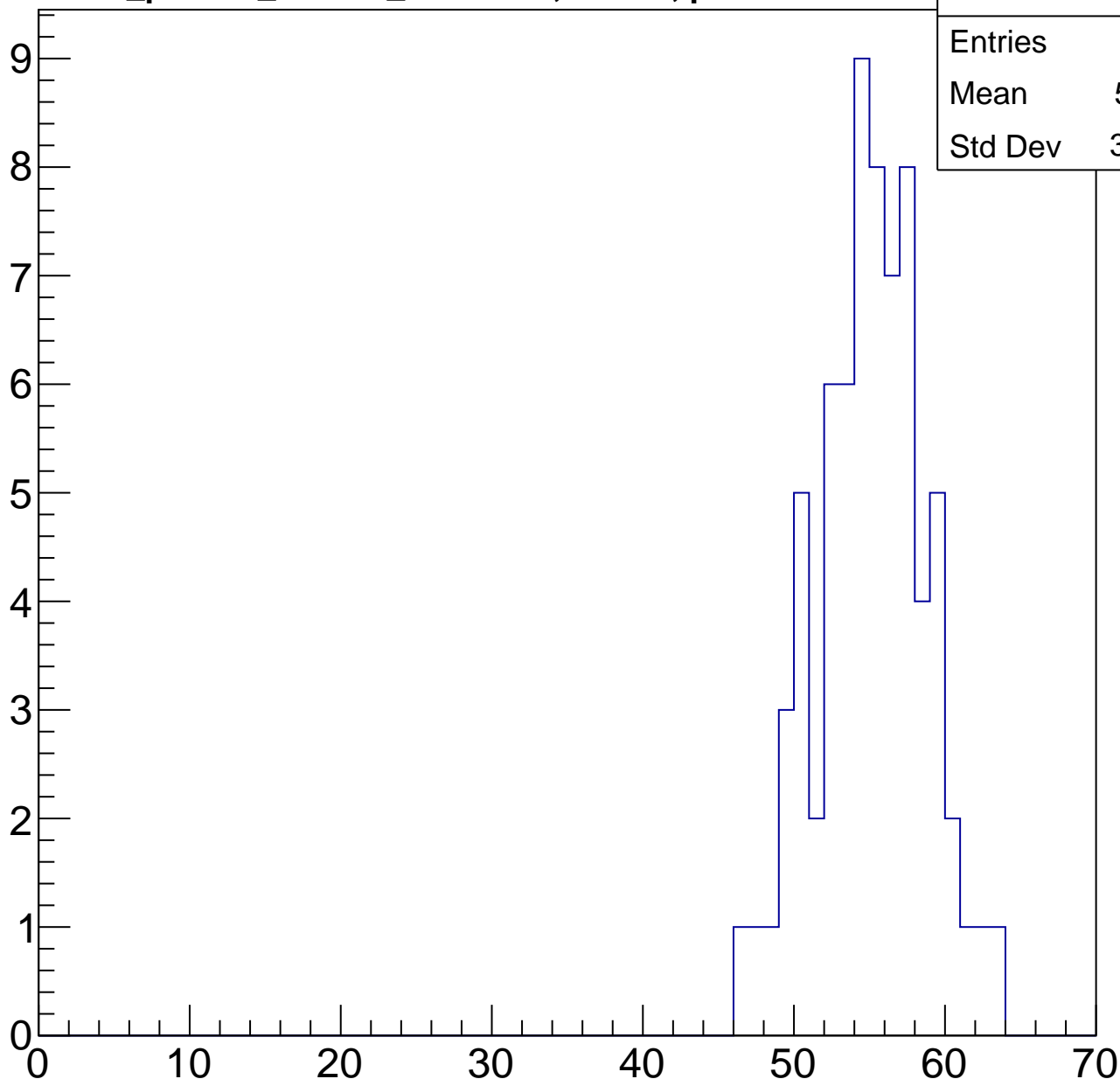
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	54.61
Std Dev	3.538

ampl



B1L102S, U2-ch11, adc5

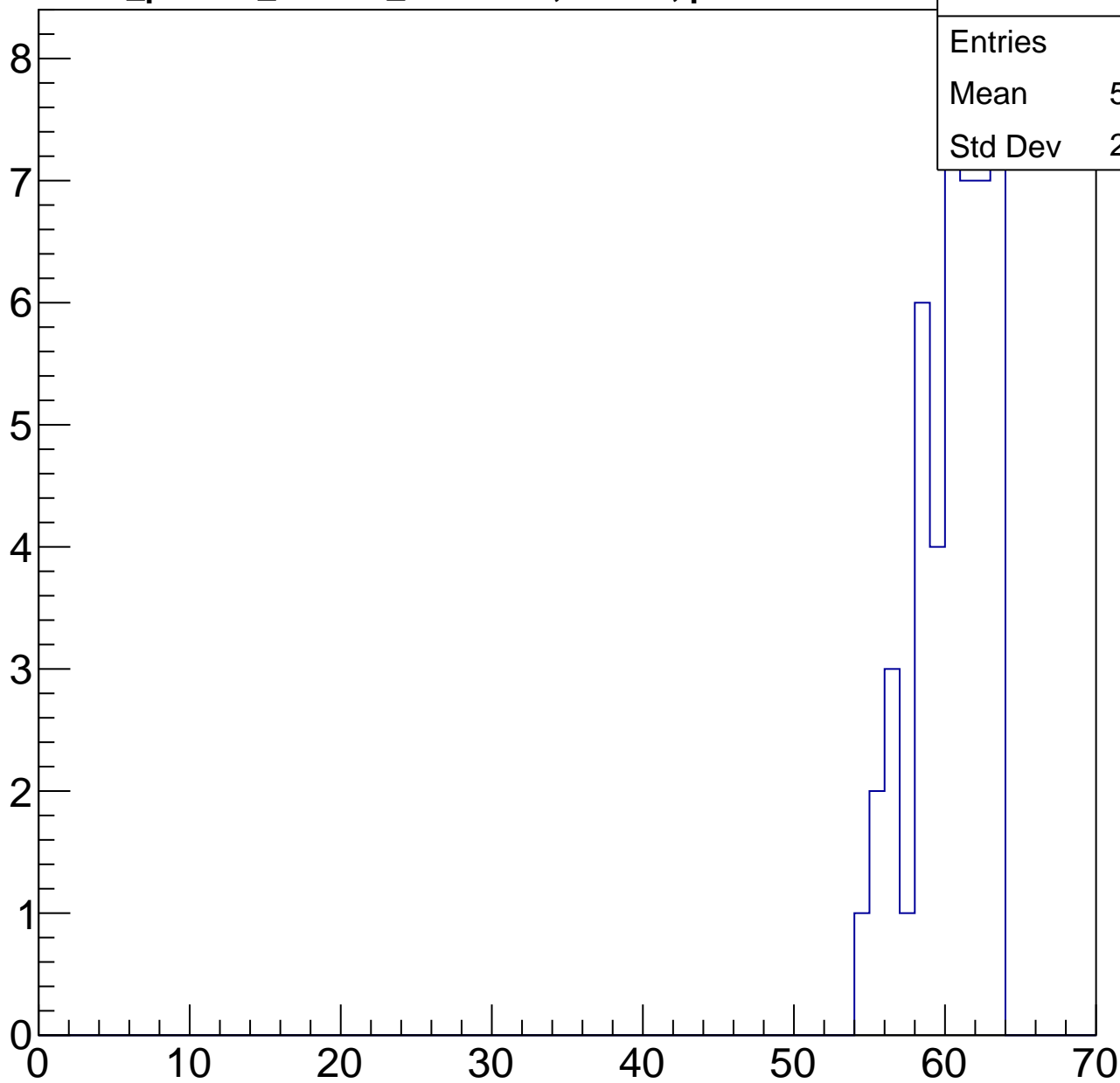
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.96
Std Dev	2.432

ampl



B1L102S, U2-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

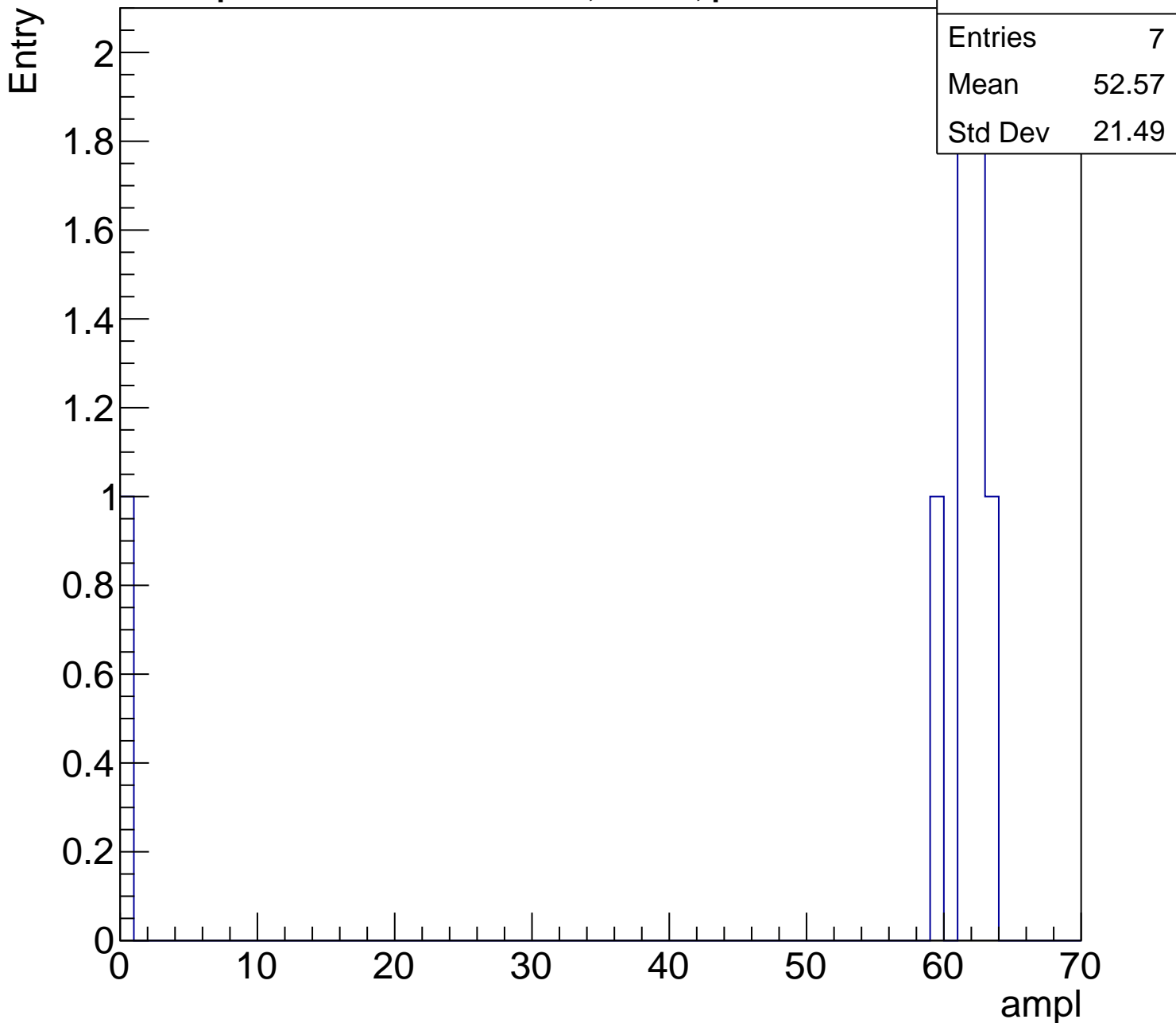
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.57
Std Dev	21.49

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch12, adc0

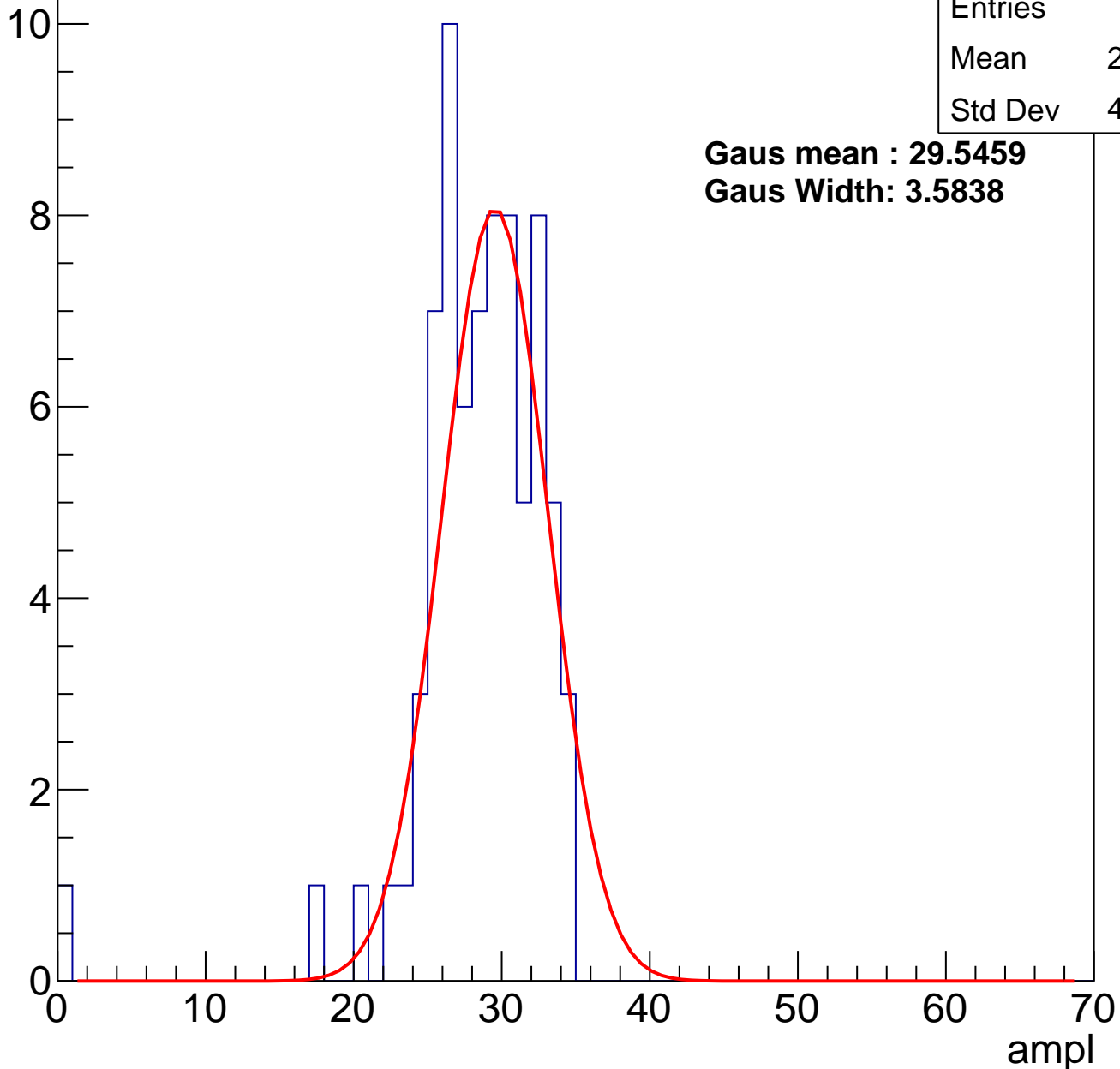
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	27.96
Std Dev	4.666

Gaus mean : 29.5459

Gaus Width: 3.5838

Entry



B1L102S, U2-ch12, adc1

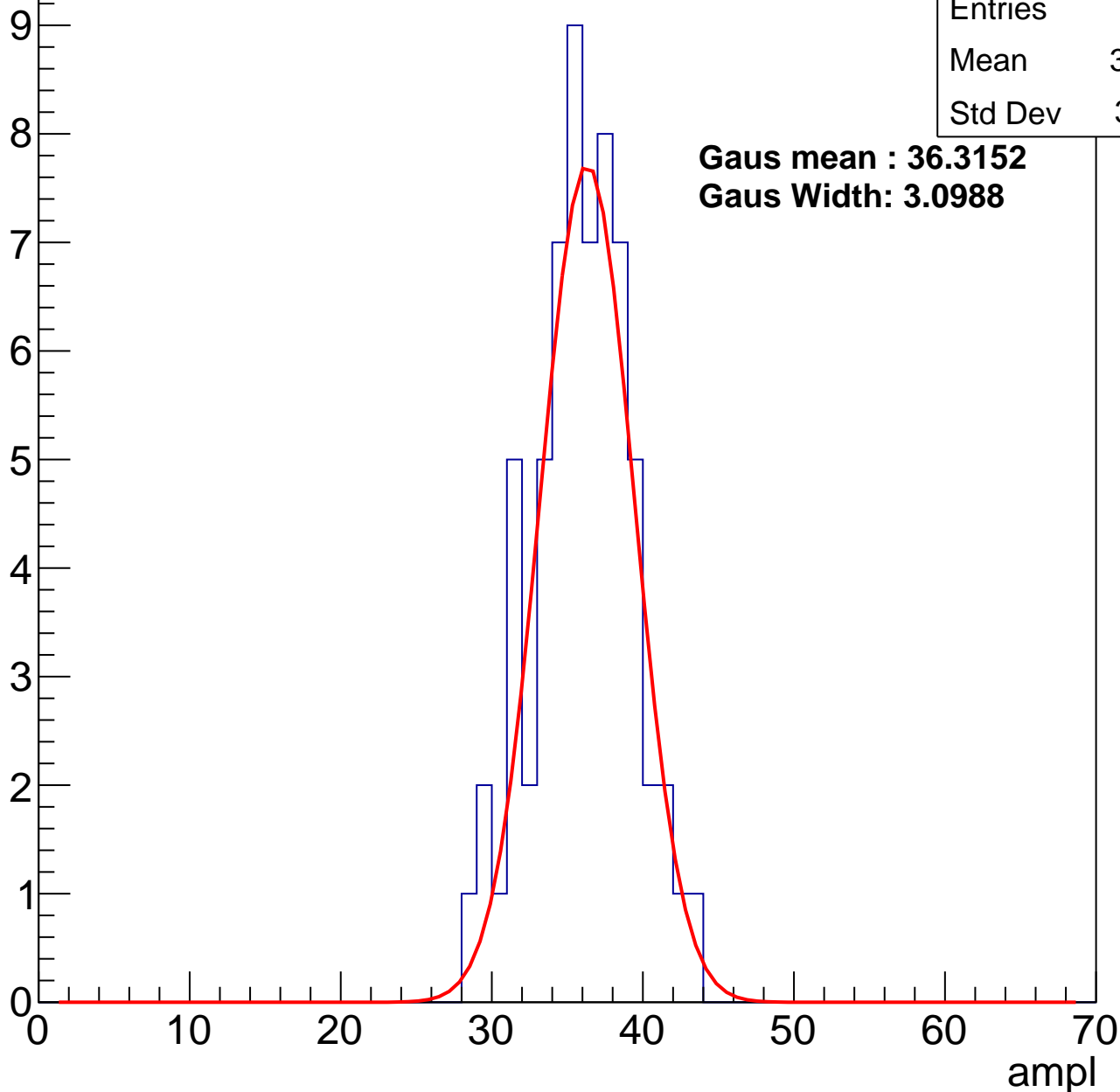
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.52
Std Dev	3.211

Gaus mean : 36.3152

Gaus Width: 3.0988



B1L102S, U2-ch12, adc2

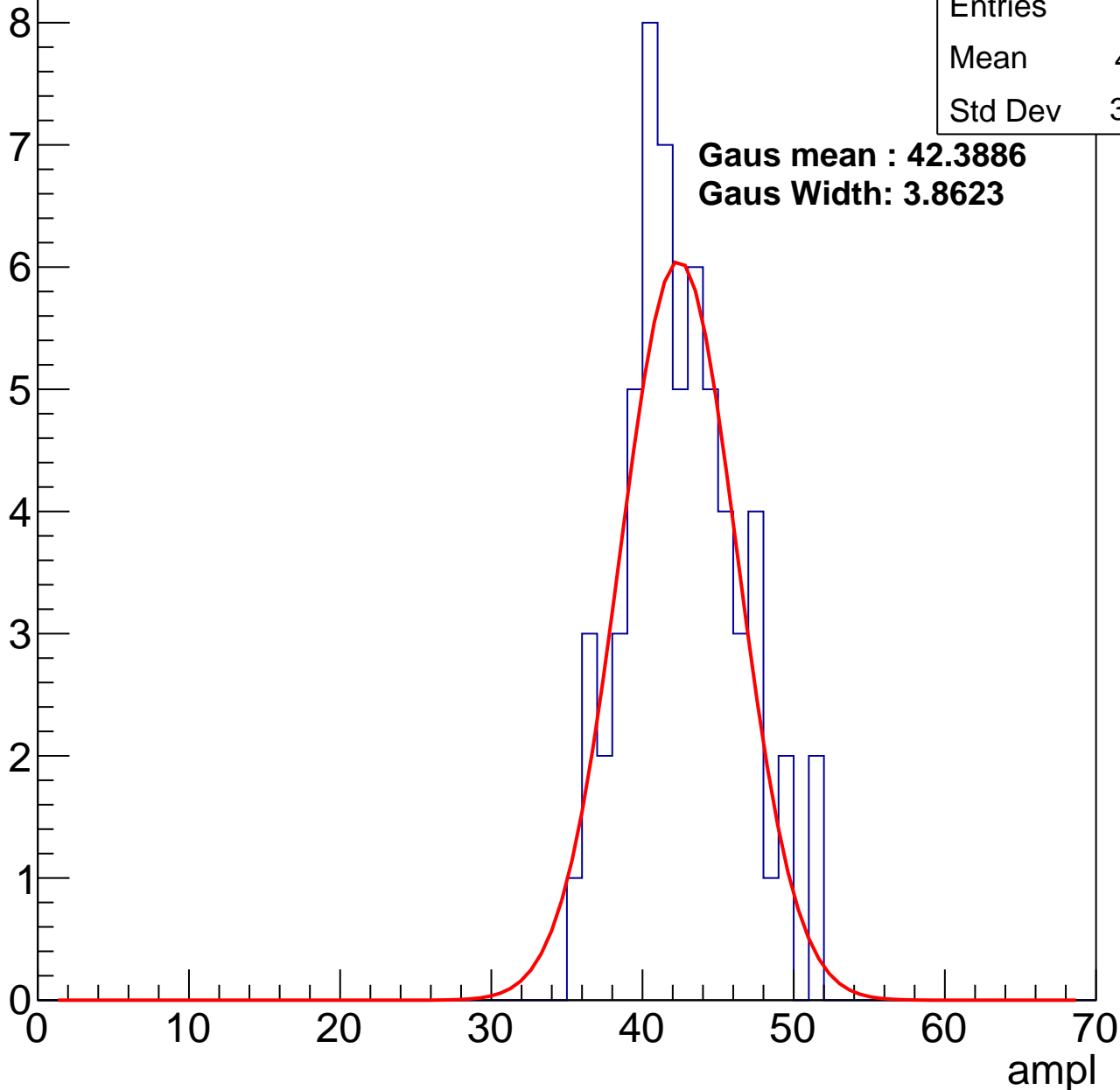
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.21
Std Dev	3.716

Gaus mean : 42.3886

Gaus Width: 3.8623

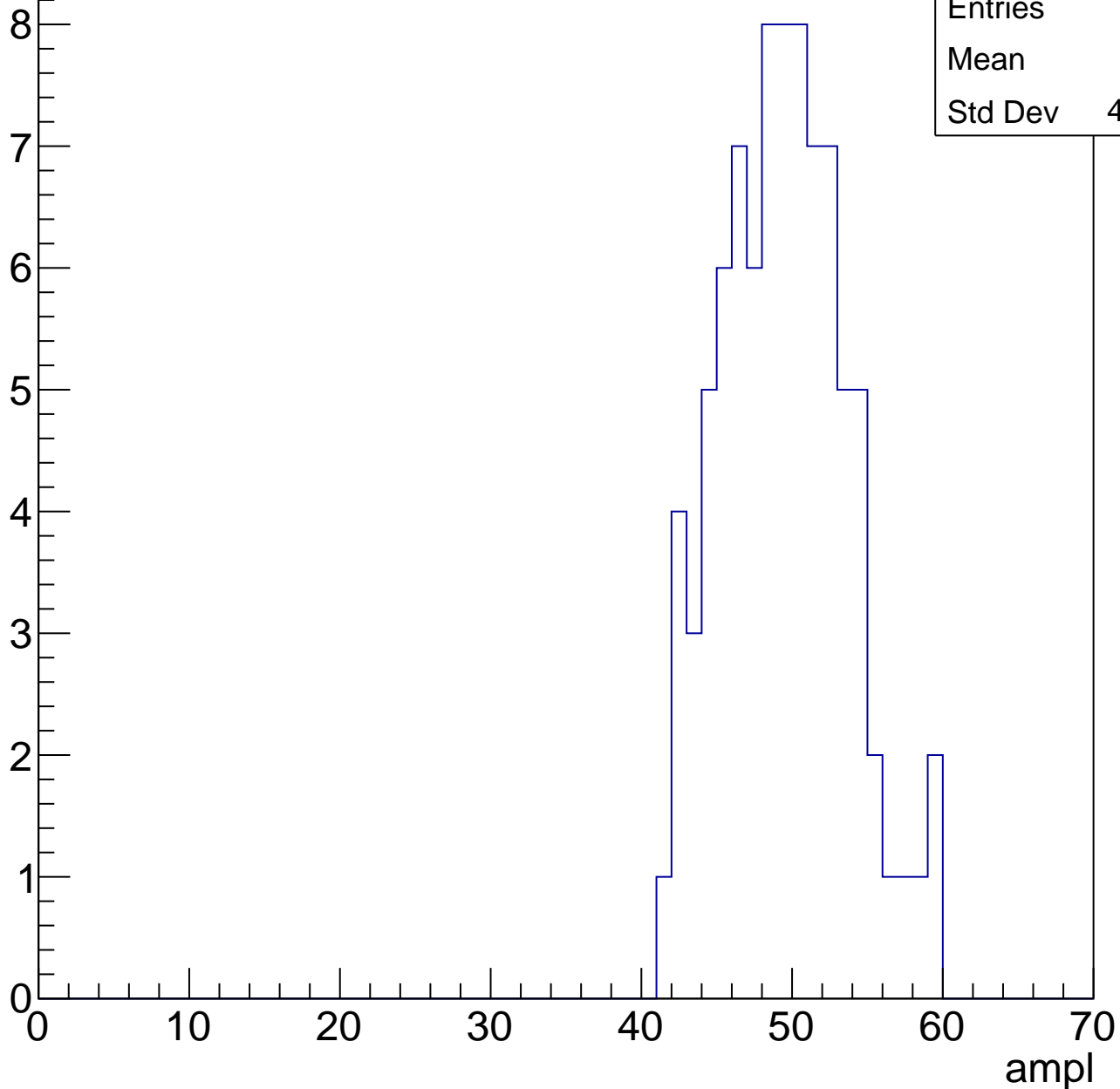


B1L102S, U2-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	49
Std Dev	4.088

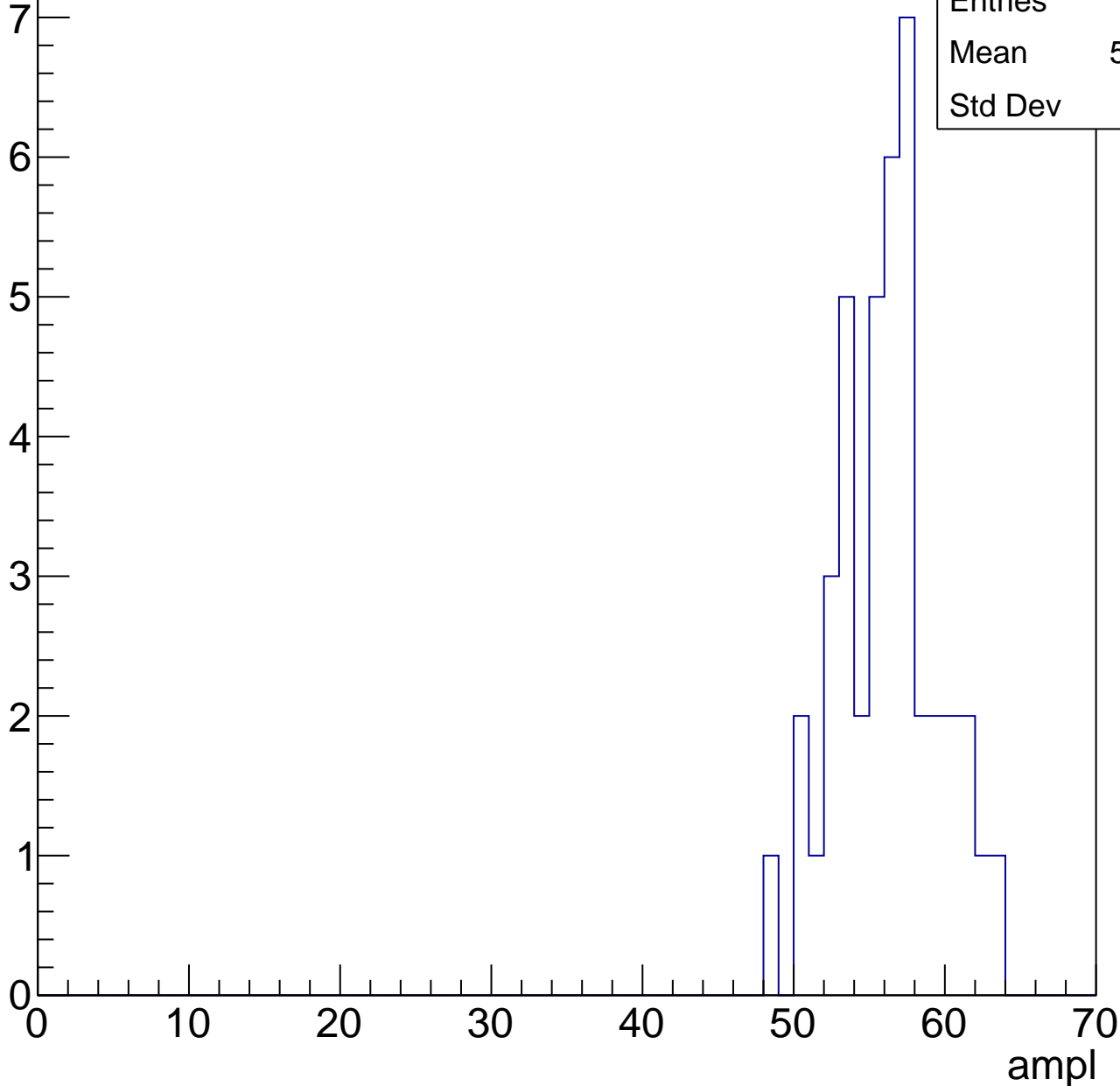


B1L102S, U2-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	55.69
Std Dev	3.32

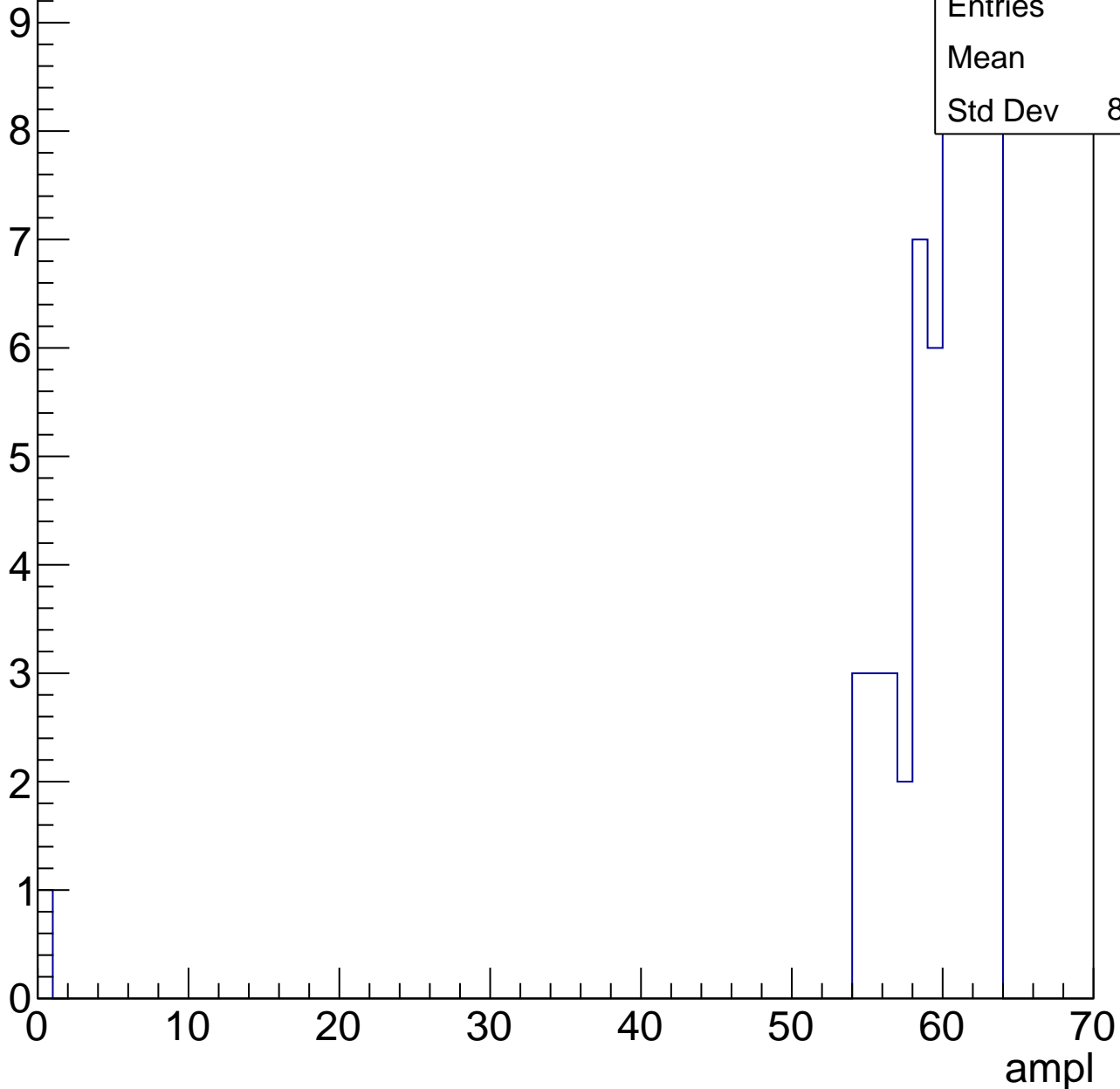


B1L102S, U2-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	58.6
Std Dev	8.185



B1L102S, U2-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L102S, U2-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch13, adc0

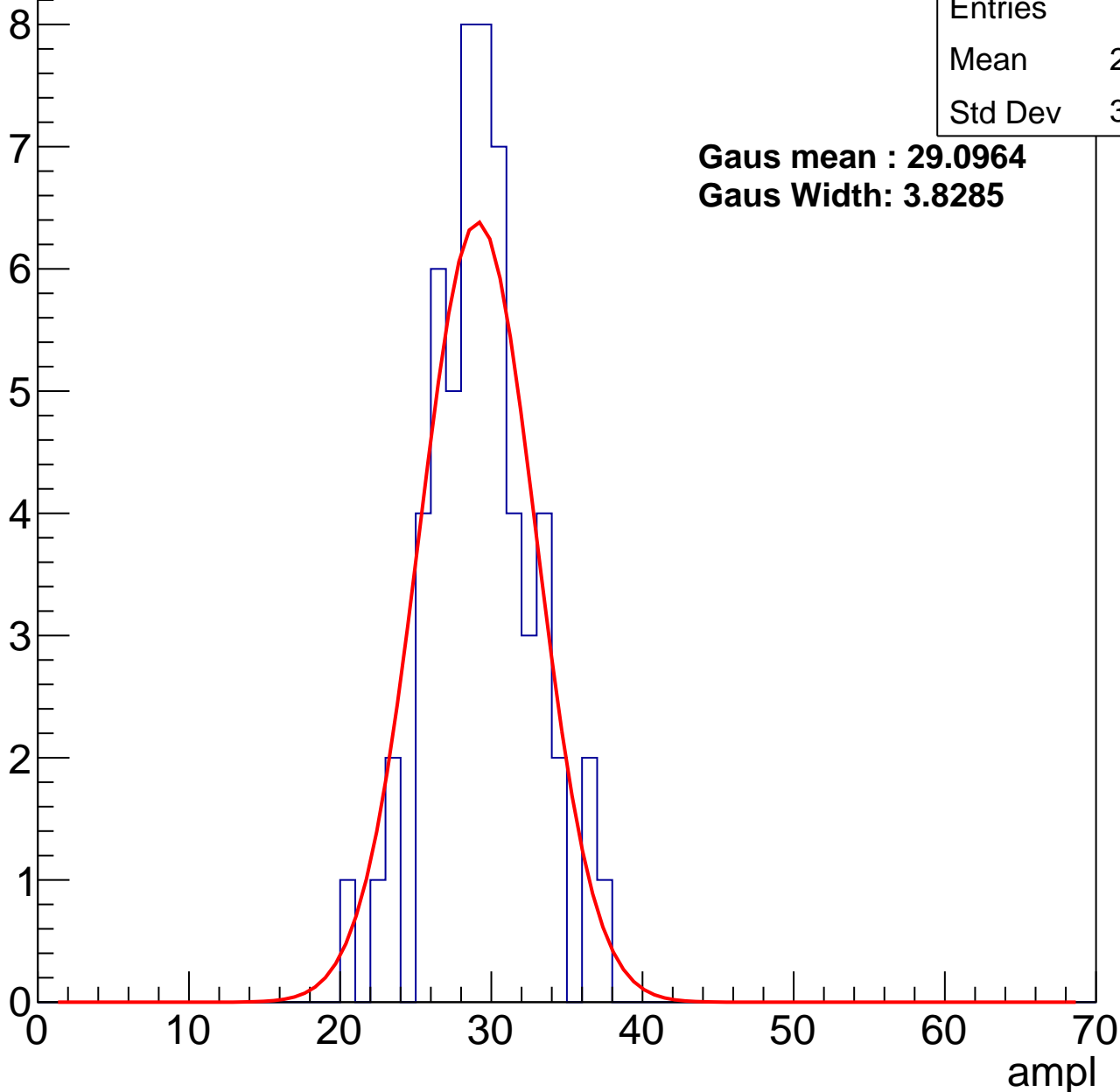
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.86
Std Dev	3.406

Gaus mean : 29.0964

Gaus Width: 3.8285



B1L102S, U2-ch13, adc1

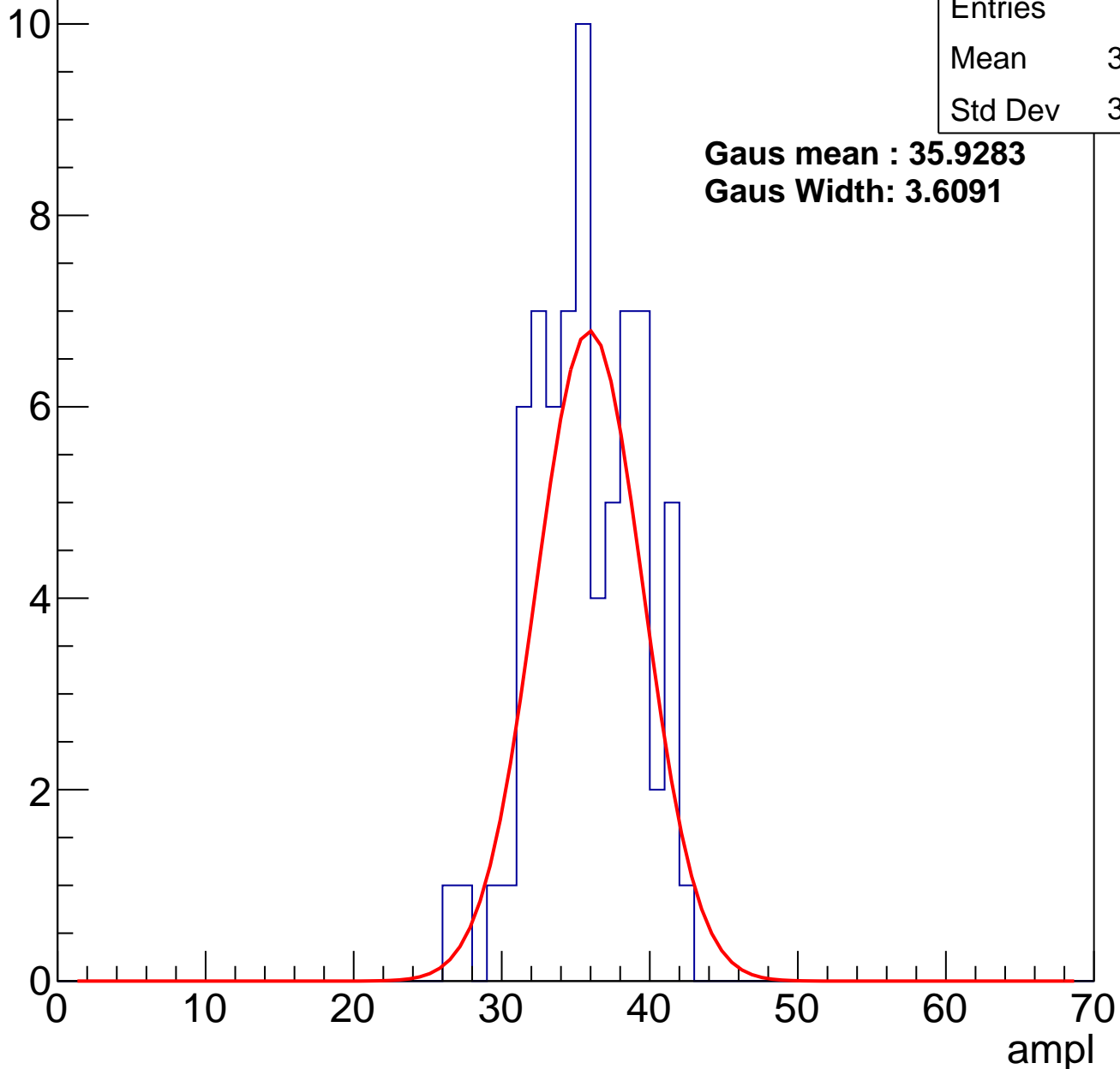
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.25
Std Dev	3.499

Gaus mean : 35.9283

Gaus Width: 3.6091

Entry



B1L102S, U2-ch13, adc2

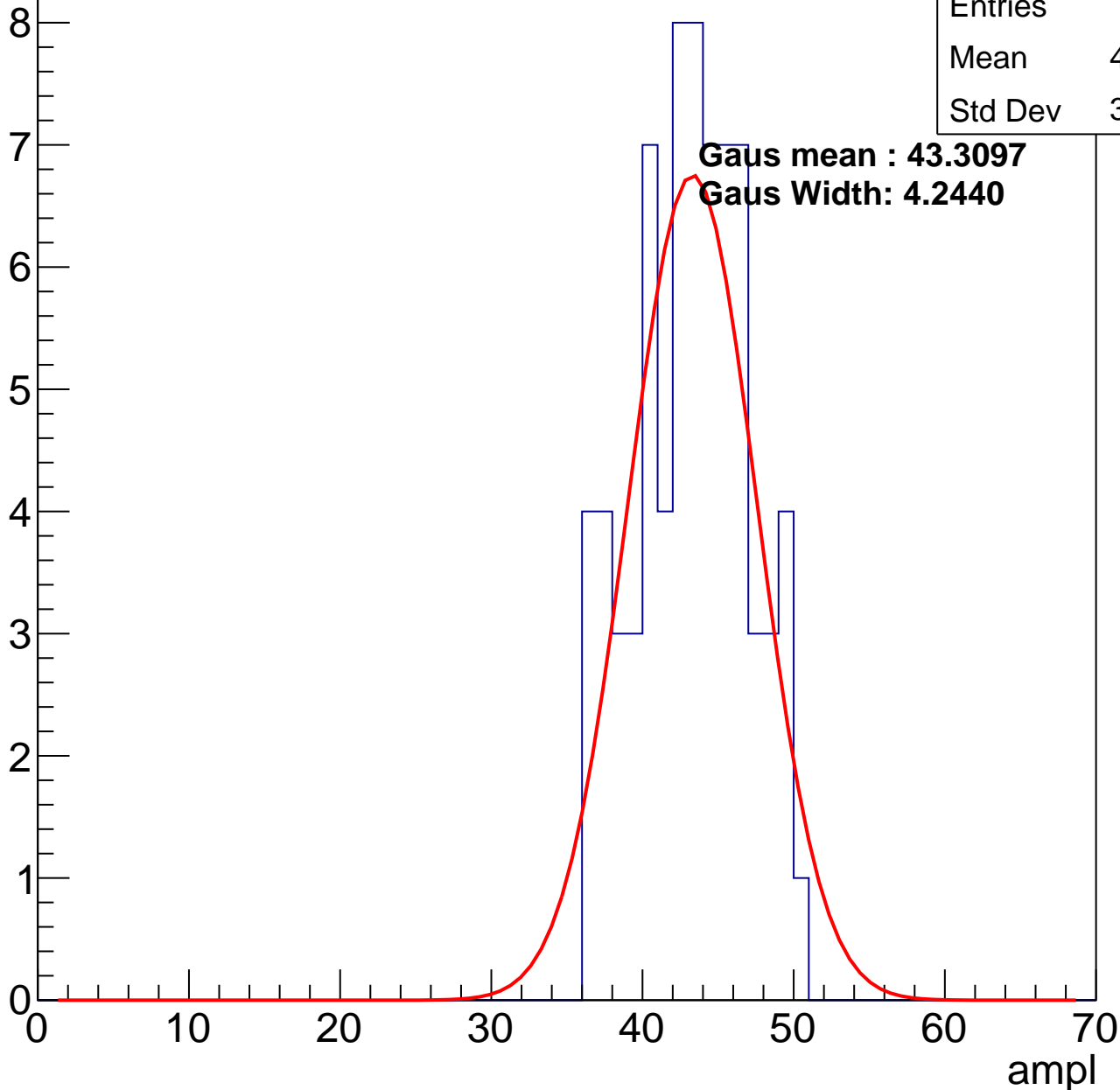
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.78
Std Dev	3.627

Gaus mean : 43.3097

Gaus Width: 4.2440

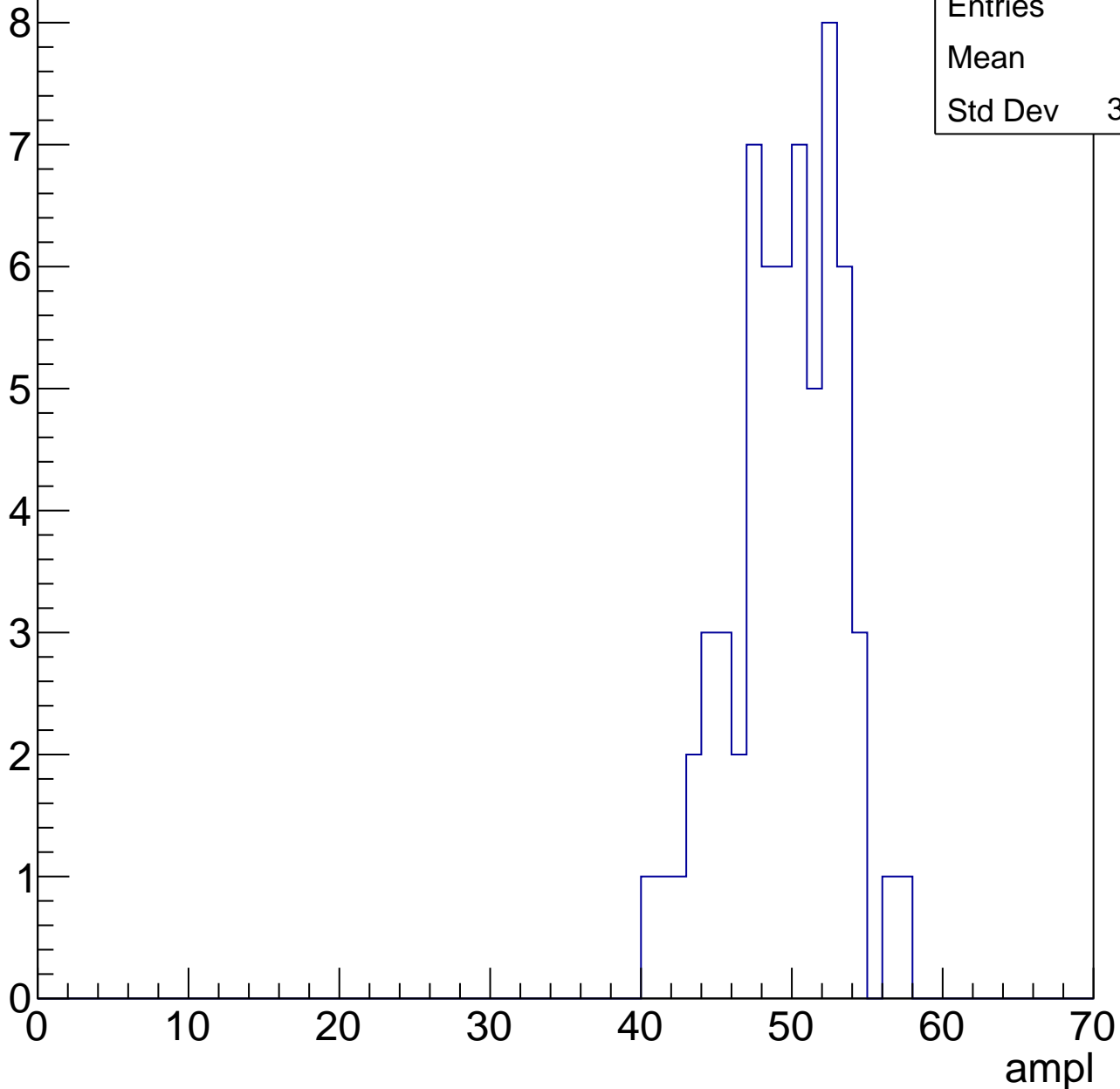


B1L102S, U2-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.1
Std Dev	3.615

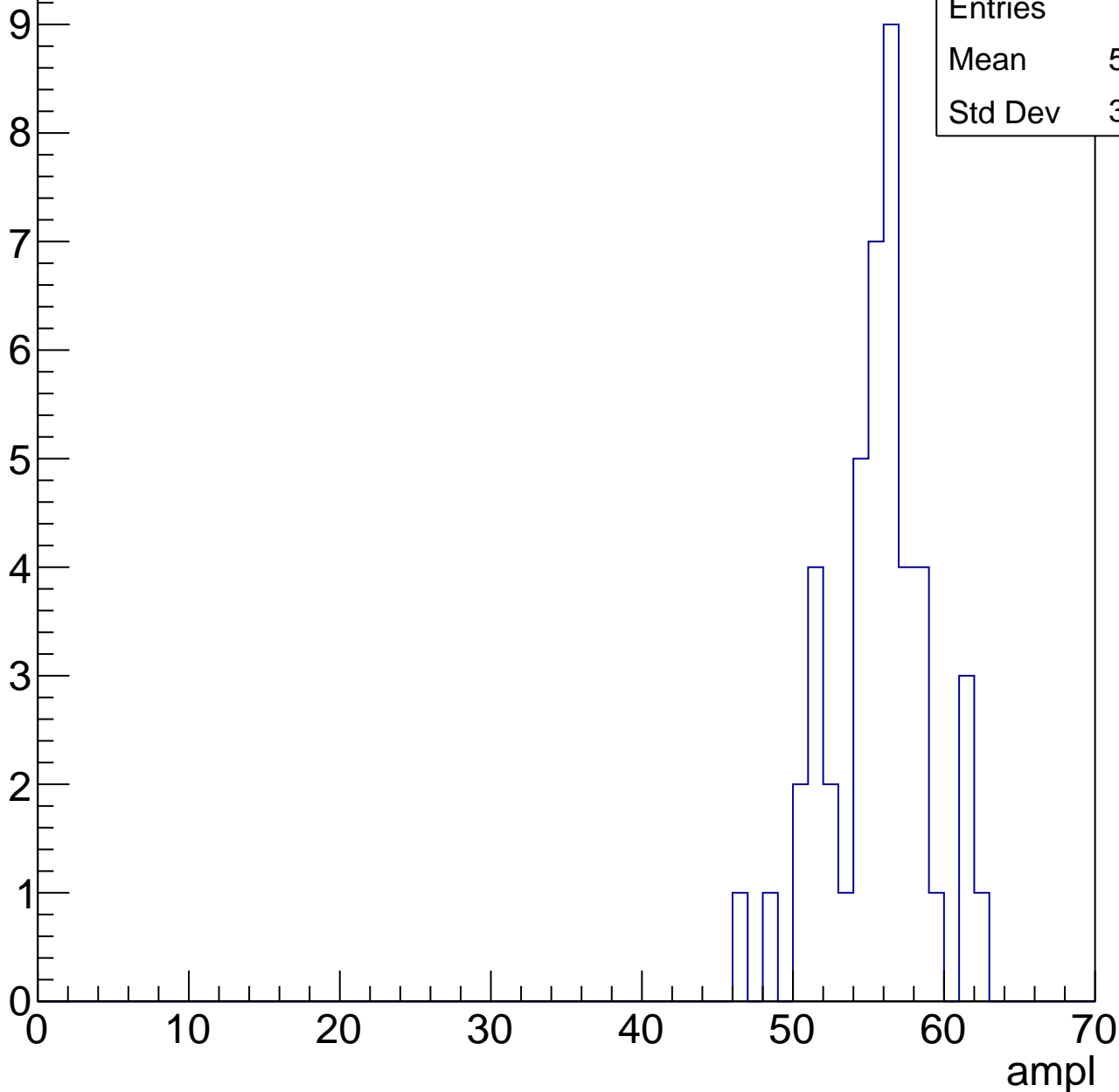


B1L102S, U2-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	55.07
Std Dev	3.349

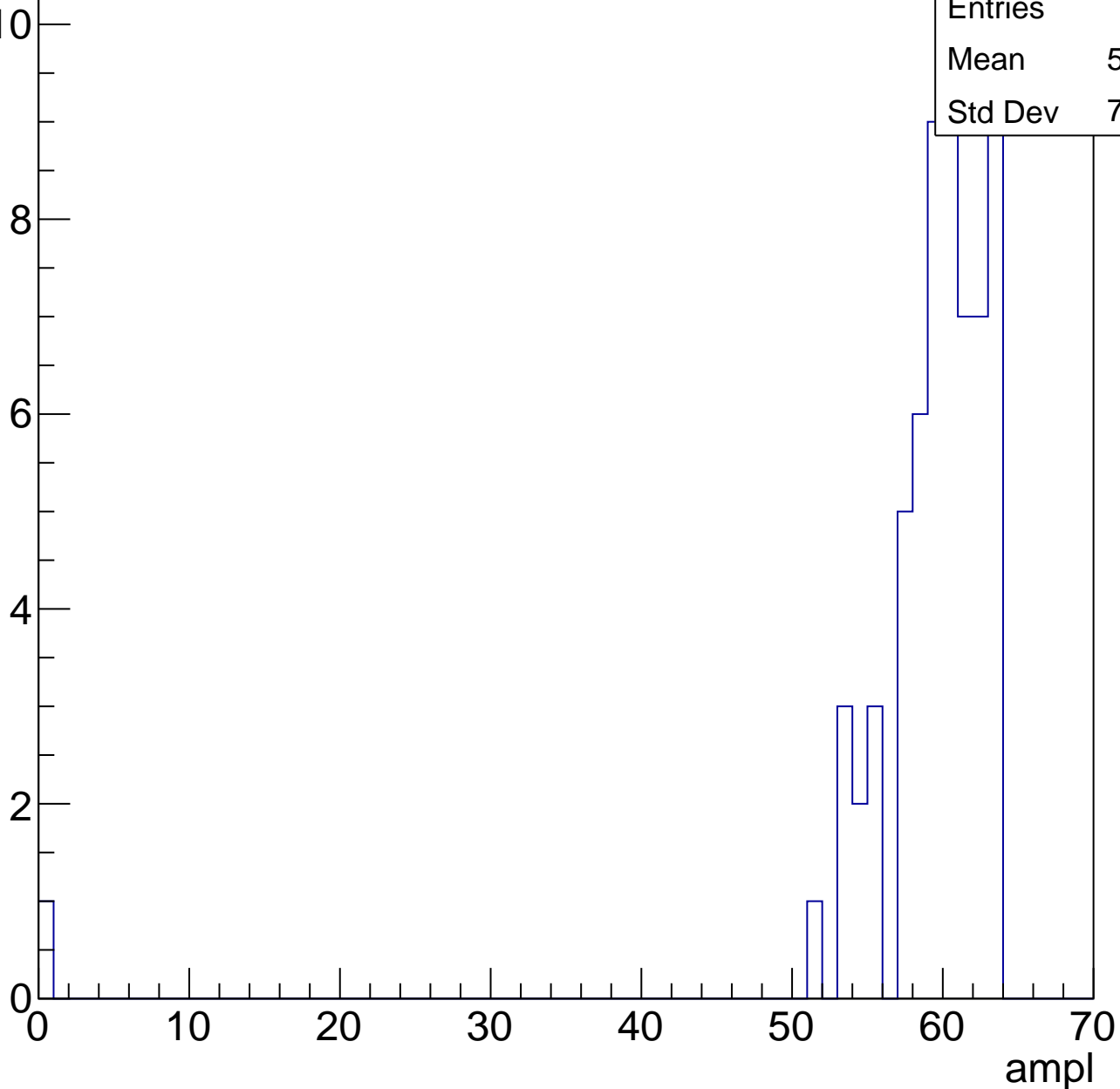


B1L102S, U2-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	58.33
Std Dev	7.962



B1L102S, U2-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

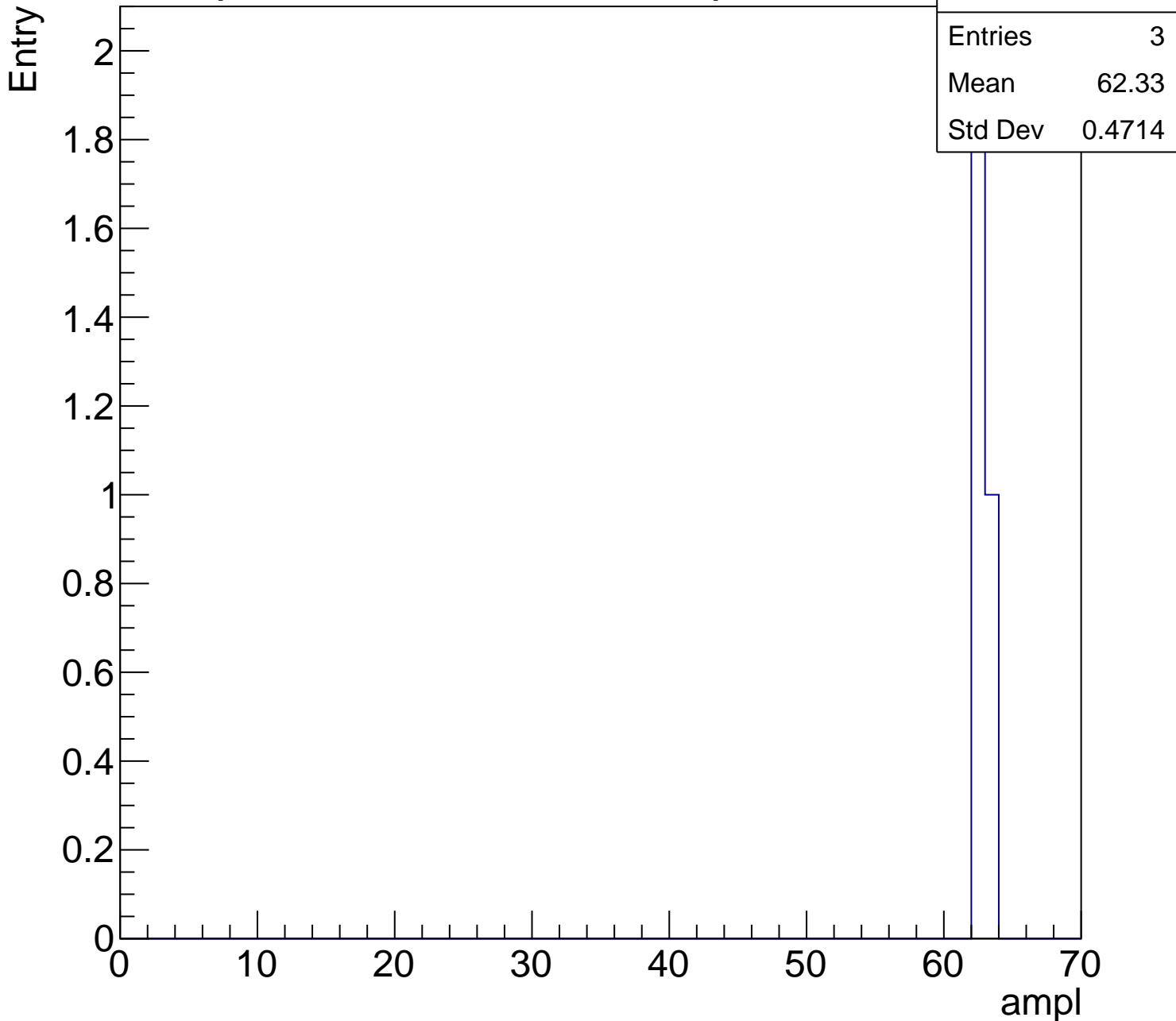
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch14, adc0

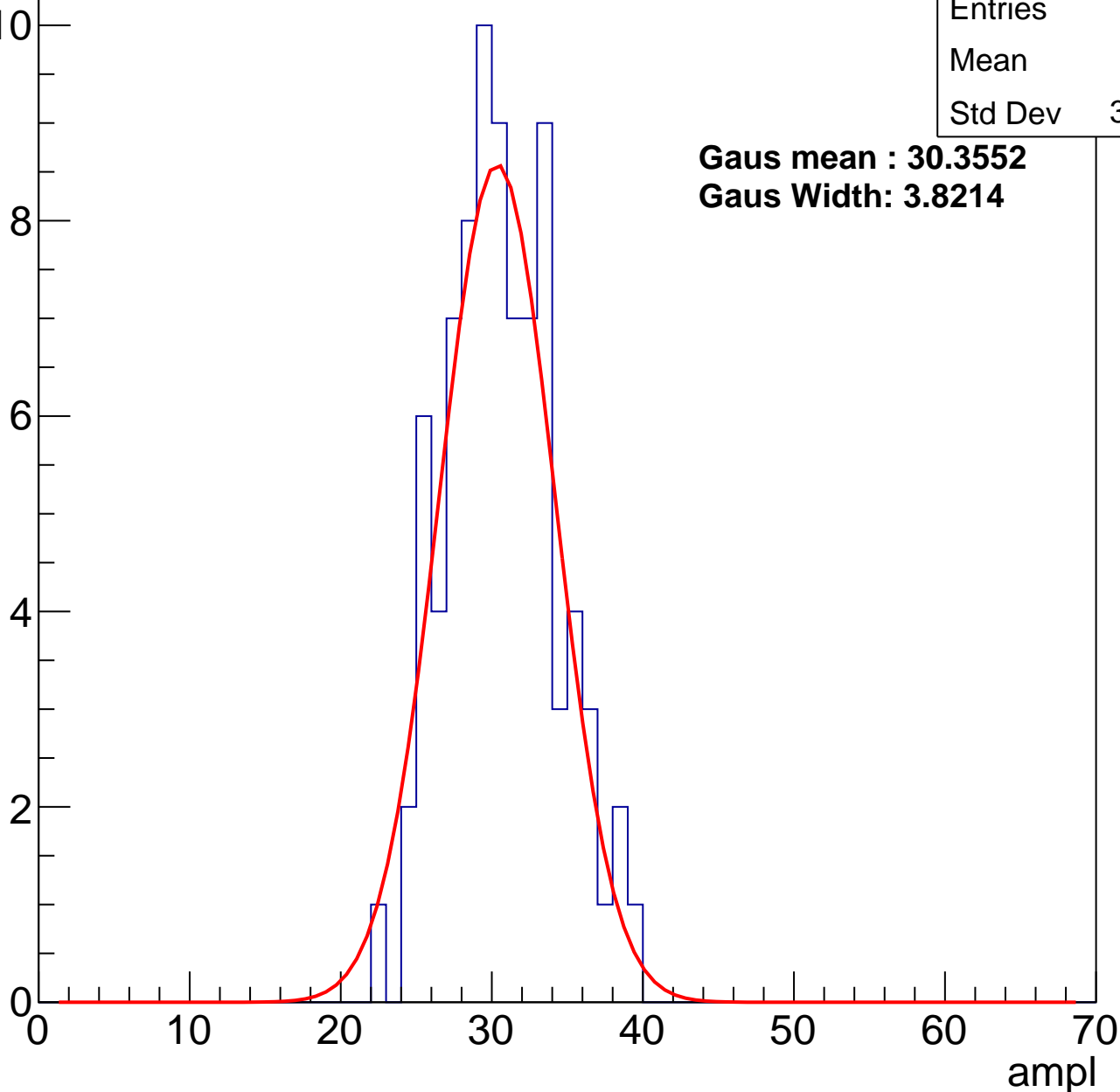
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	30.2
Std Dev	3.588

Gaus mean : 30.3552

Gaus Width: 3.8214



B1L102S, U2-ch14, adc1

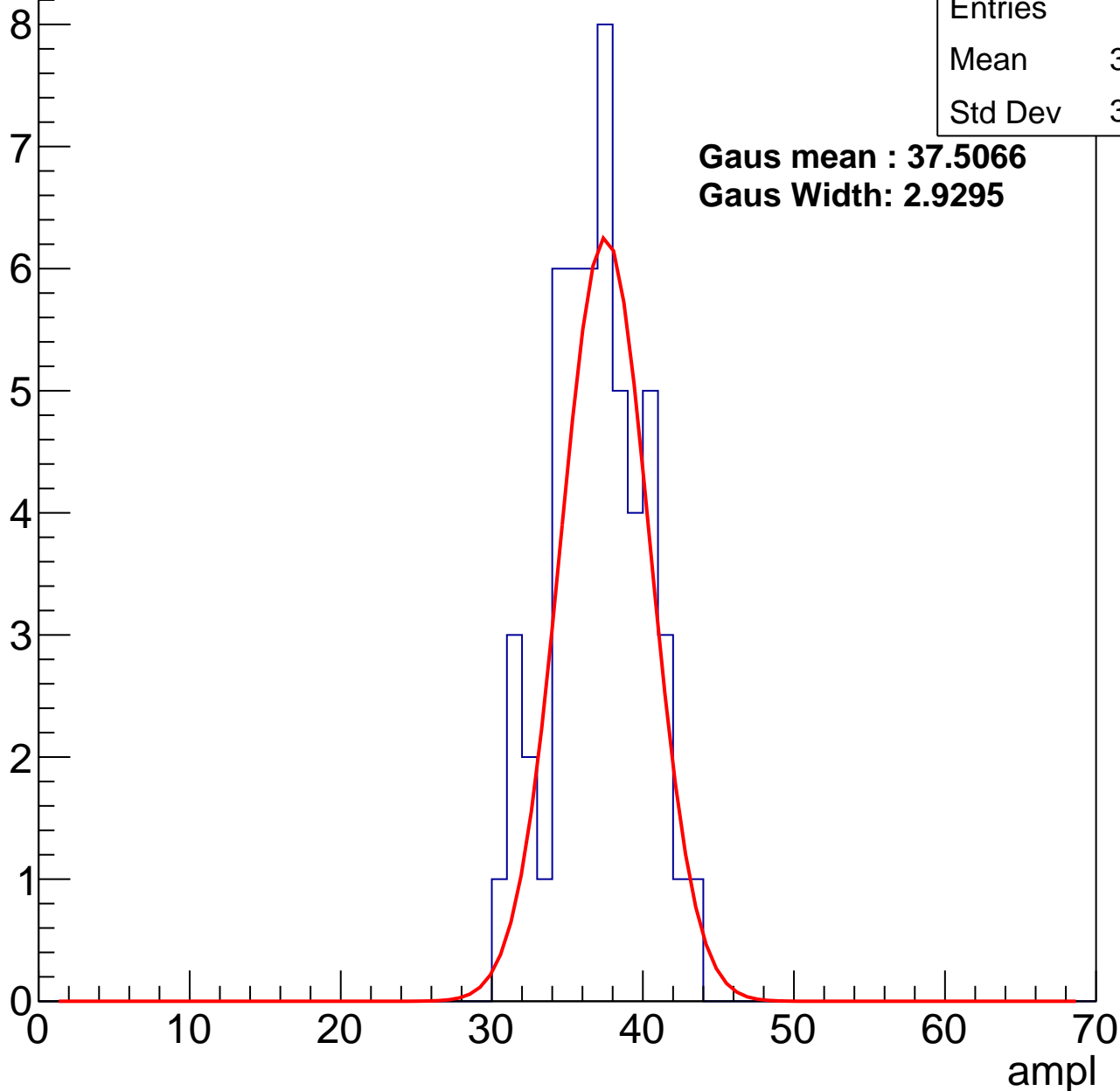
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	36.54
Std Dev	3.009

Gaus mean : 37.5066

Gaus Width: 2.9295



B1L102S, U2-ch14, adc2

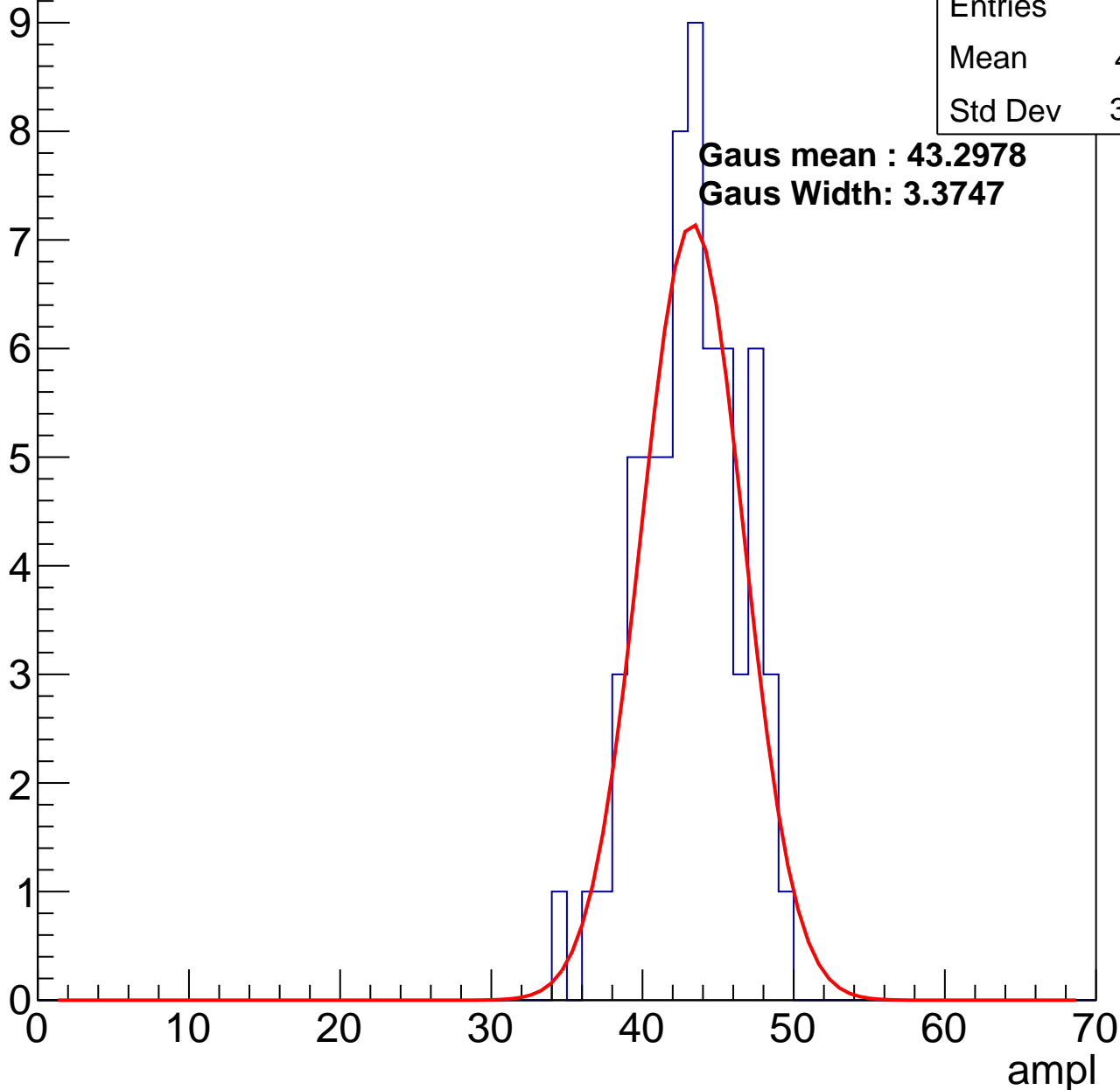
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.71
Std Dev	3.219

Gaus mean : 43.2978

Gaus Width: 3.3747

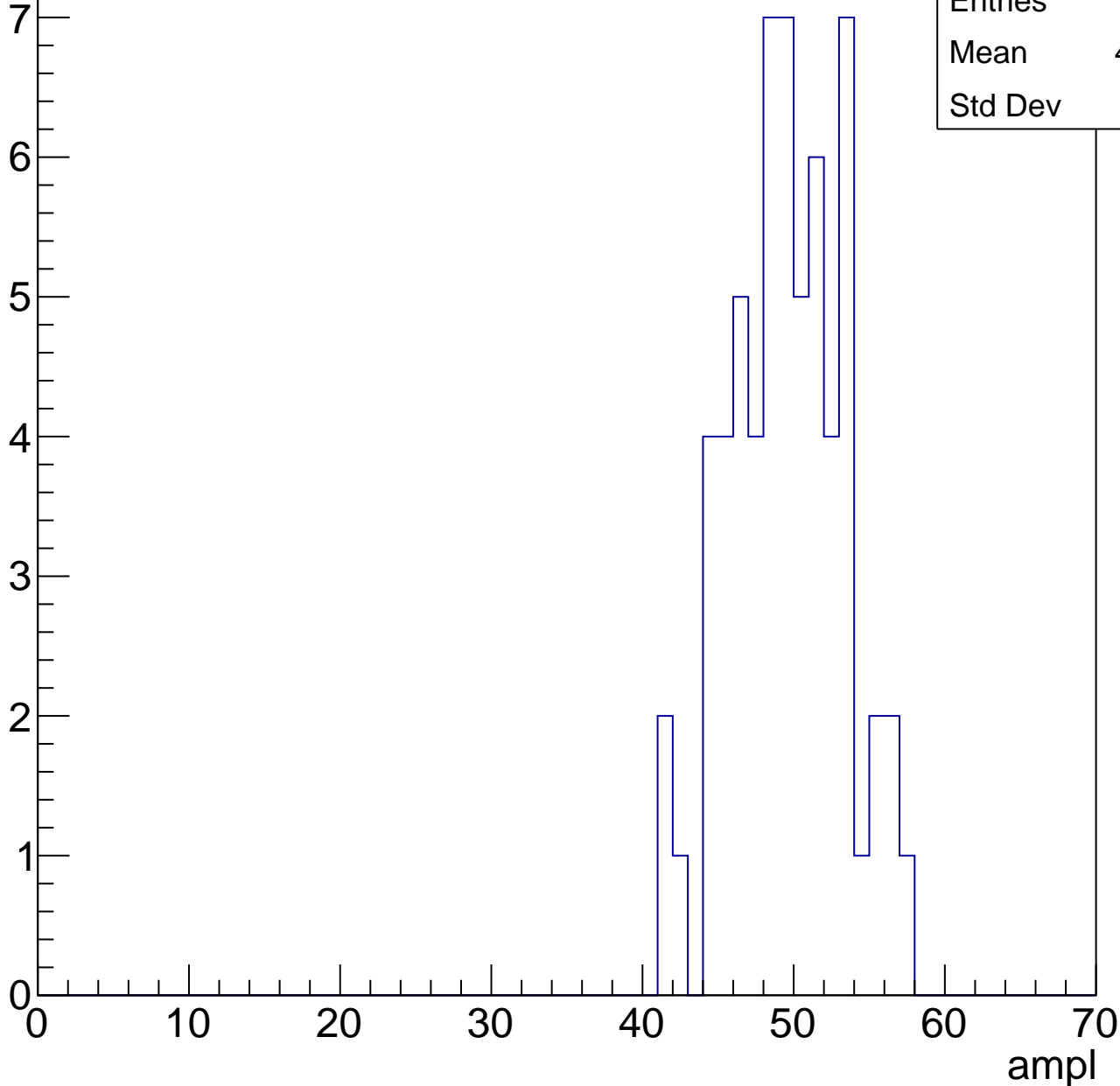


B1L102S, U2-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.11
Std Dev	3.69

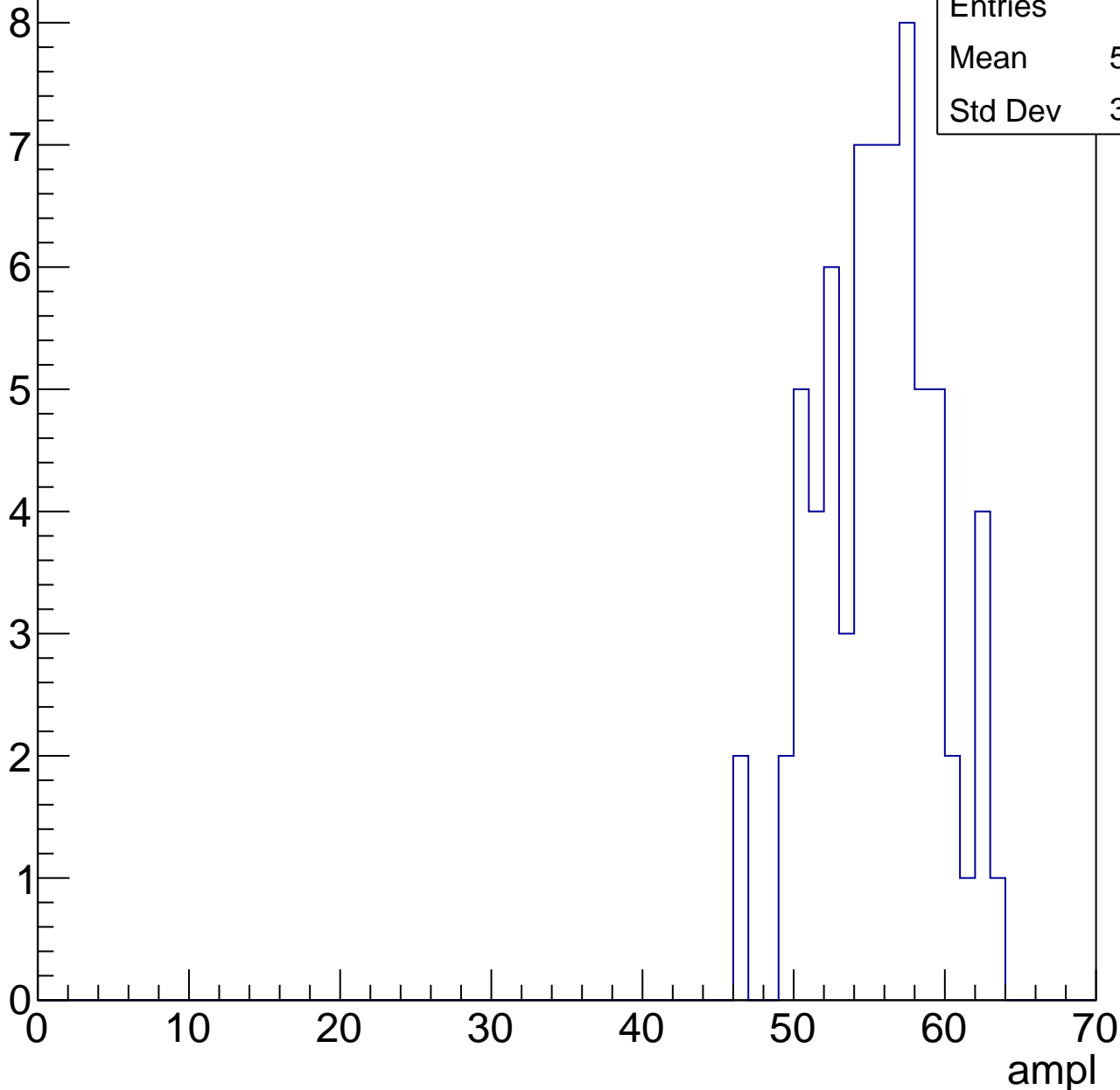


B1L102S, U2-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	55.12
Std Dev	3.817



B1L102S, U2-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

41

Mean

60

Std Dev

2.419

ampl

0

10

20

30

40

50

60

70

B1L102S, U2-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

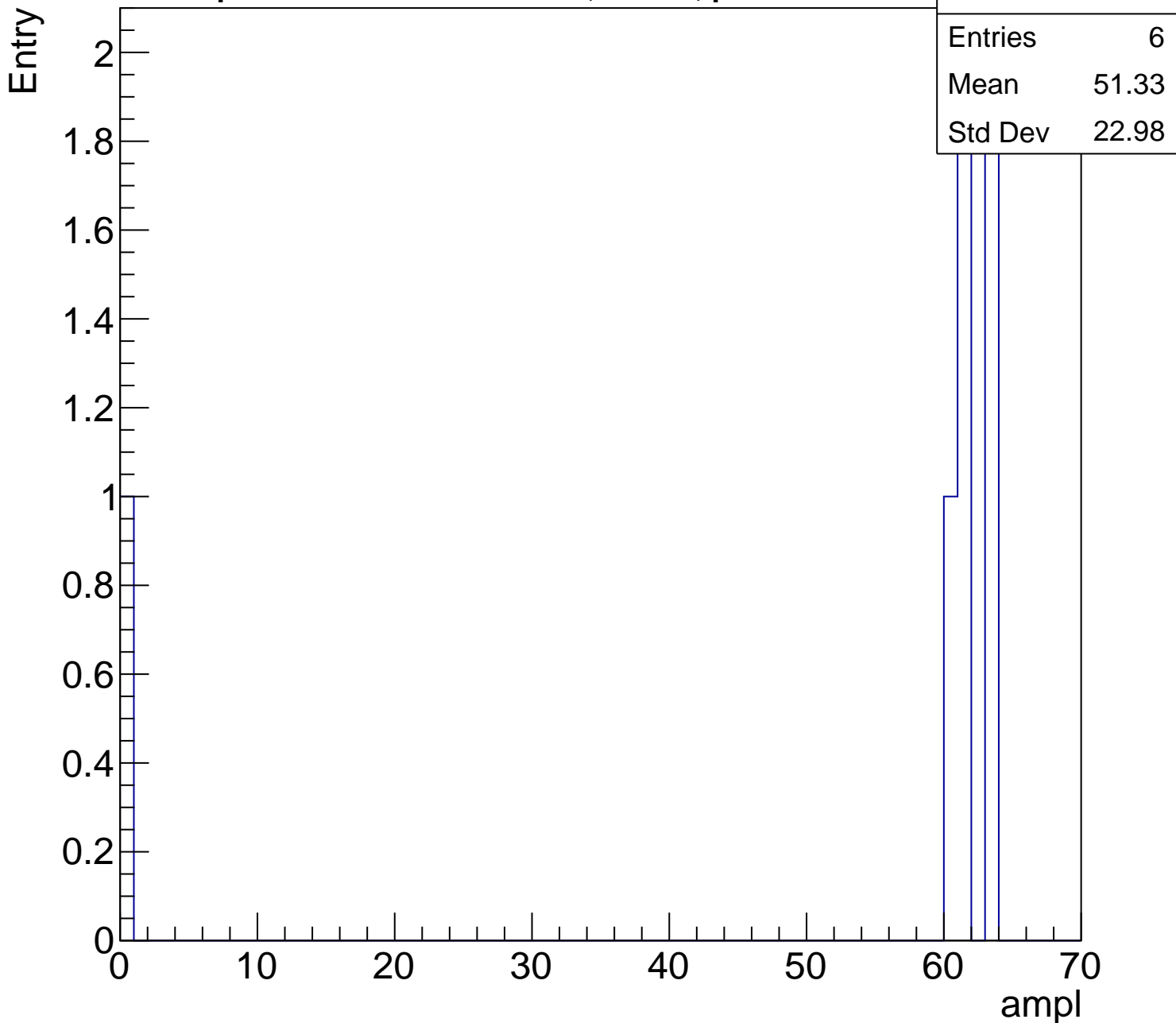
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

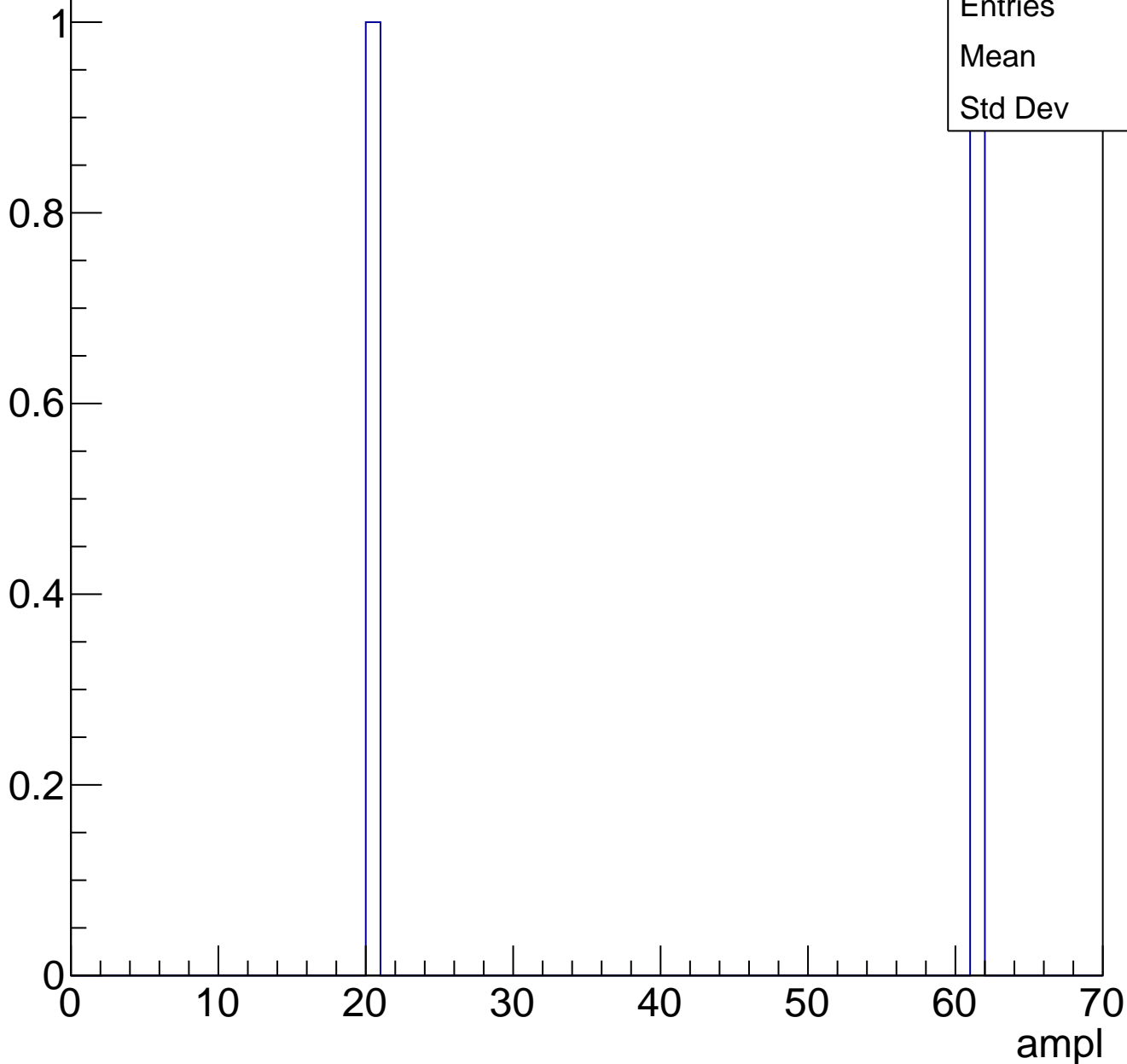
ampl



B1L102S, U2-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch15, adc0

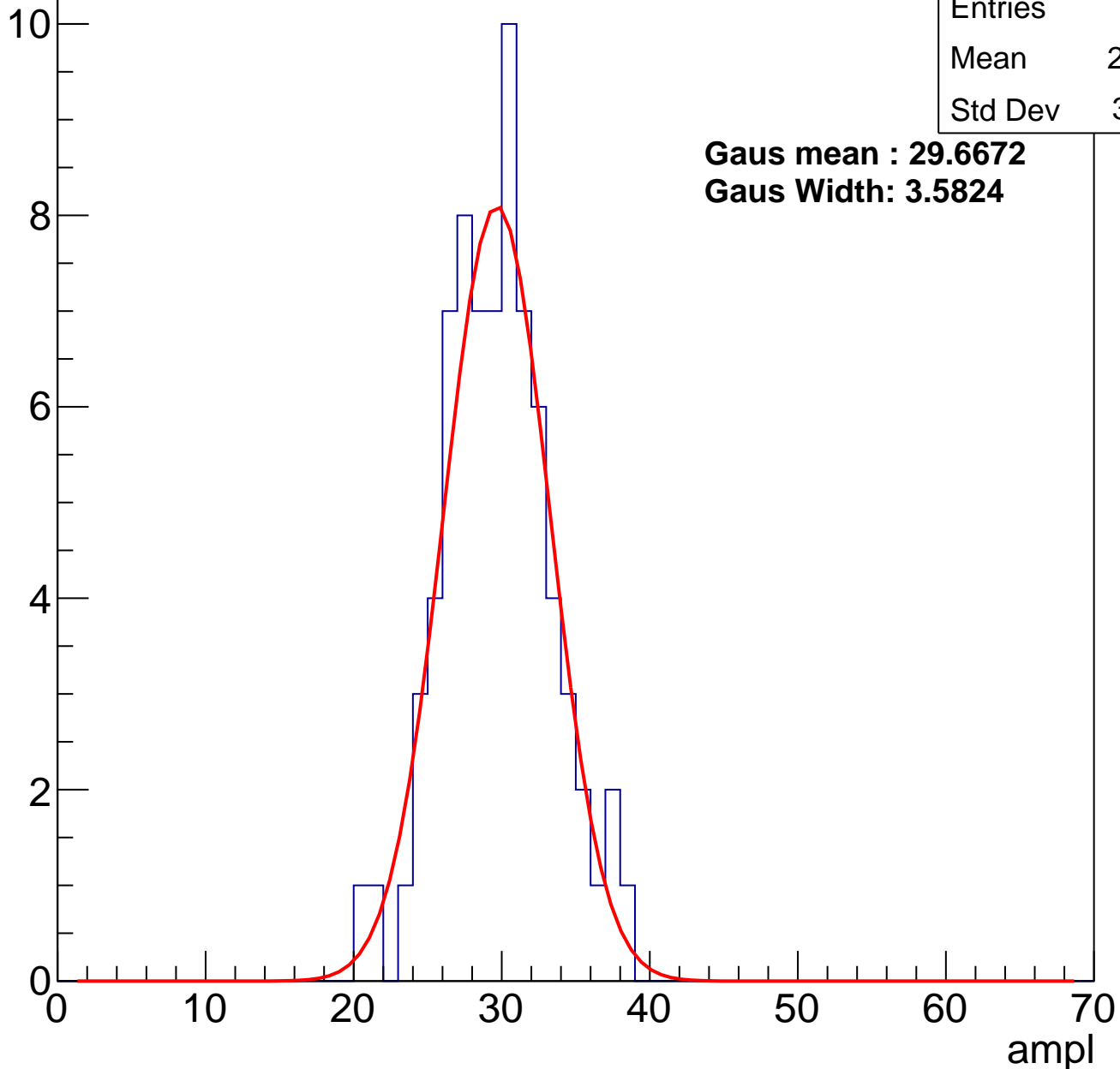
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	29.25
Std Dev	3.611

Gaus mean : 29.6672

Gaus Width: 3.5824

Entry



B1L102S, U2-ch15, adc1

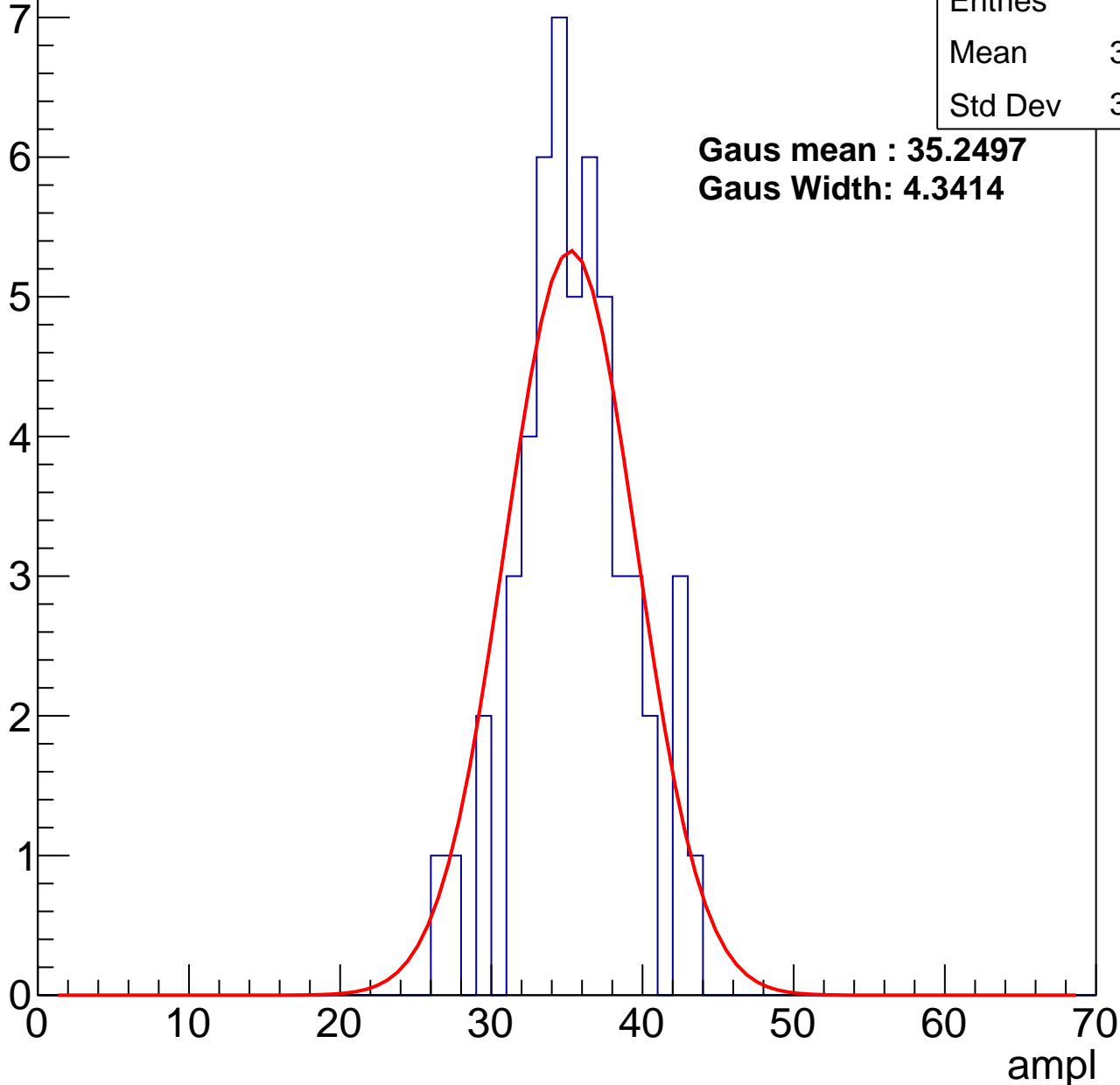
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.08
Std Dev	3.652

Gaus mean : 35.2497

Gaus Width: 4.3414



B1L102S, U2-ch15, adc2

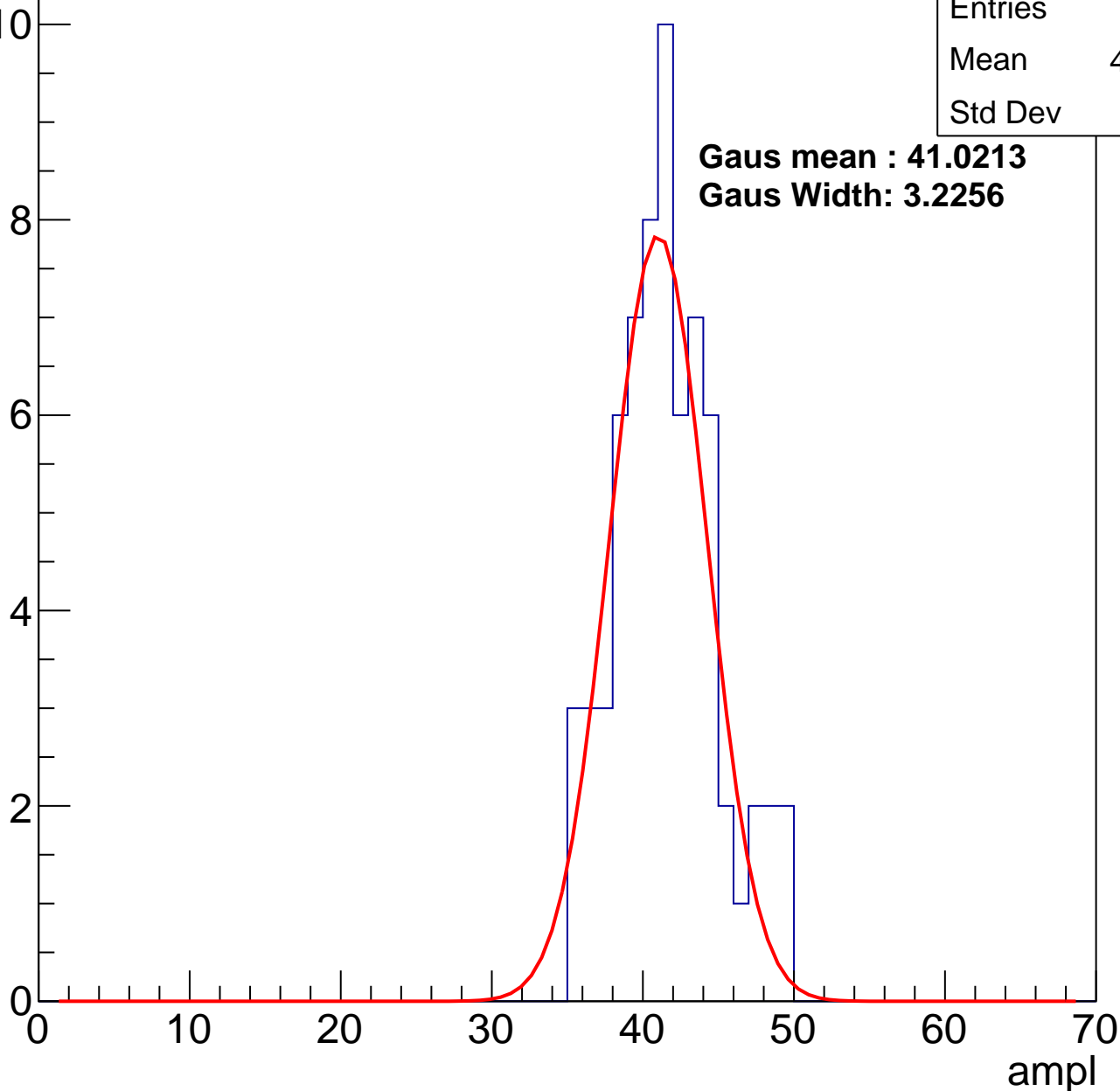
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.12
Std Dev	3.35

Gaus mean : 41.0213

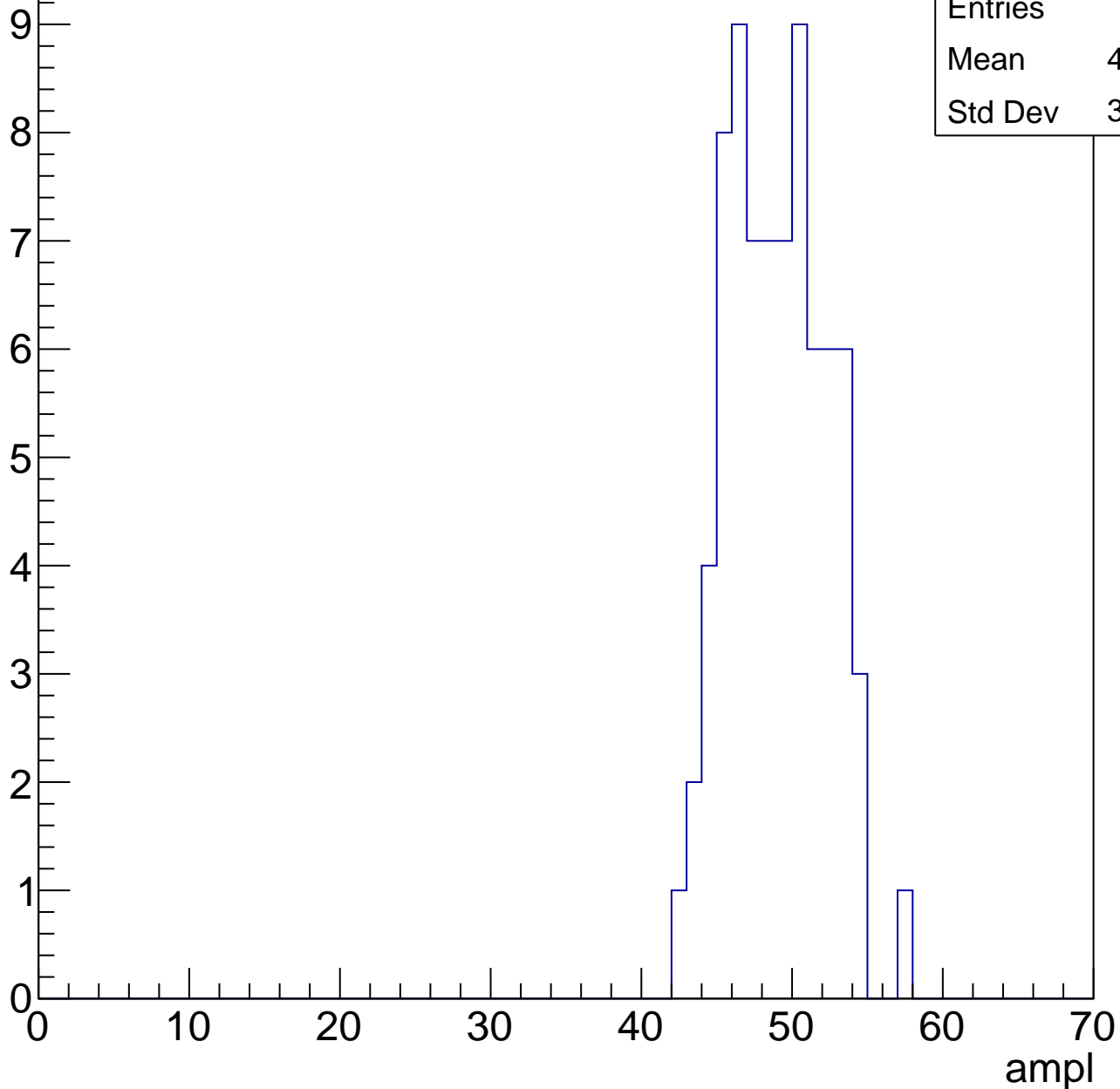
Gaus Width: 3.2256



B1L102S, U2-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



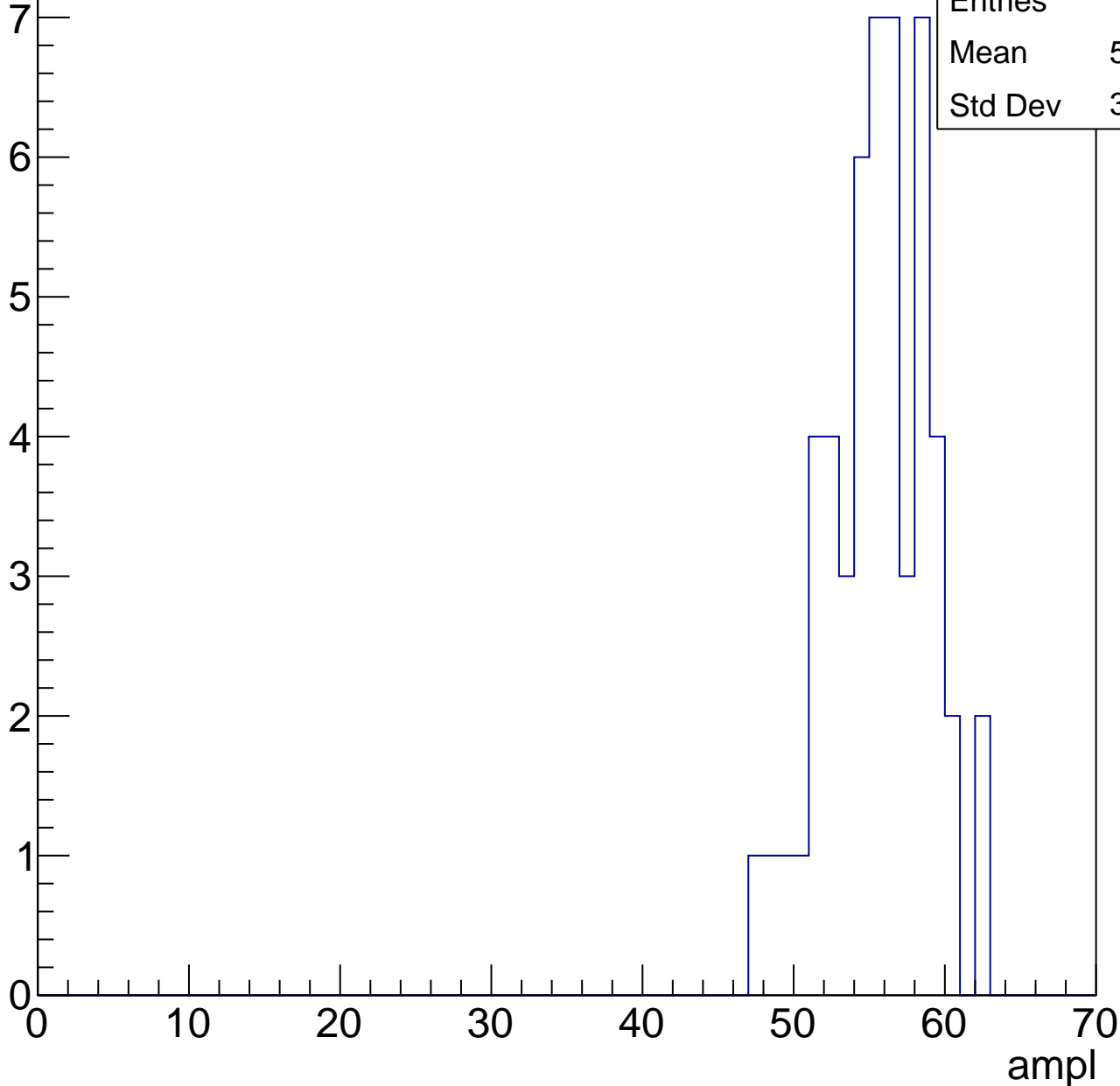
Entries	76
Mean	48.57
Std Dev	3.189

B1L102S, U2-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.15
Std Dev	3.322

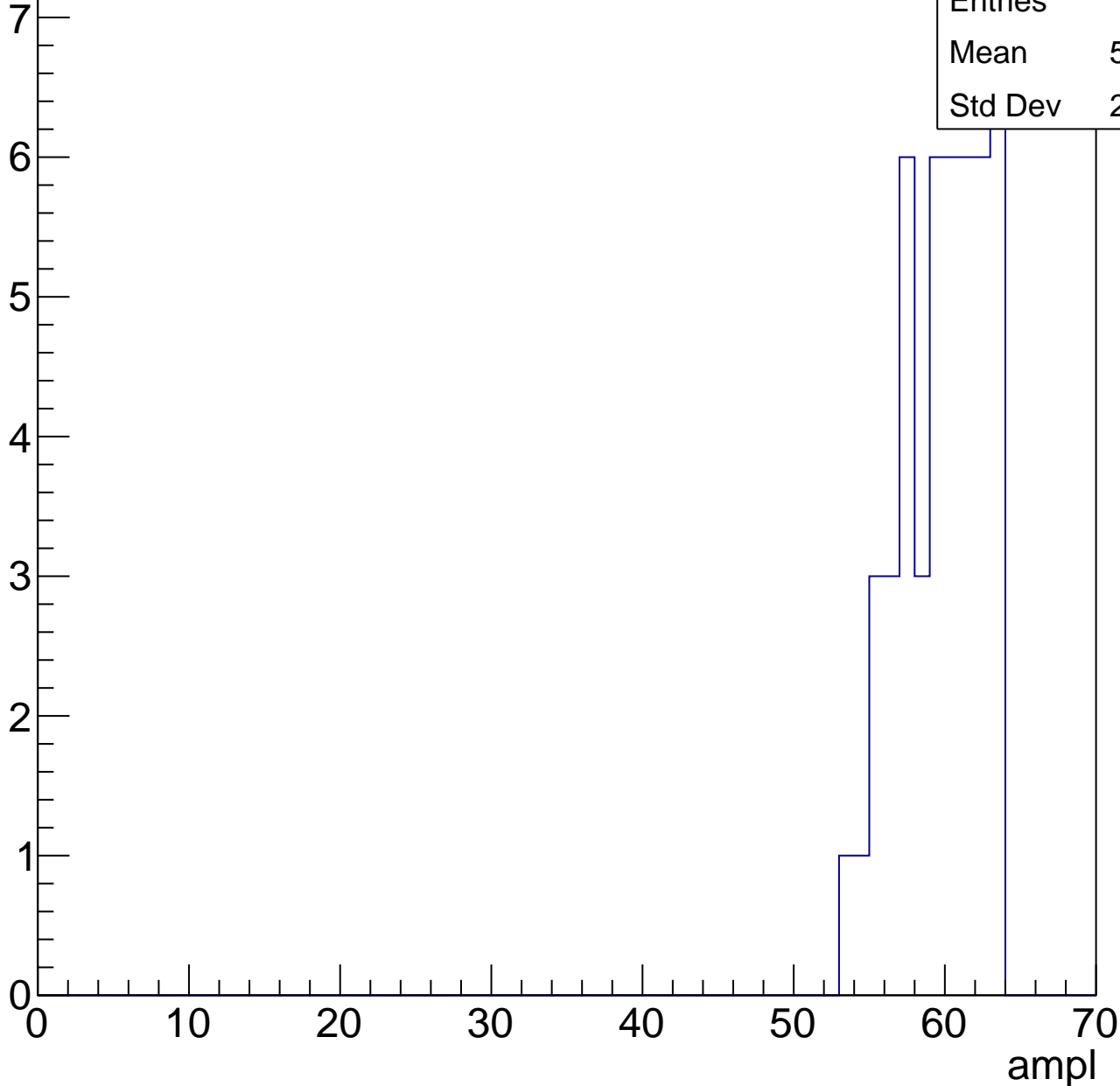


B1L102S, U2-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

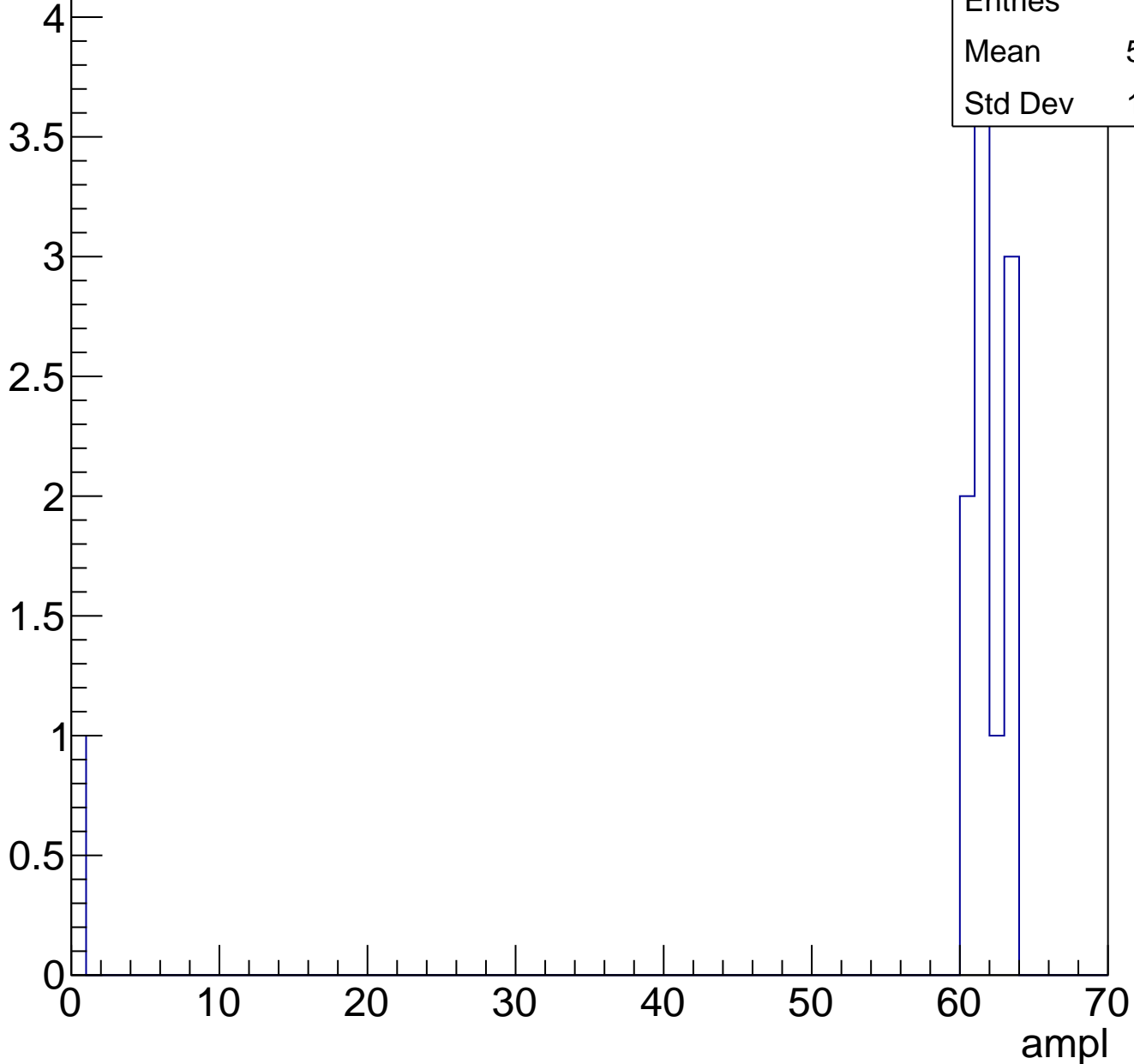
Entries	48
Mean	59.35
Std Dev	2.712



B1L102S, U2-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

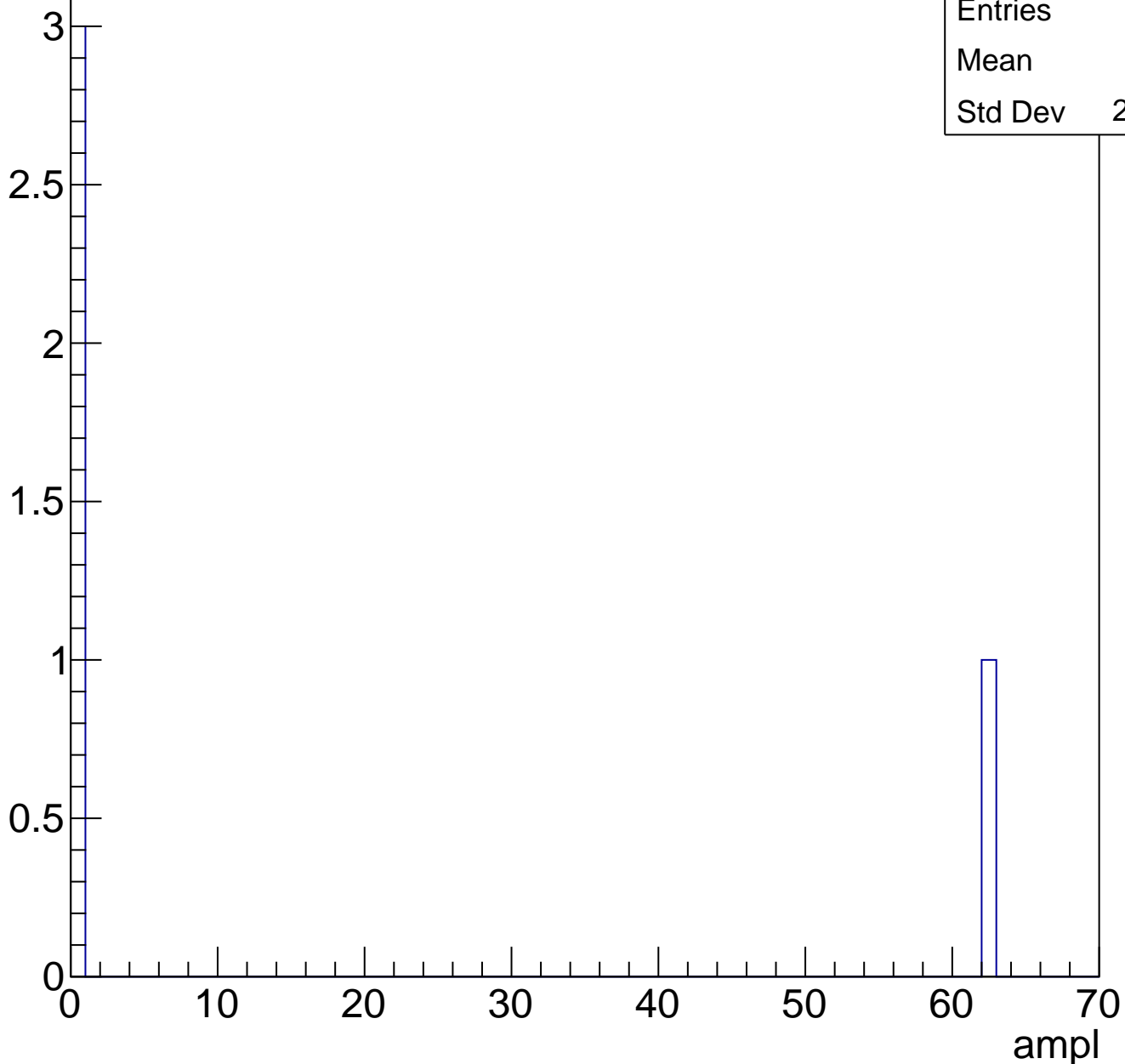
Entry



B1L102S, U2-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	15.5
Std Dev	26.85

B1L102S, U2-ch16, adc0

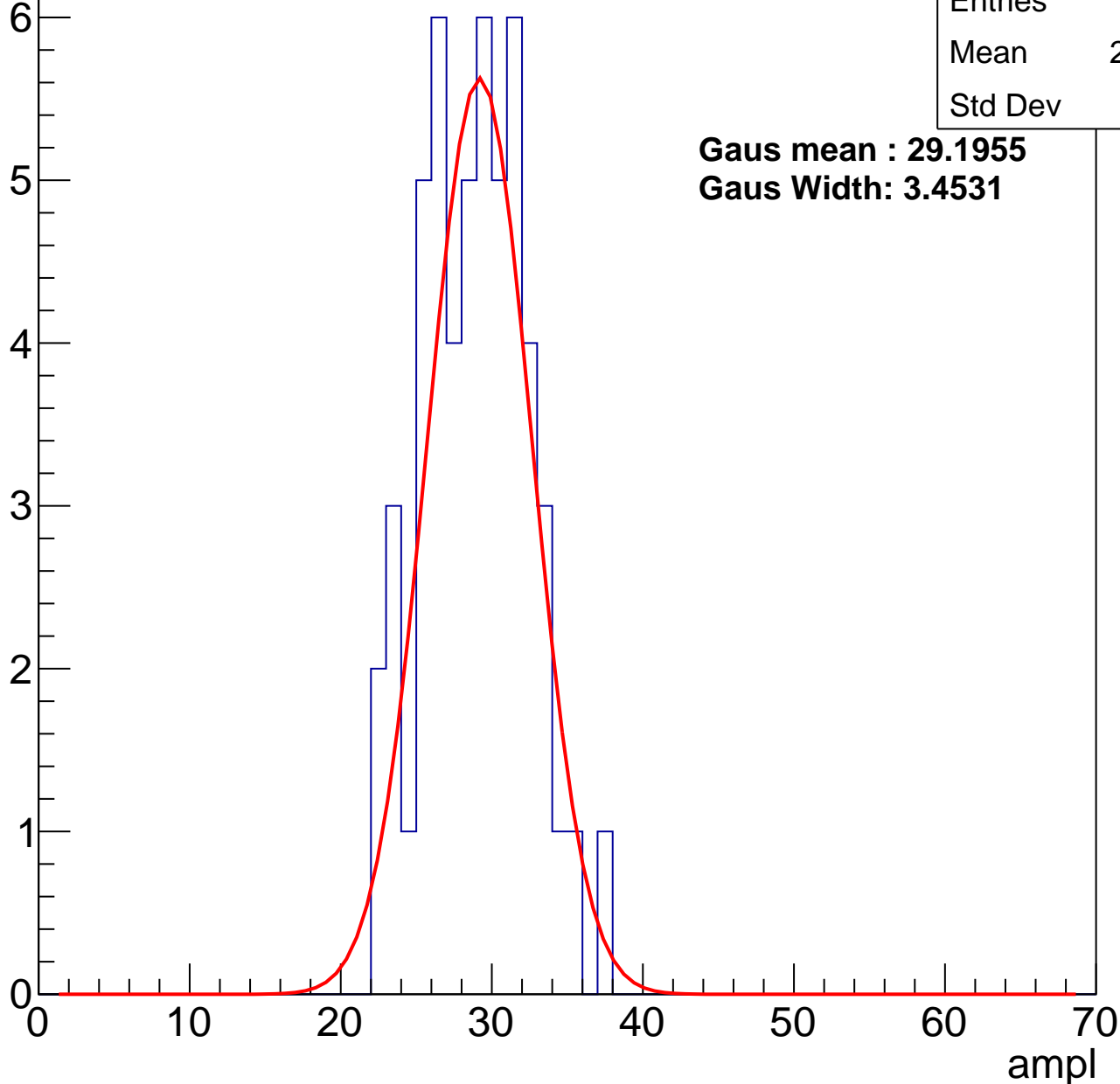
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	28.47
Std Dev	3.39

Gaus mean : 29.155

Gaus Width: 3.4531



B1L102S, U2-ch16, adc1

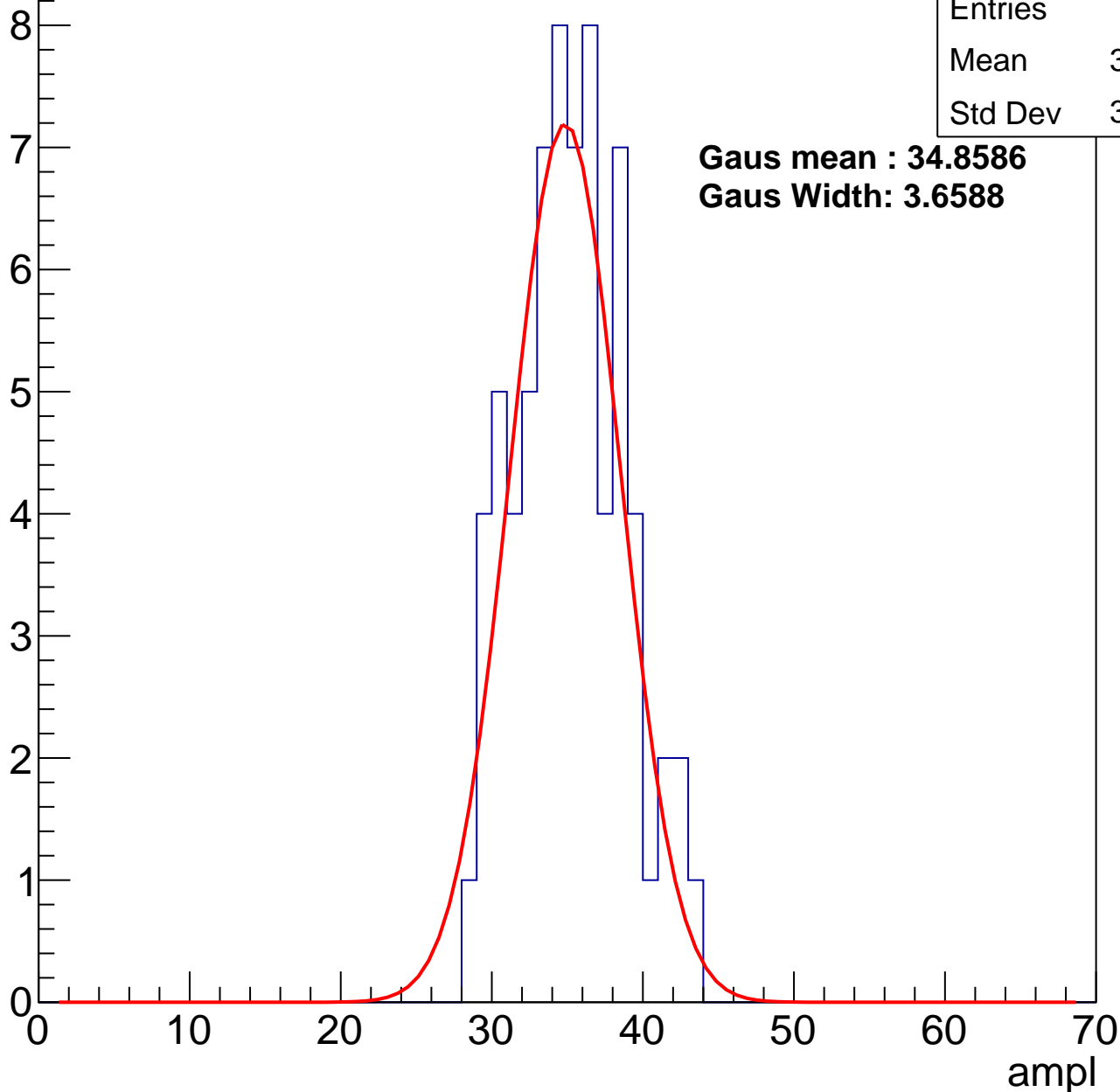
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.76
Std Dev	3.523

Gaus mean : 34.8586

Gaus Width: 3.6588



B1L102S, U2-ch16, adc2

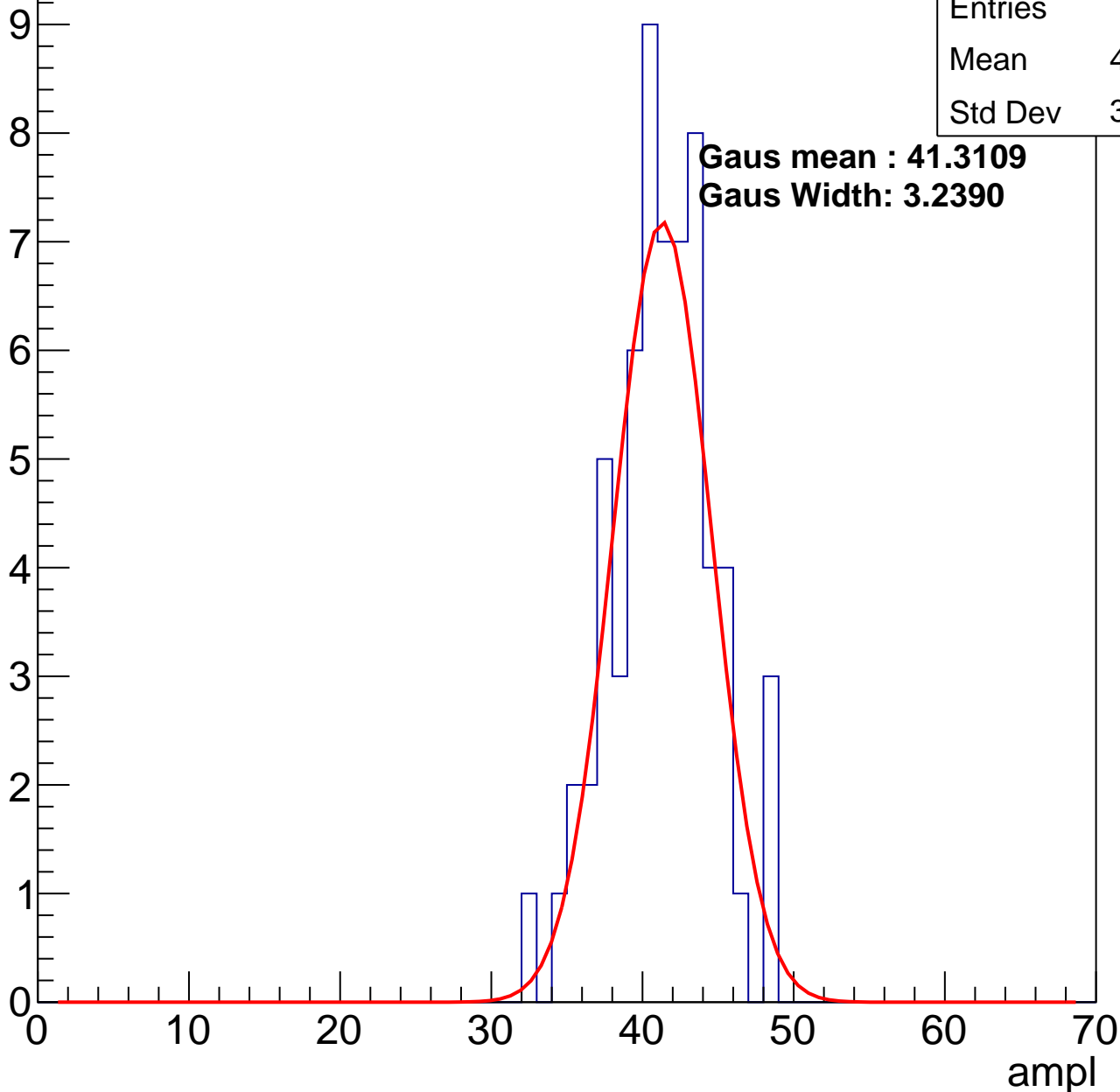
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	40.83
Std Dev	3.336

Gaus mean : 41.3109

Gaus Width: 3.2390

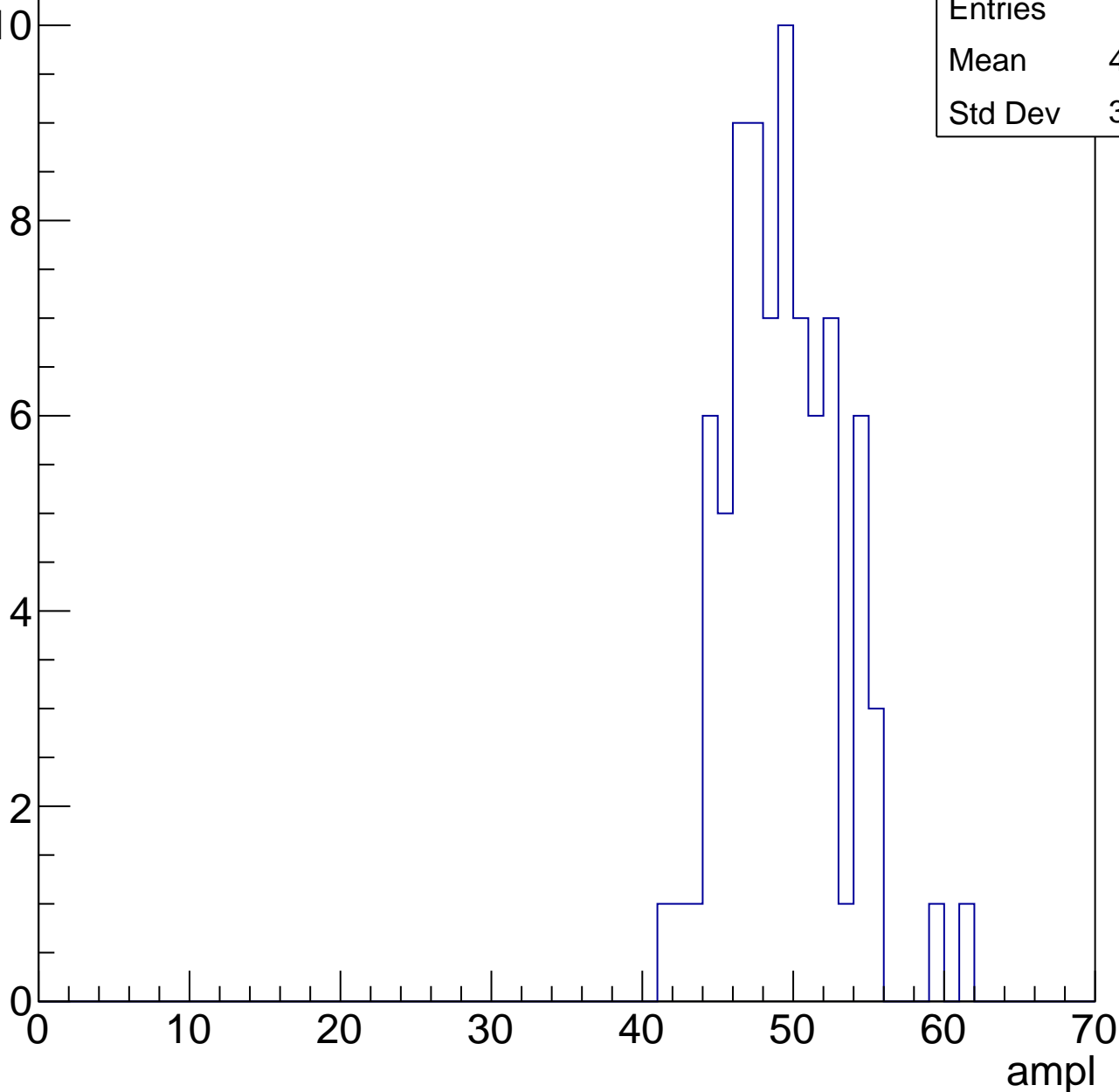


B1L102S, U2-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	48.89
Std Dev	3.709



B1L102S, U2-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

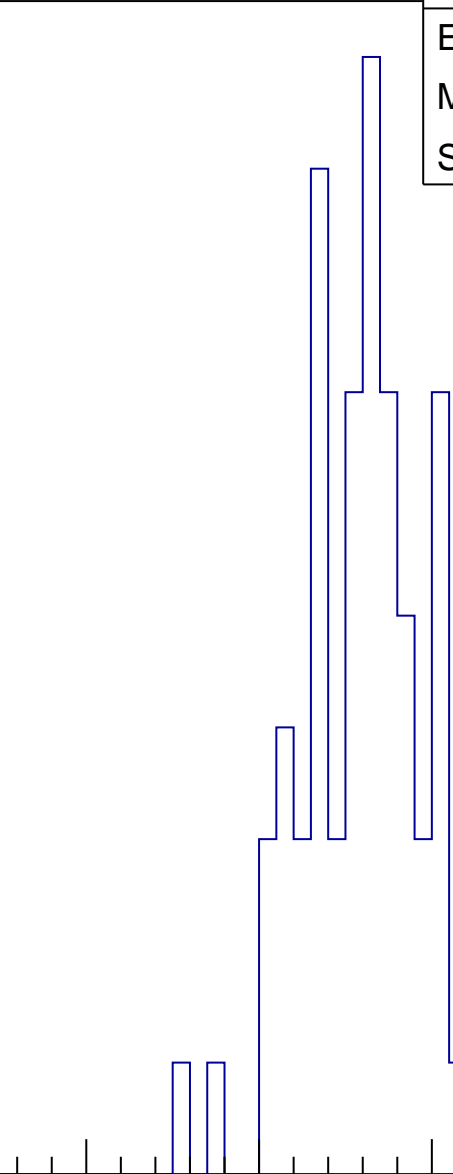
Entries	68
Mean	55.65
Std Dev	3.685

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

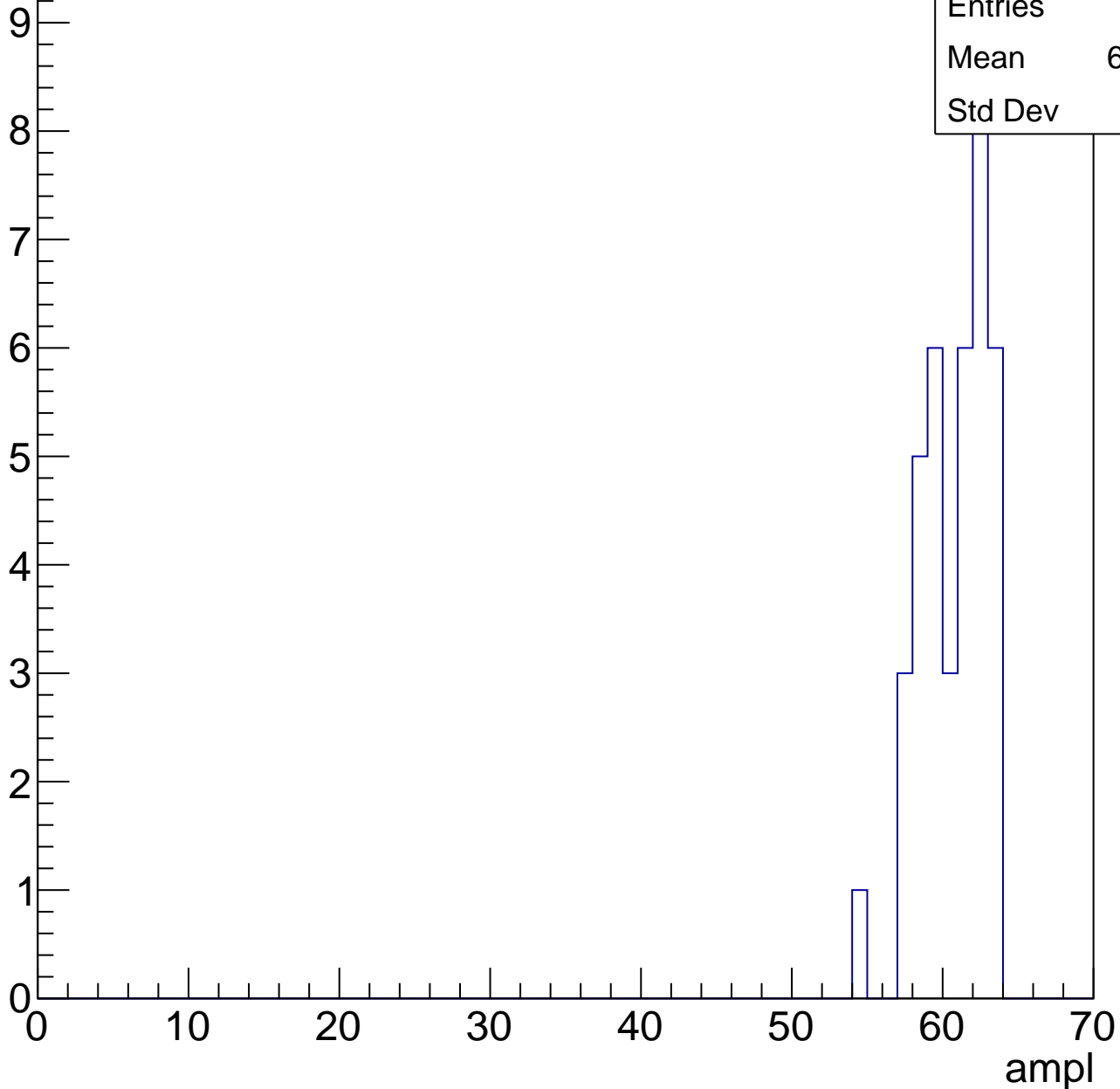


B1L102S, U2-ch16, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	60.28
Std Dev	2.16



B1L102S, U2-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L102S, U2-ch17, adc0

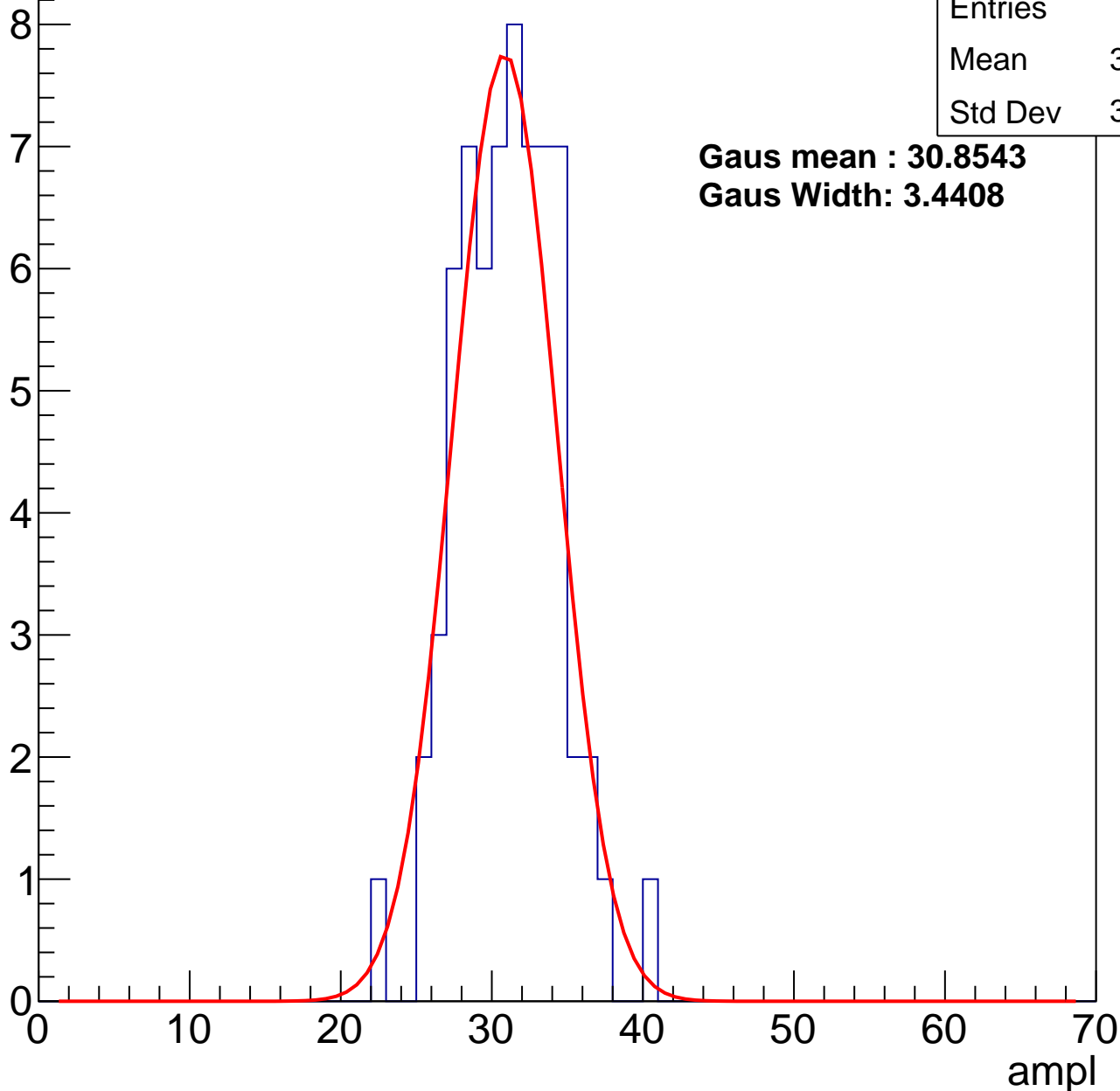
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.63
Std Dev	3.245

Gaus mean : 30.8543

Gaus Width: 3.4408



B1L102S, U2-ch17, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	54
Mean	36.46
Std Dev	3.23

Gaus mean : 37.4217

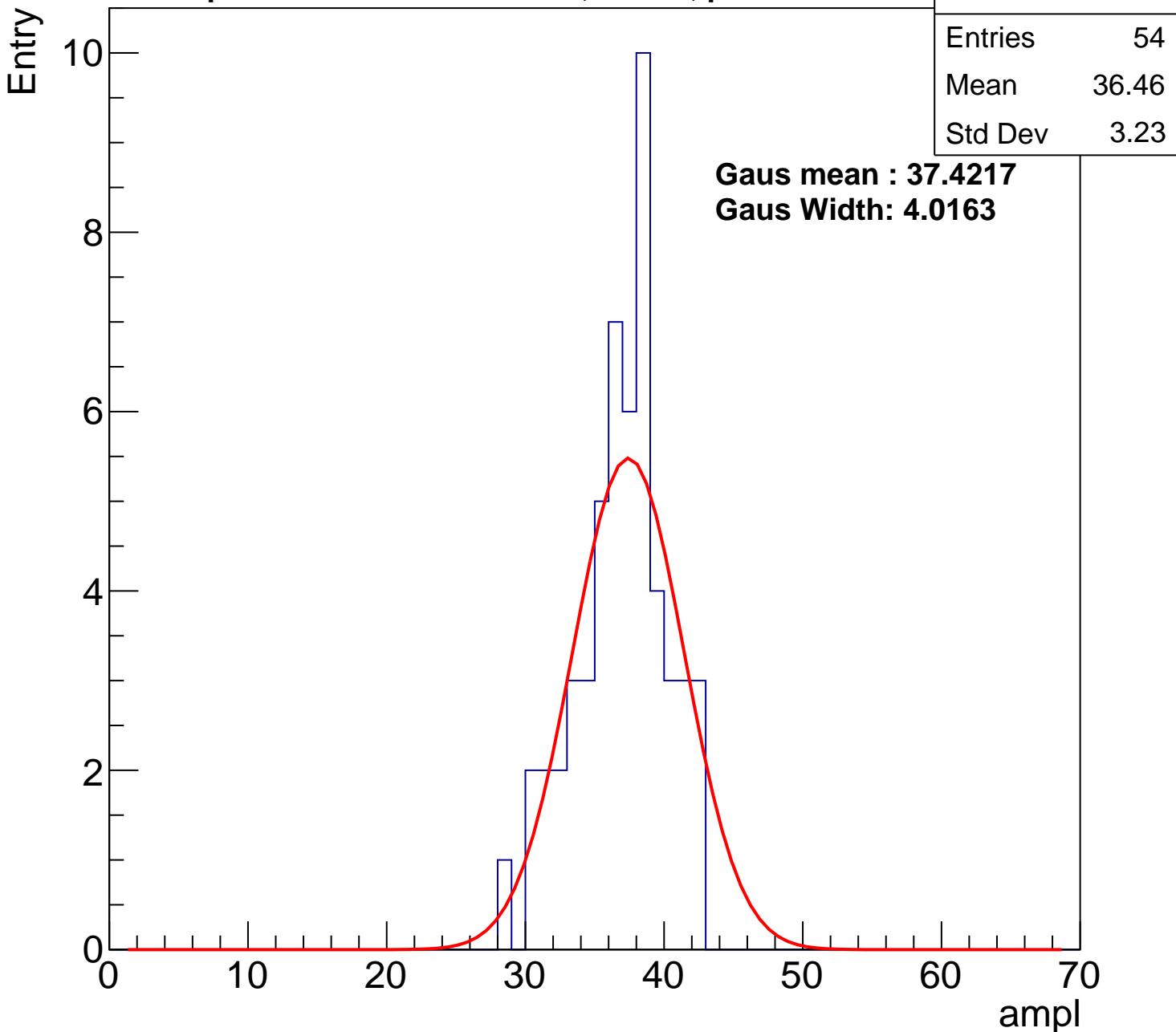
Gaus Width: 4.0163

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch17, adc2

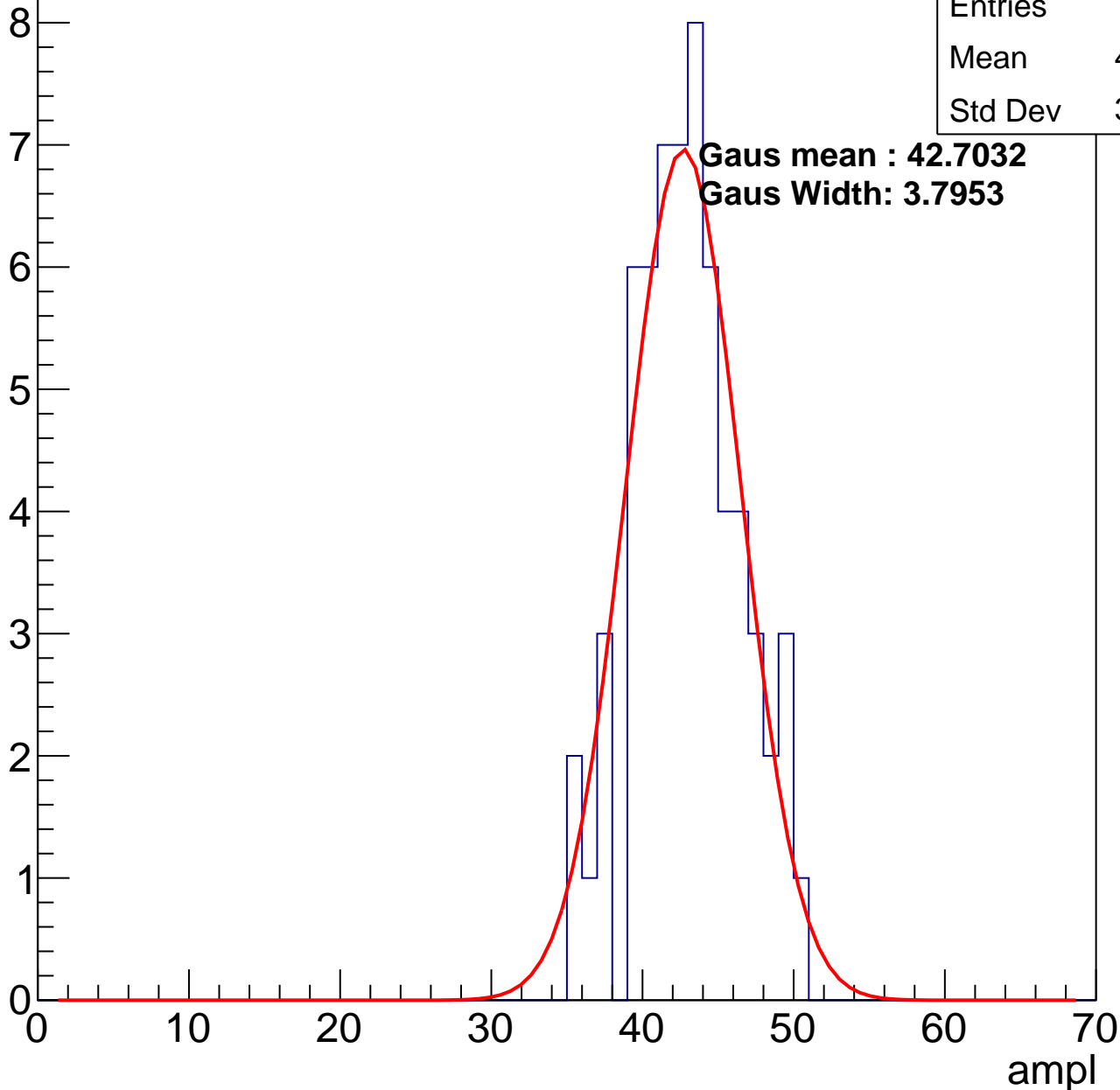
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.51
Std Dev	3.491

Gaus mean : 42.7032

Gaus Width: 3.7953

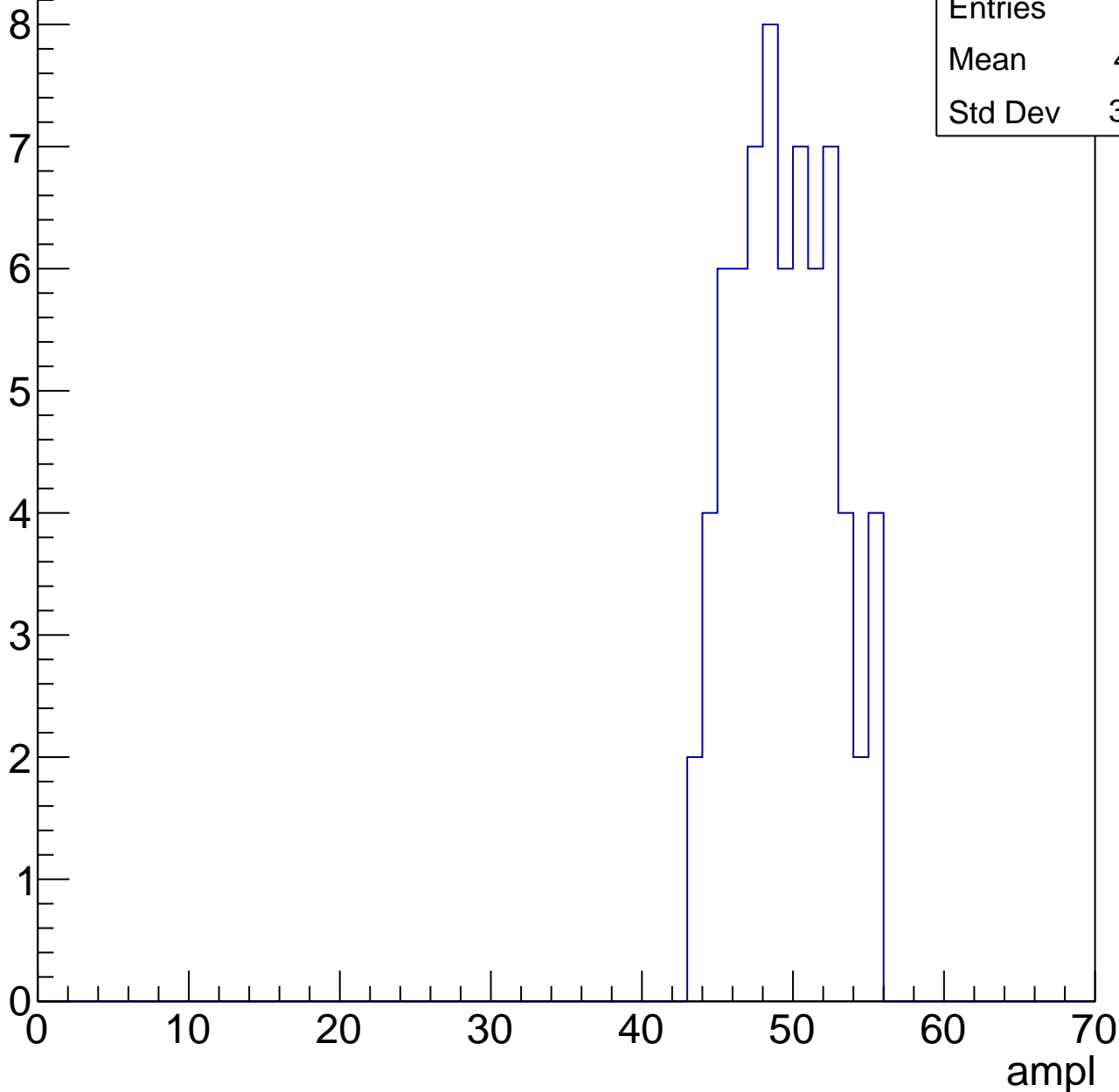


B1L102S, U2-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.91
Std Dev	3.207

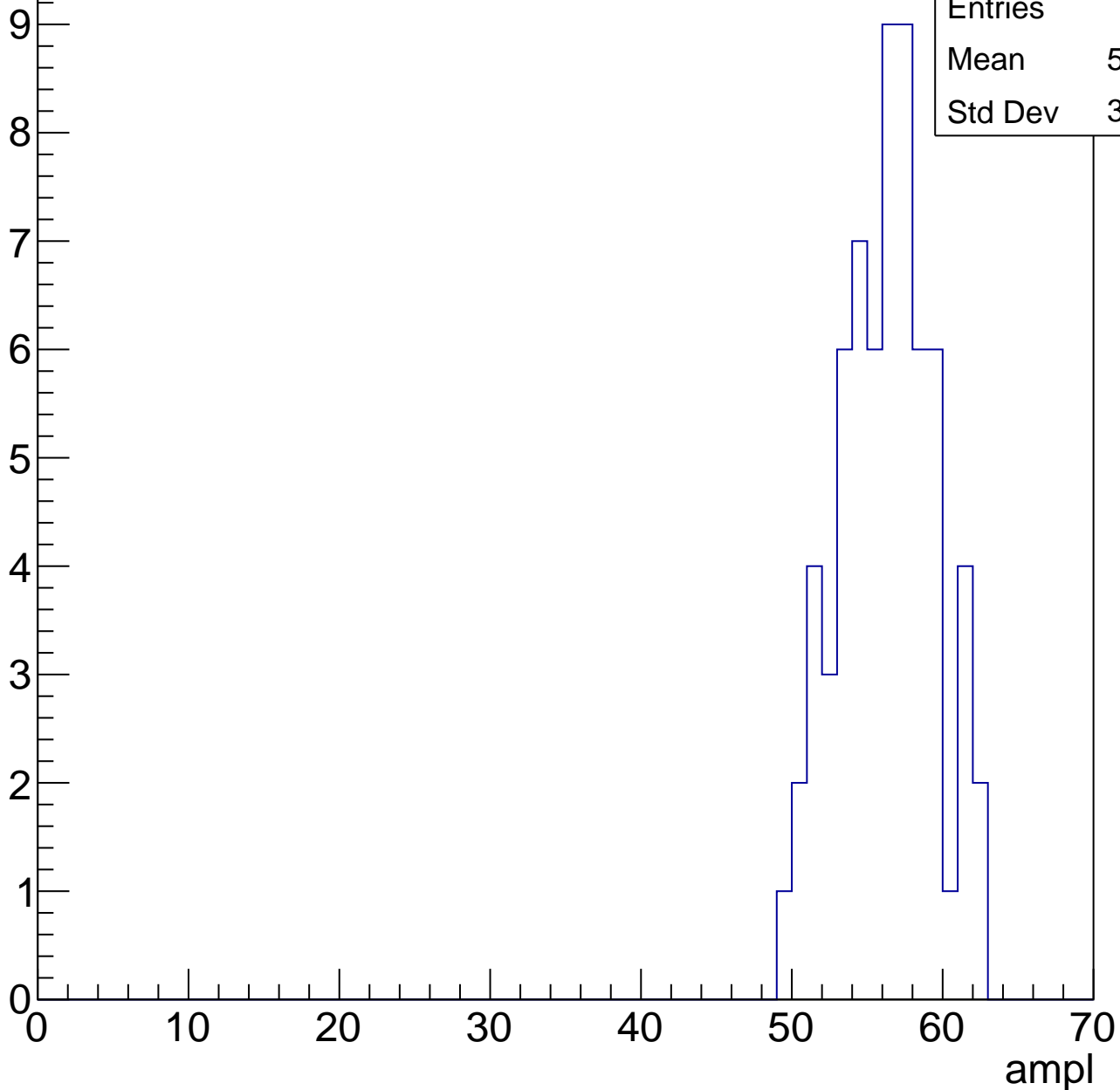


B1L102S, U2-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.79
Std Dev	3.087

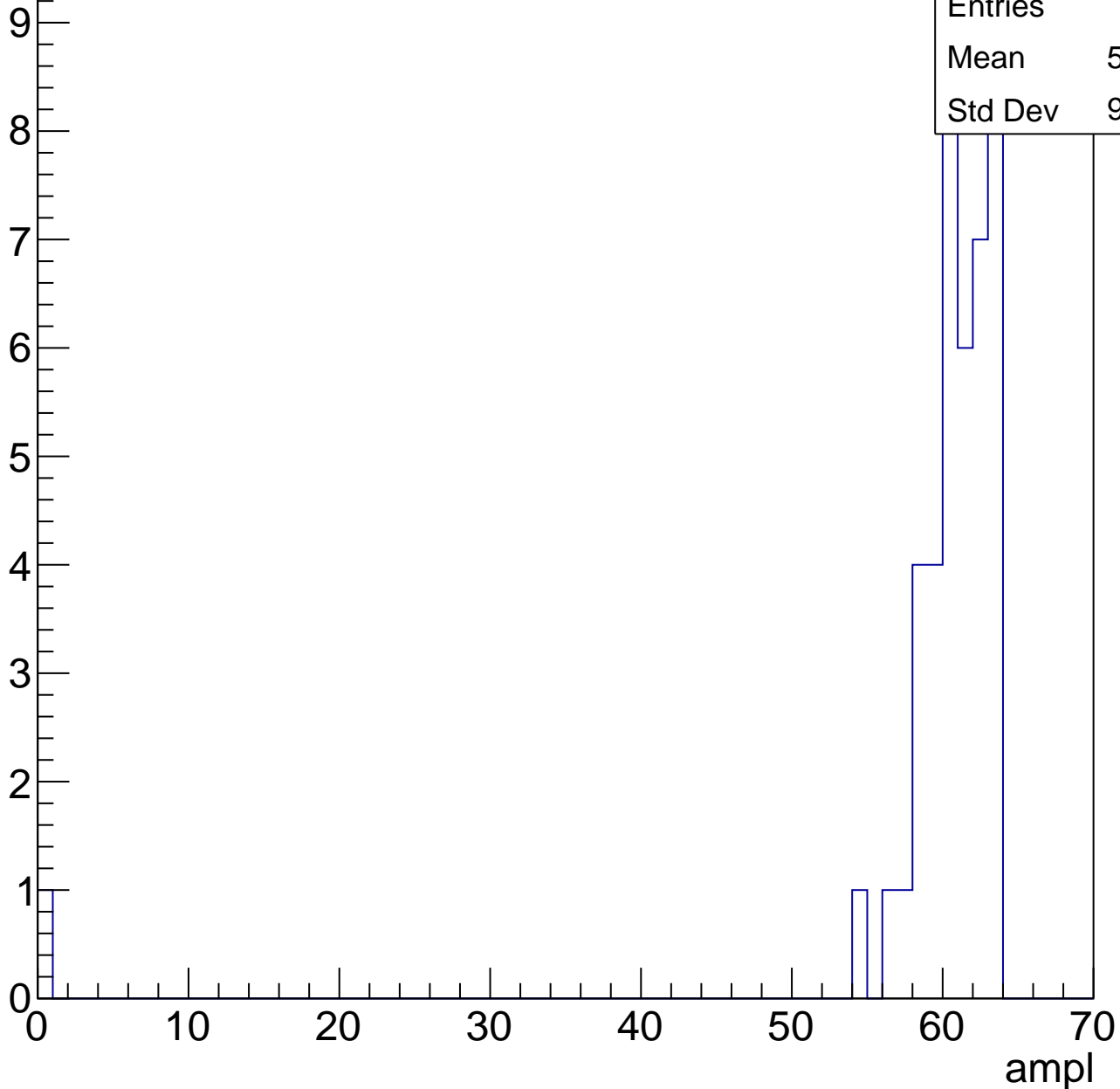


B1L102S, U2-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.12
Std Dev	9.354



B1L102S, U2-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch18, adc0

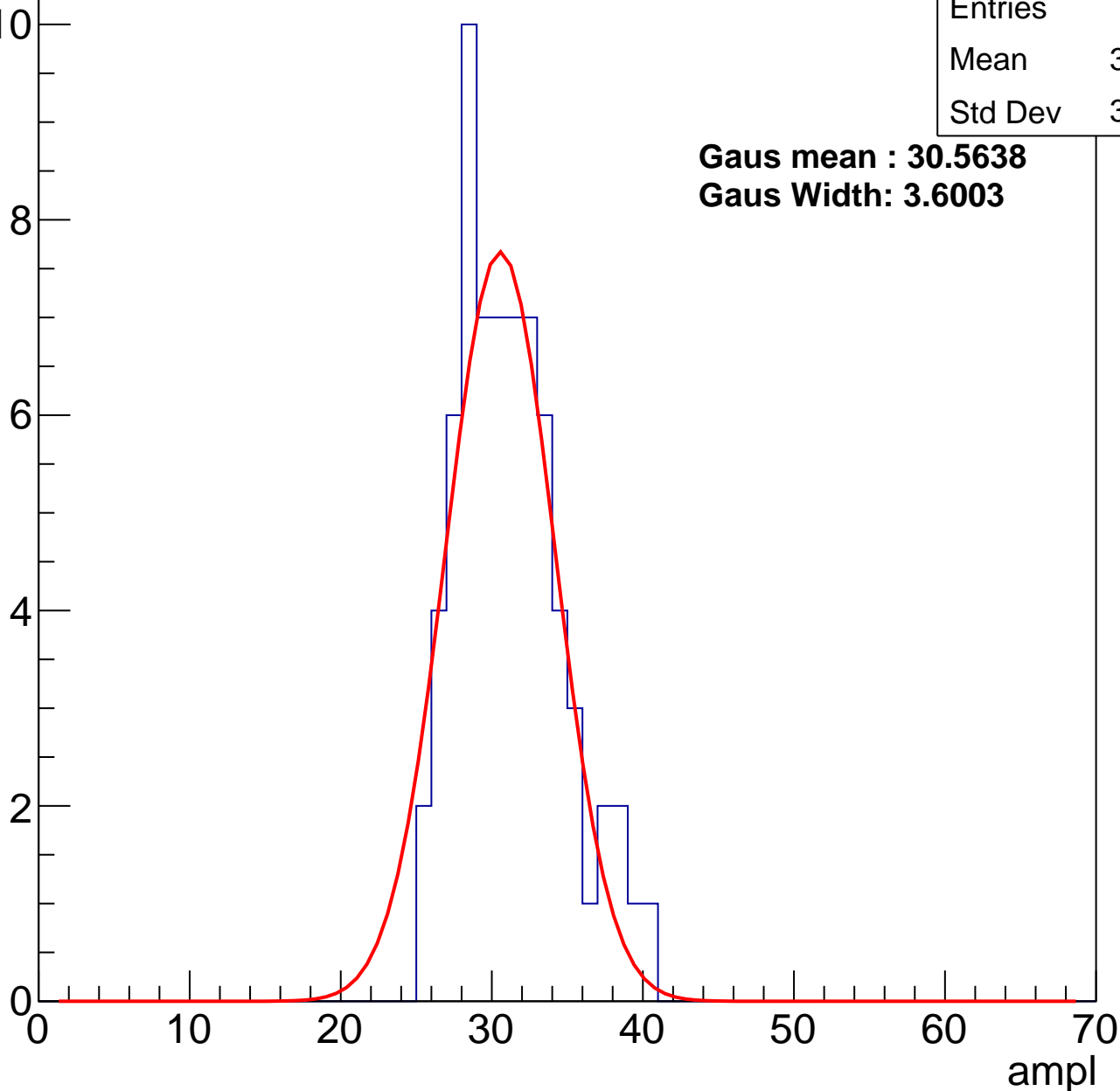
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	30.77
Std Dev	3.473

Gaus mean : 30.5638

Gaus Width: 3.6003



B1L102S, U2-ch18, adc1

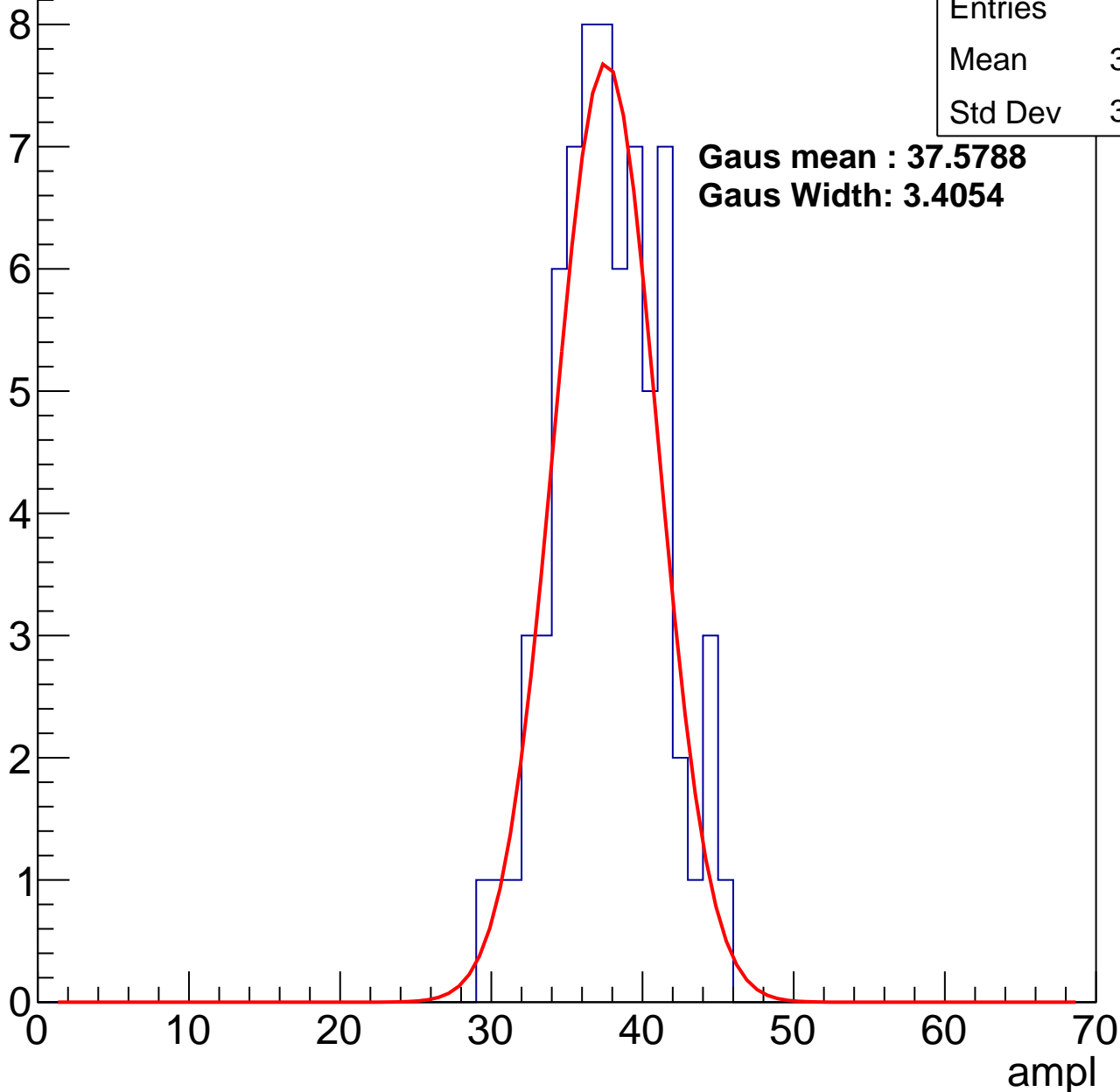
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	37.29
Std Dev	3.473

Gaus mean : 37.5788

Gaus Width: 3.4054



B1L102S, U2-ch18, adc2

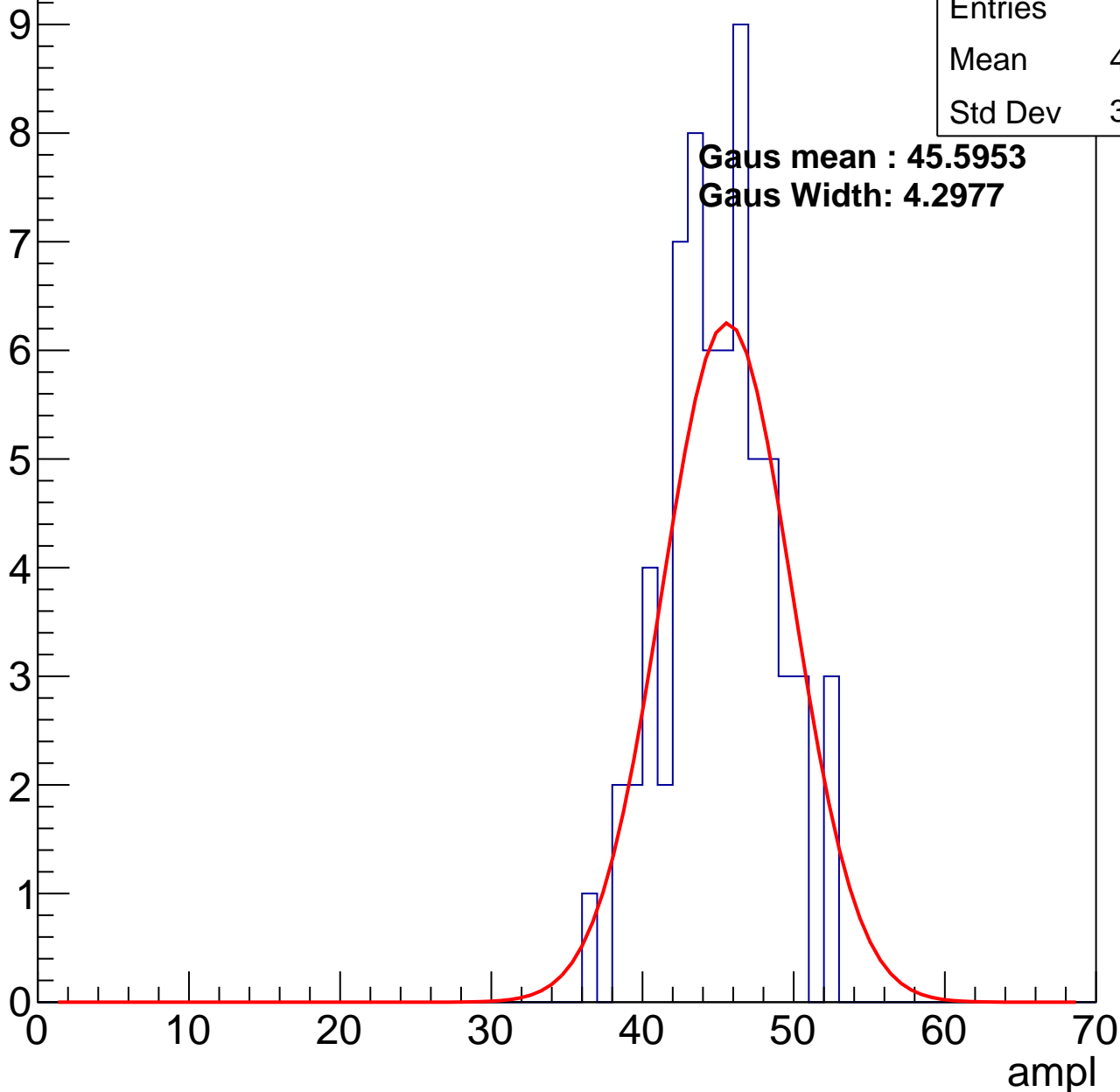
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	44.64
Std Dev	3.523

Gaus mean : 45.5953

Gaus Width: 4.2977

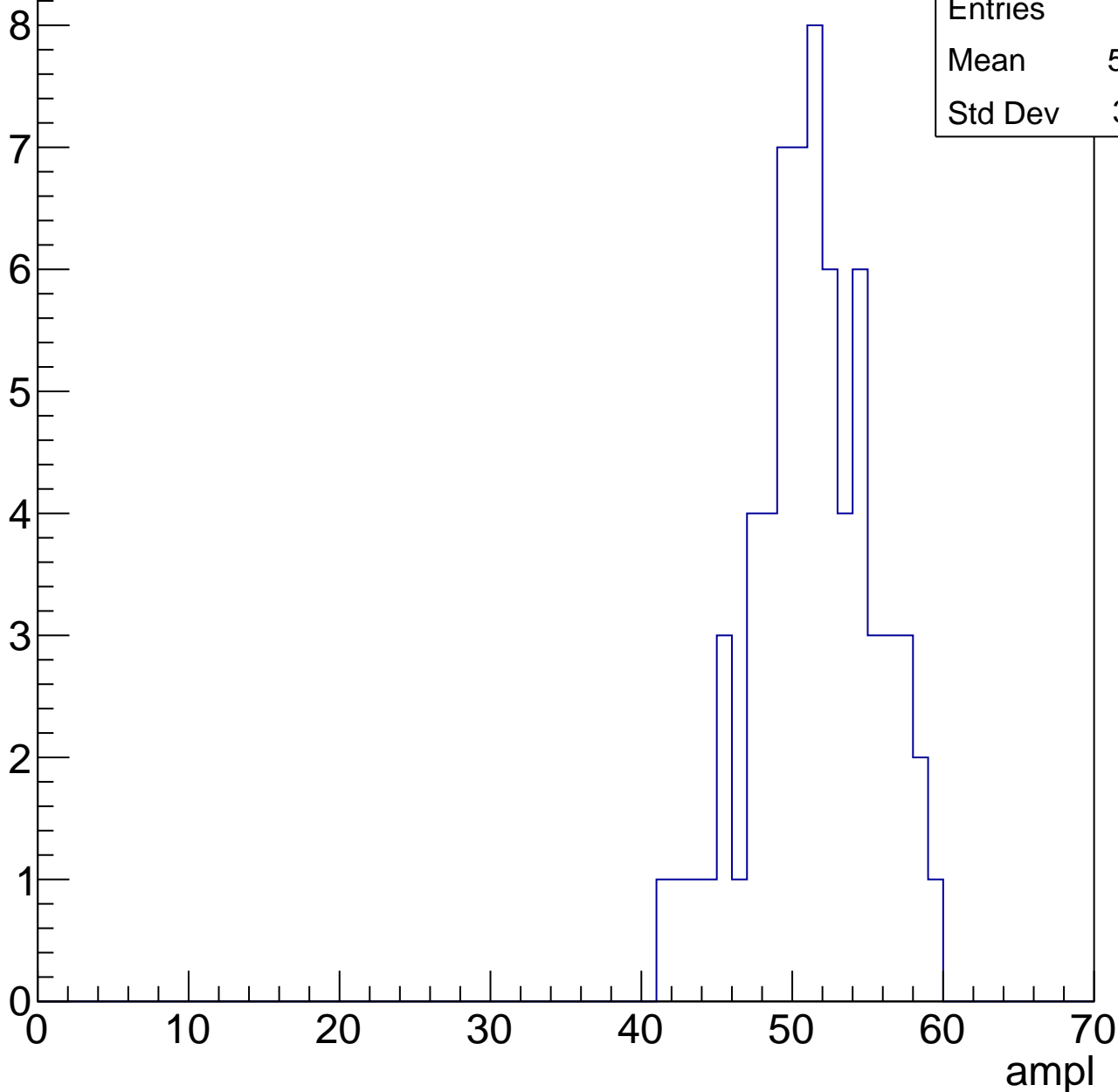


B1L102S, U2-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	50.89
Std Dev	3.951

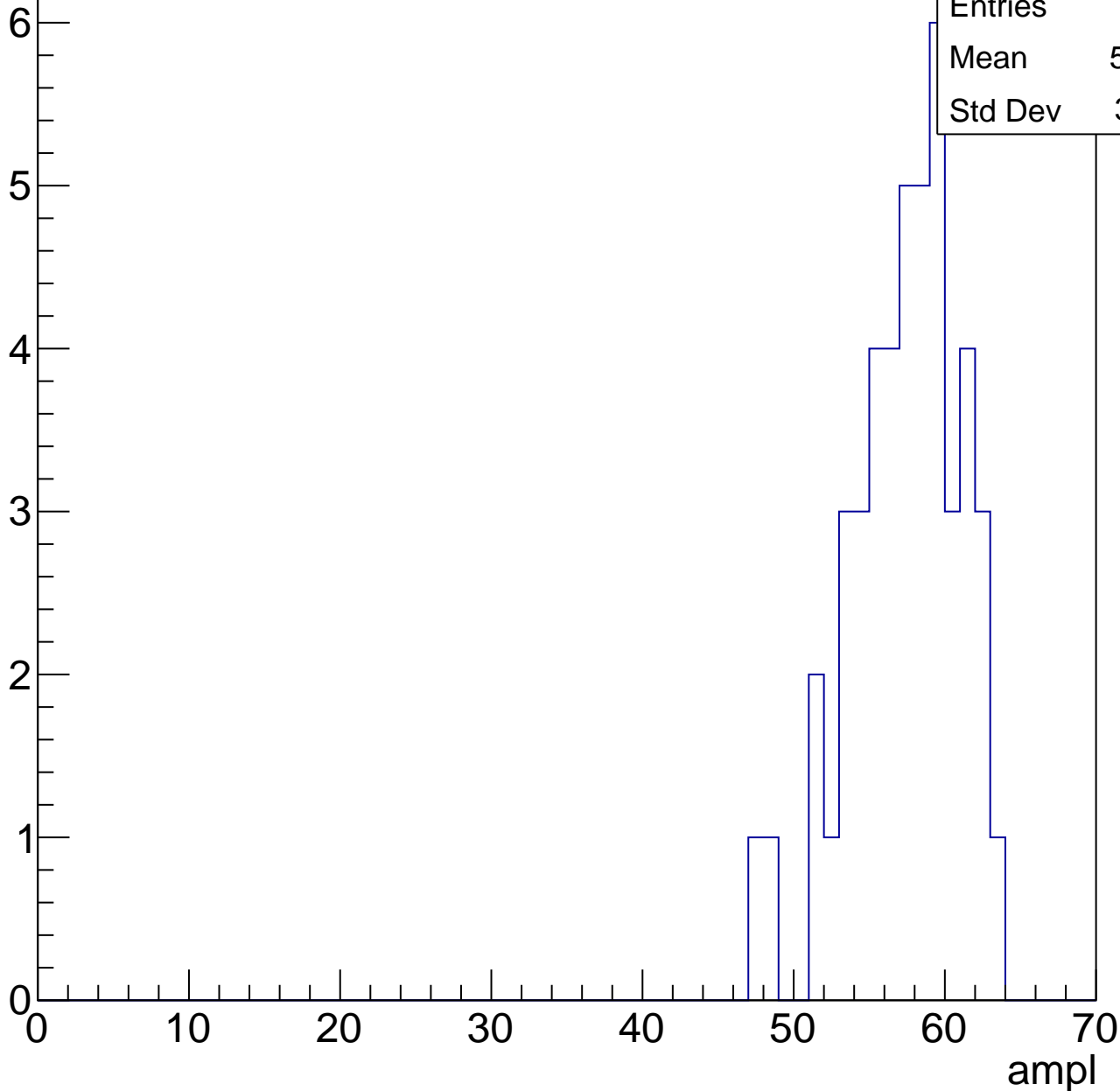


B1L102S, U2-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

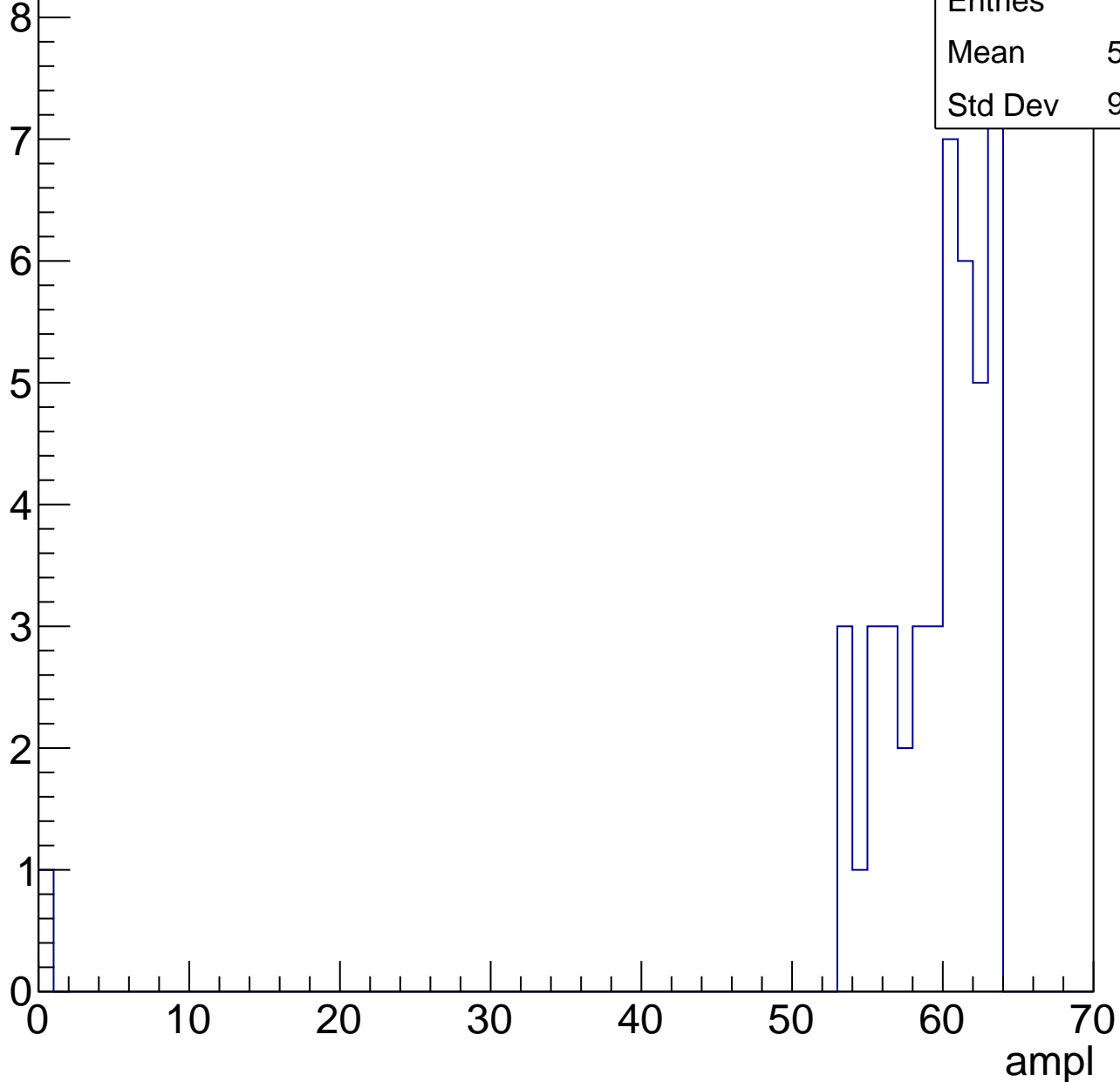
Entries	46
Mean	56.87
Std Dev	3.621



B1L102S, U2-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L102S, U2-ch19, adc0

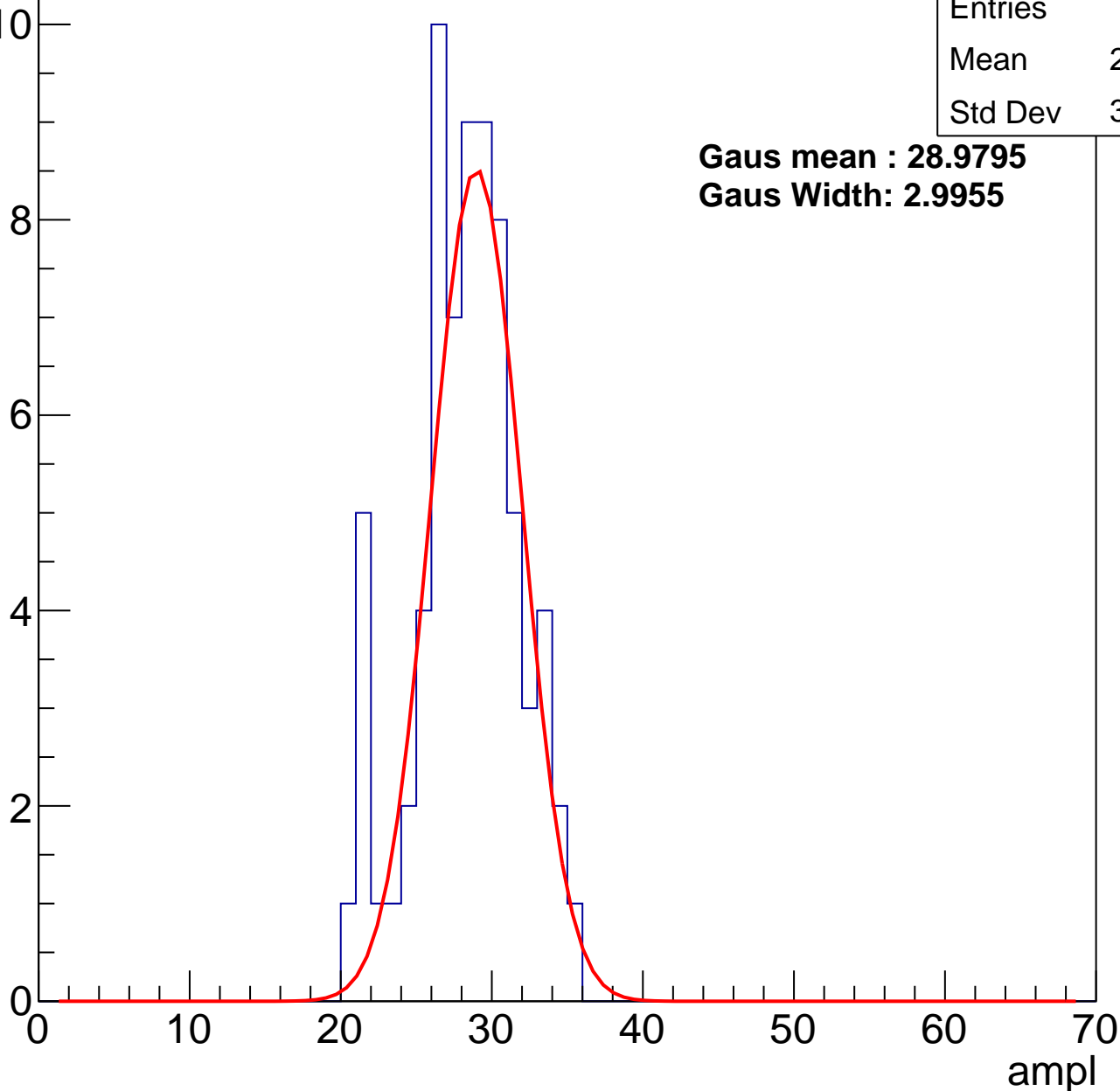
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.86
Std Dev	3.413

Gaus mean : 28.9795

Gaus Width: 2.9955



B1L102S, U2-ch19, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.18
Std Dev	3.467

Gaus mean : 35.9093

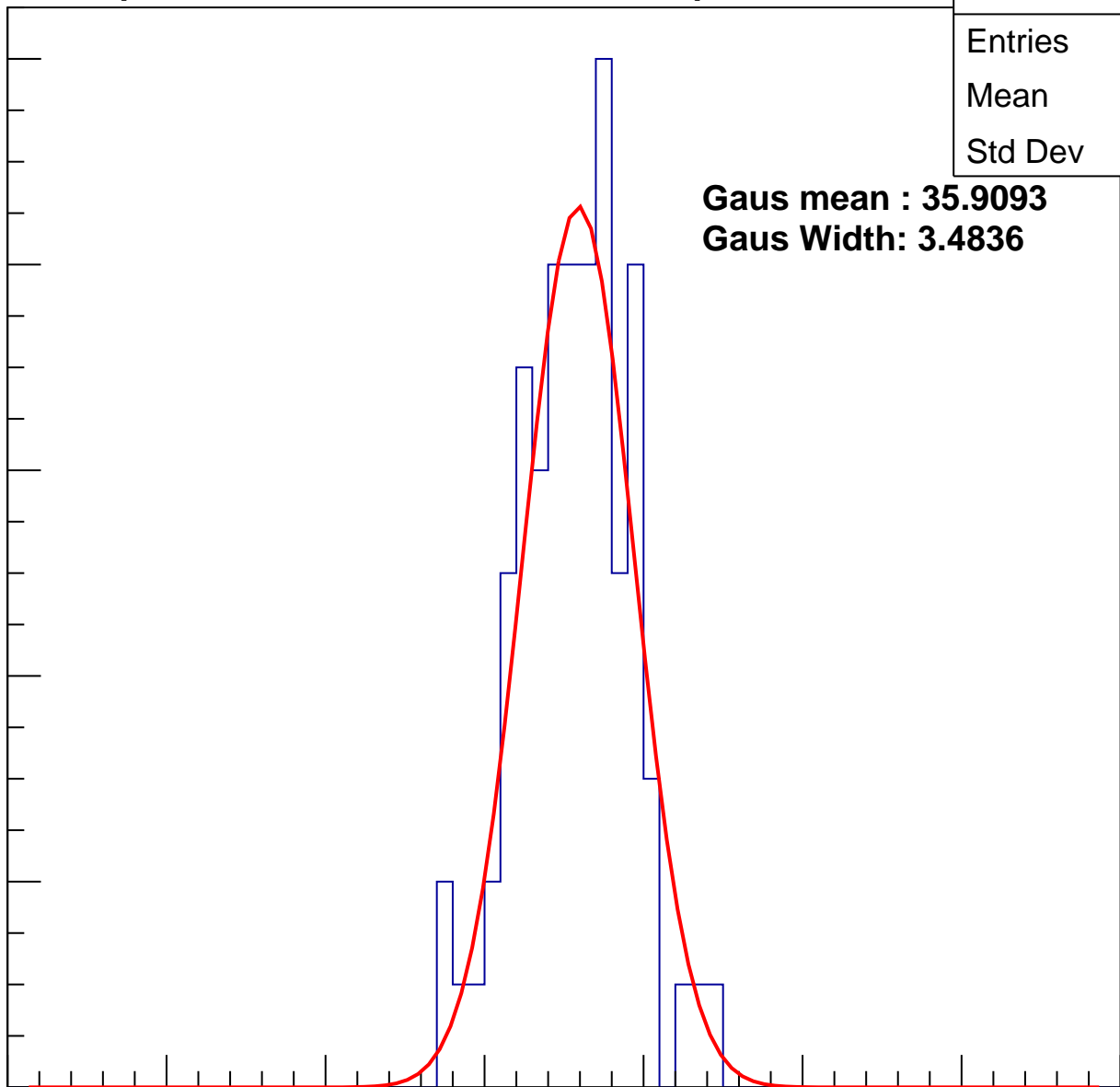
Gaus Width: 3.4836

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch19, adc2

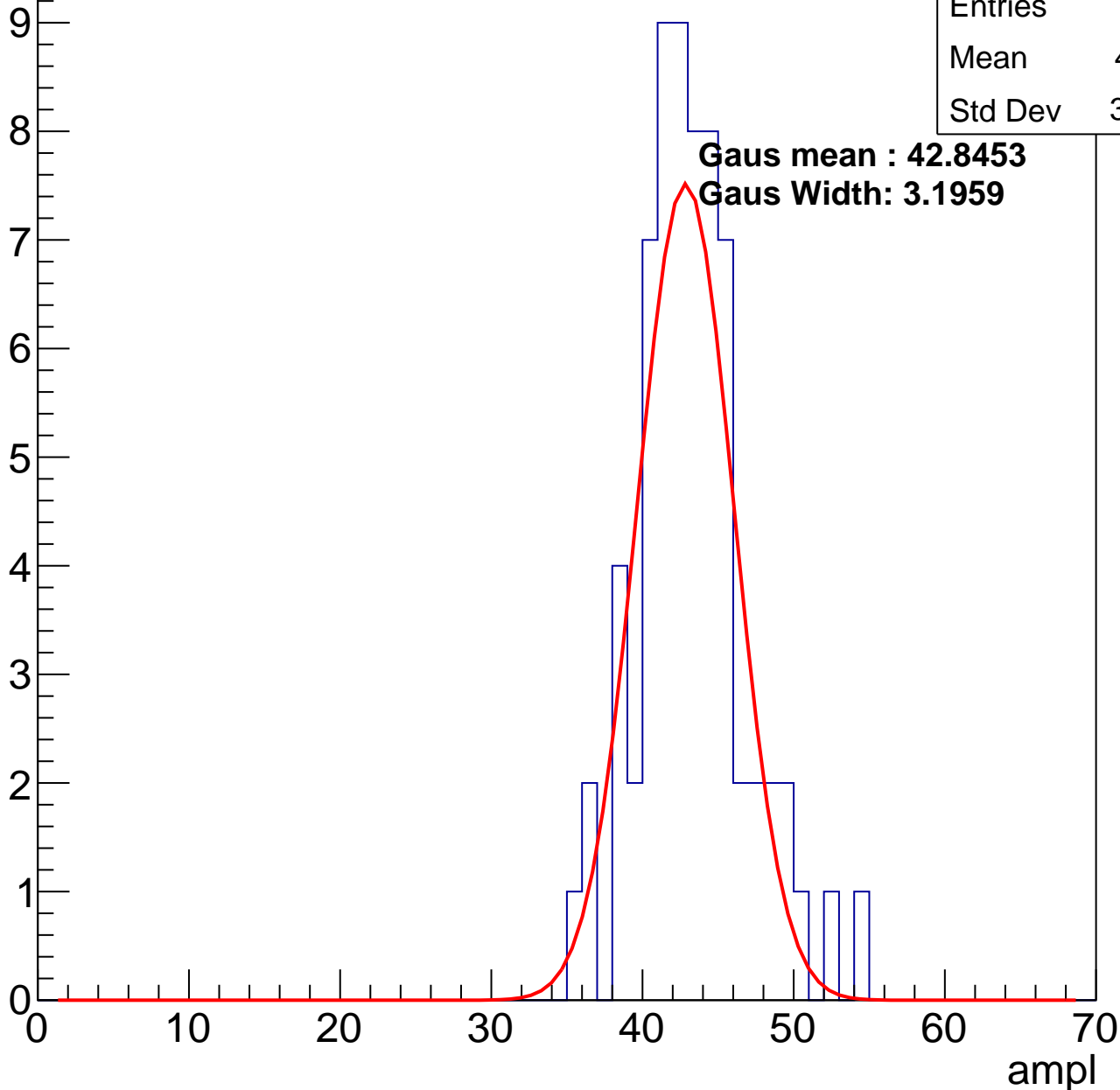
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.81
Std Dev	3.553

Gaus mean : 42.8453

Gaus Width: 3.1959

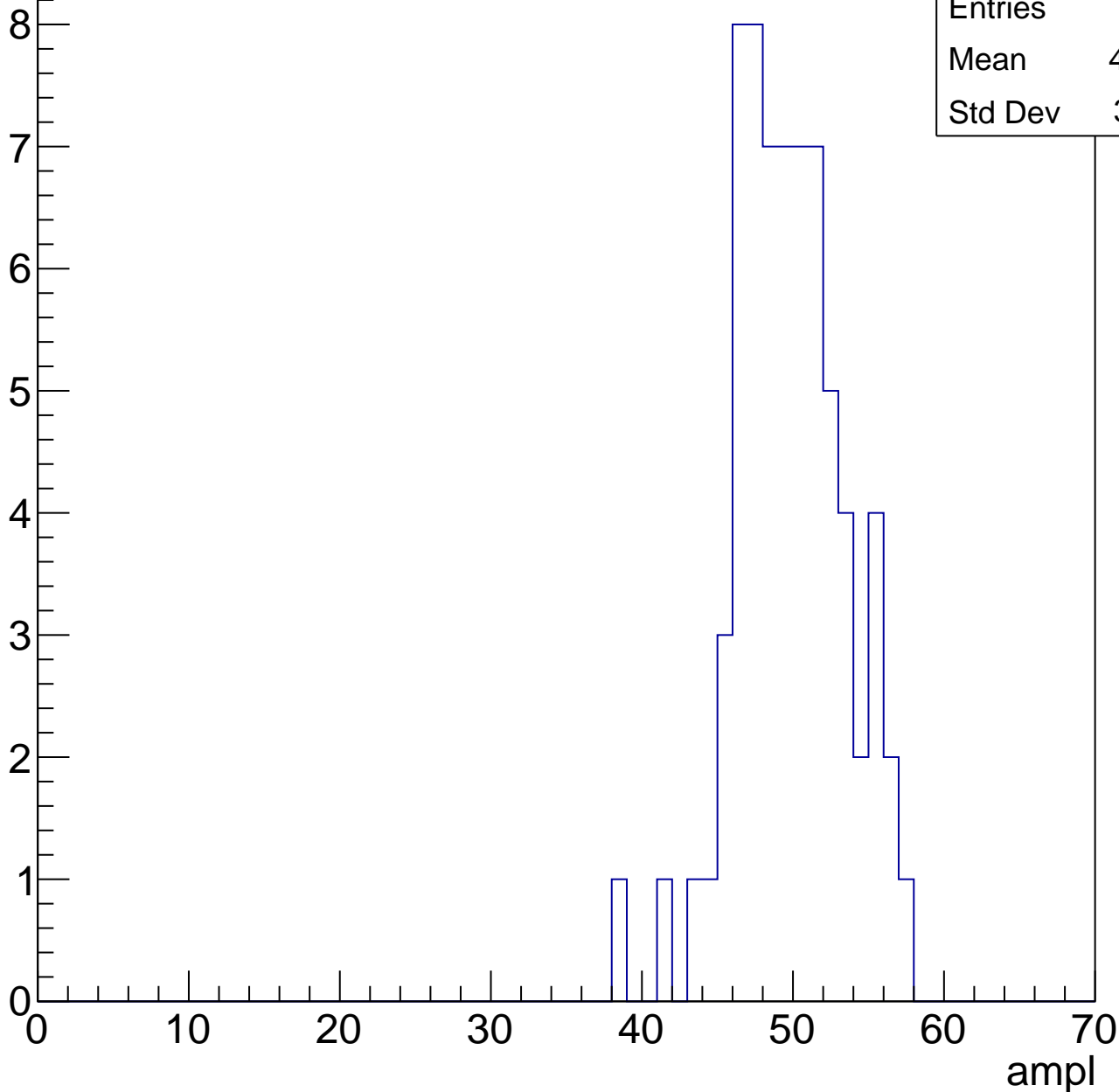


B1L102S, U2-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.28
Std Dev	3.611

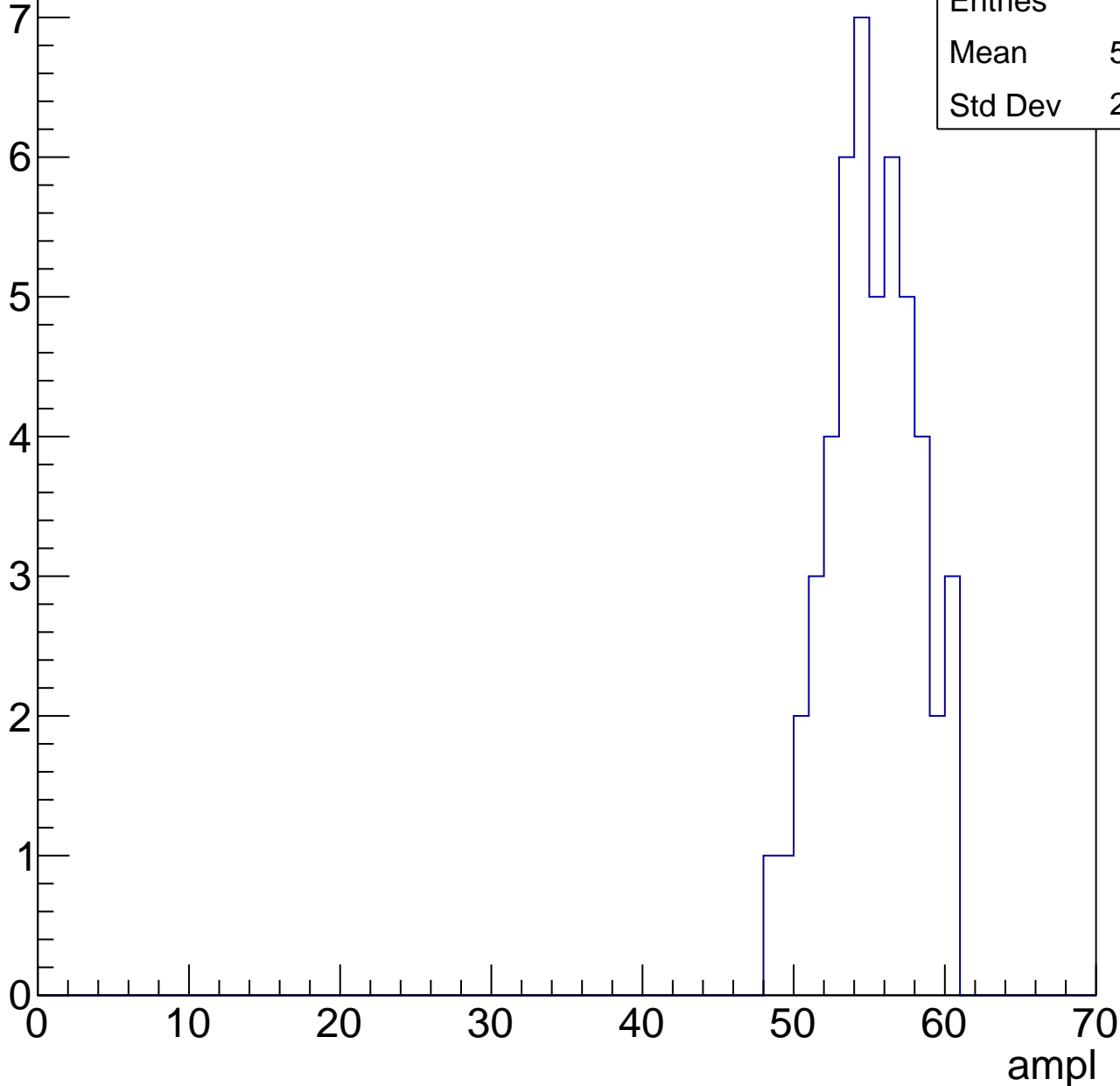


B1L102S, U2-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.69
Std Dev	2.908



B1L102S, U2-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

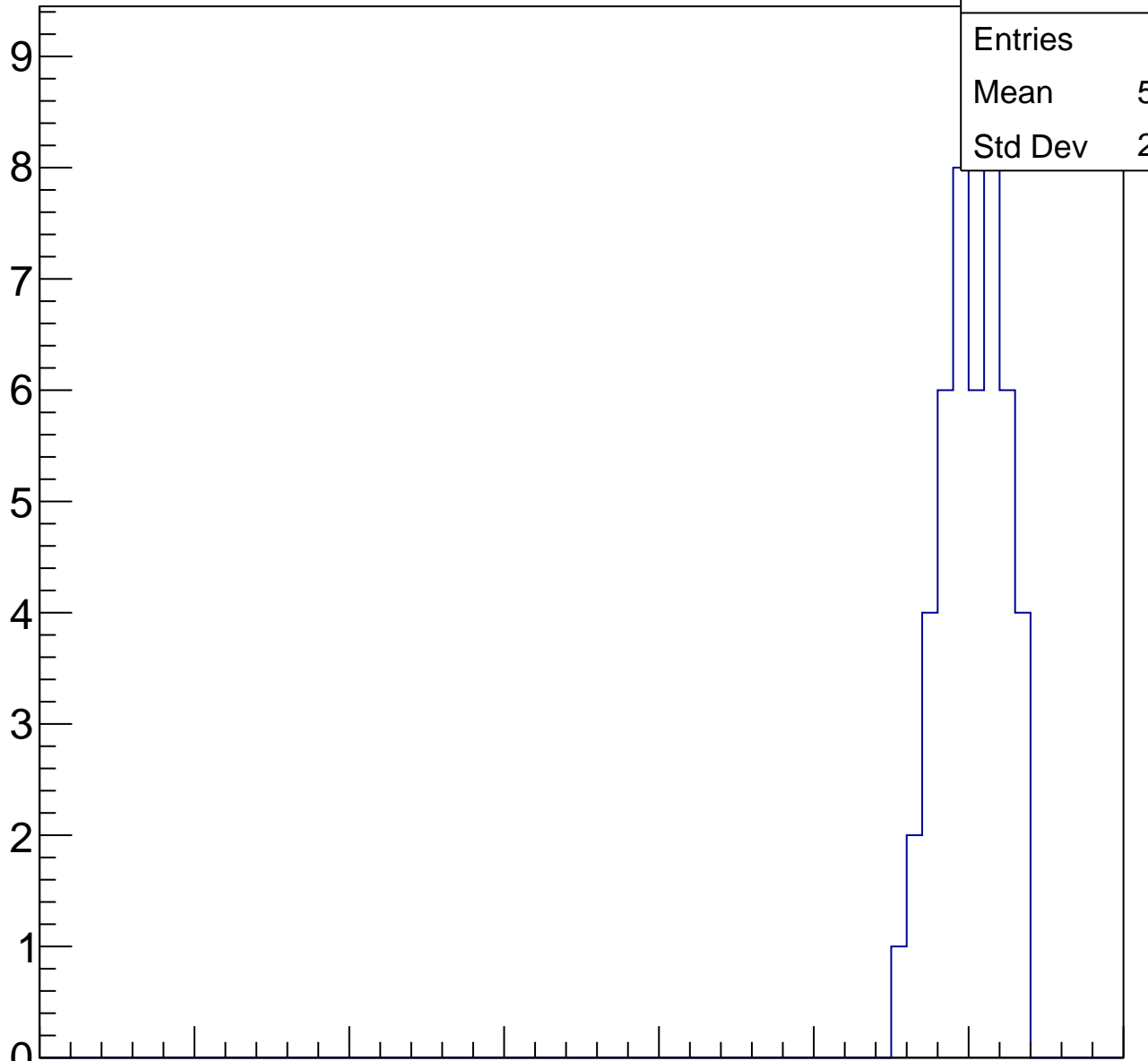
Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.74
Std Dev	2.037

ampl

0 10 20 30 40 50 60 70

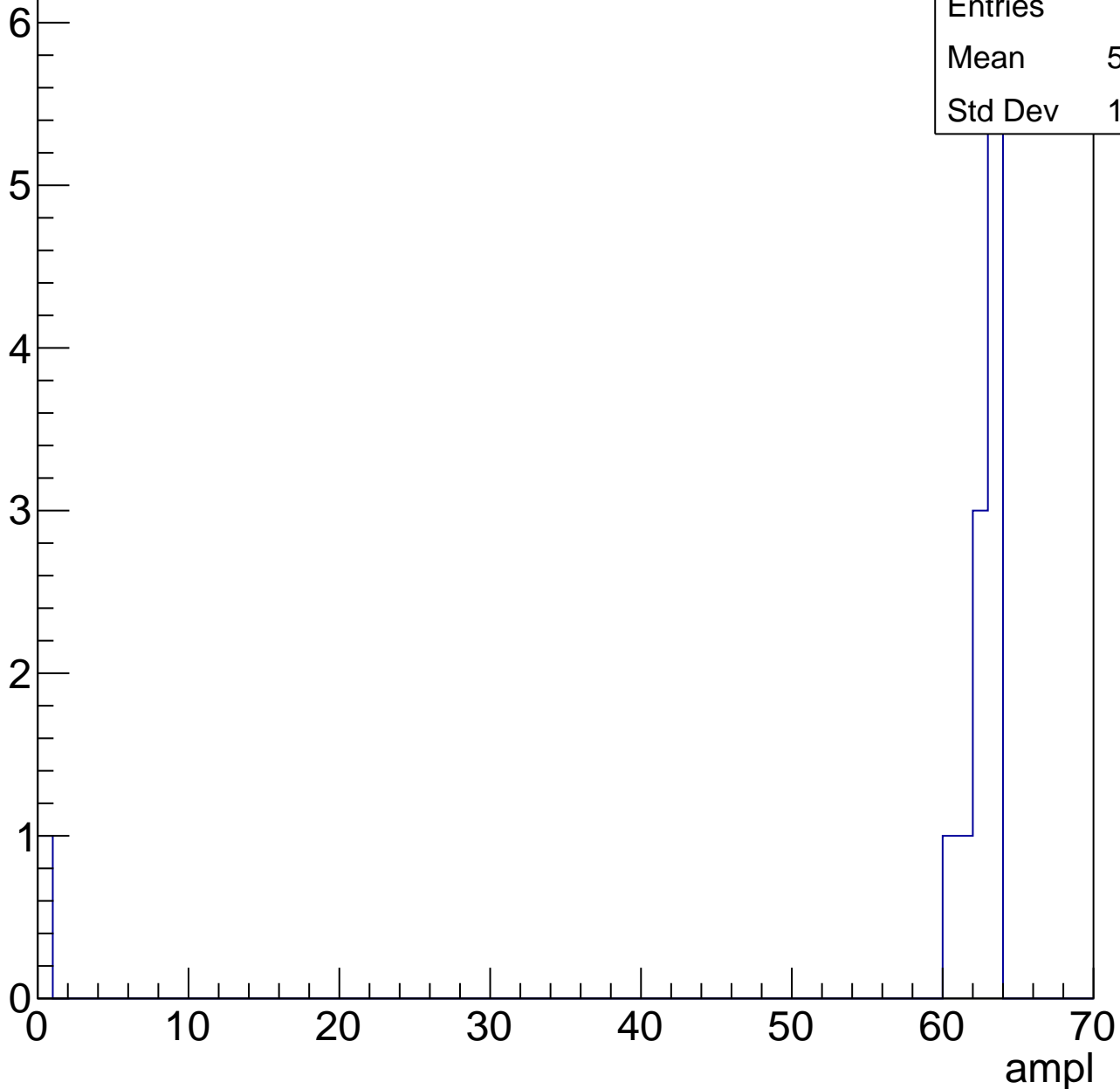


B1L102S, U2-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	57.08
Std Dev	17.24



B1L102S, U2-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch20, adc0

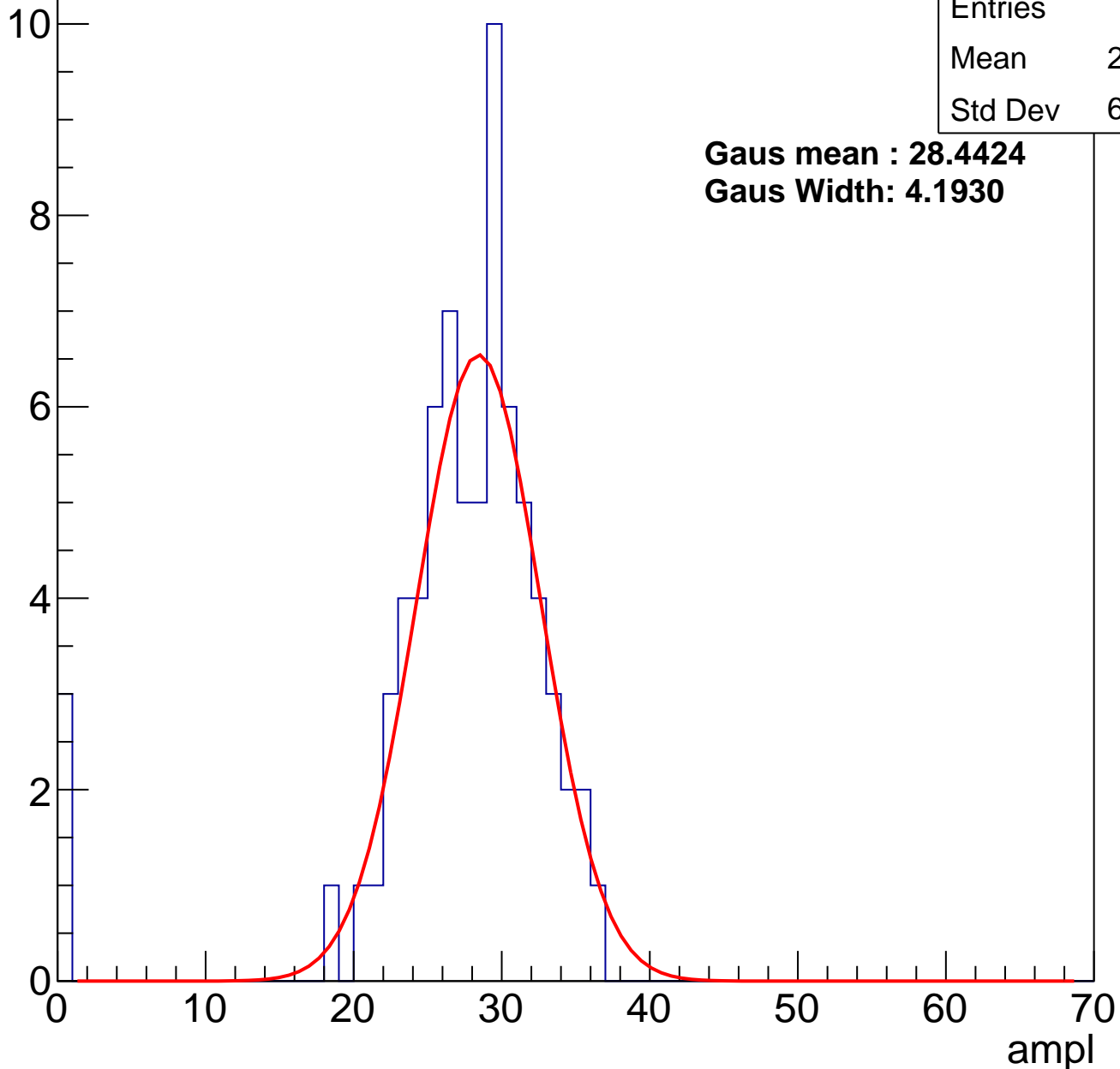
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	26.66
Std Dev	6.667

Gaus mean : 28.4424

Gaus Width: 4.1930

Entry



B1L102S, U2-ch20, adc1

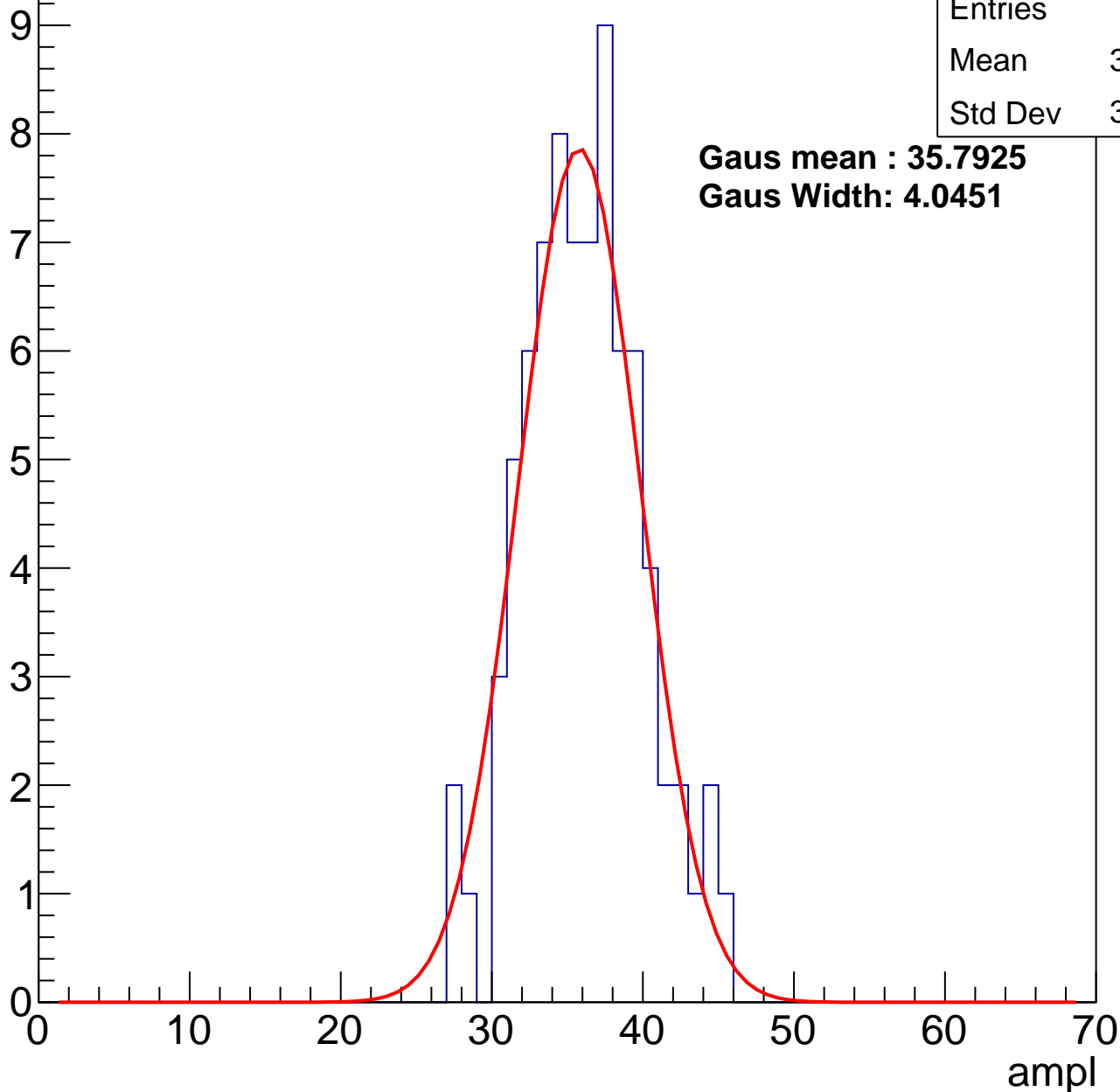
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.65
Std Dev	3.862

Gaus mean : 35.7925

Gaus Width: 4.0451



B1L102S, U2-ch20, adc2

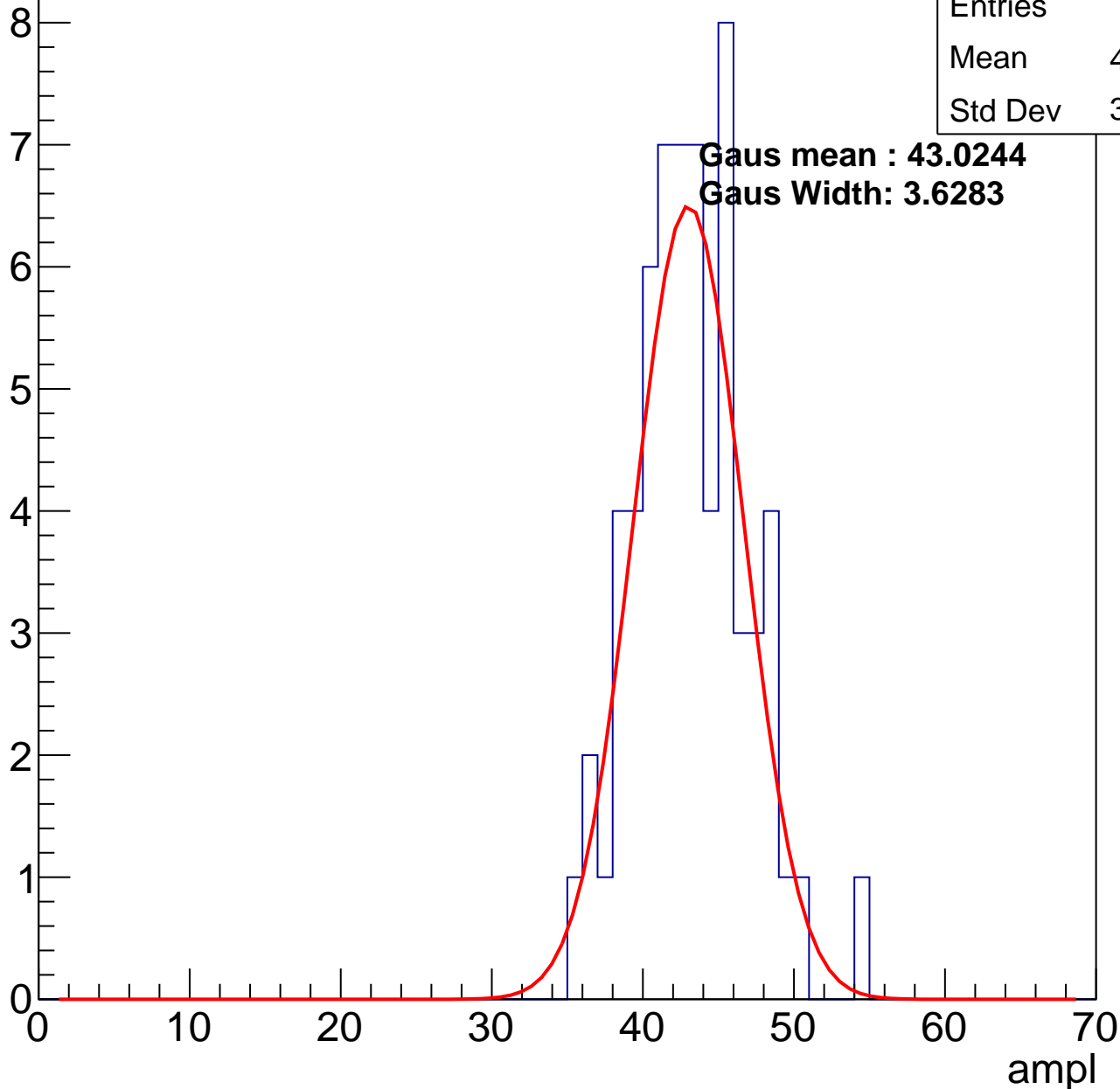
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.72
Std Dev	3.672

Gaus mean : 43.0244

Gaus Width: 3.6283

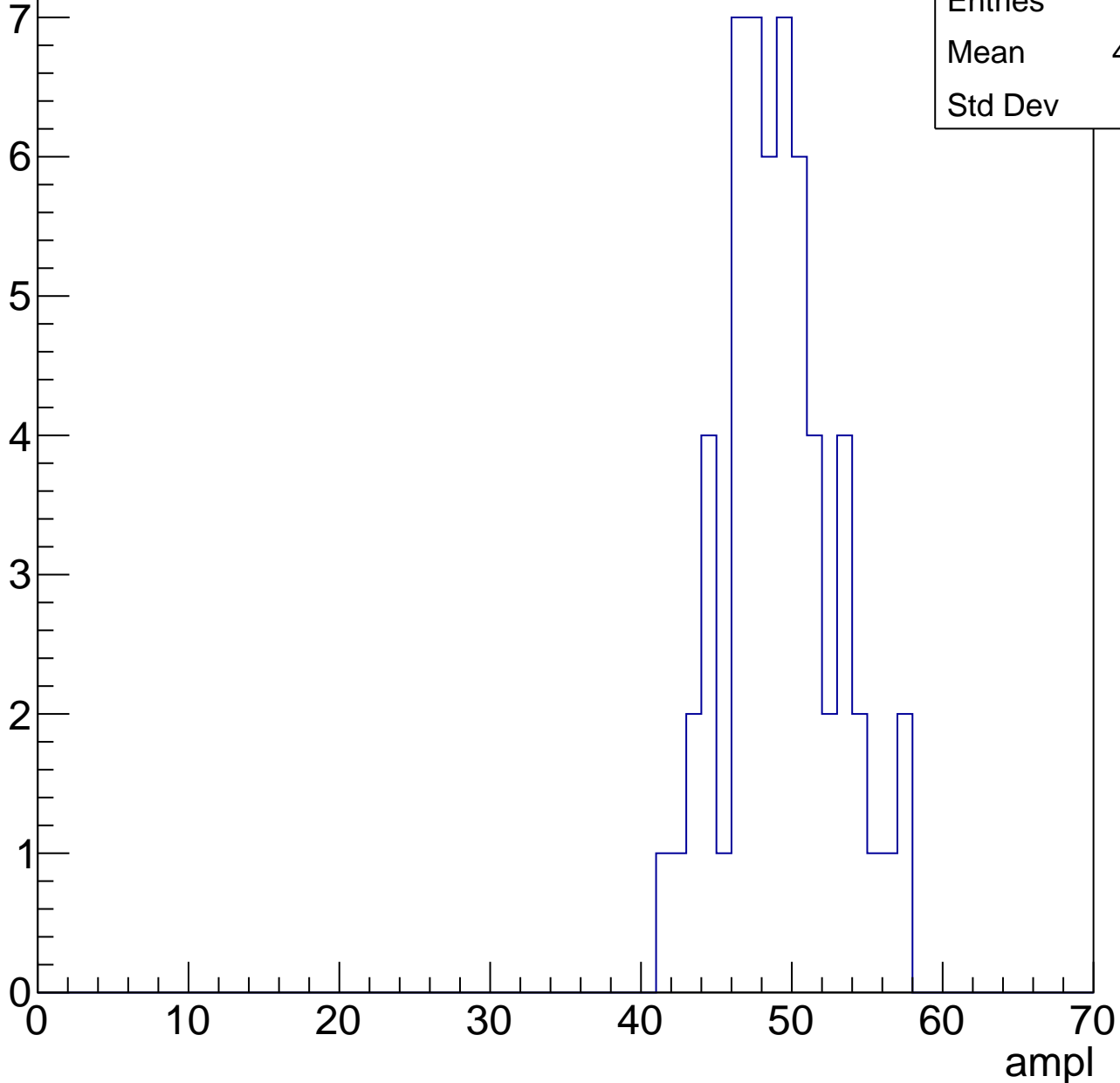


B1L102S, U2-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.71
Std Dev	3.62

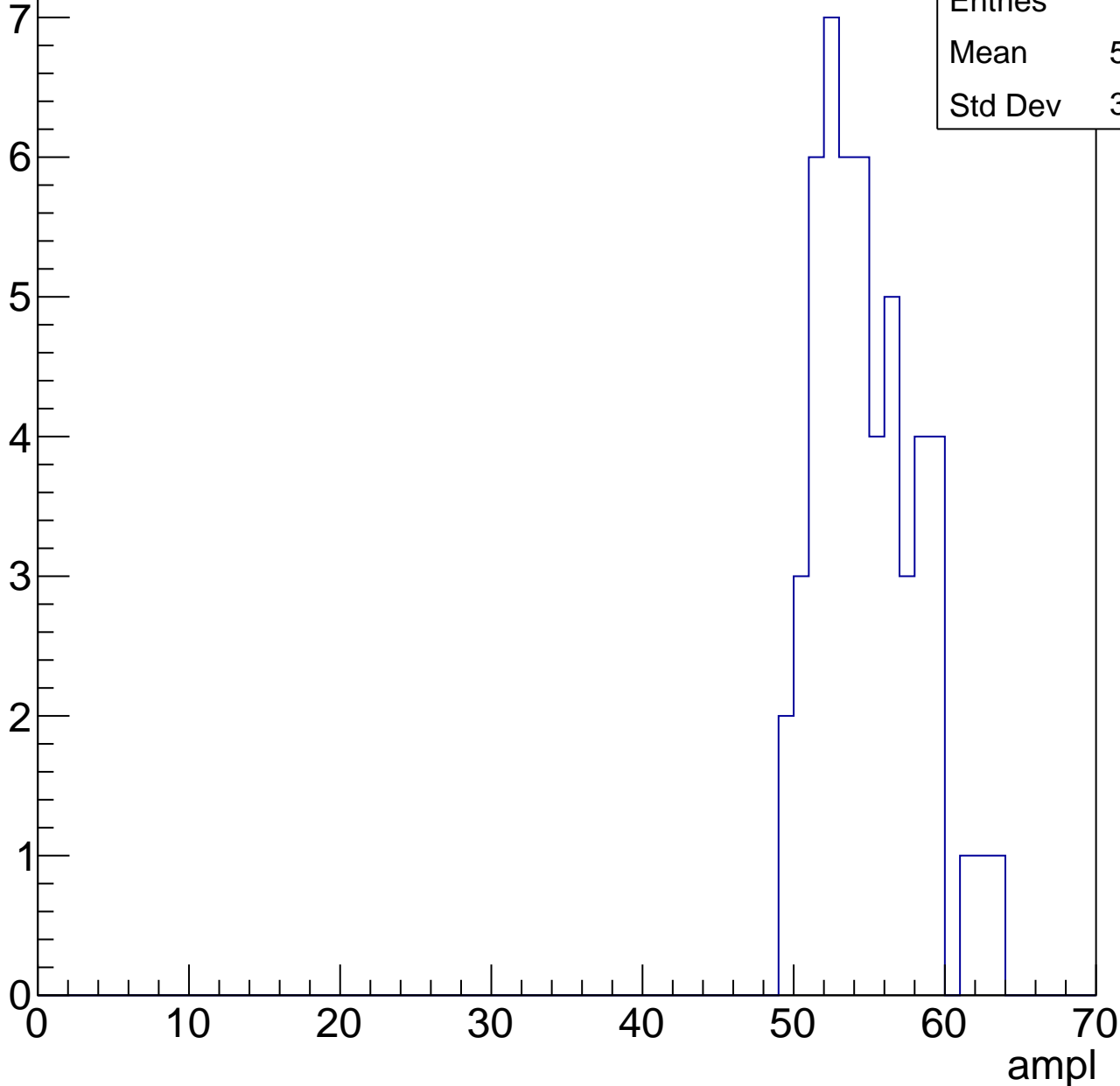


B1L102S, U2-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

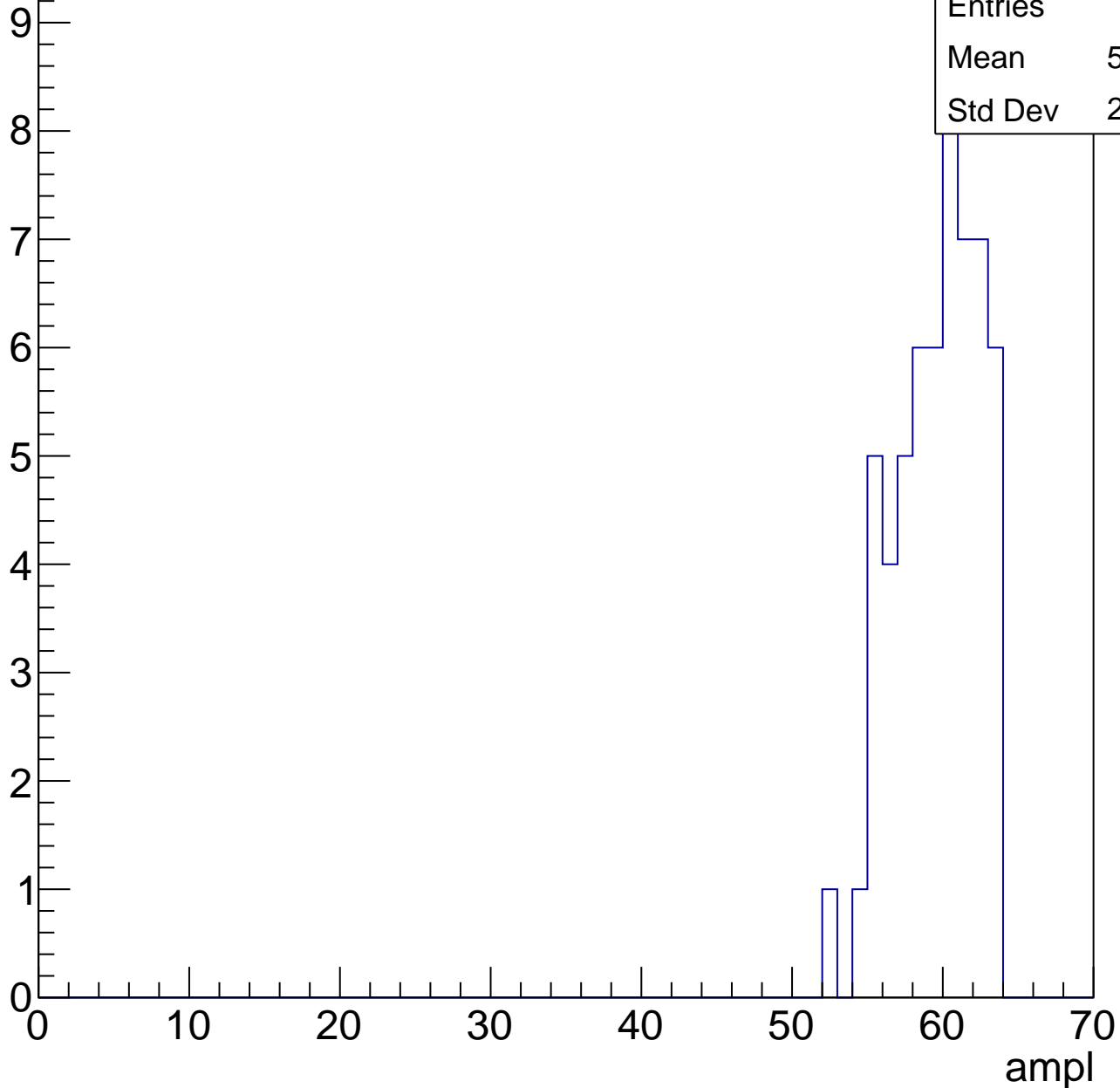
Entries	53
Mean	54.43
Std Dev	3.322



B1L102S, U2-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch20, adc6

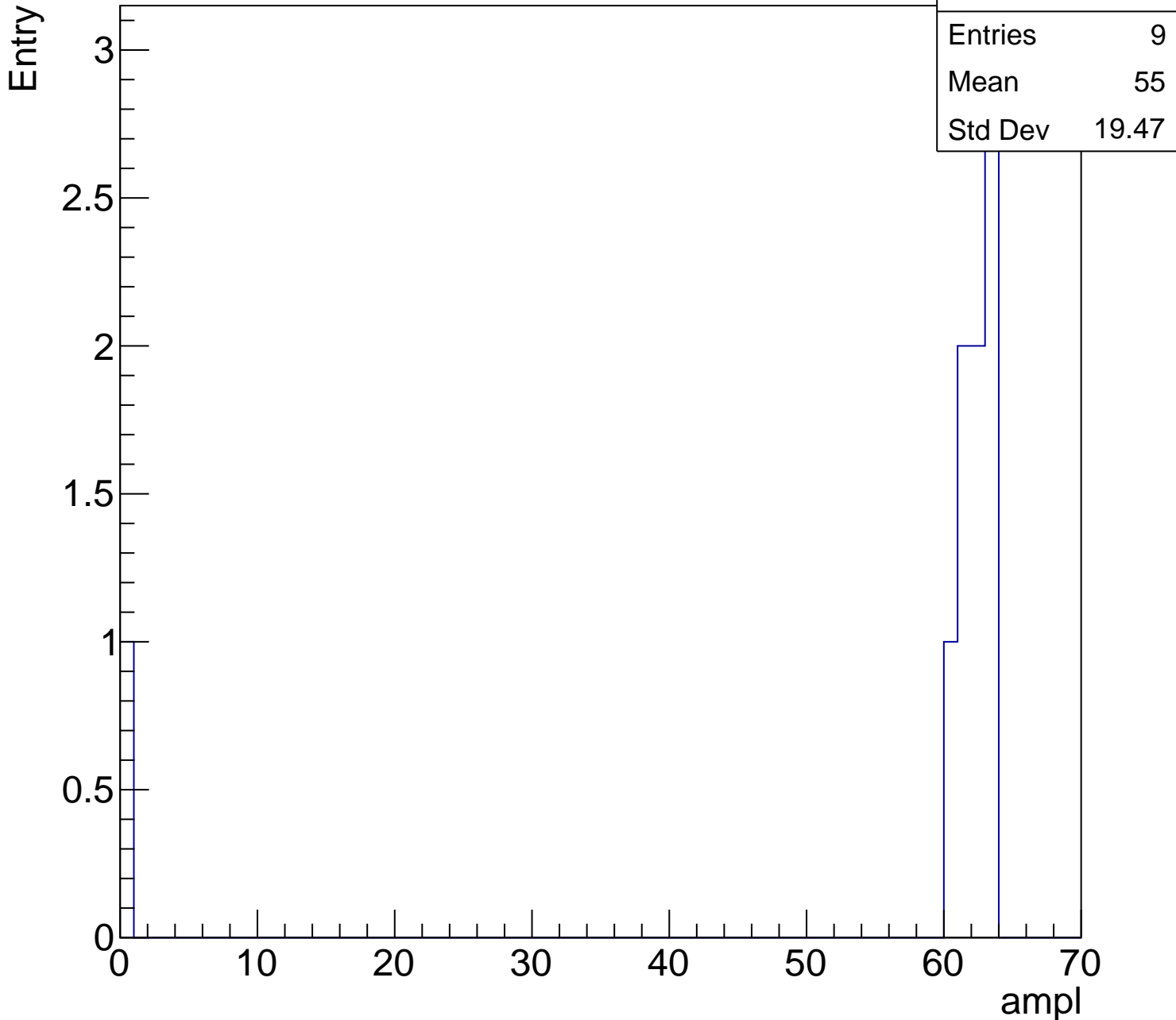
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	55
Std Dev	19.47

ampl



B1L102S, U2-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch21, adc0

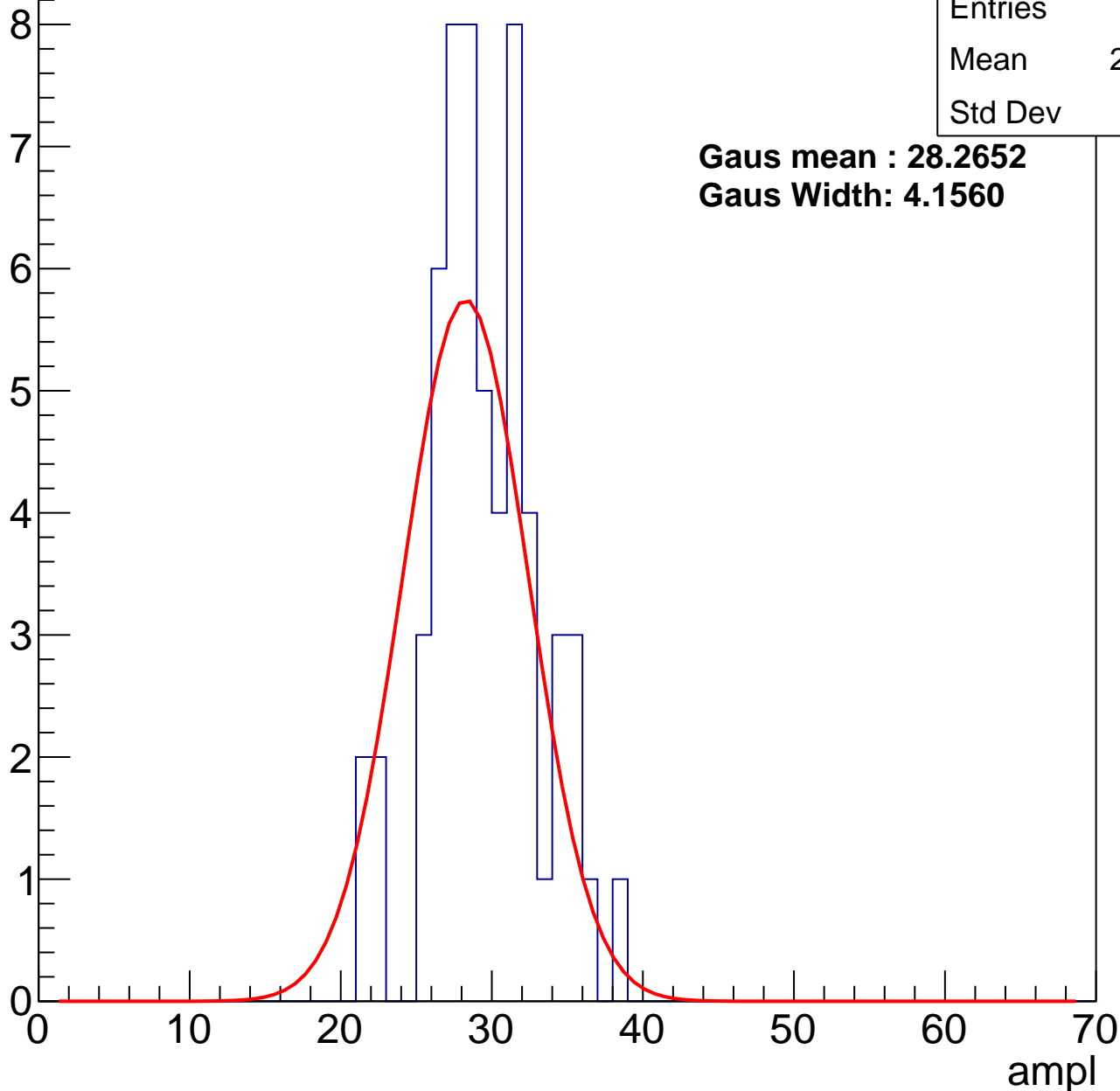
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.02
Std Dev	3.61

Gaus mean : 28.2652

Gaus Width: 4.1560



B1L102S, U2-ch21, adc1

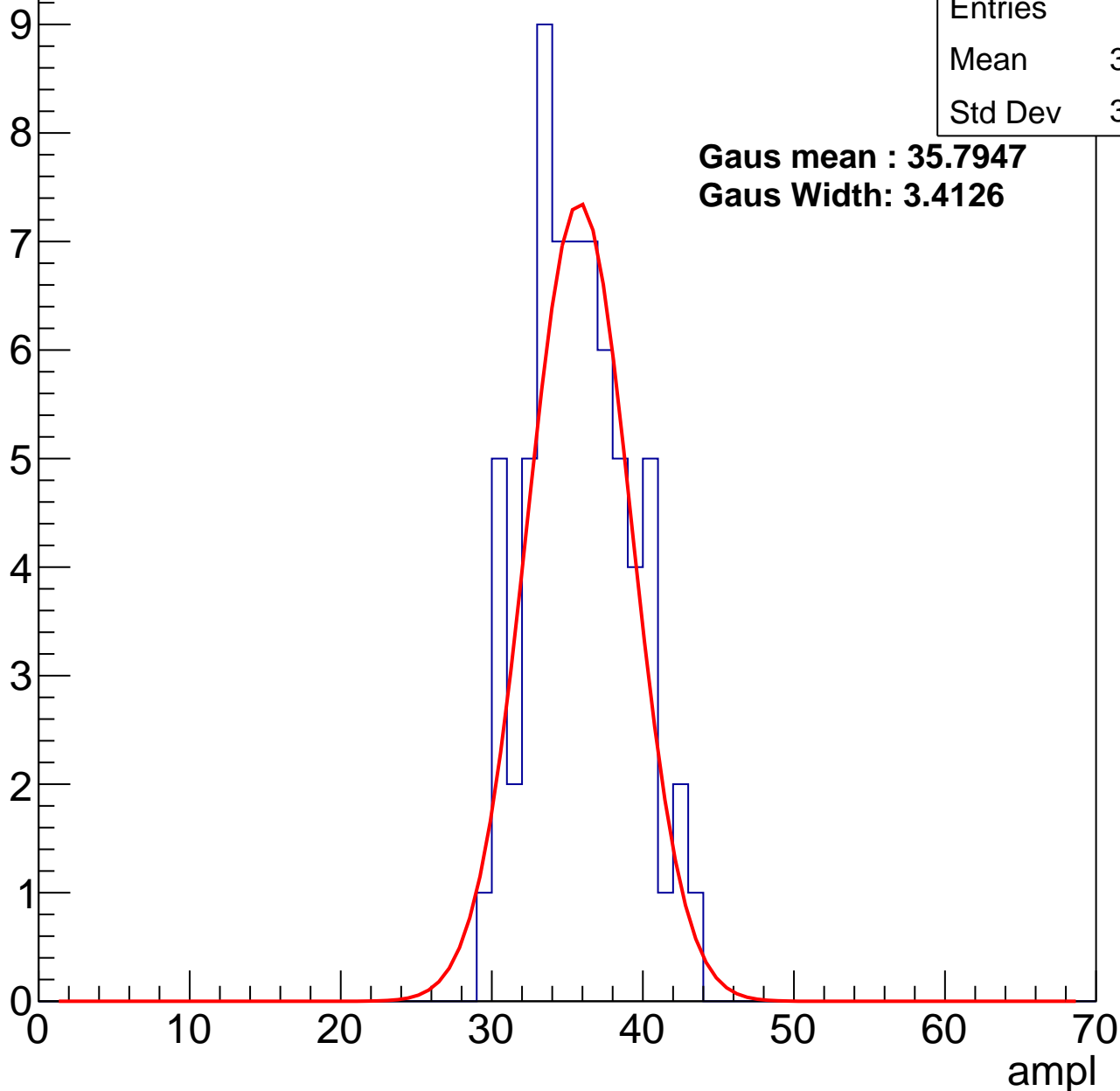
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.36
Std Dev	3.313

Gaus mean : 35.7947

Gaus Width: 3.4126



B1L102S, U2-ch21, adc2

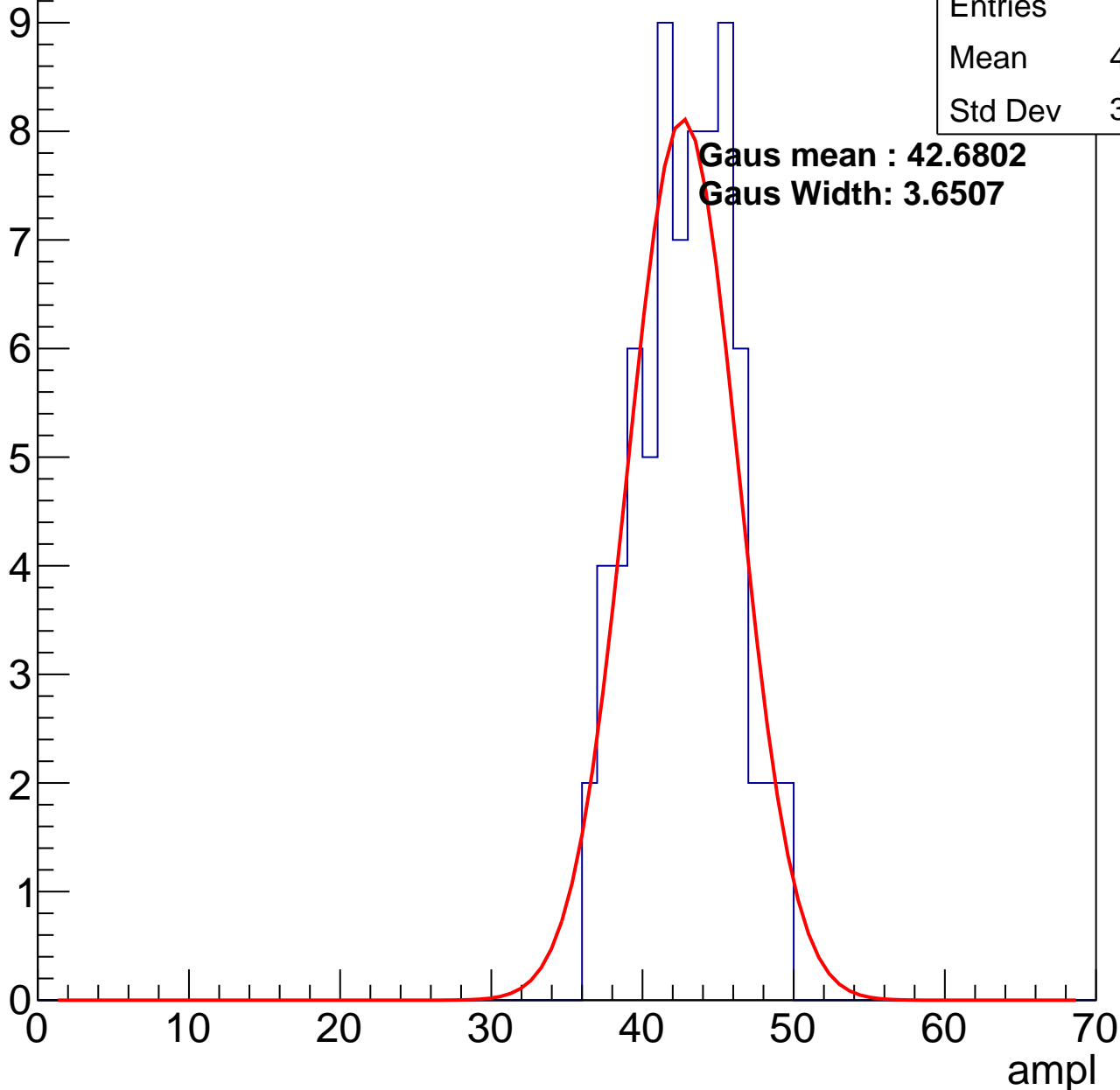
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.35
Std Dev	3.177

Gaus mean : 42.6802

Gaus Width: 3.6507

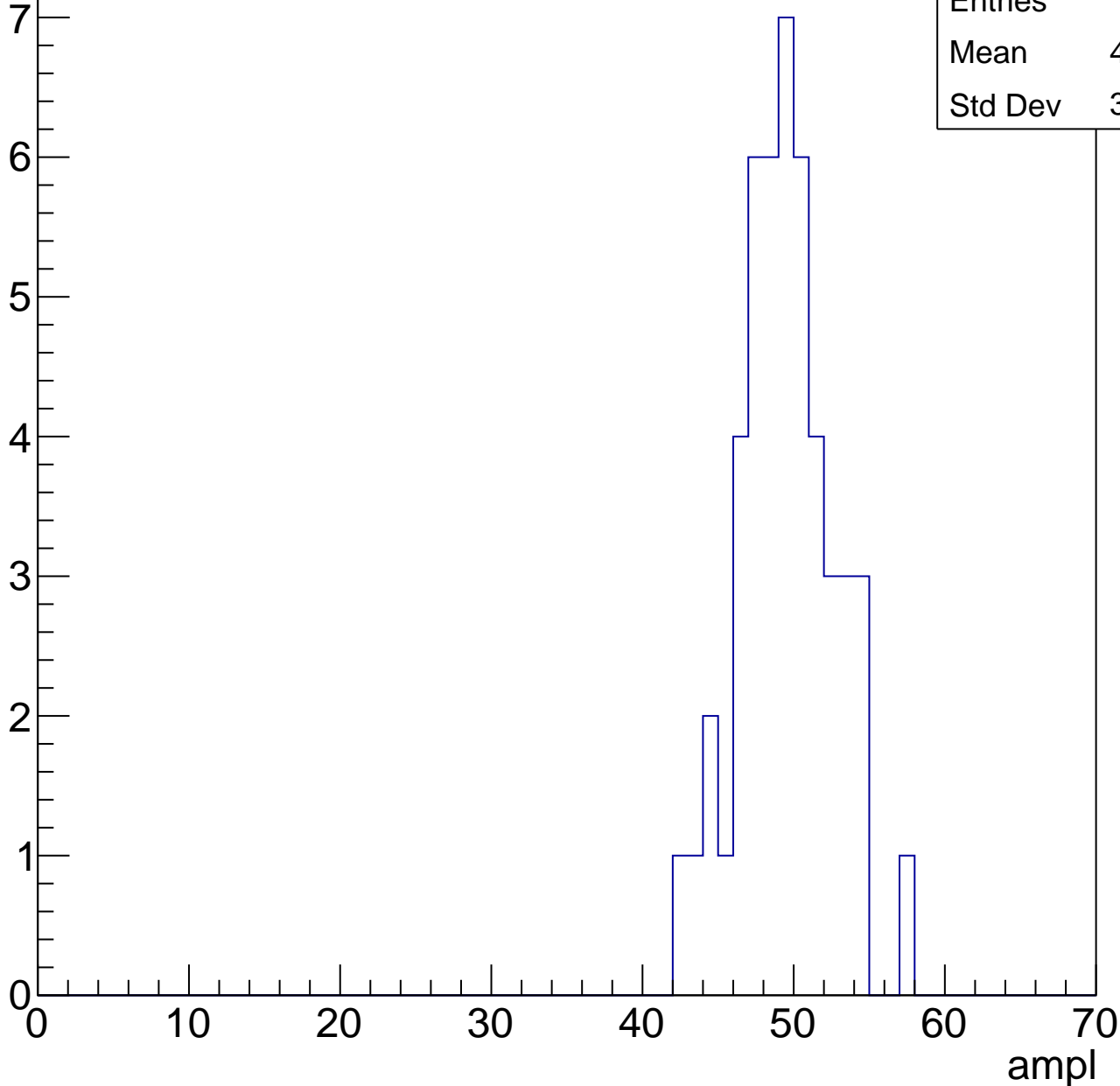


B1L102S, U2-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	49.02
Std Dev	3.072

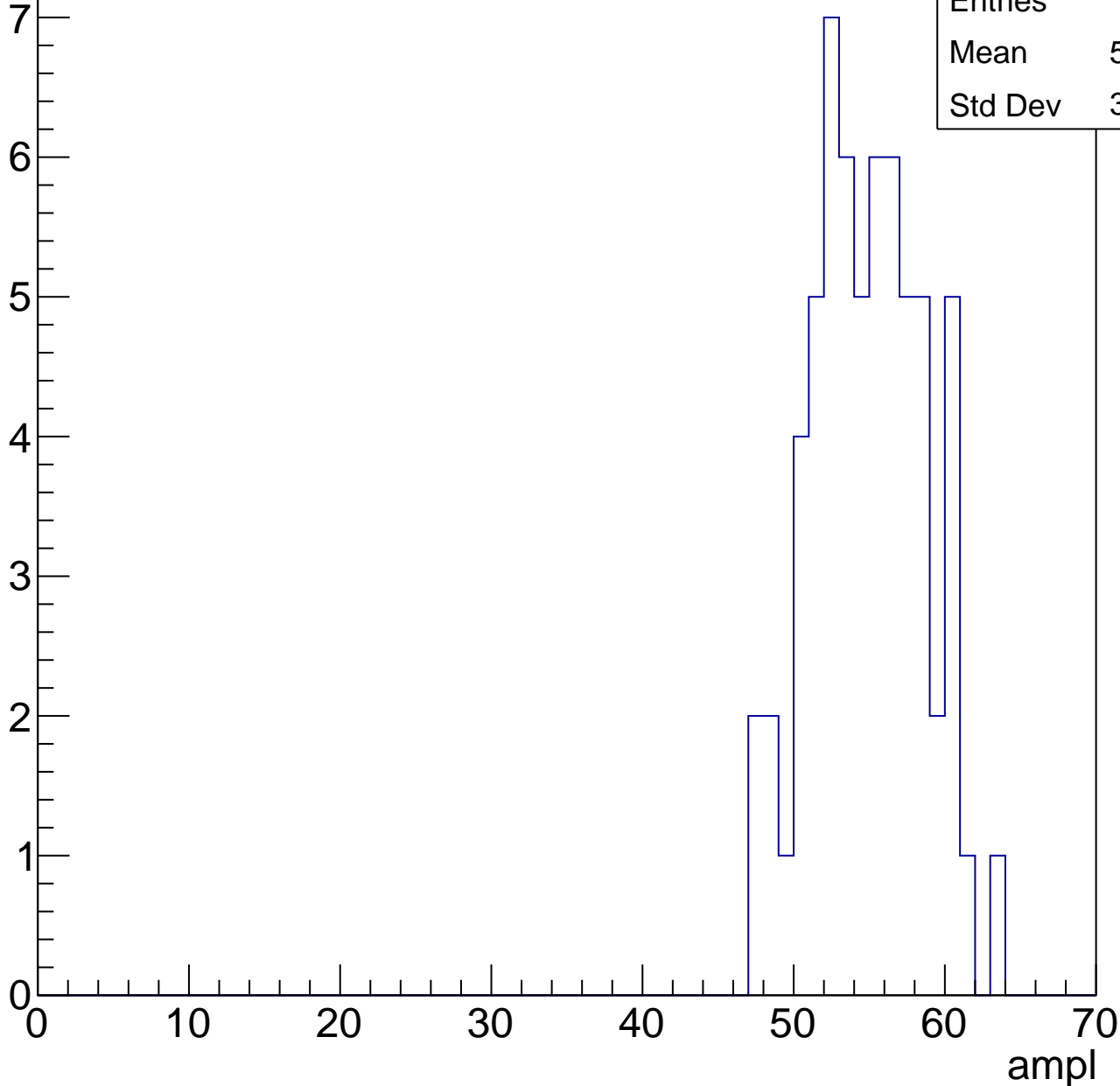


B1L102S, U2-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

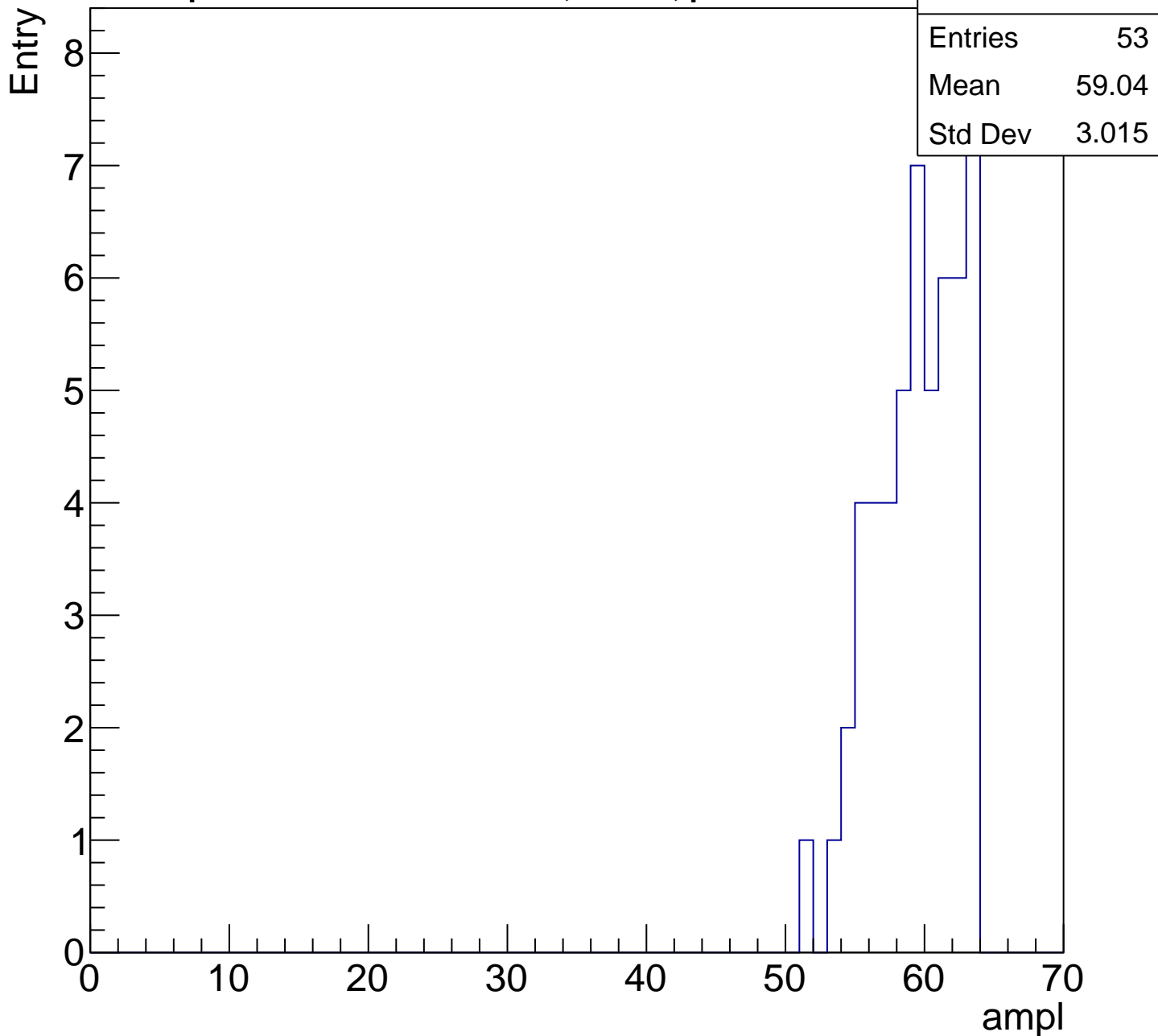
Entry

Entries	63
Mean	54.43
Std Dev	3.655



B1L102S, U2-ch21, adc5

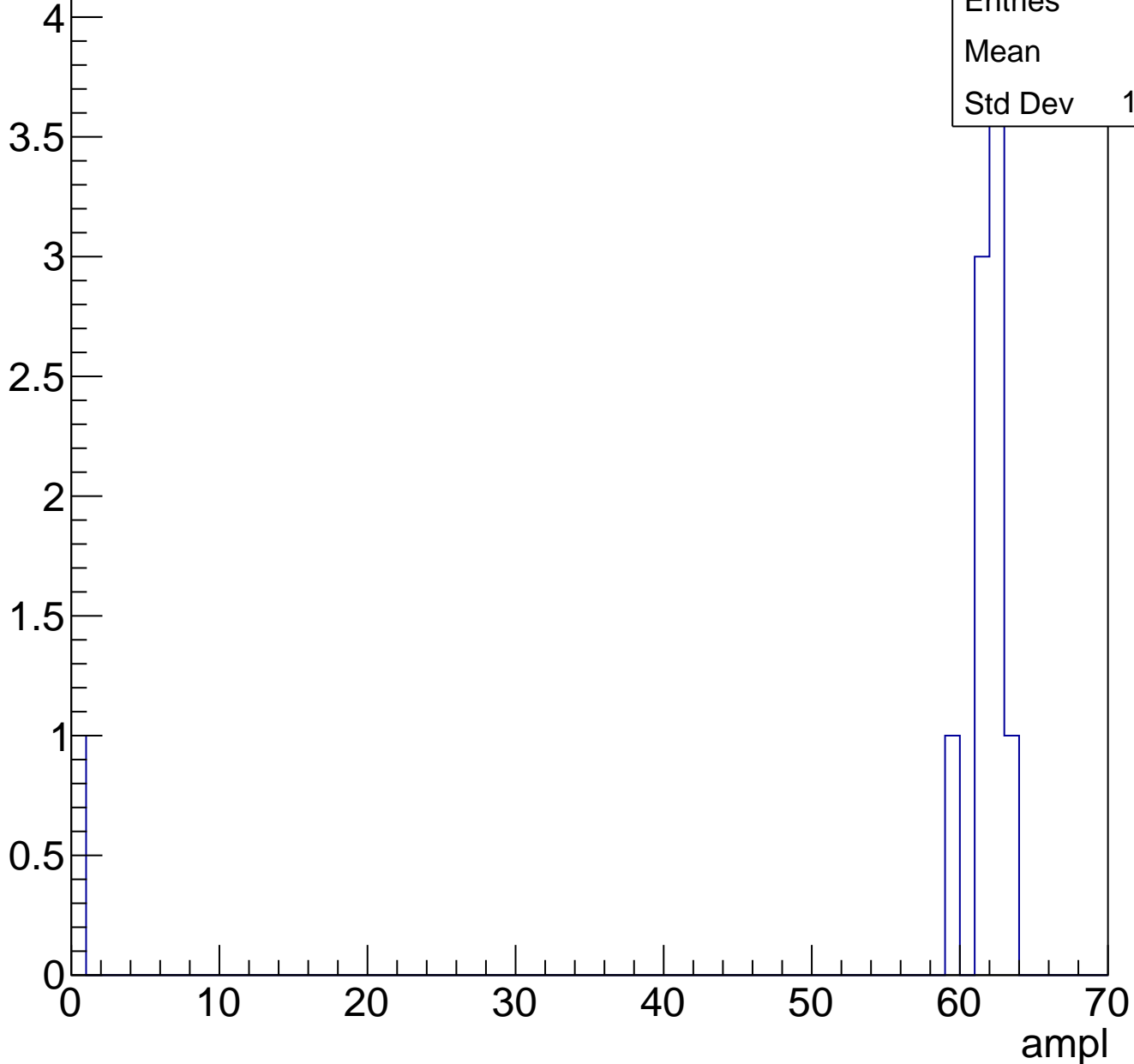
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch22, adc0

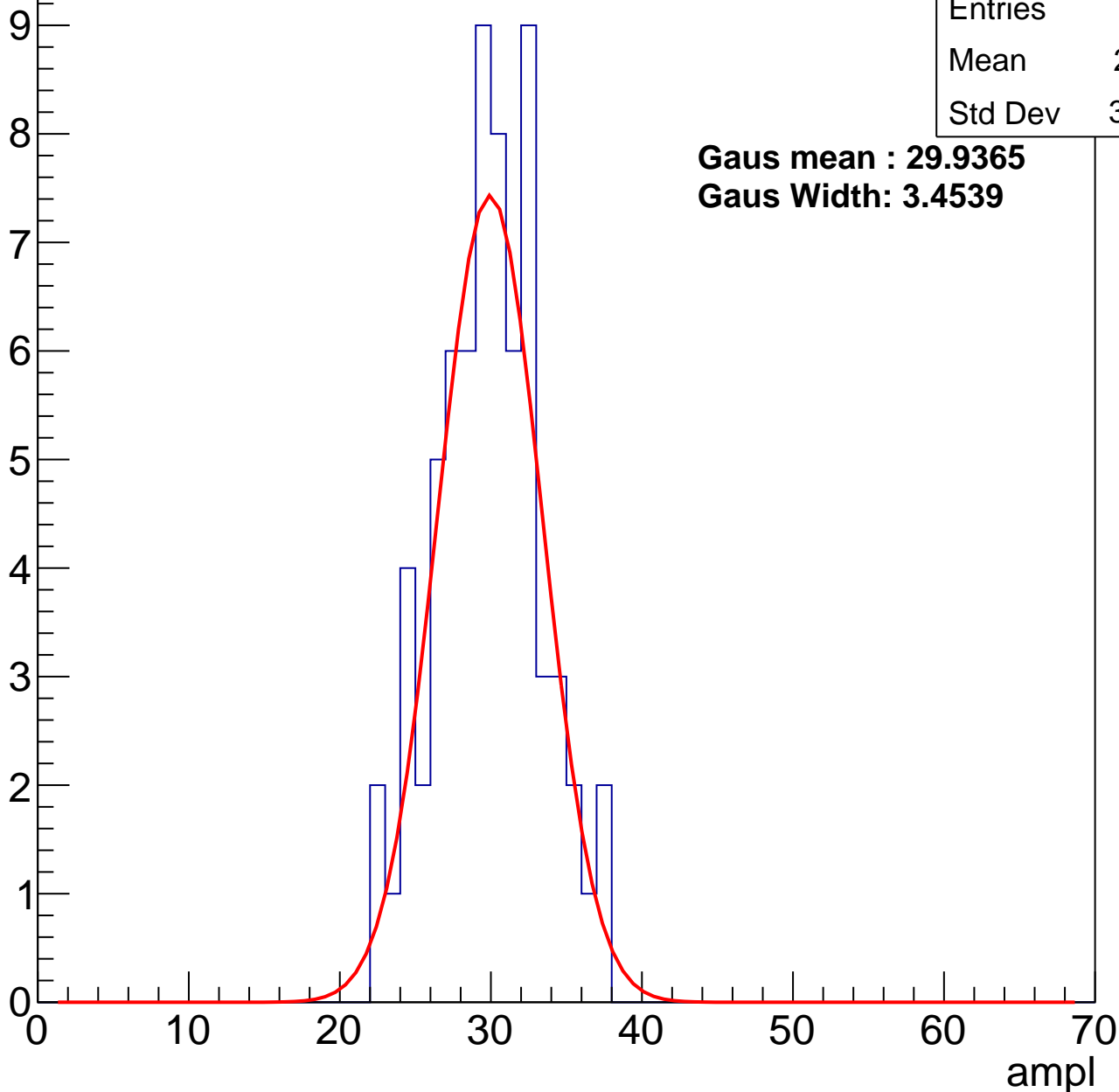
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.41
Std Dev	3.436

Gaus mean : 29.9365

Gaus Width: 3.4539



B1L102S, U2-ch22, adc1

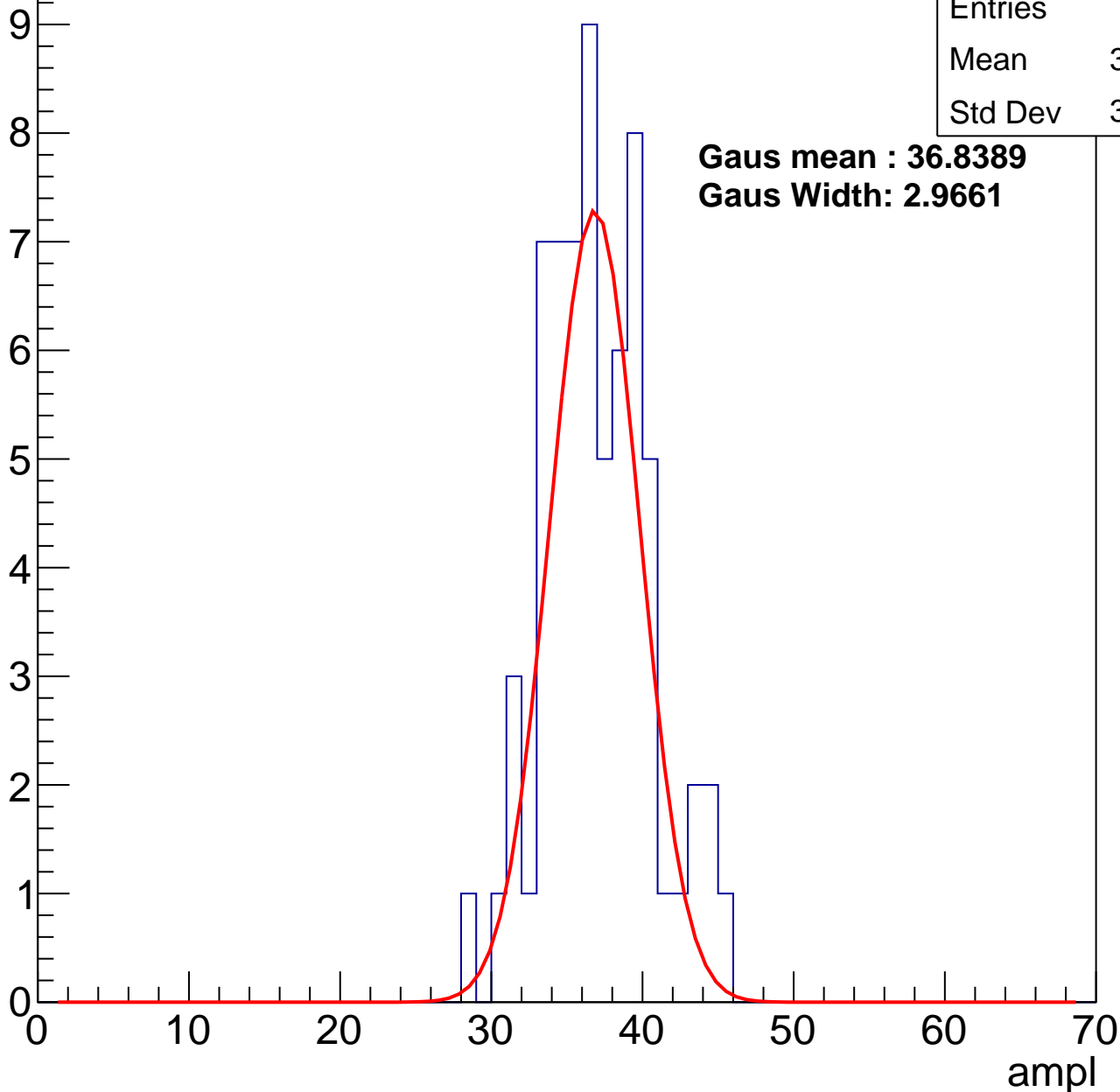
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.54
Std Dev	3.496

Gaus mean : 36.8389

Gaus Width: 2.9661



B1L102S, U2-ch22, adc2

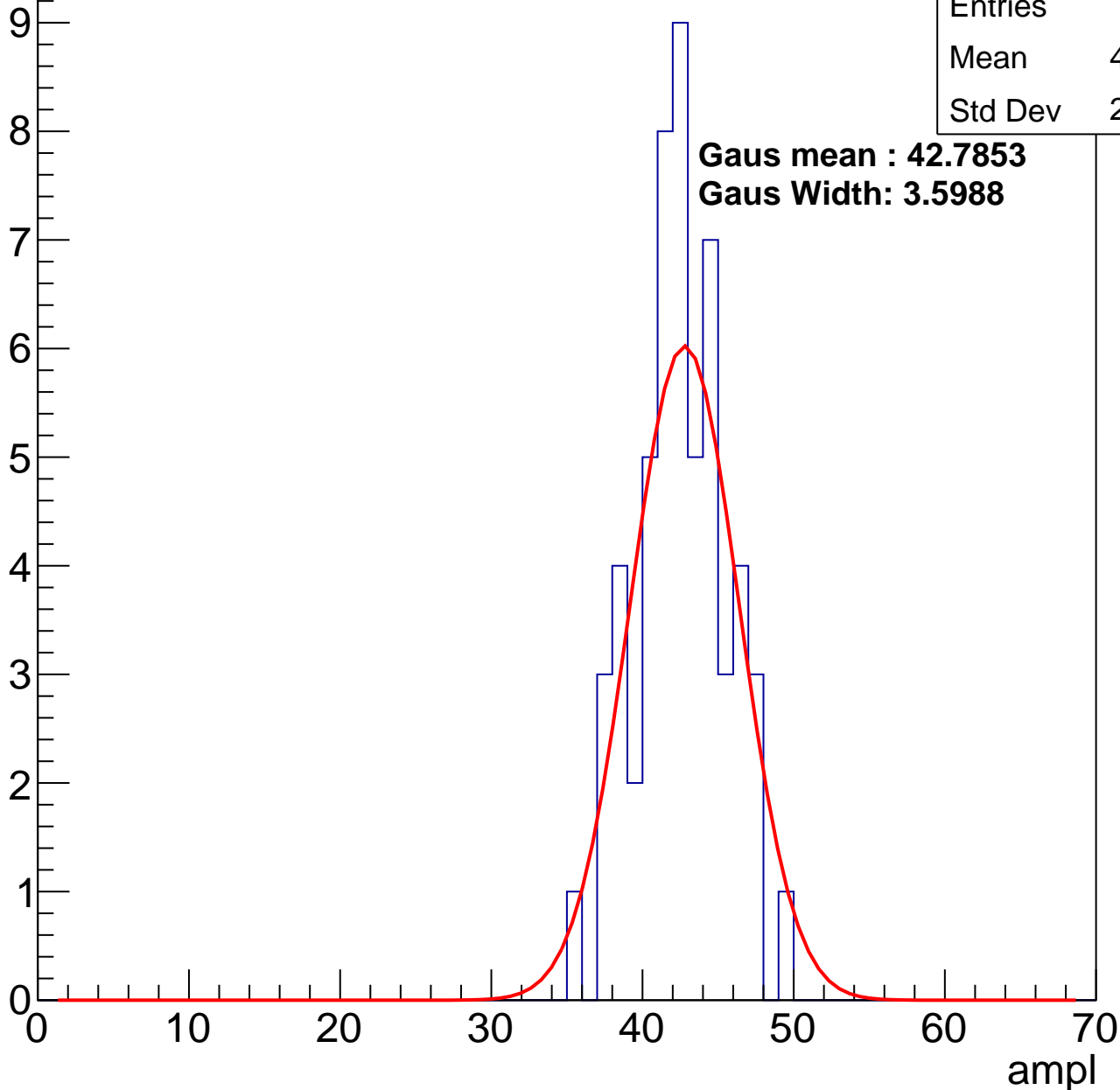
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.07
Std Dev	2.959

Gaus mean : 42.7853

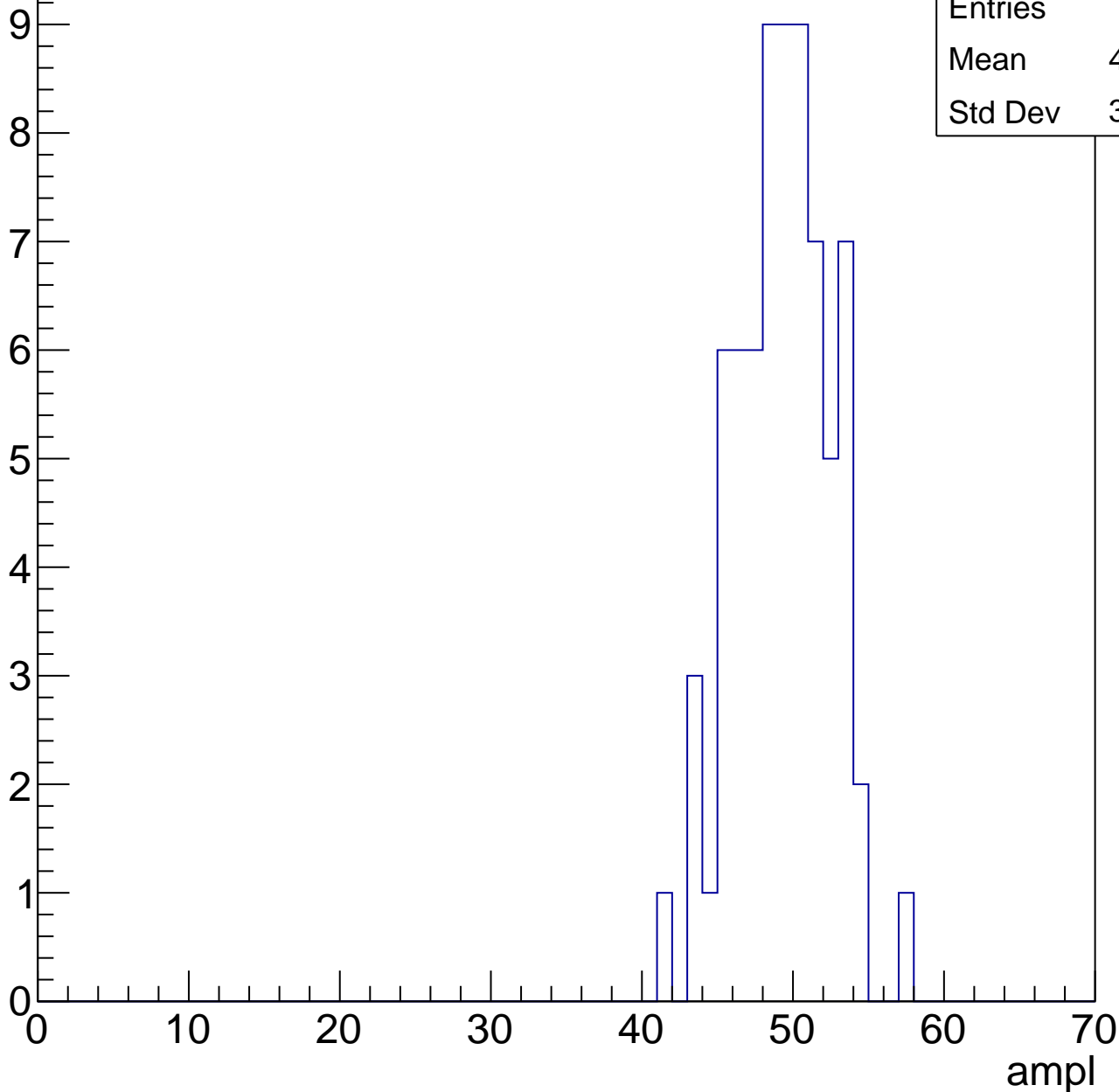
Gaus Width: 3.5988



B1L102S, U2-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



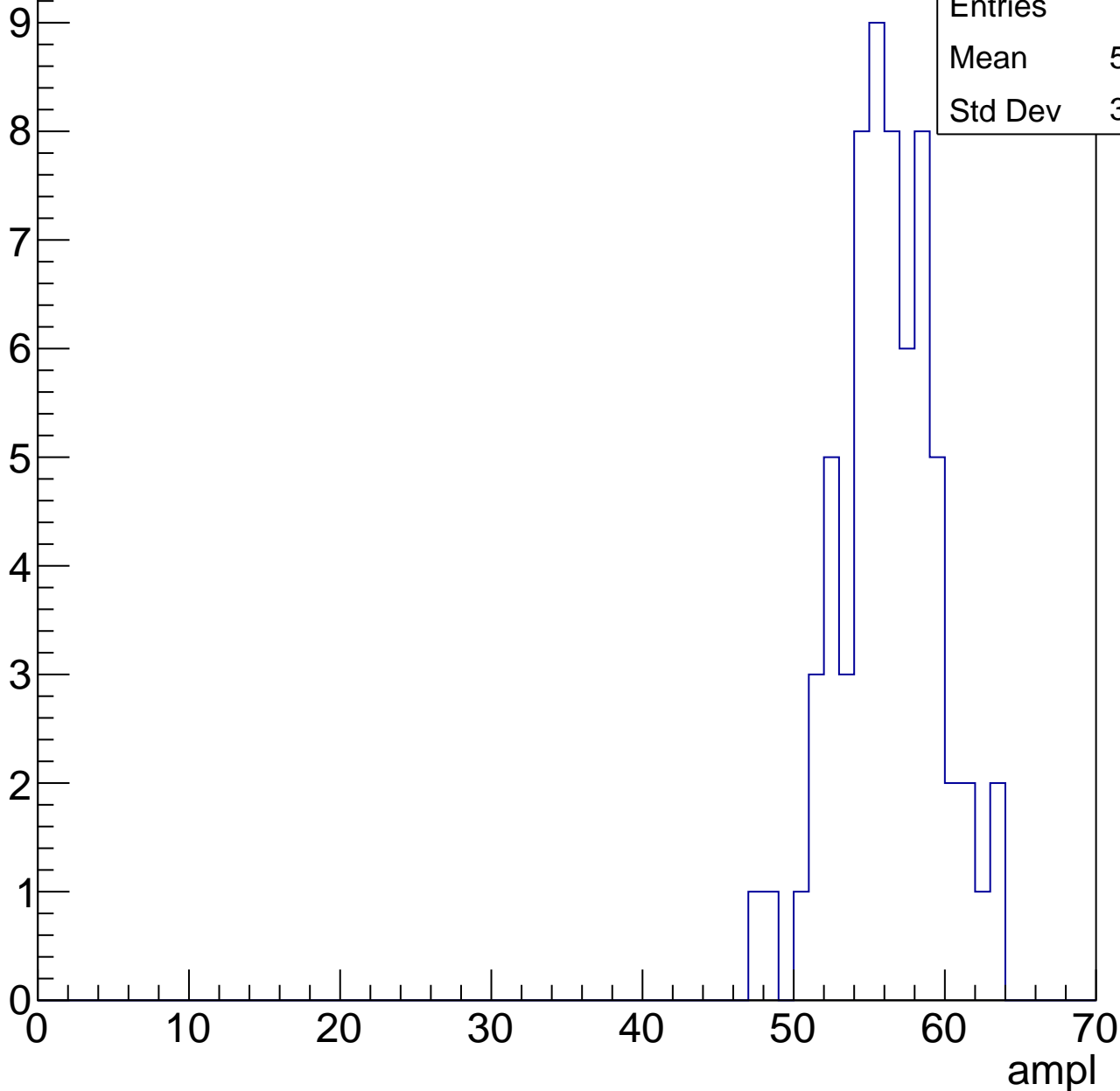
Entries	72
Mean	48.86
Std Dev	3.088

B1L102S, U2-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.74
Std Dev	3.278

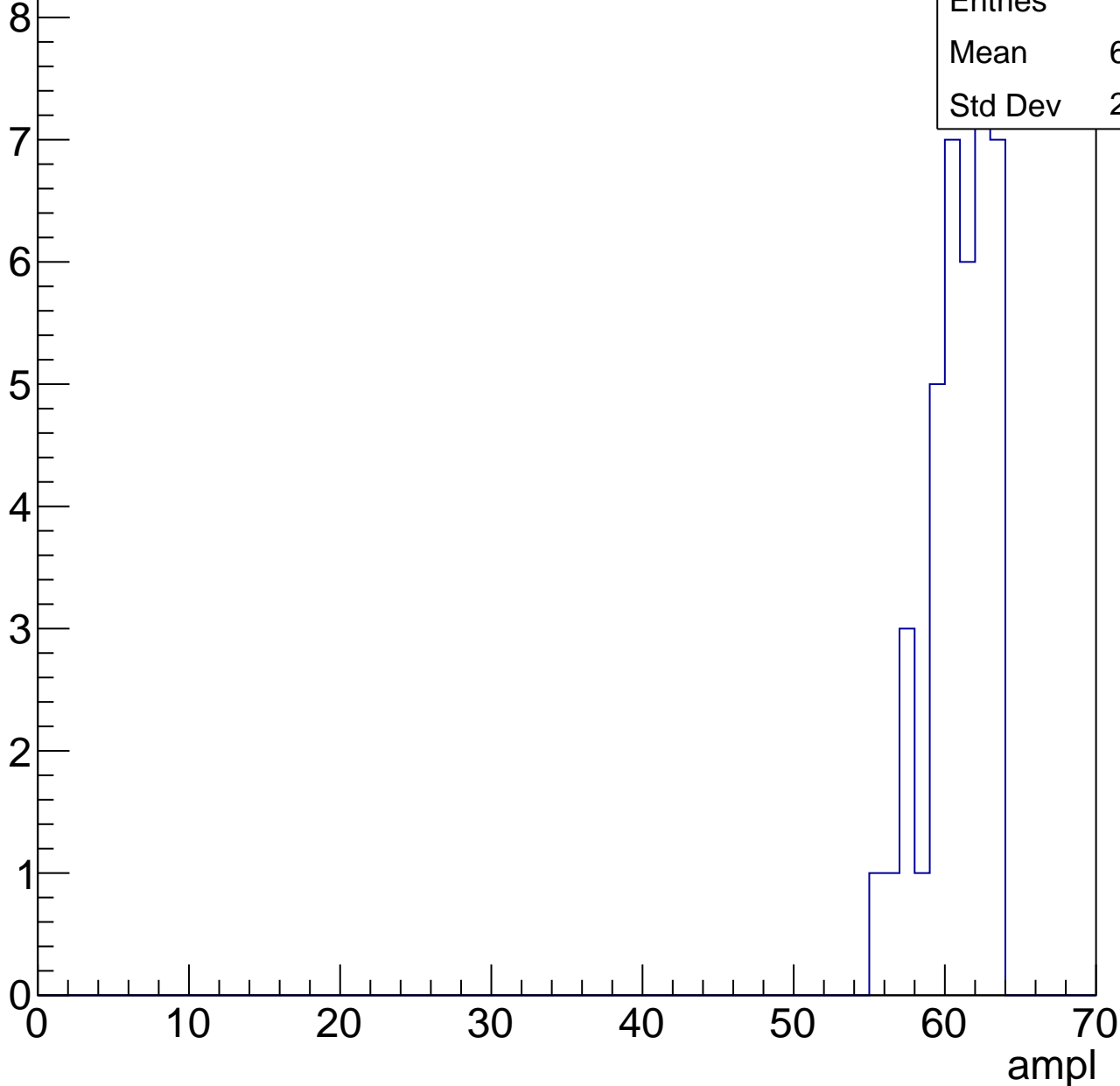


B1L102S, U2-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

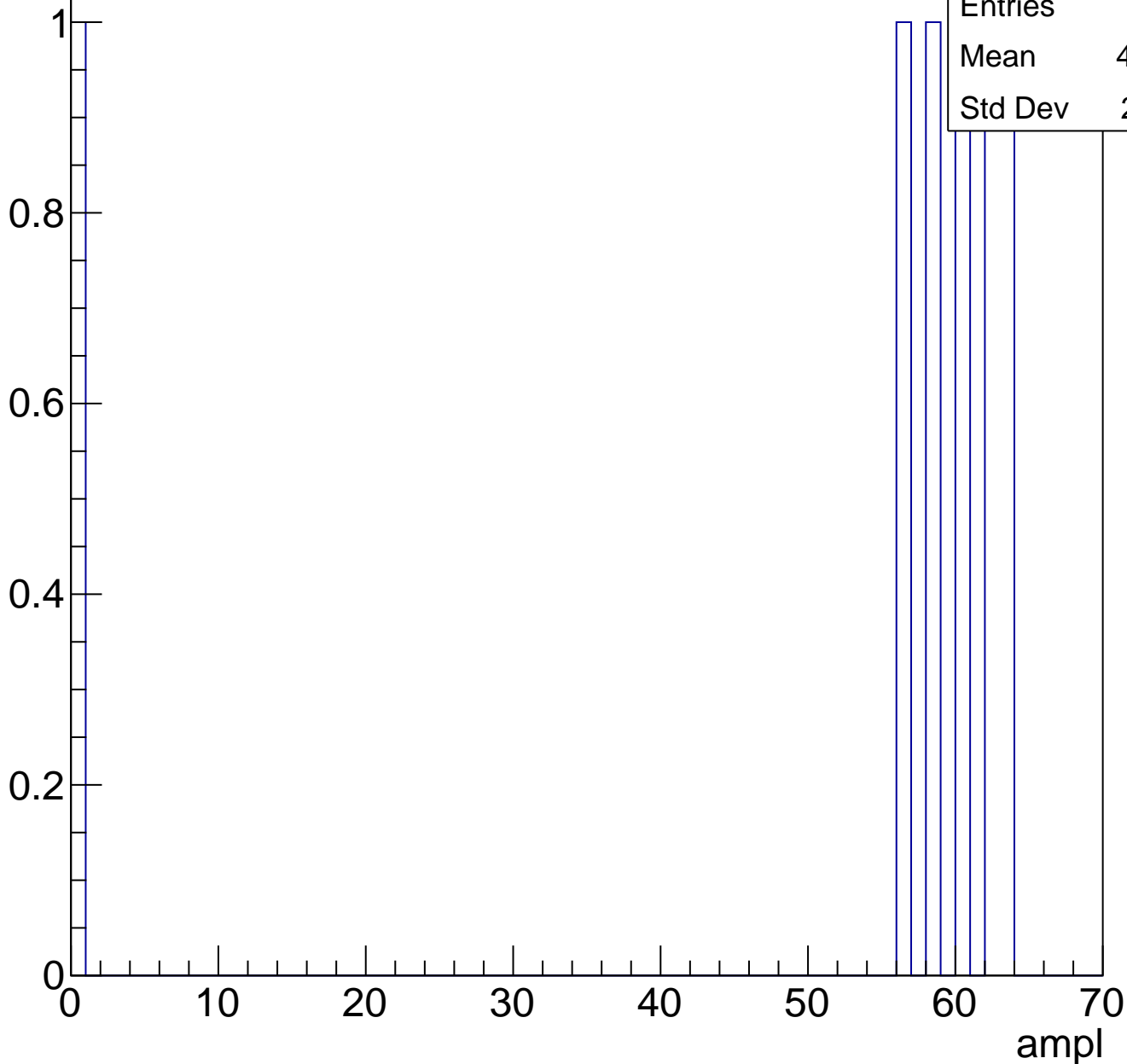
Entries	39
Mean	60.46
Std Dev	2.086



B1L102S, U2-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	61
Std Dev	0

ampl

B1L102S, U2-ch23, adc0

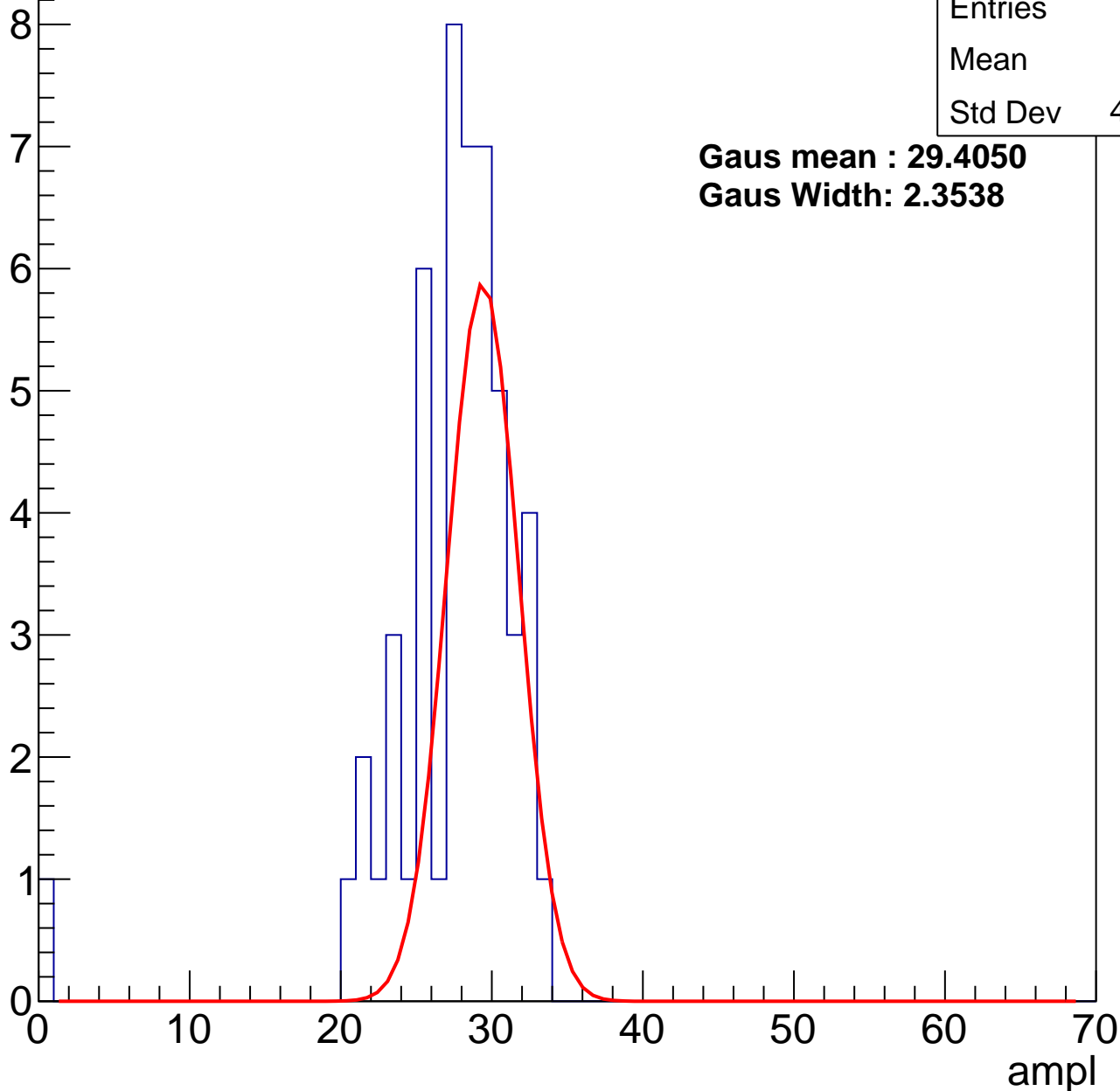
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	26.9
Std Dev	4.892

Gaus mean : 29.4050

Gaus Width: 2.3538



B1L102S, U2-ch23, adc1

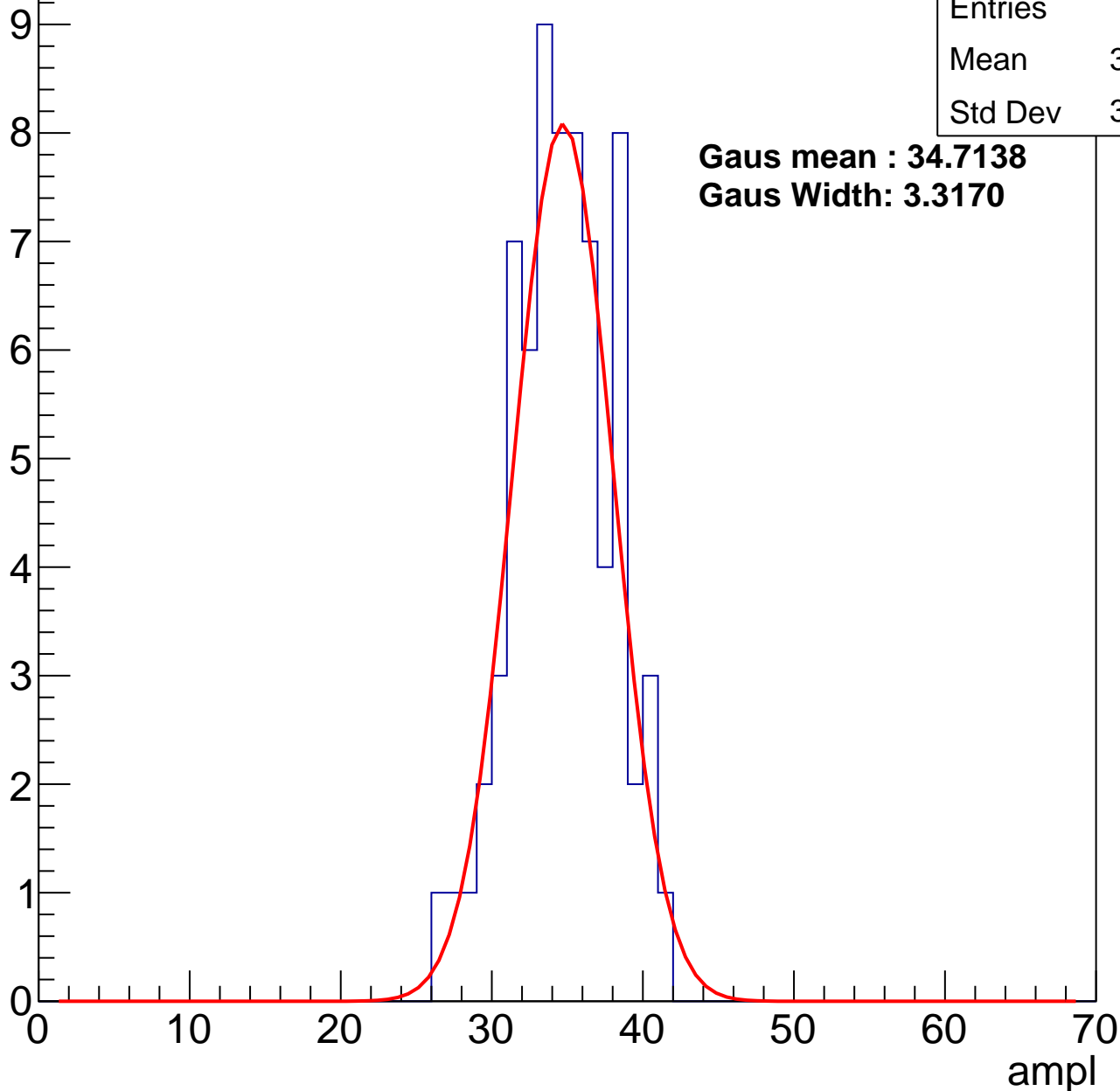
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	34.23
Std Dev	3.242

Gaus mean : 34.7138

Gaus Width: 3.3170



B1L102S, U2-ch23, adc2

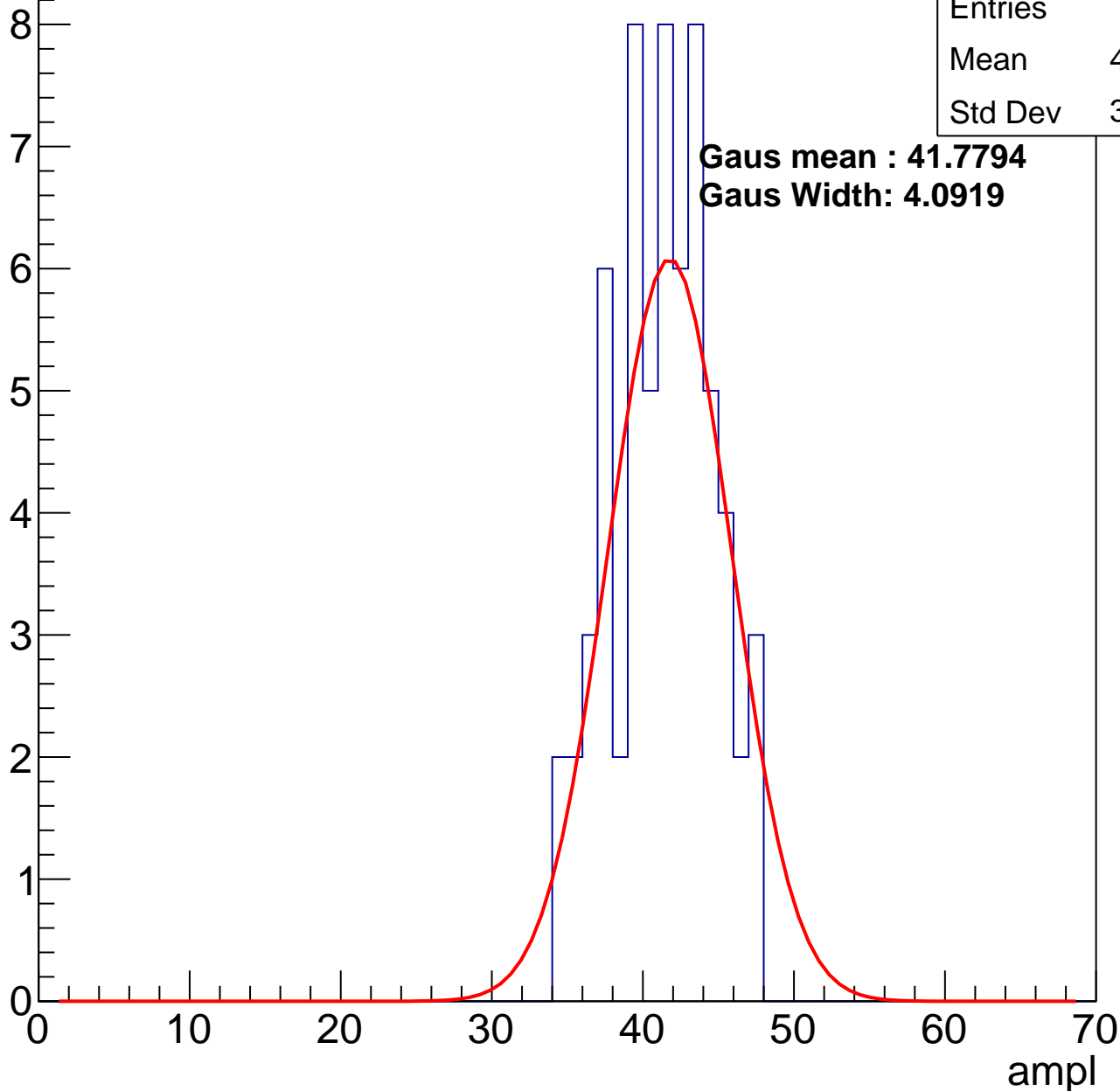
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	40.83
Std Dev	3.305

Gaus mean : 41.7794

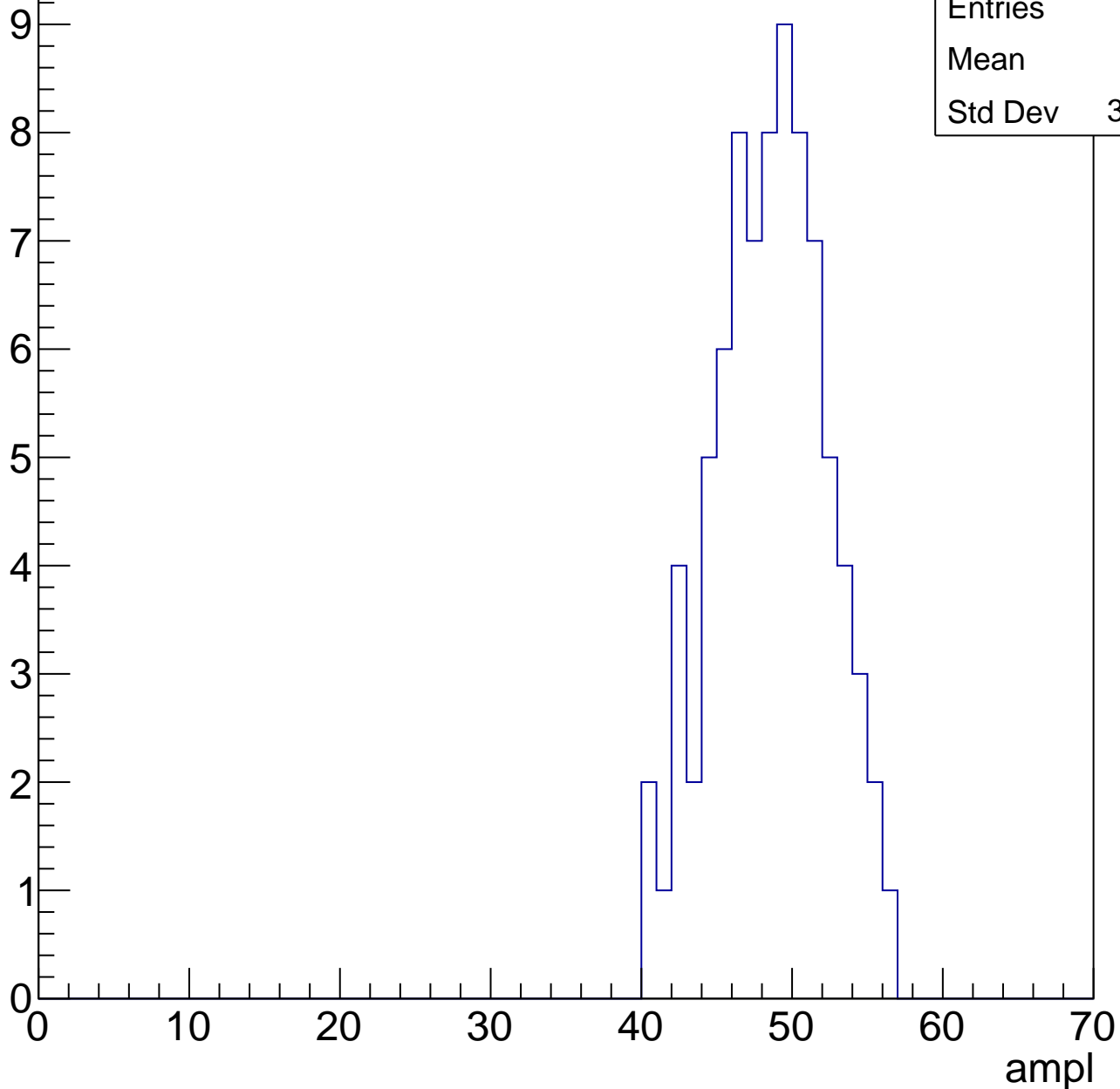
Gaus Width: 4.0919



B1L102S, U2-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

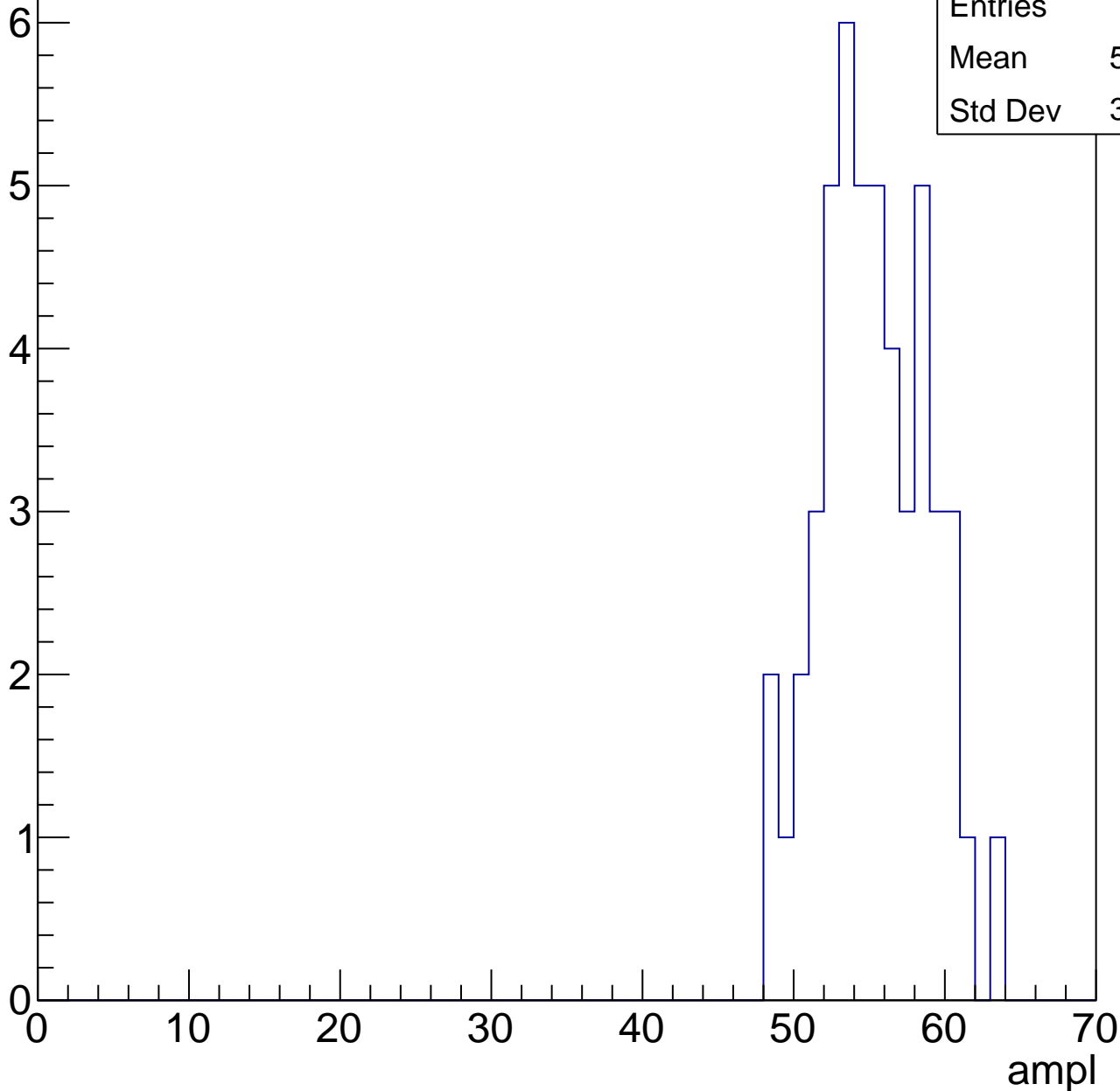


B1L102S, U2-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.84
Std Dev	3.466

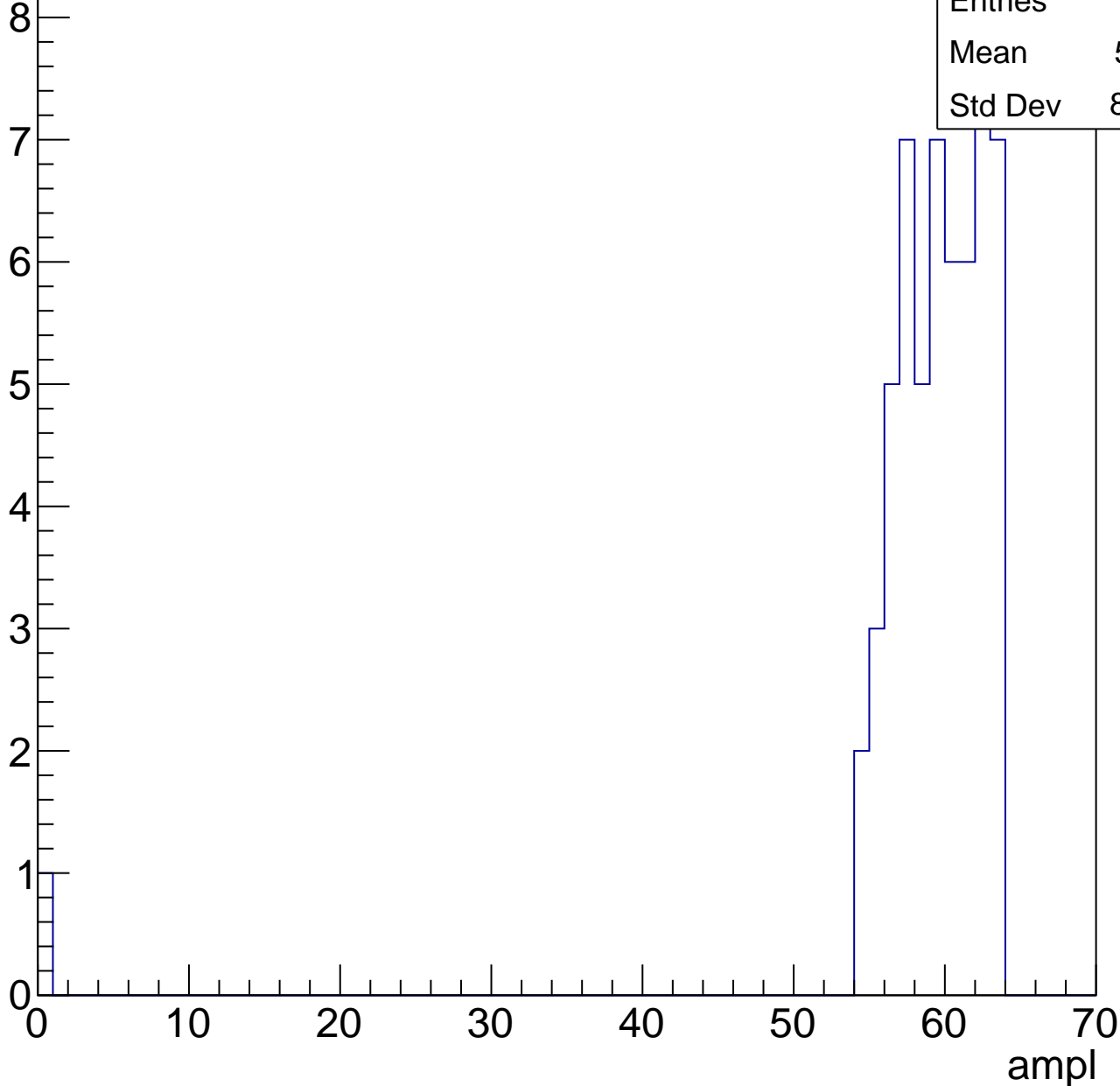


B1L102S, U2-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.21
Std Dev	8.203



B1L102S, U2-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

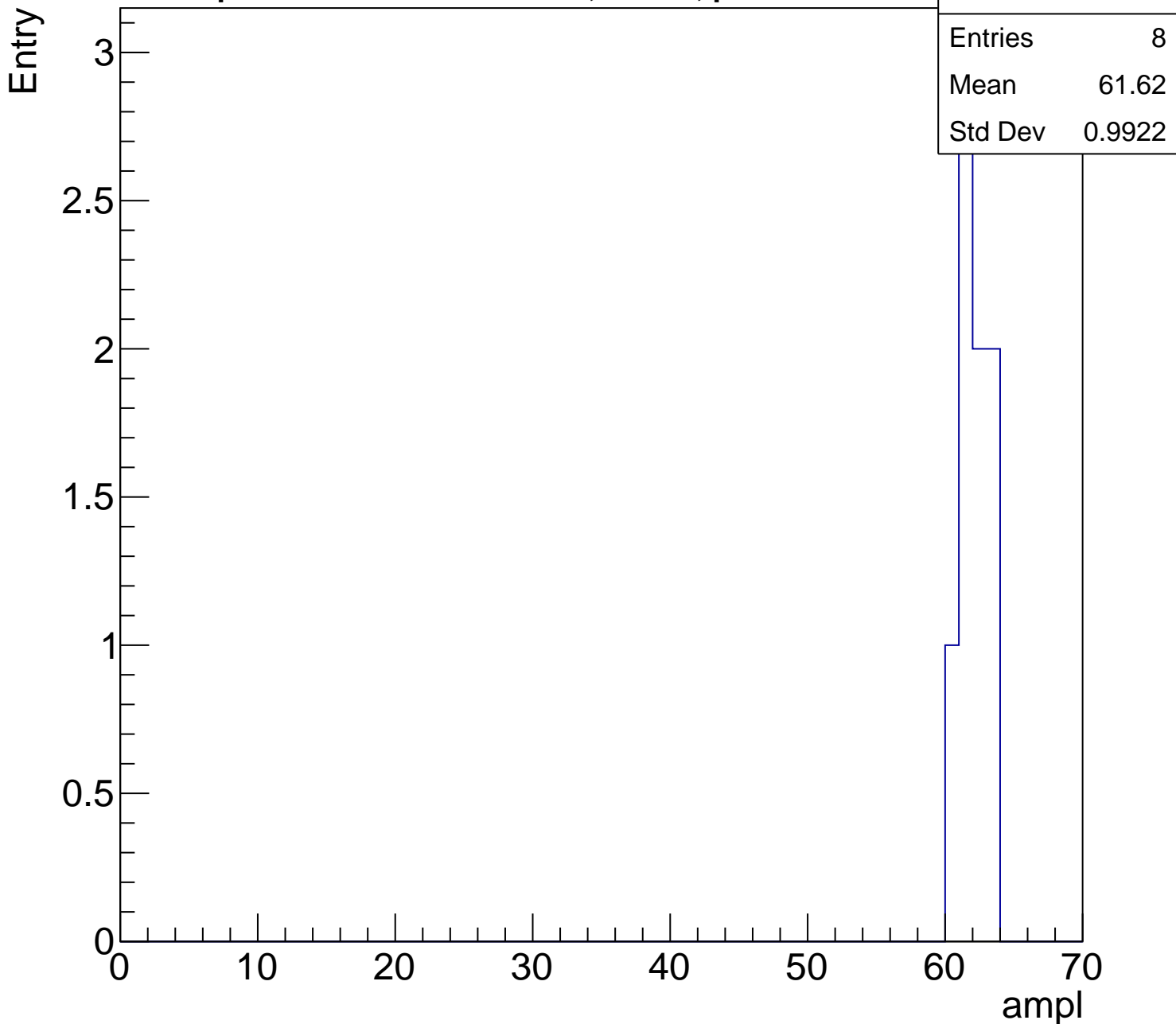
8

Mean

61.62

Std Dev

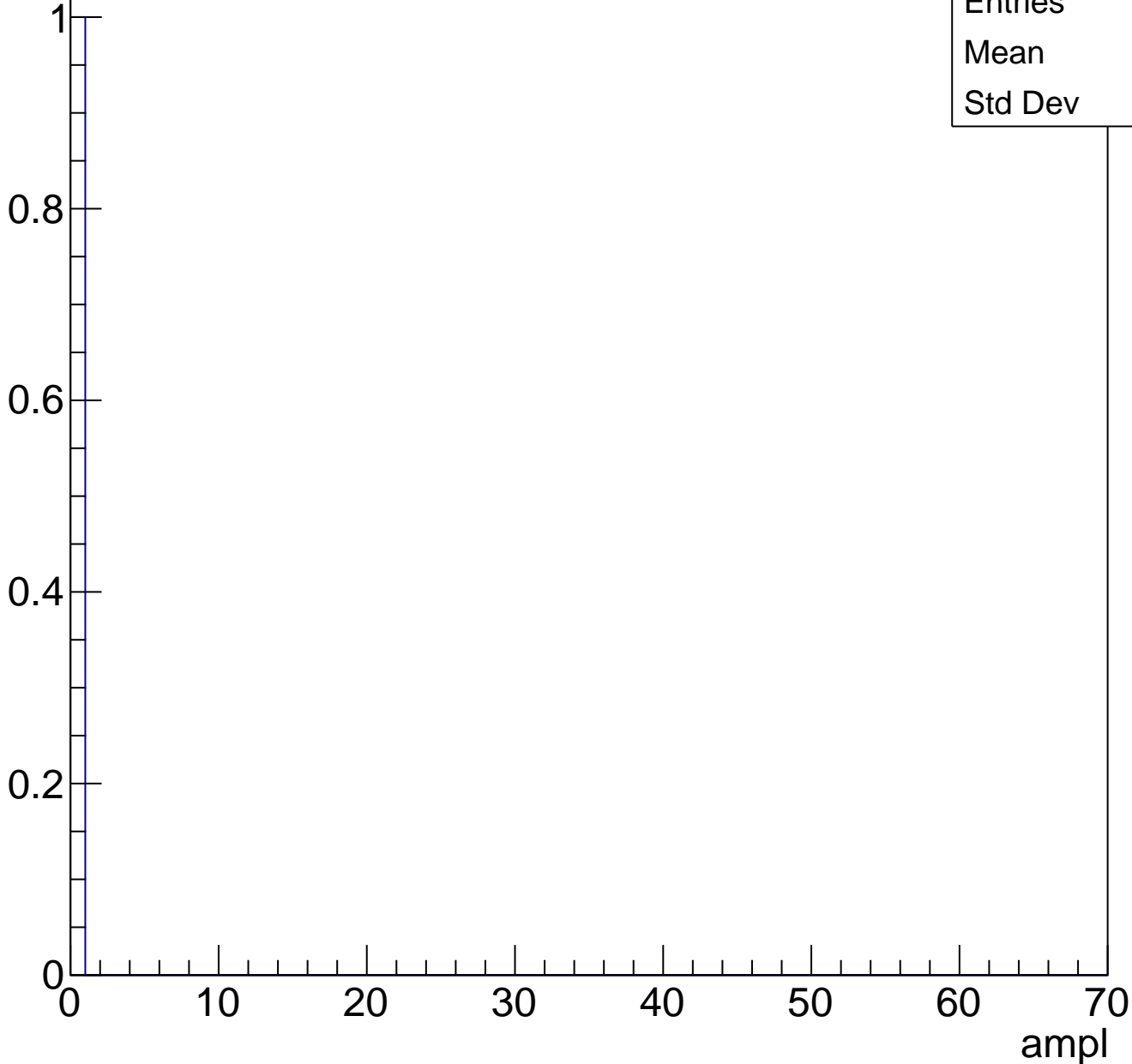
0.9922



B1L102S, U2-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch24, adc0

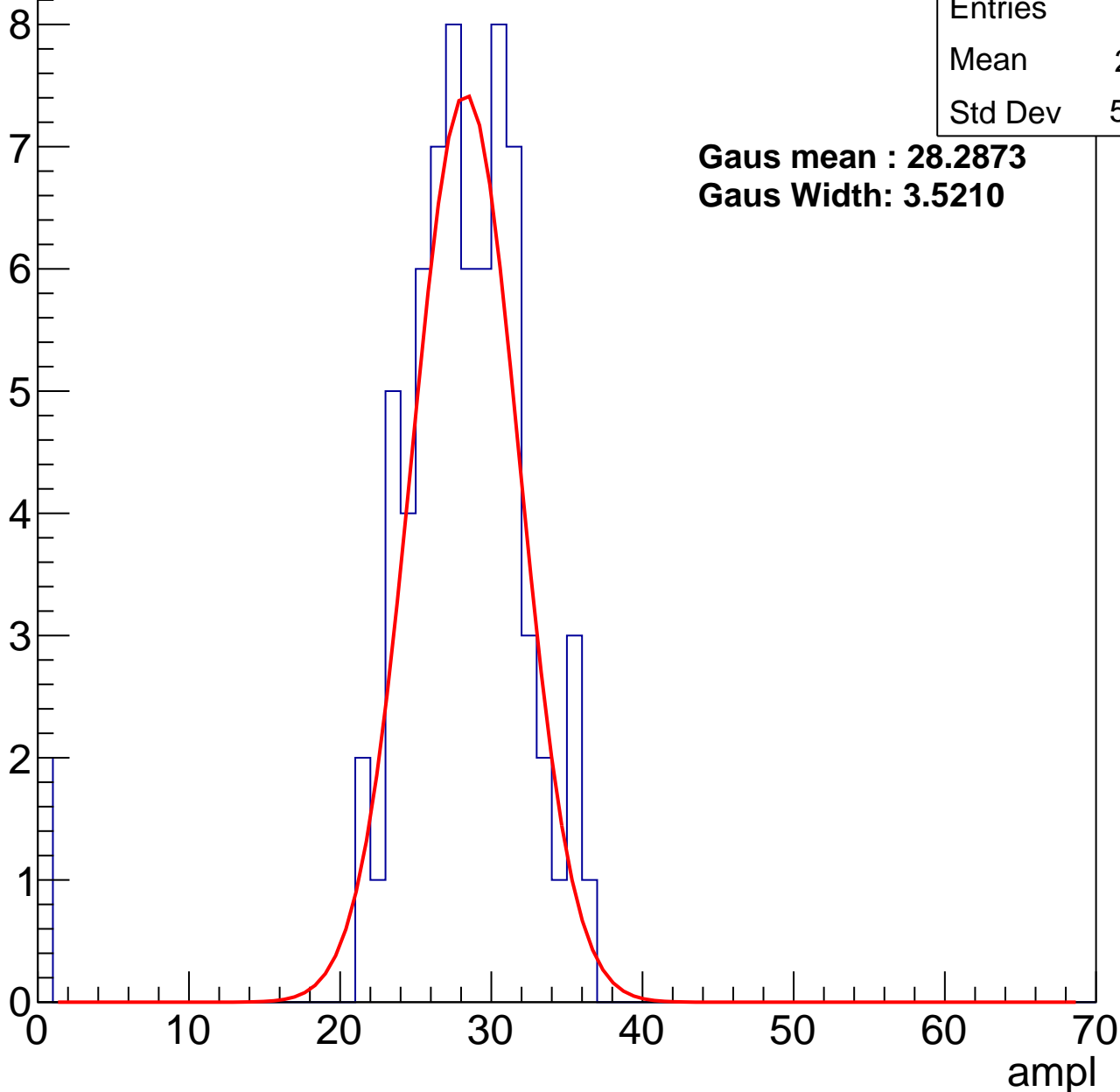
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.21
Std Dev	5.752

Gaus mean : 28.2873

Gaus Width: 3.5210



B1L102S, U2-ch24, adc1

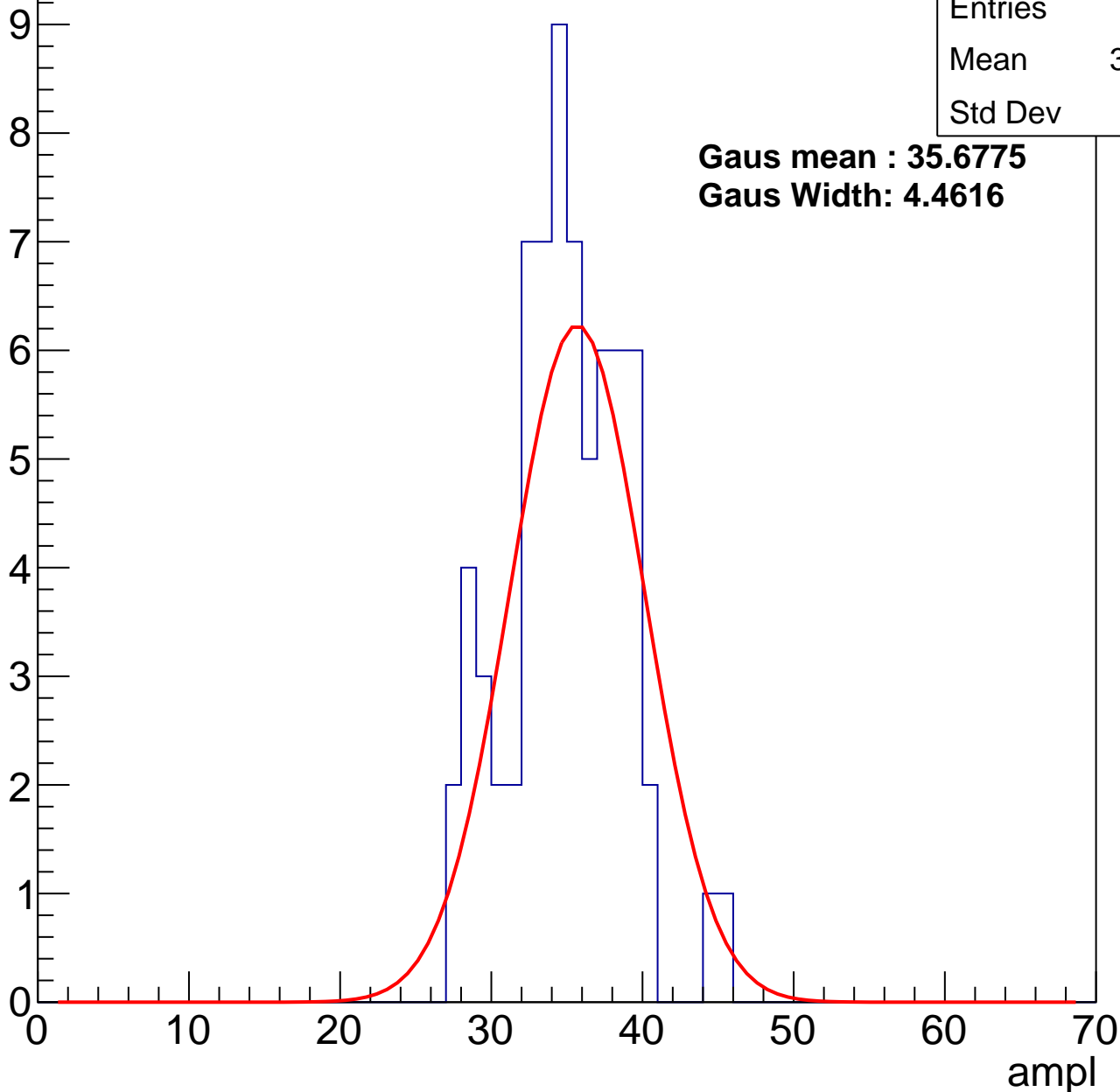
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.49
Std Dev	3.79

Gaus mean : 35.6775

Gaus Width: 4.4616



B1L102S, U2-ch24, adc2

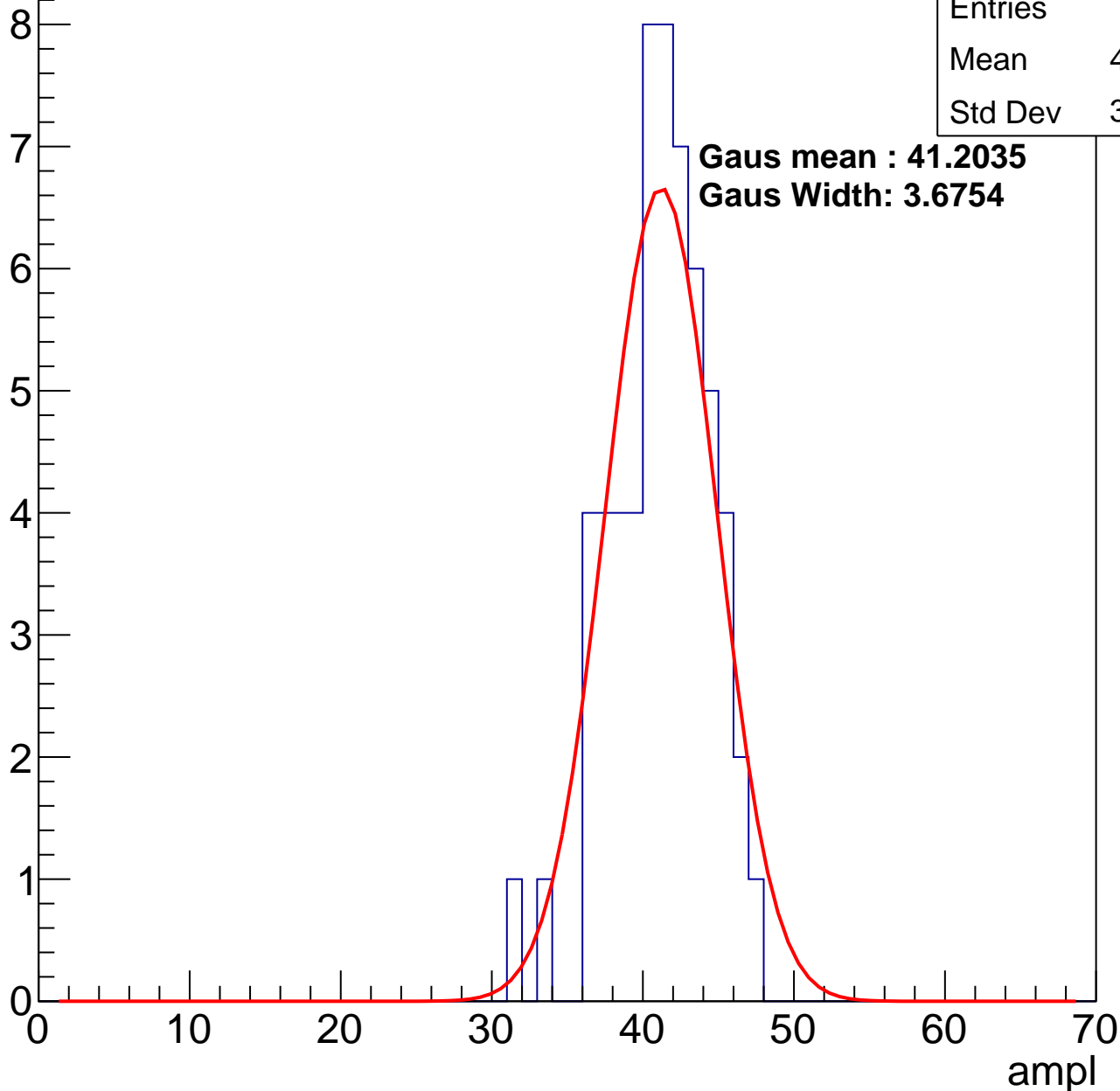
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	40.73
Std Dev	3.215

Gaus mean : 41.2035

Gaus Width: 3.6754

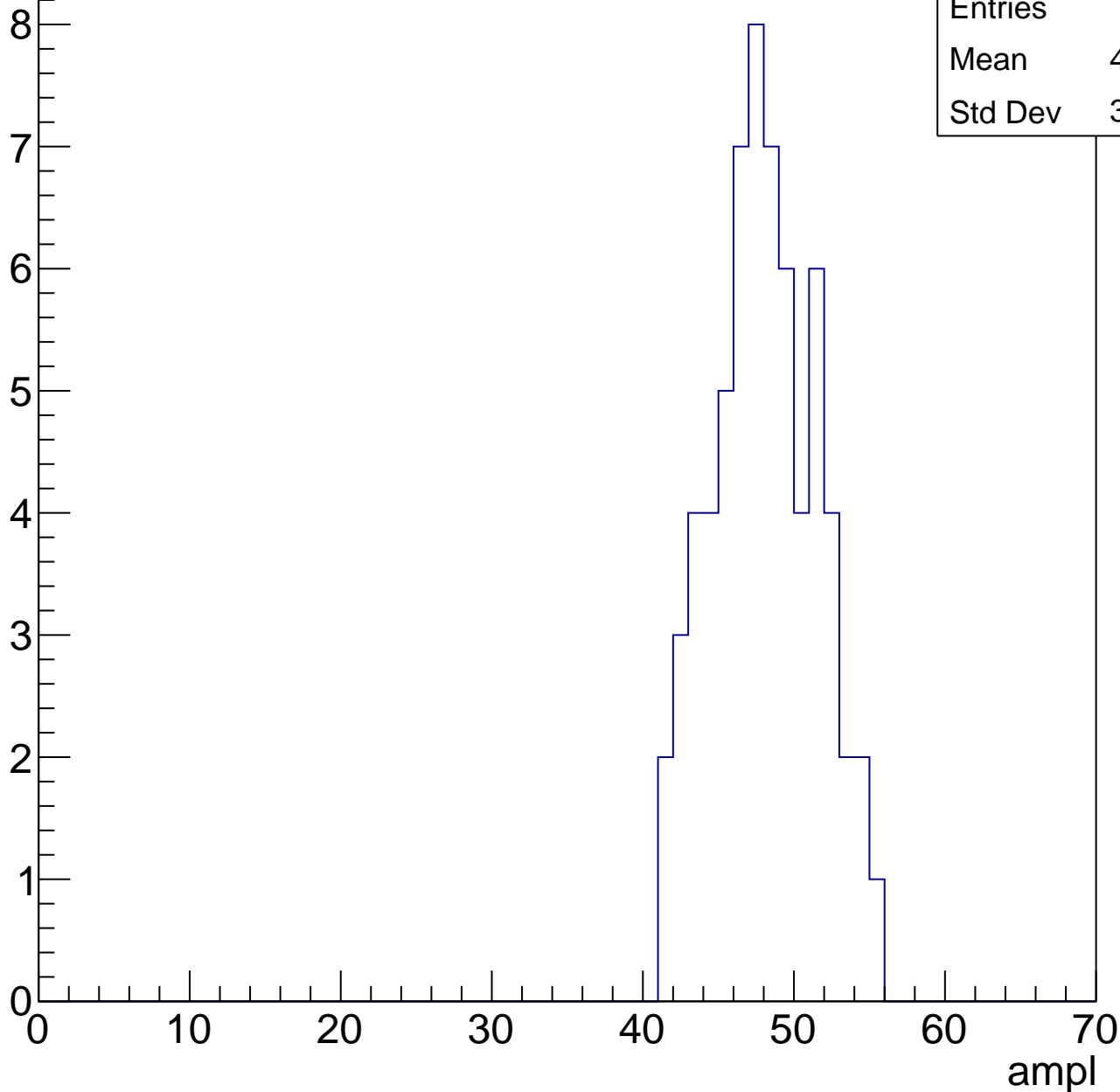


B1L102S, U2-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

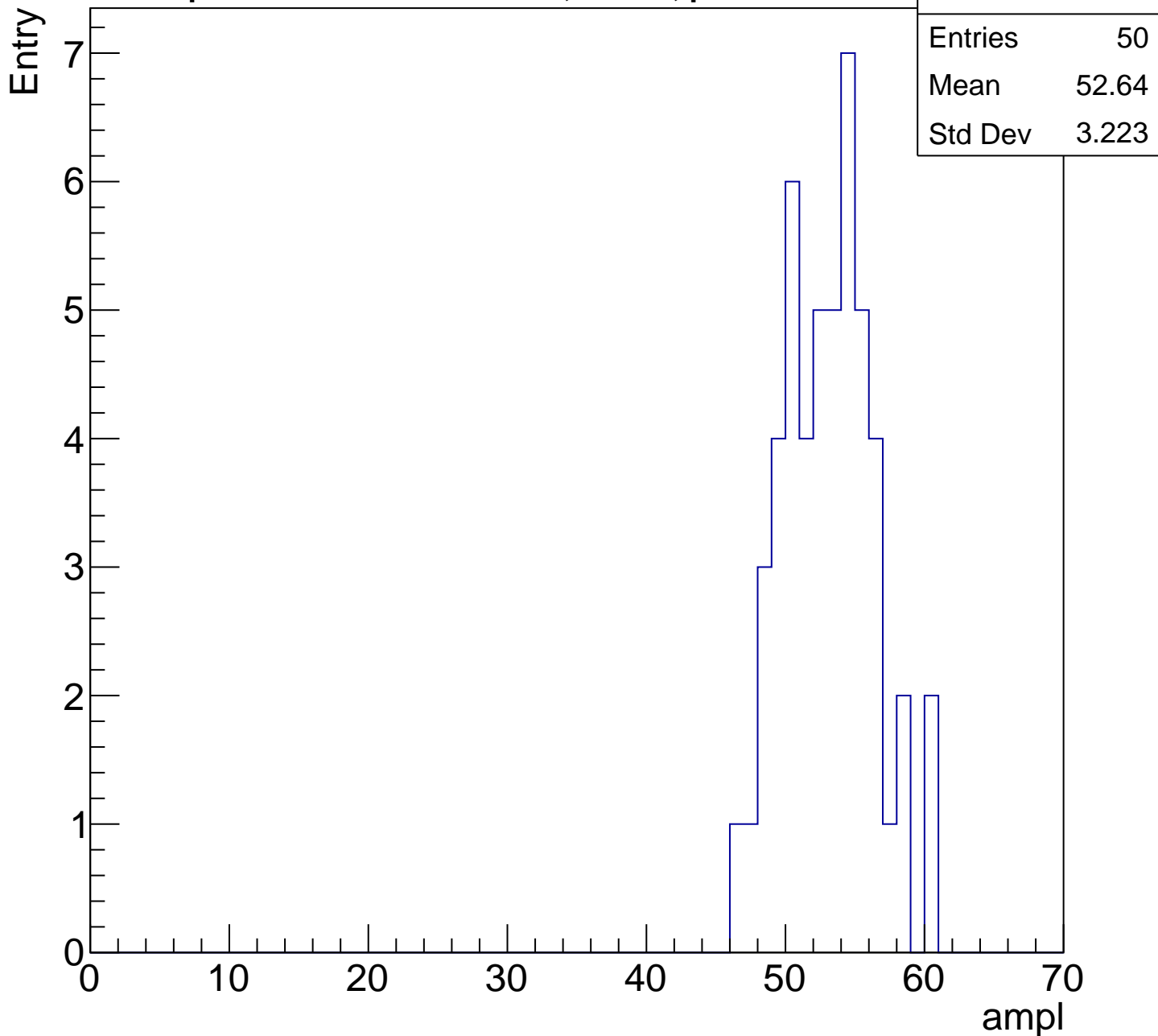
Entry

Entries	65
Mean	47.57
Std Dev	3.397



B1L102S, U2-ch24, adc4

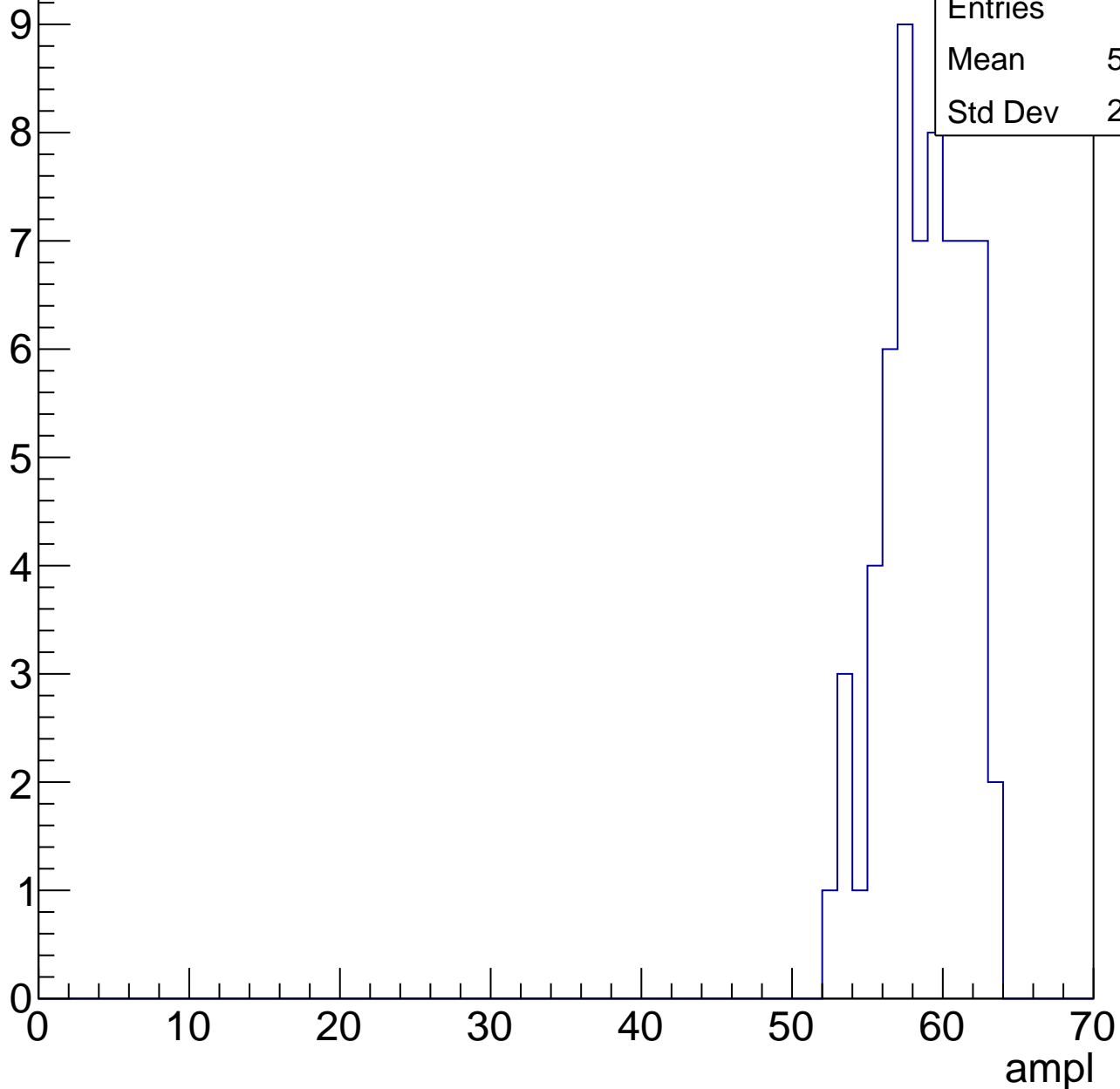
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



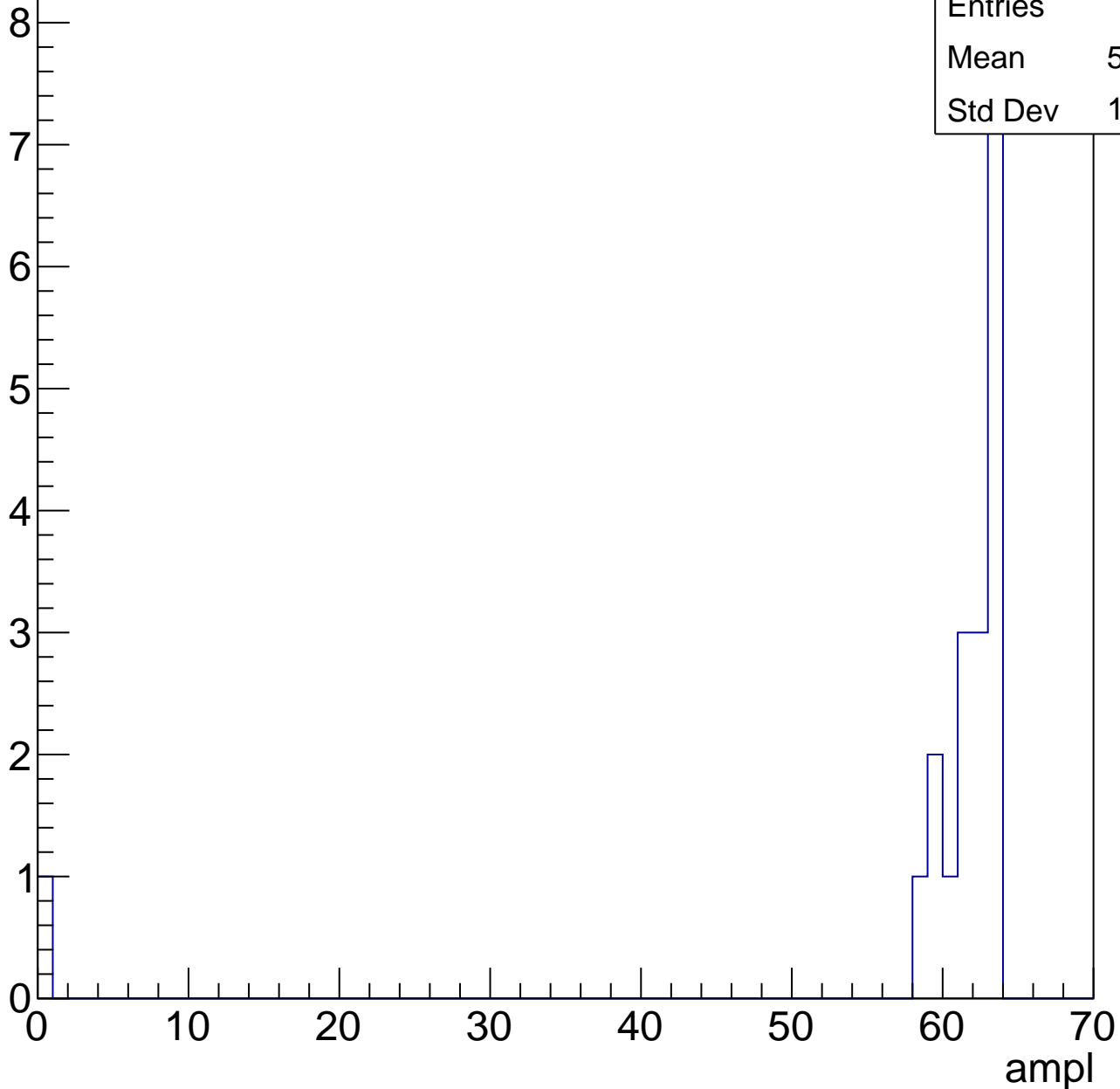
Entries	62
Mean	58.37
Std Dev	2.689

B1L102S, U2-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

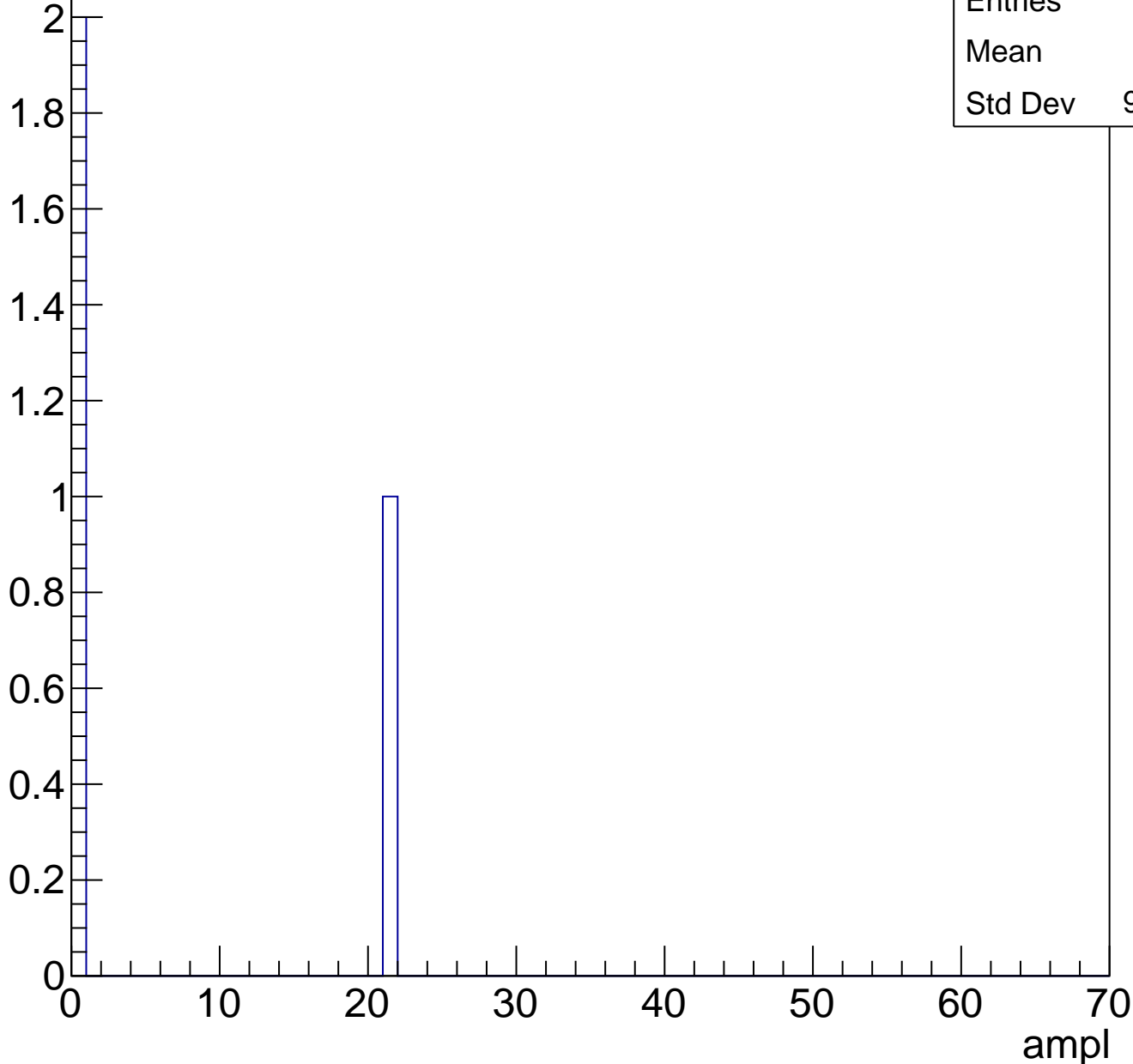
Entries	19
Mean	58.37
Std Dev	13.85



B1L102S, U2-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U2-ch25, adc0

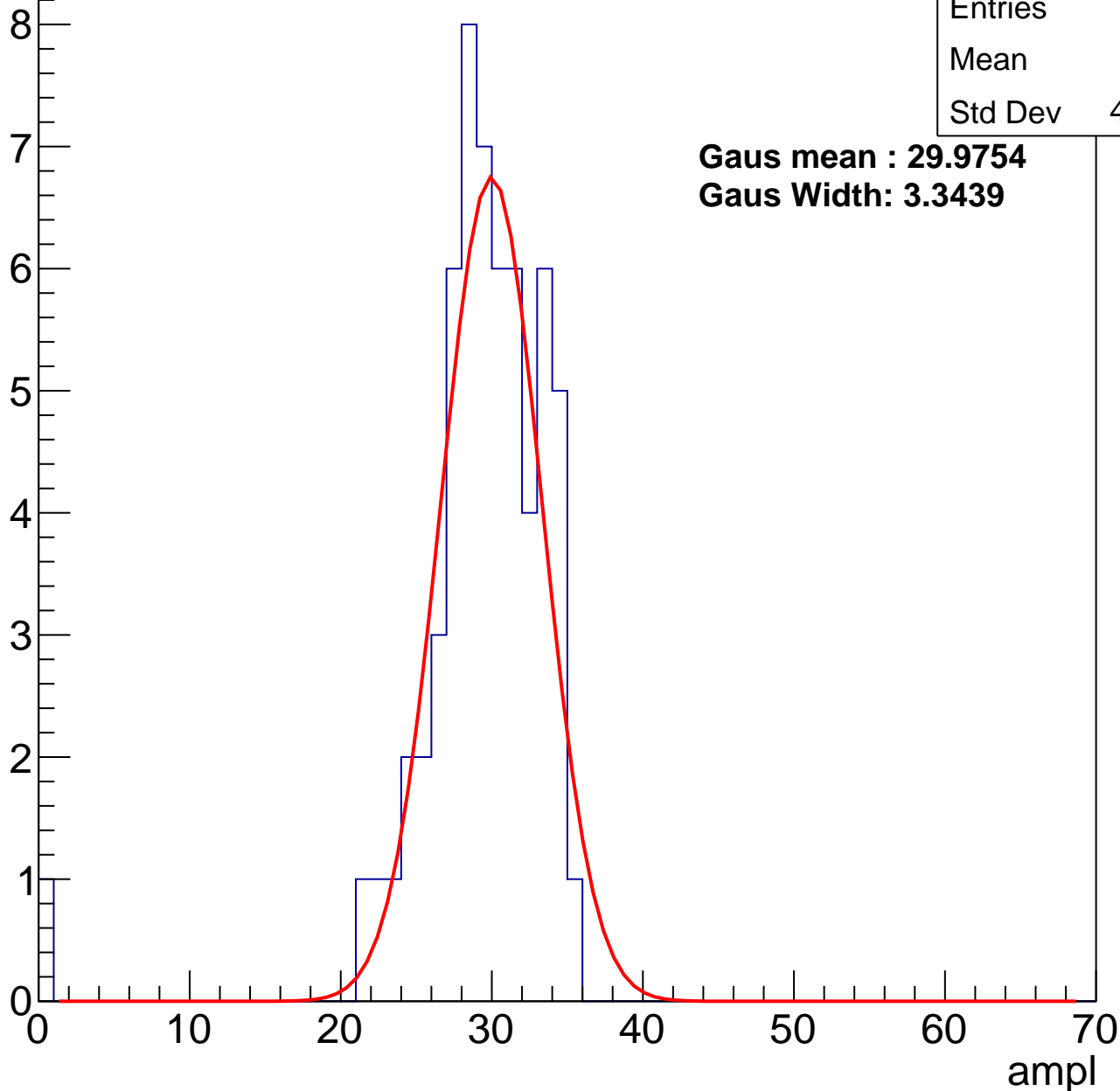
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.8
Std Dev	4.919

Gaus mean : 29.9754

Gaus Width: 3.3439



B1L102S, U2-ch25, adc1

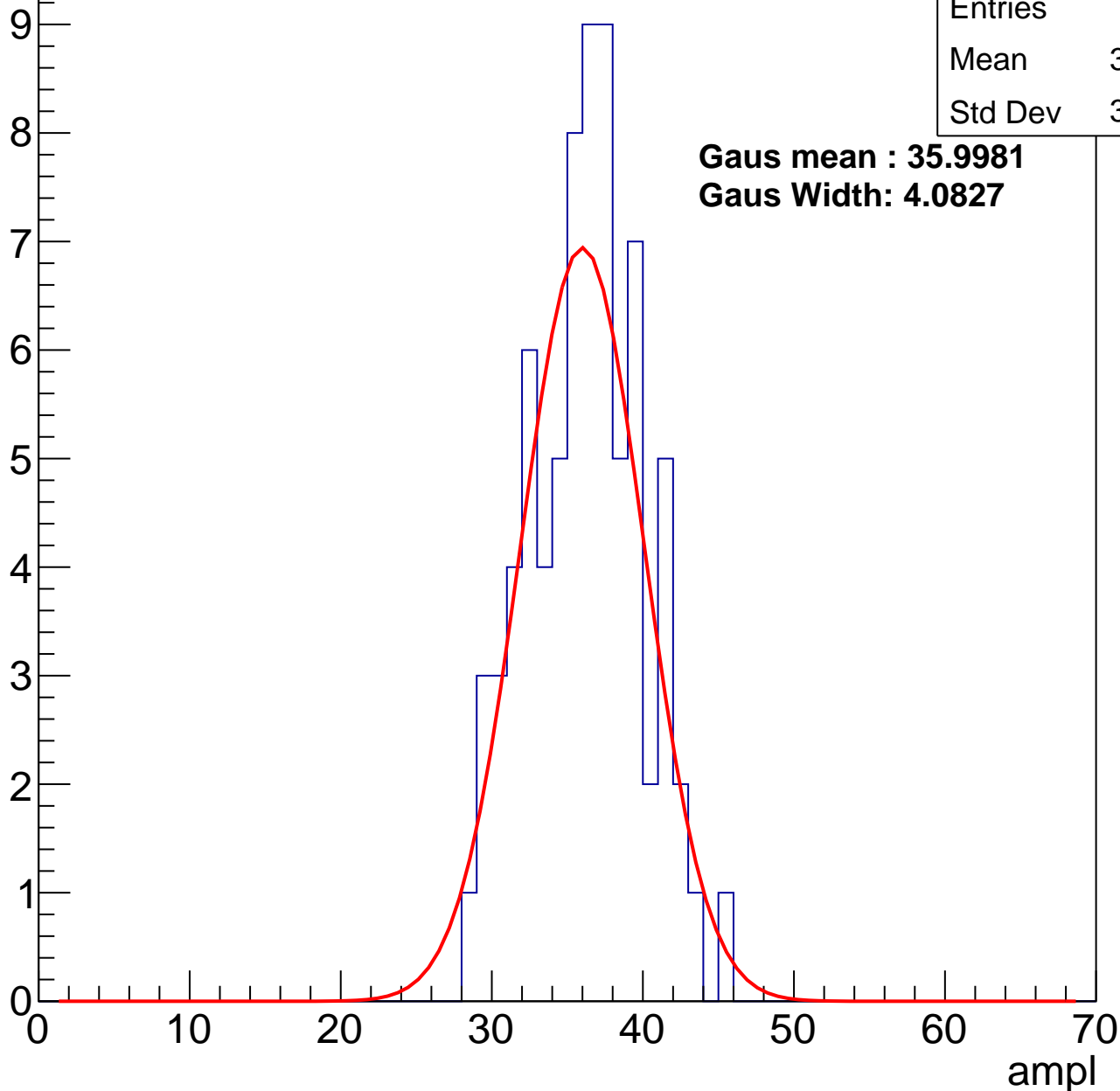
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.73
Std Dev	3.696

Gaus mean : 35.9981

Gaus Width: 4.0827



B1L102S, U2-ch25, adc2

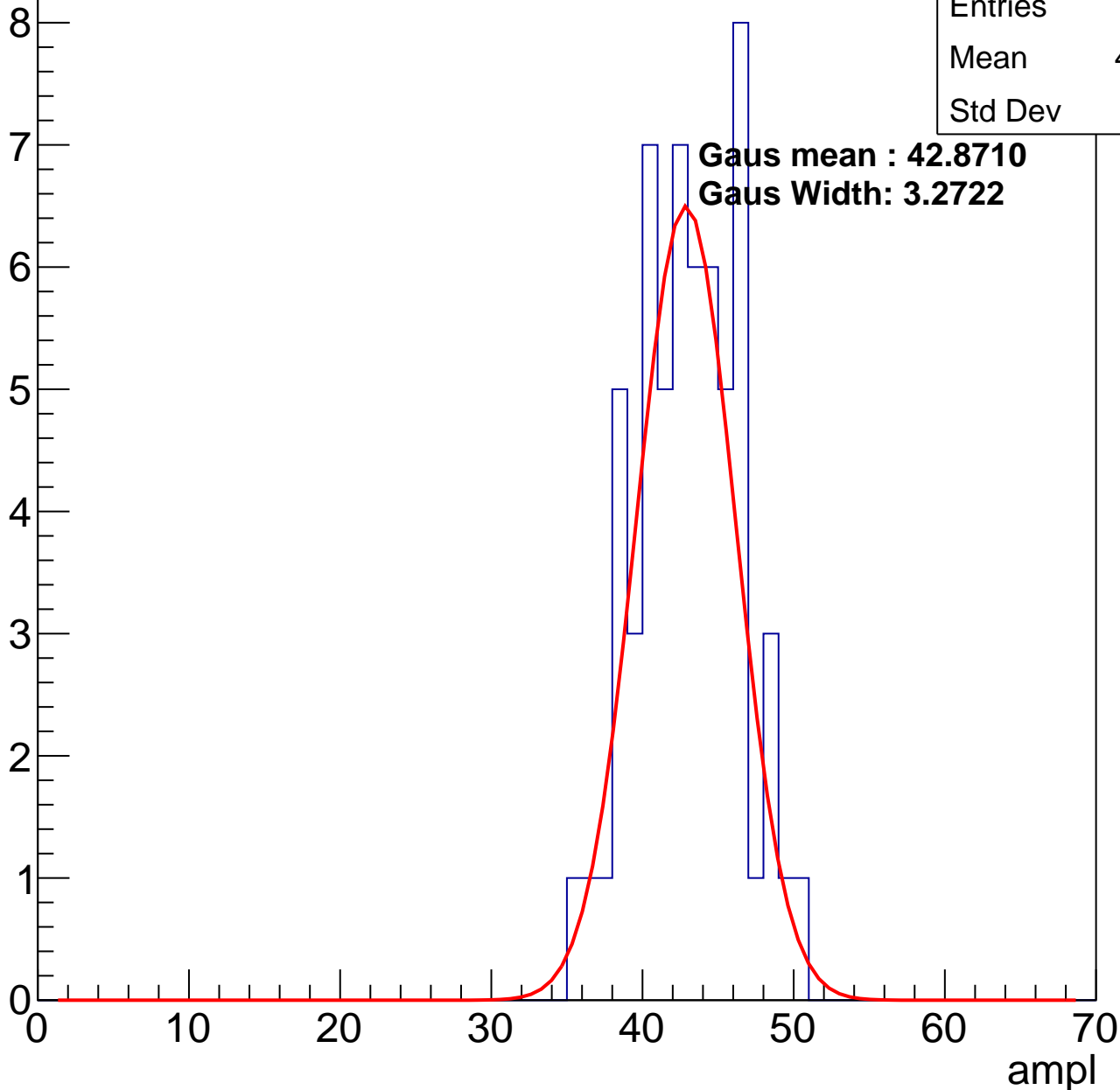
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.61
Std Dev	3.33

Gaus mean : 42.8710

Gaus Width: 3.2722

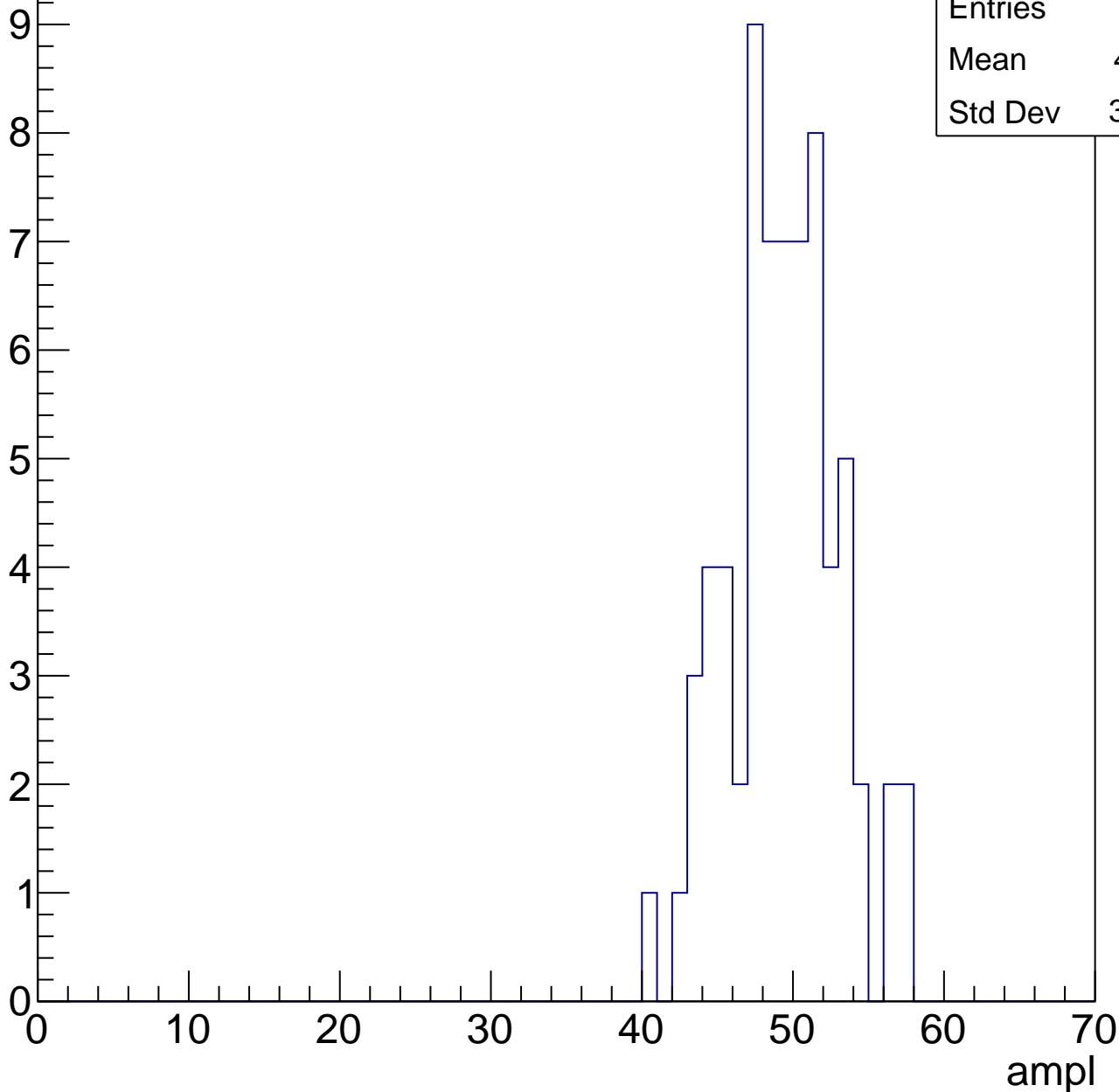


B1L102S, U2-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.91
Std Dev	3.625

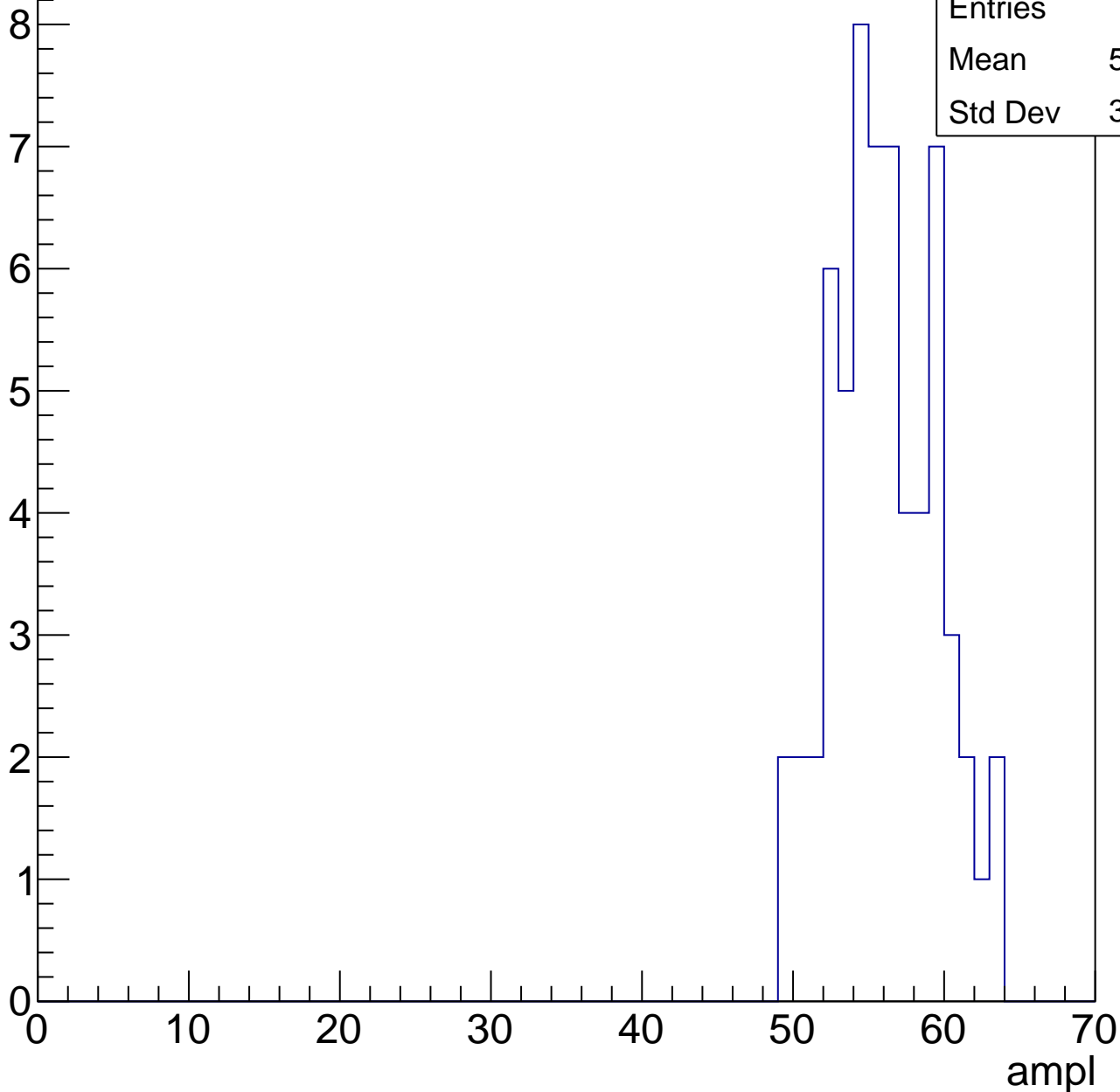


B1L102S, U2-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.63
Std Dev	3.376



B1L102S, U2-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	44
Mean	58.41
Std Dev	9.198

ampl

0

10

20

30

40

50

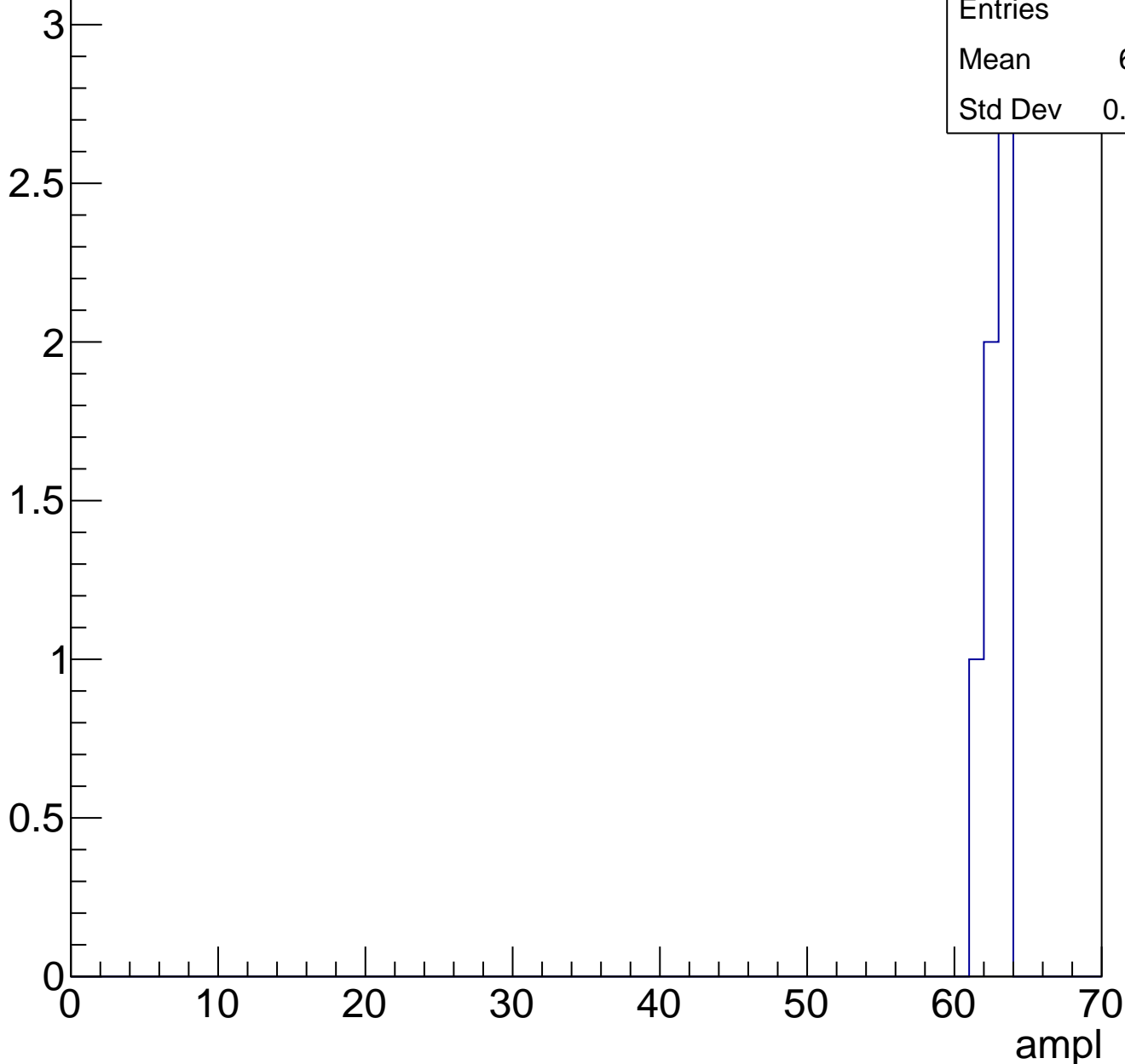
60

70

B1L102S, U2-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch26, adc0

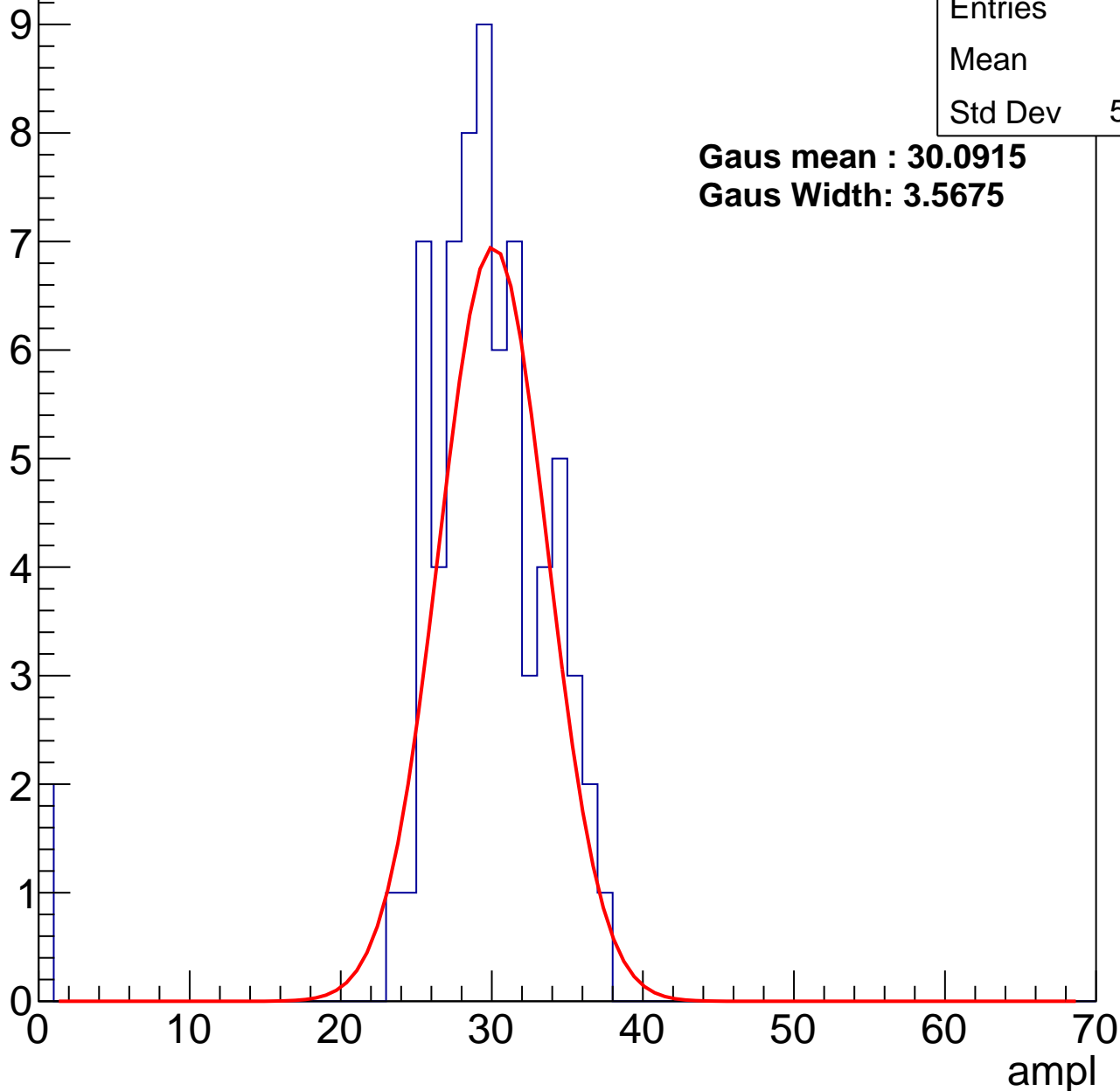
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.7
Std Dev	5.905

Gaus mean : 30.0915

Gaus Width: 3.5675



B1L102S, U2-ch26, adc1

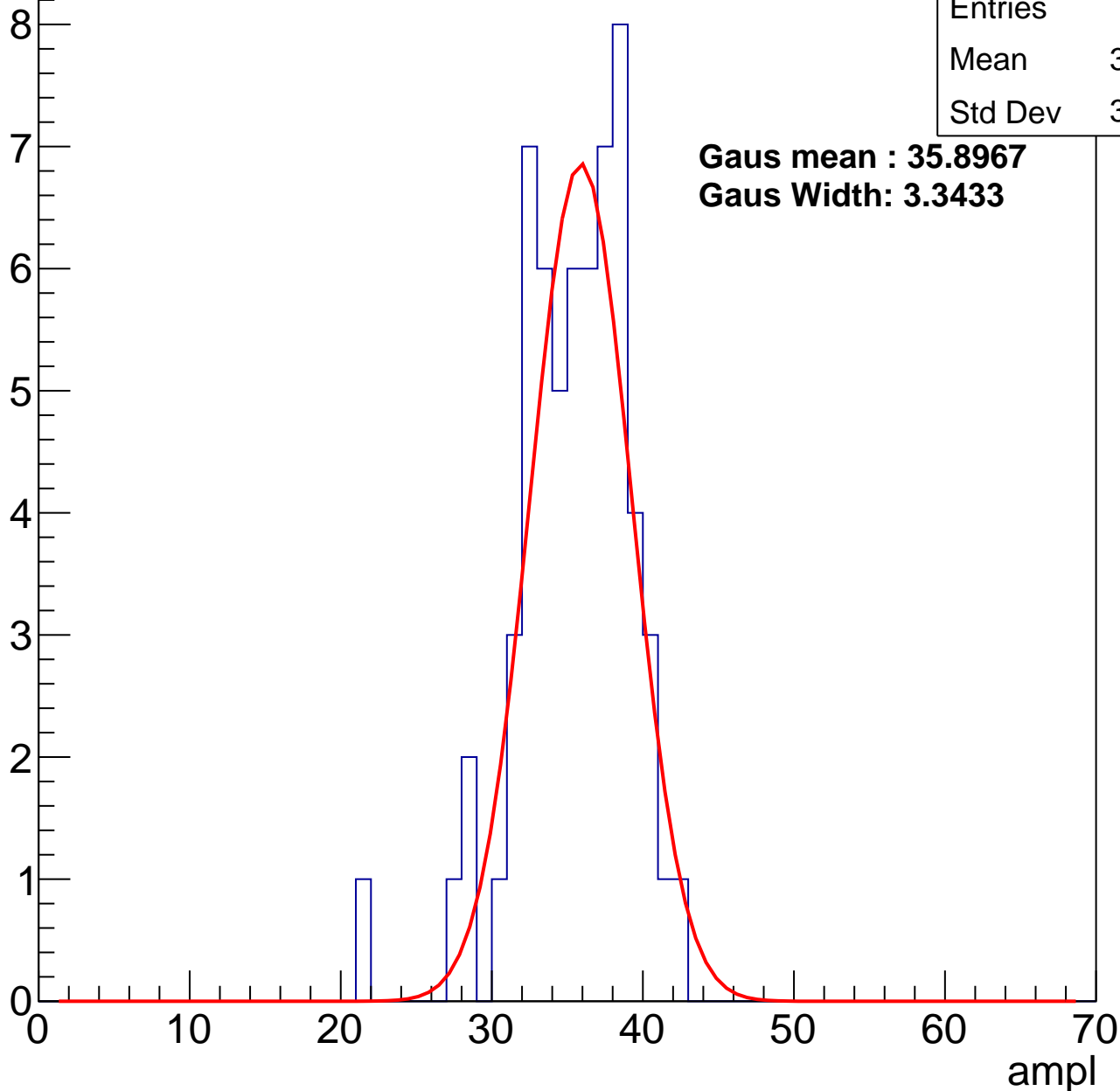
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.95
Std Dev	3.709

Gaus mean : 35.8967

Gaus Width: 3.3433



B1L102S, U2-ch26, adc2

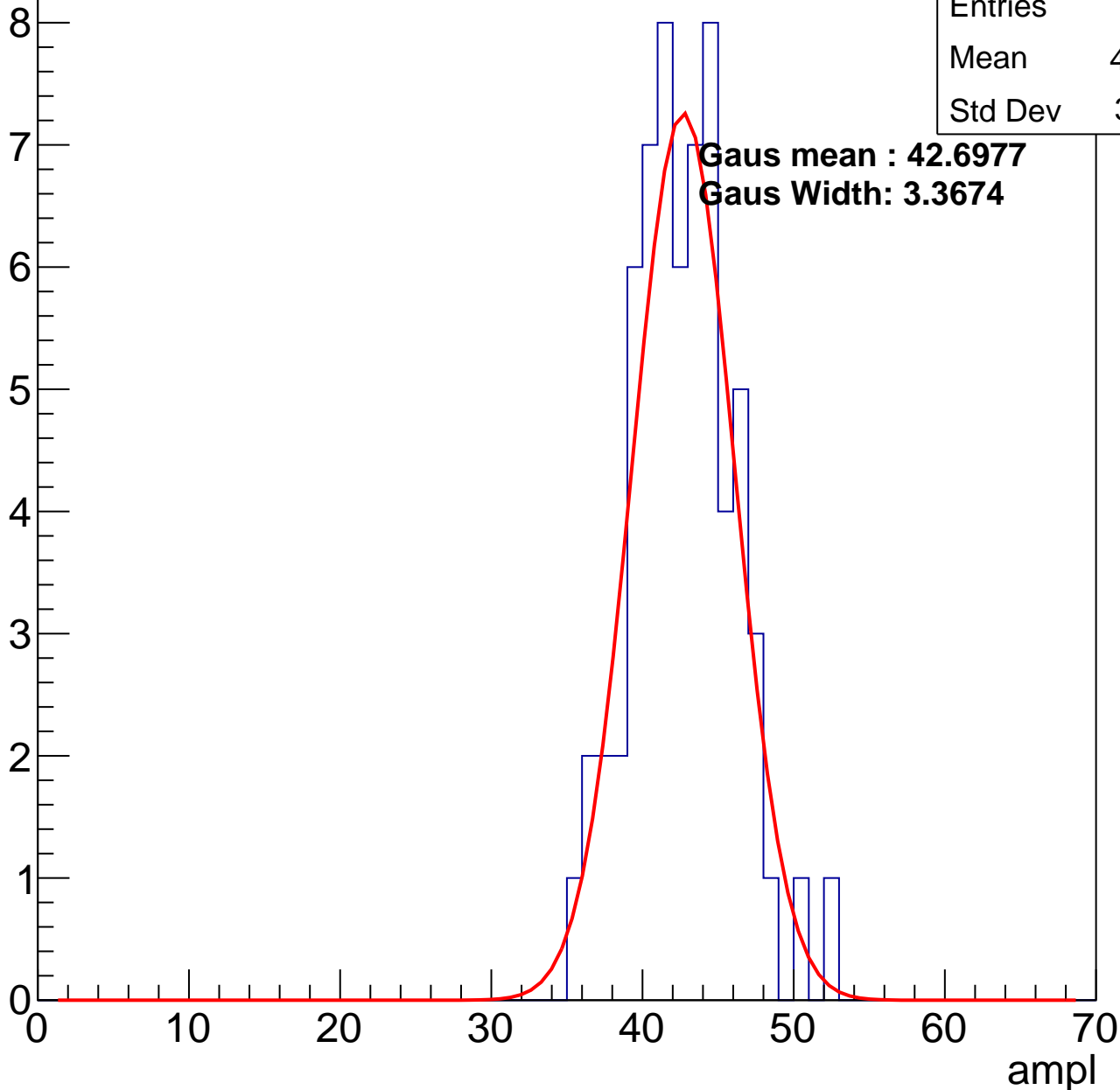
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.27
Std Dev	3.351

Gaus mean : 42.6977

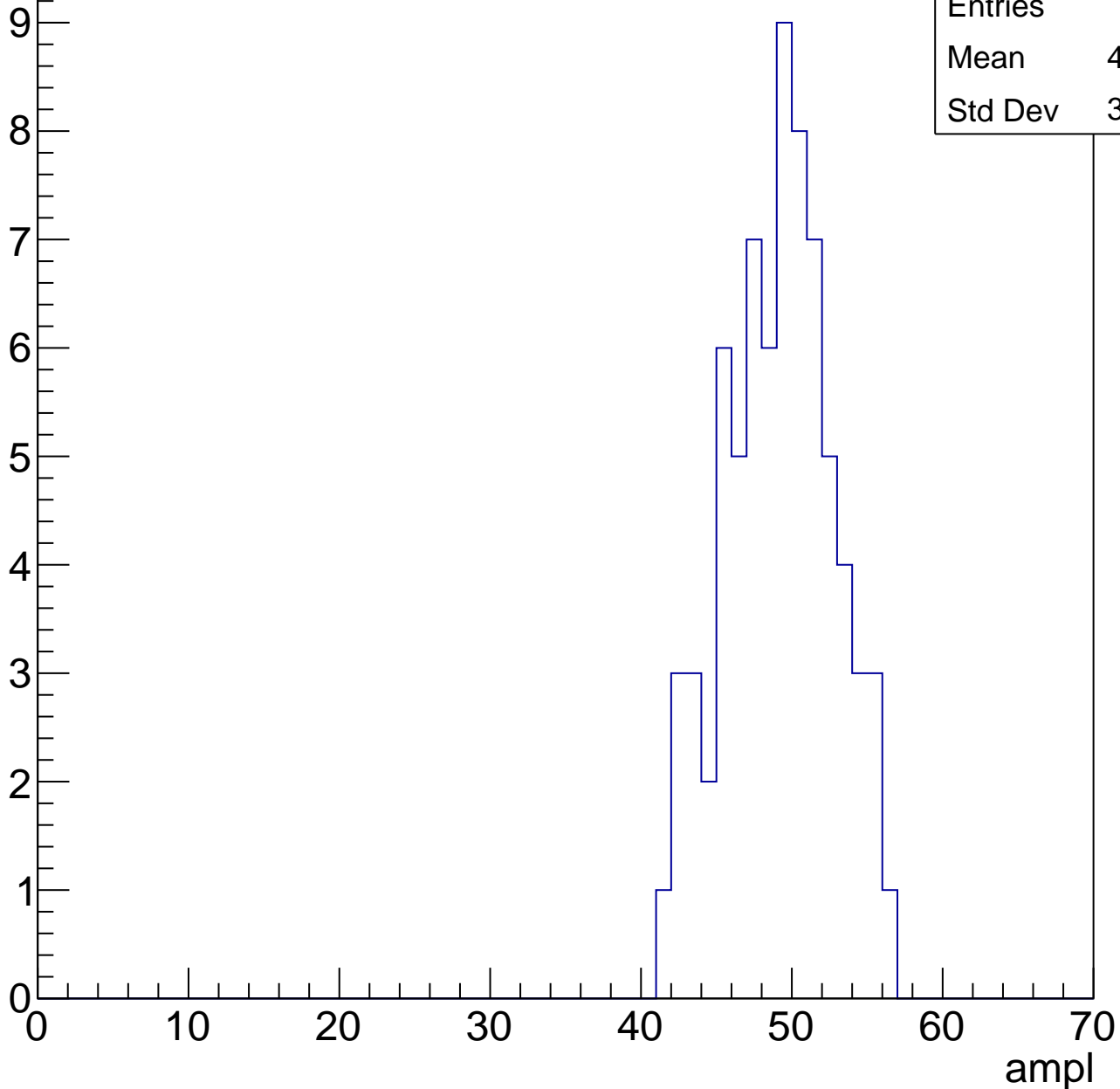
Gaus Width: 3.3674



B1L102S, U2-ch26, adc3

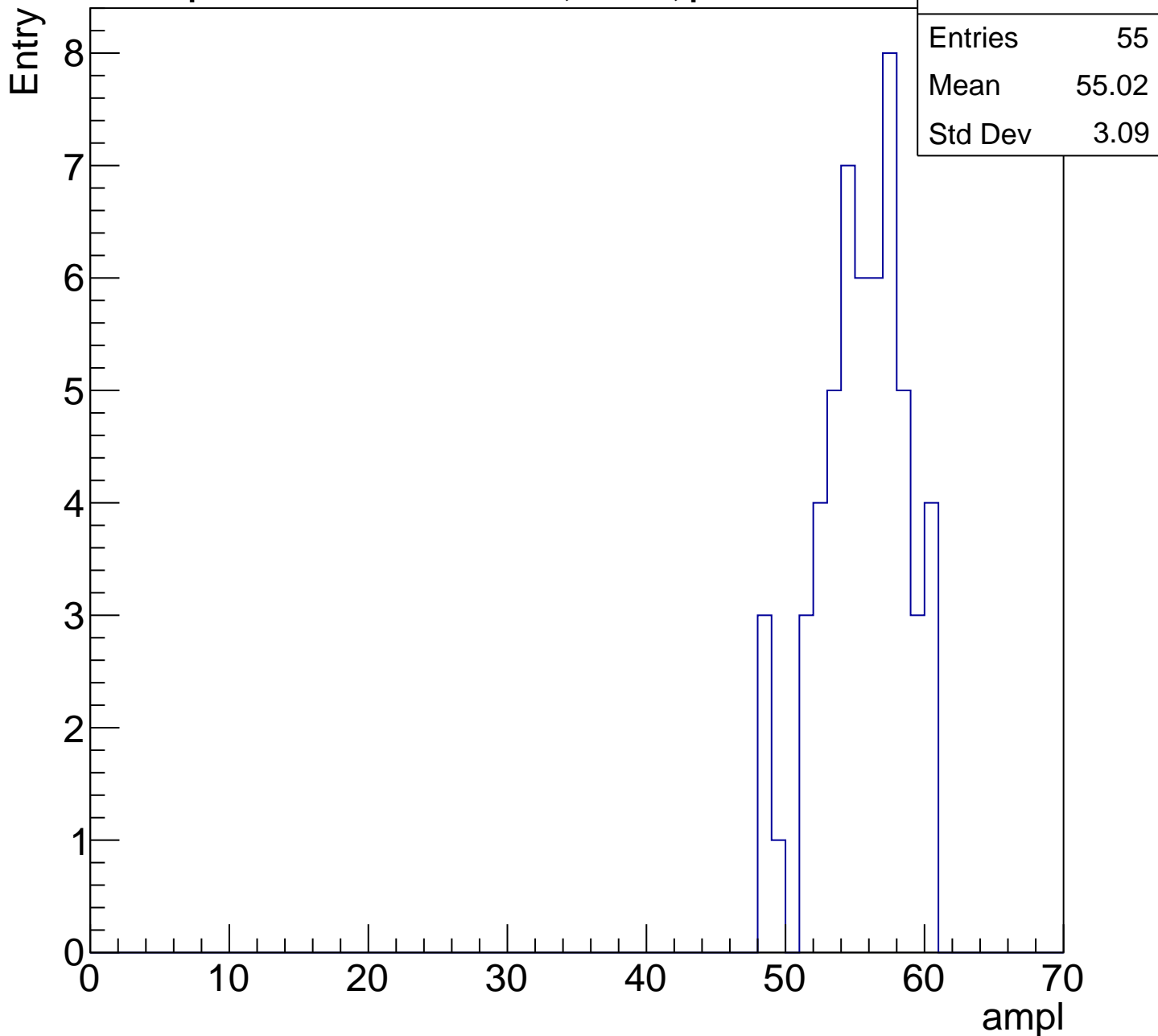
calib_packv5_042523_0143.root, FC#11, port A2

Entry



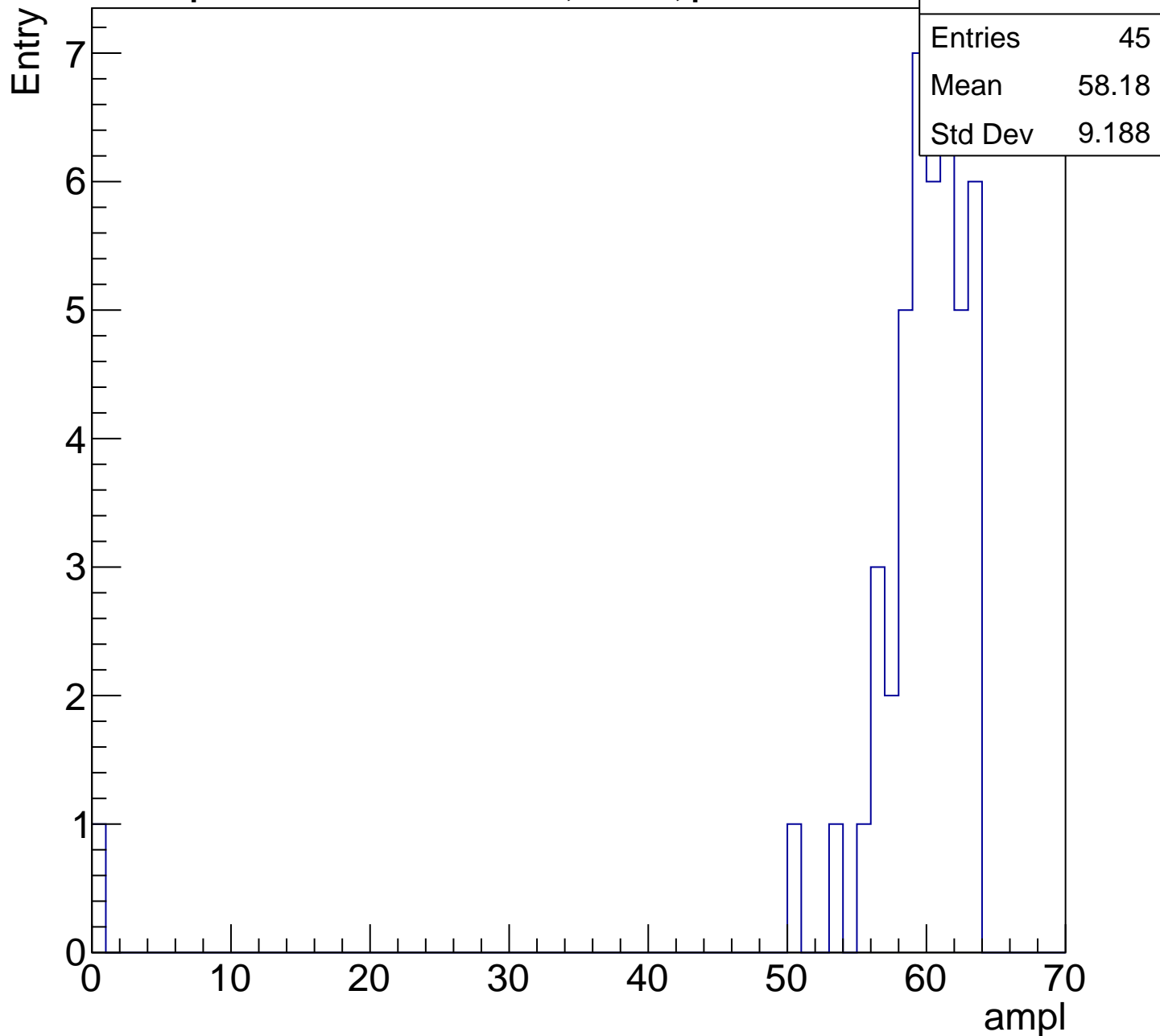
B1L102S, U2-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

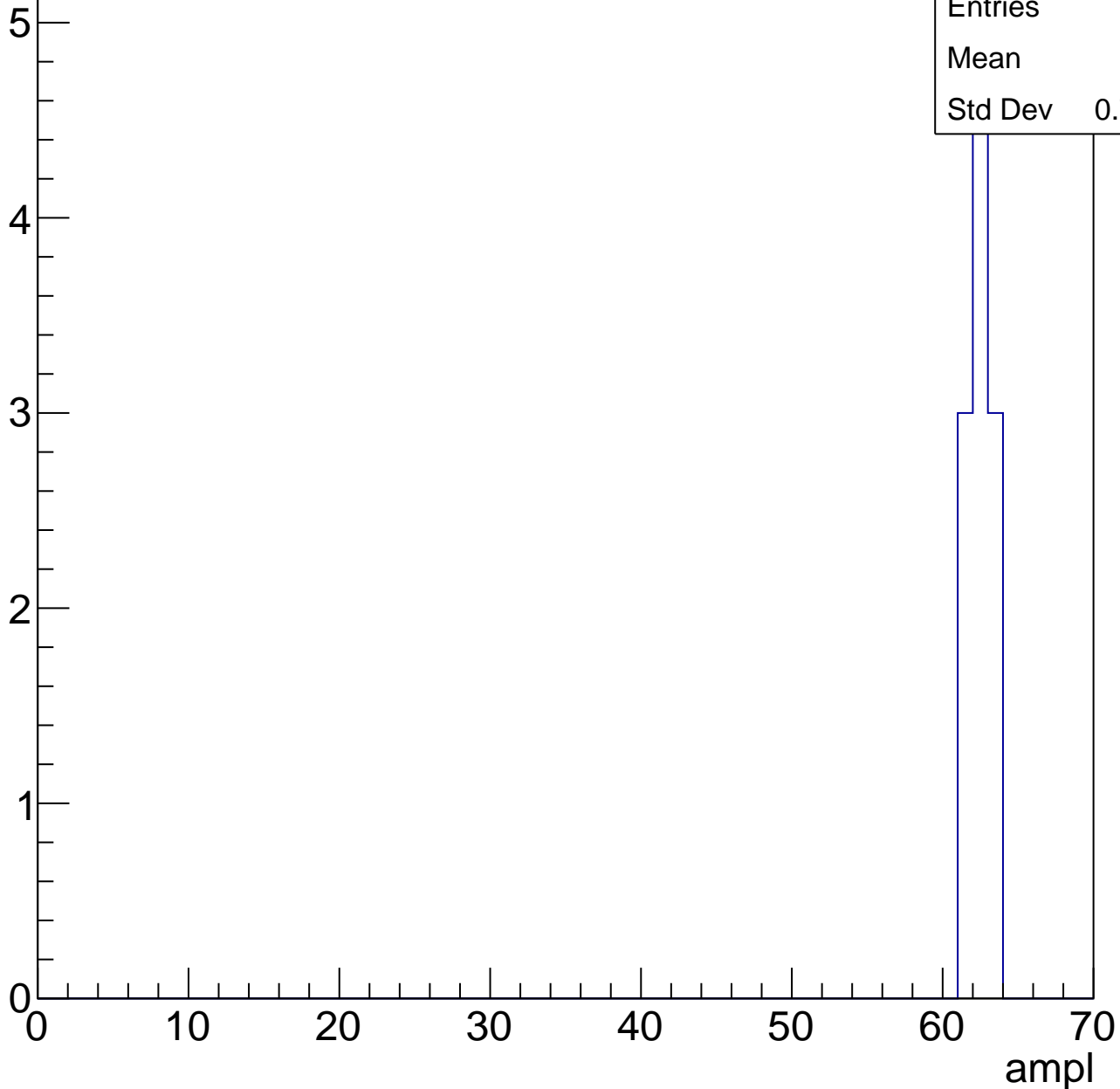


B1L102S, U2-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62
Std Dev	0.7385



B1L102S, U2-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch27, adc0

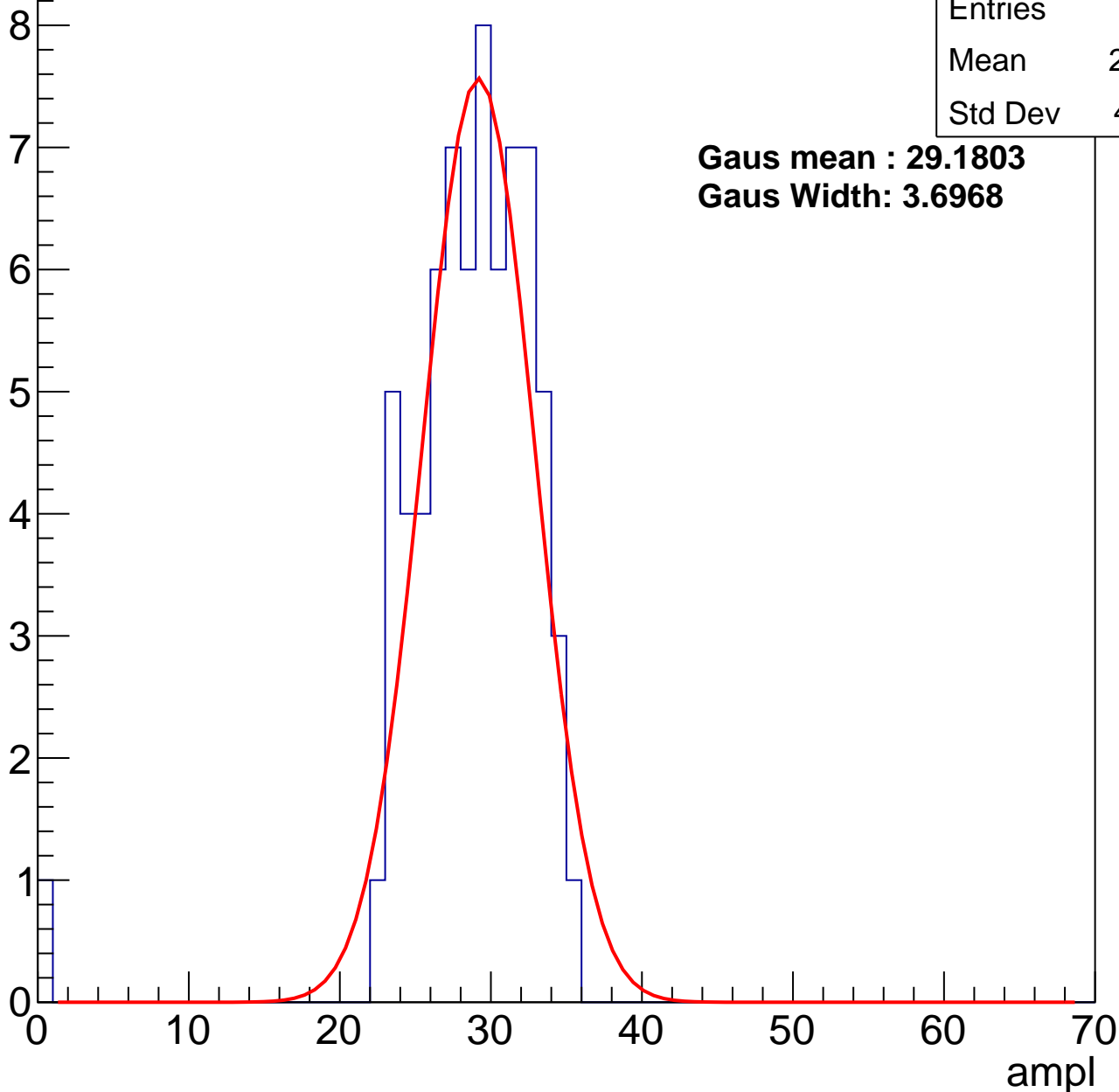
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.18
Std Dev	4.691

Gaus mean : 29.1803

Gaus Width: 3.6968



B1L102S, U2-ch27, adc1

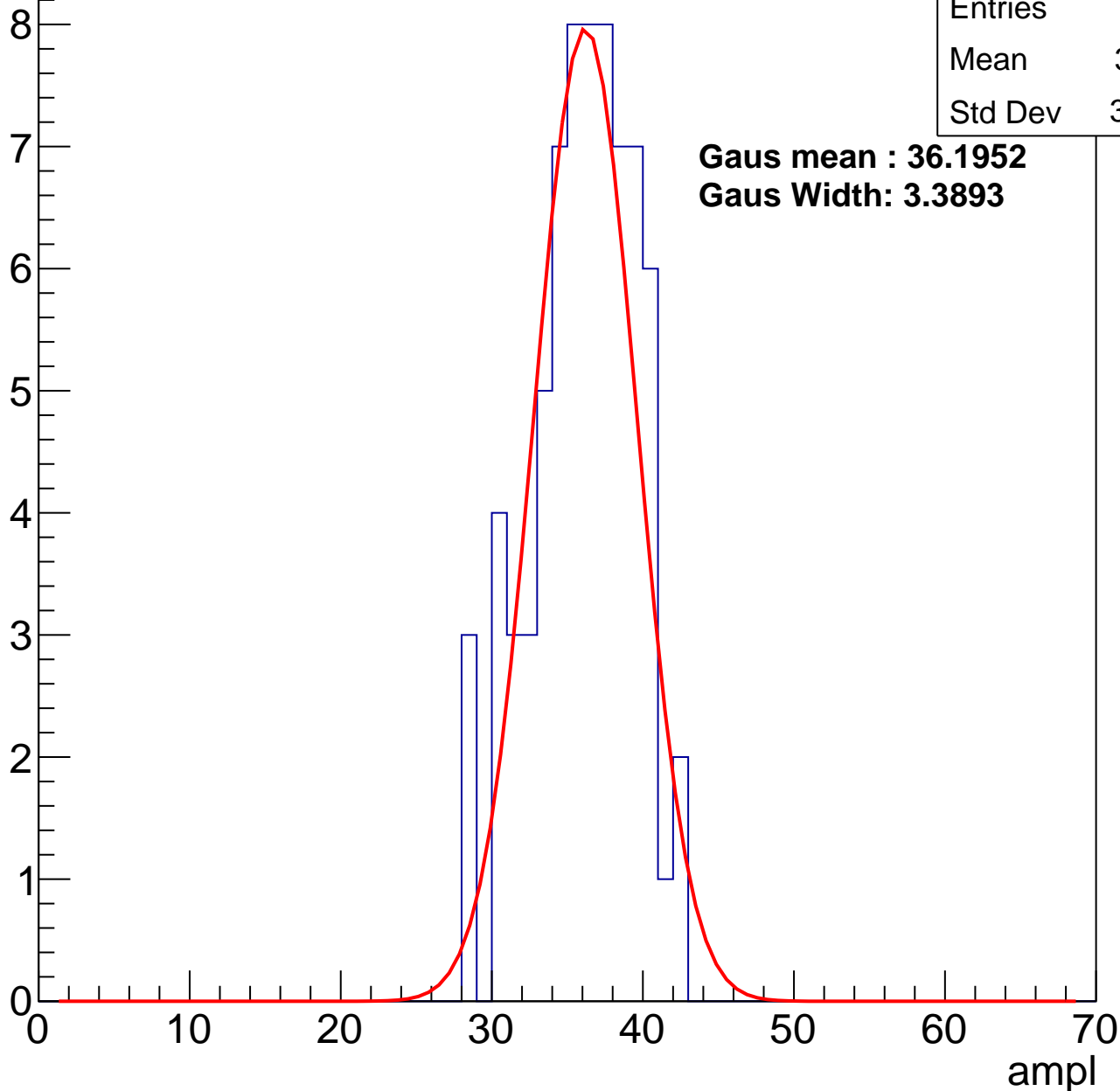
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.61
Std Dev	3.377

Gaus mean : 36.1952

Gaus Width: 3.3893



B1L102S, U2-ch27, adc2

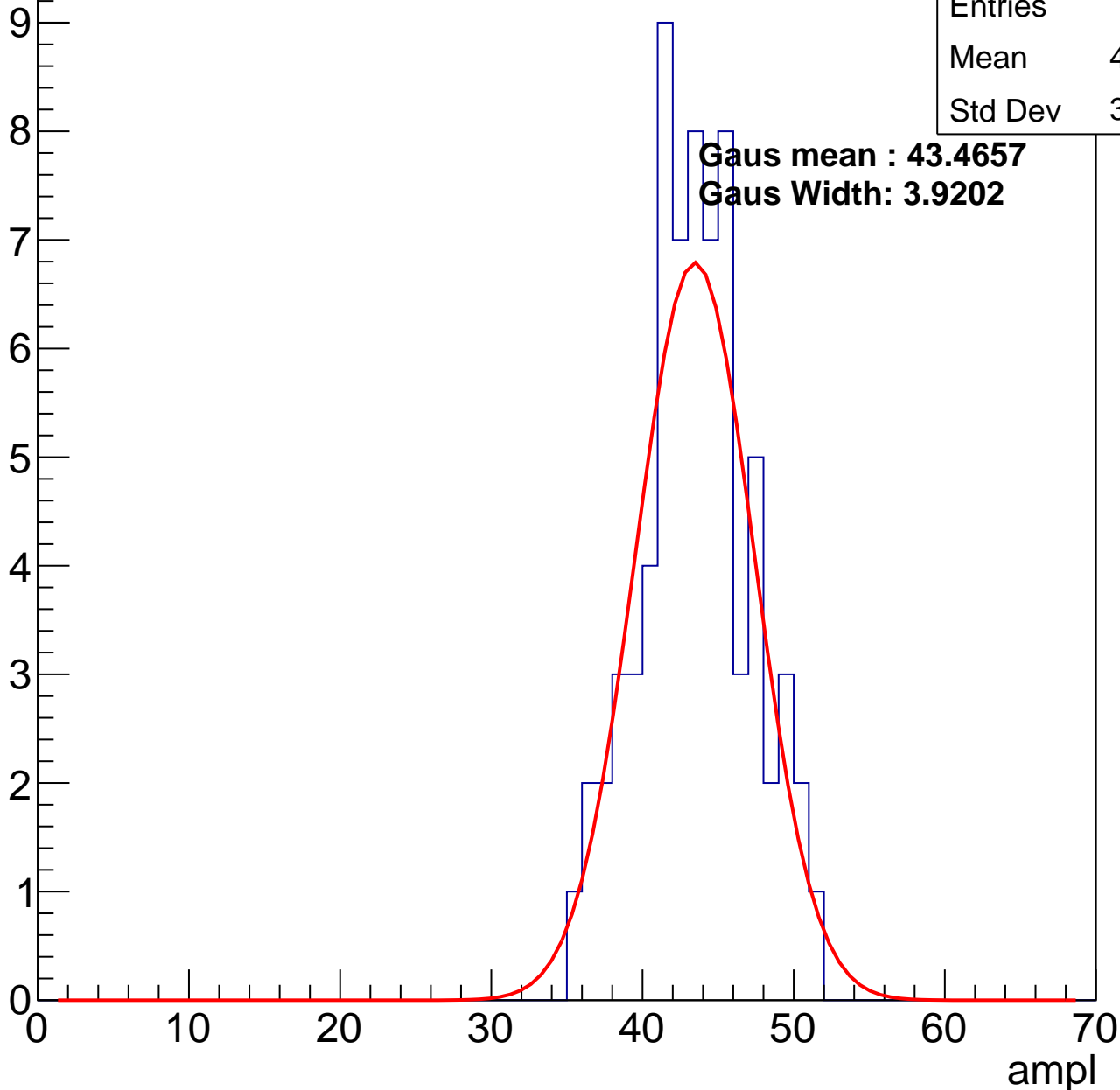
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.06
Std Dev	3.589

Gaus mean : 43.4657

Gaus Width: 3.9202

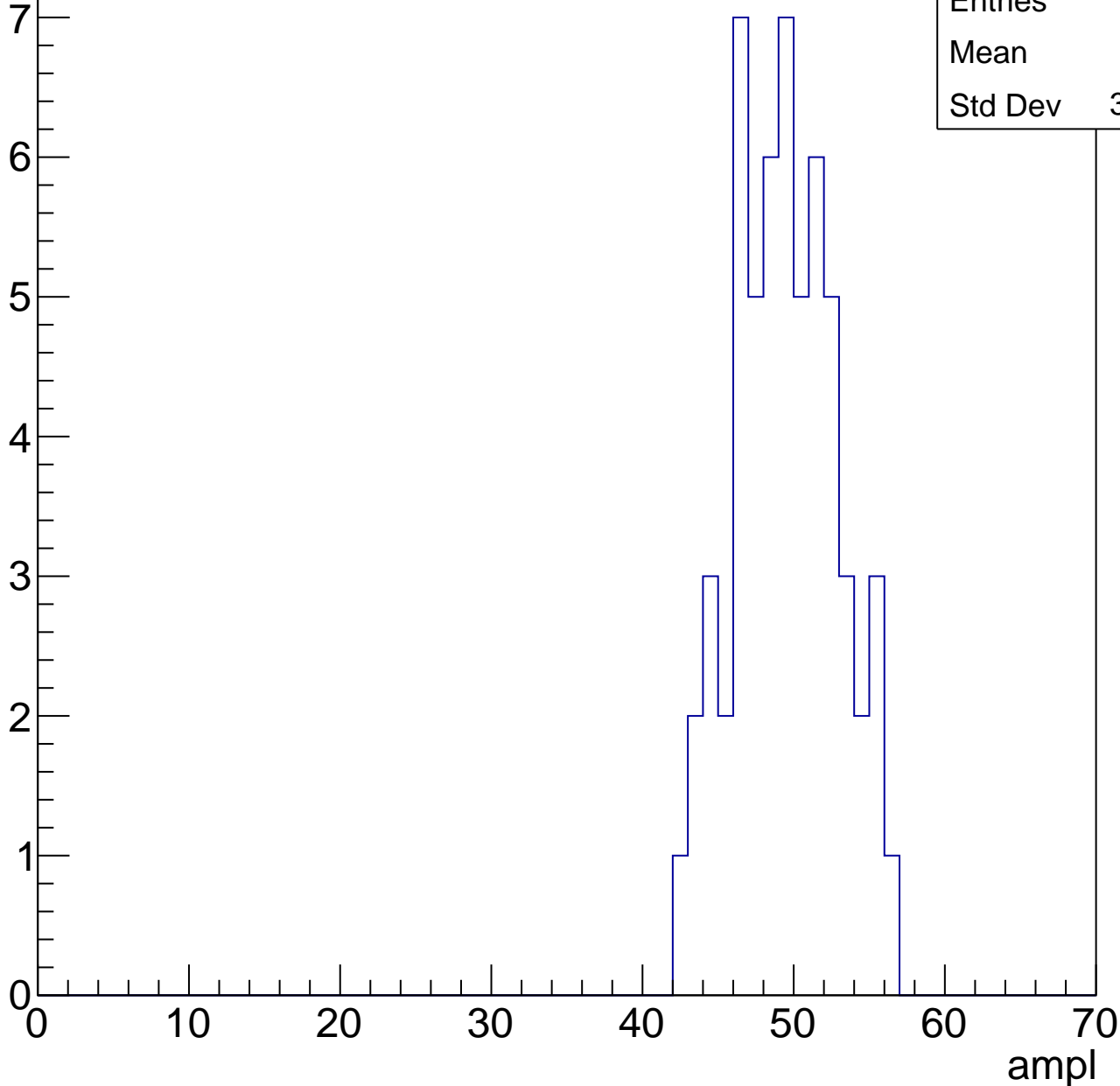


B1L102S, U2-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

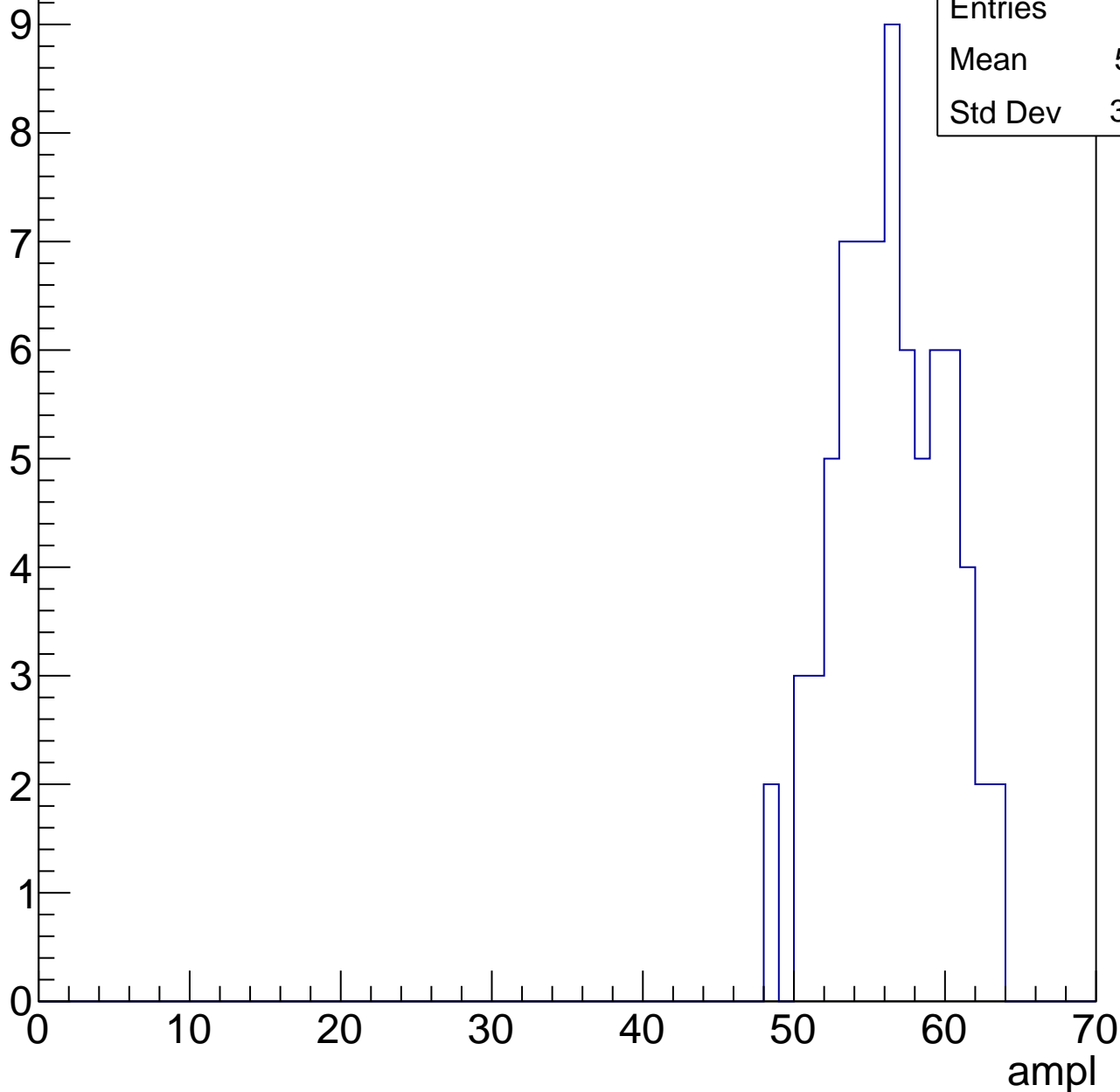
Entries	58
Mean	49
Std Dev	3.337



B1L102S, U2-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

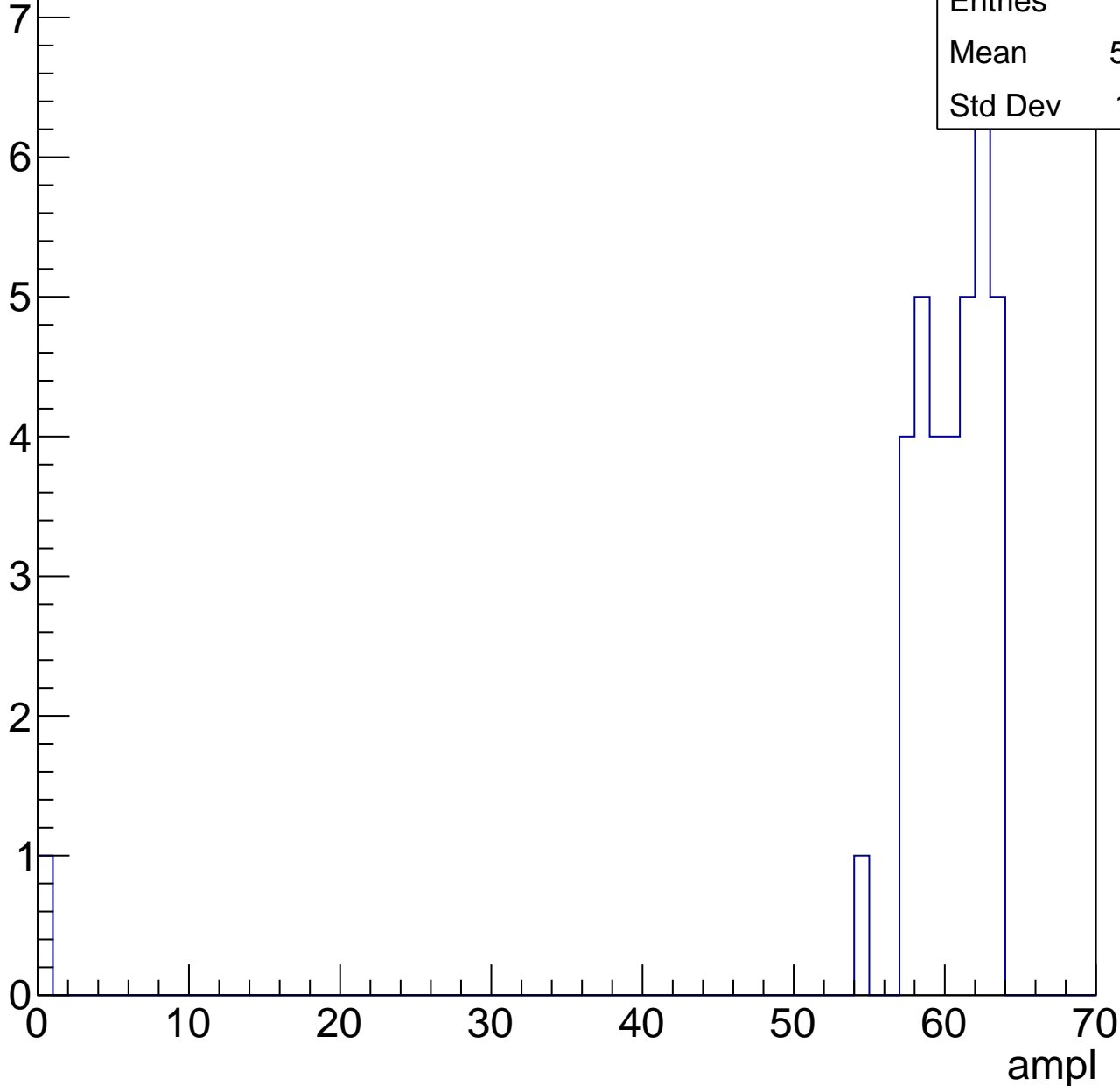


B1L102S, U2-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	58.39
Std Dev	10.11



B1L102S, U2-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch28, adc0

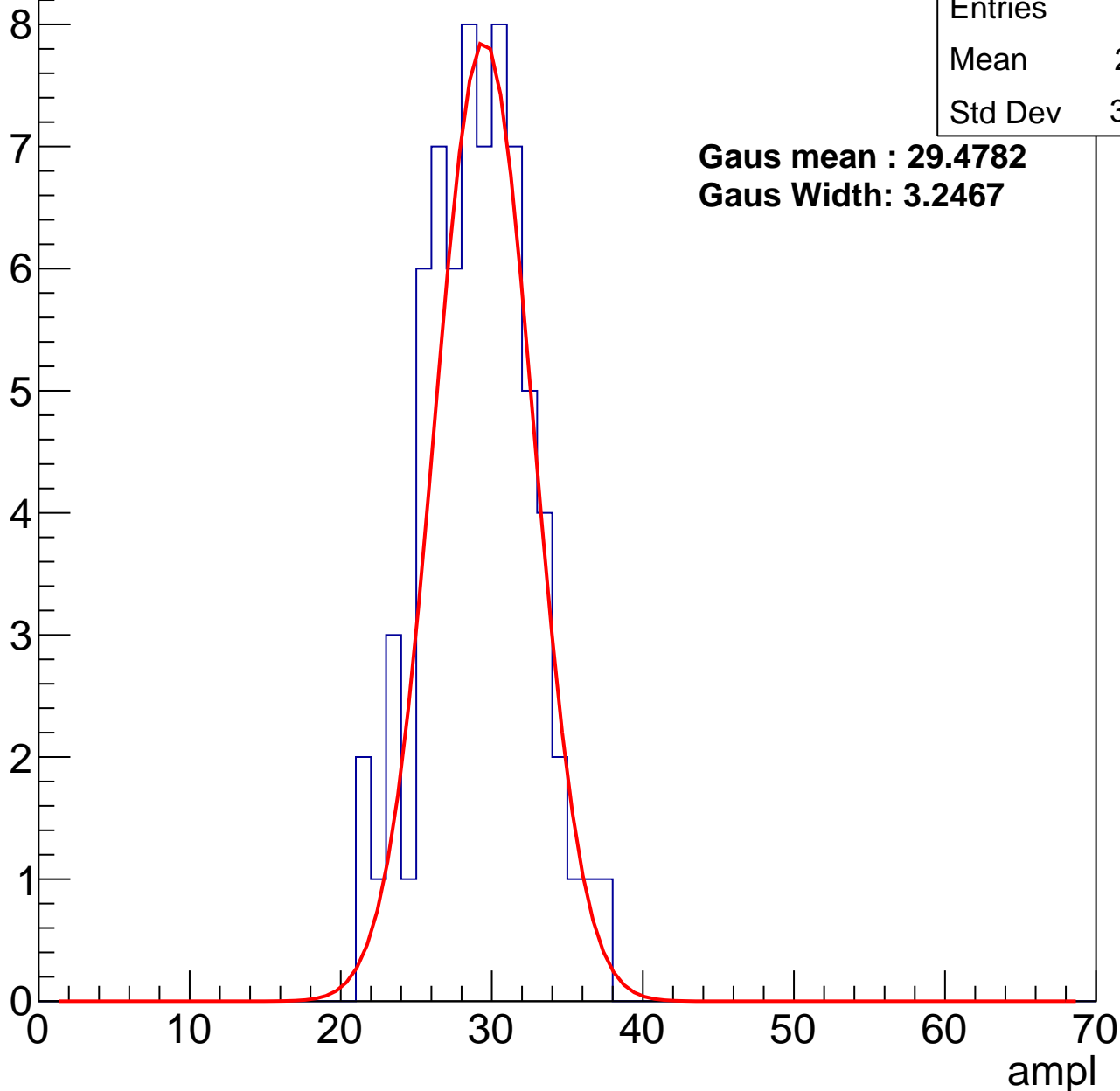
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.61
Std Dev	3.453

Gaus mean : 29.4782

Gaus Width: 3.2467



B1L102S, U2-ch28, adc1

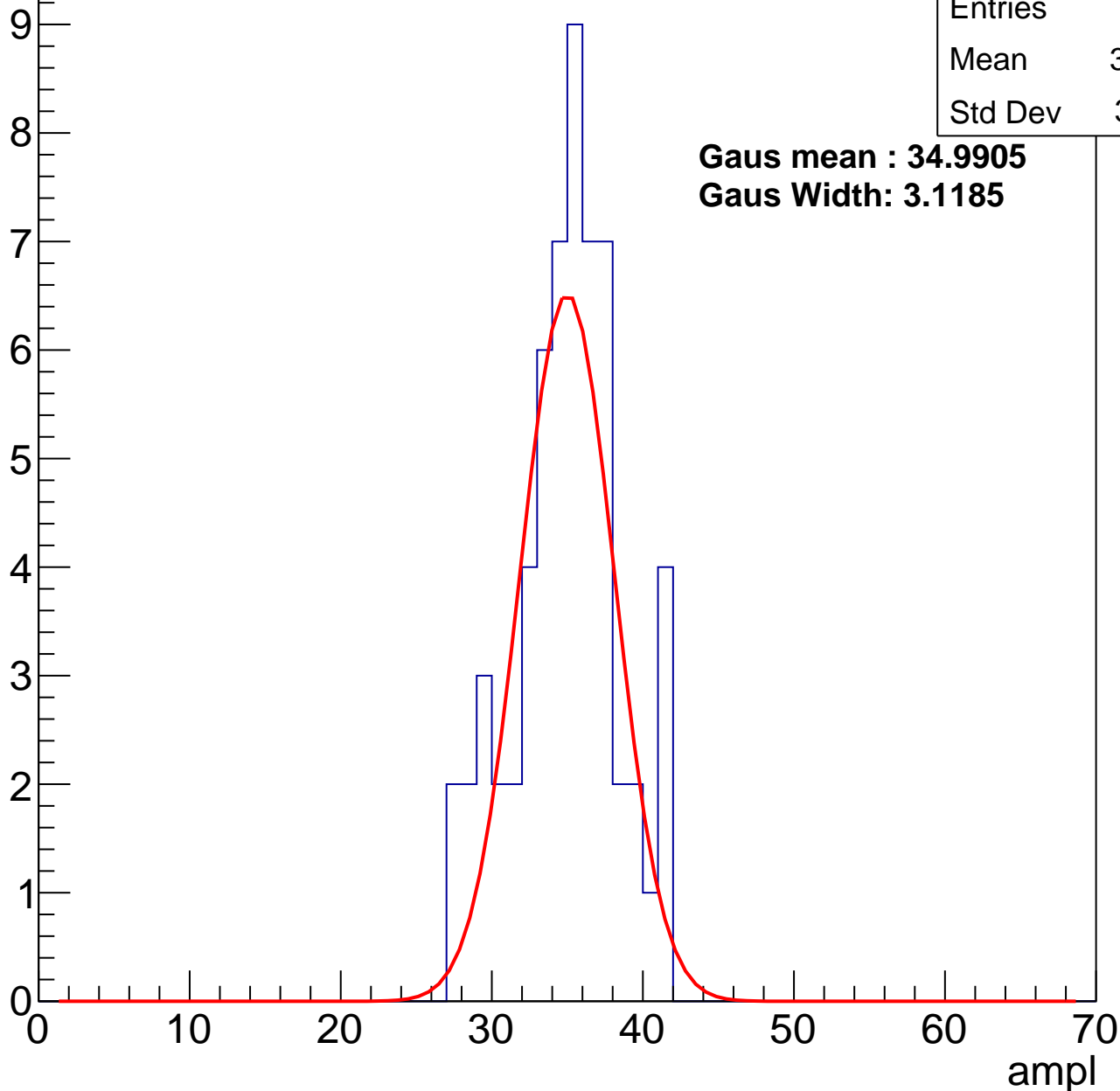
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.45
Std Dev	3.461

Gaus mean : 34.9905

Gaus Width: 3.1185



B1L102S, U2-ch28, adc2

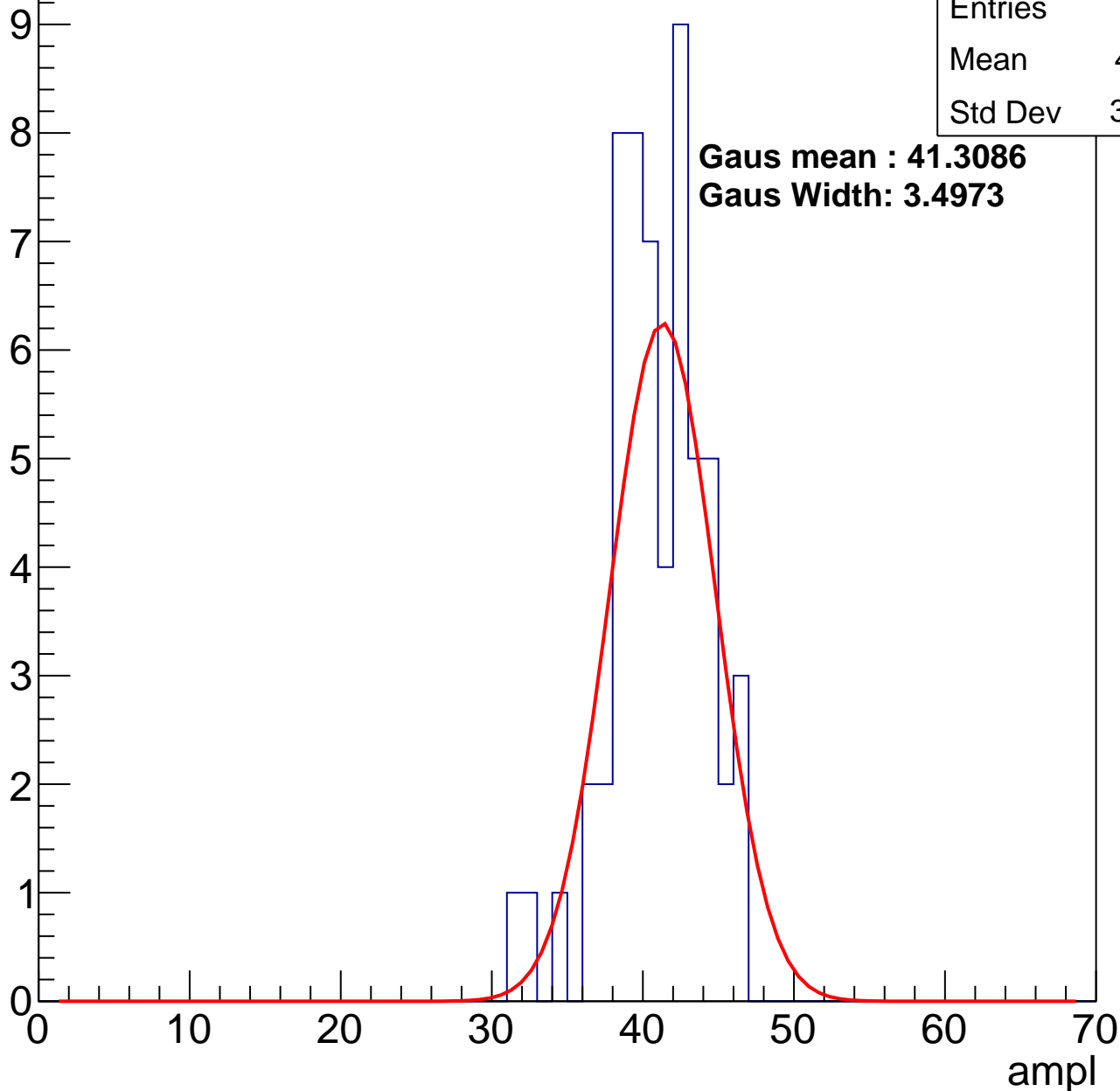
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.41
Std Dev	3.173

Gaus mean : 41.3086

Gaus Width: 3.4973

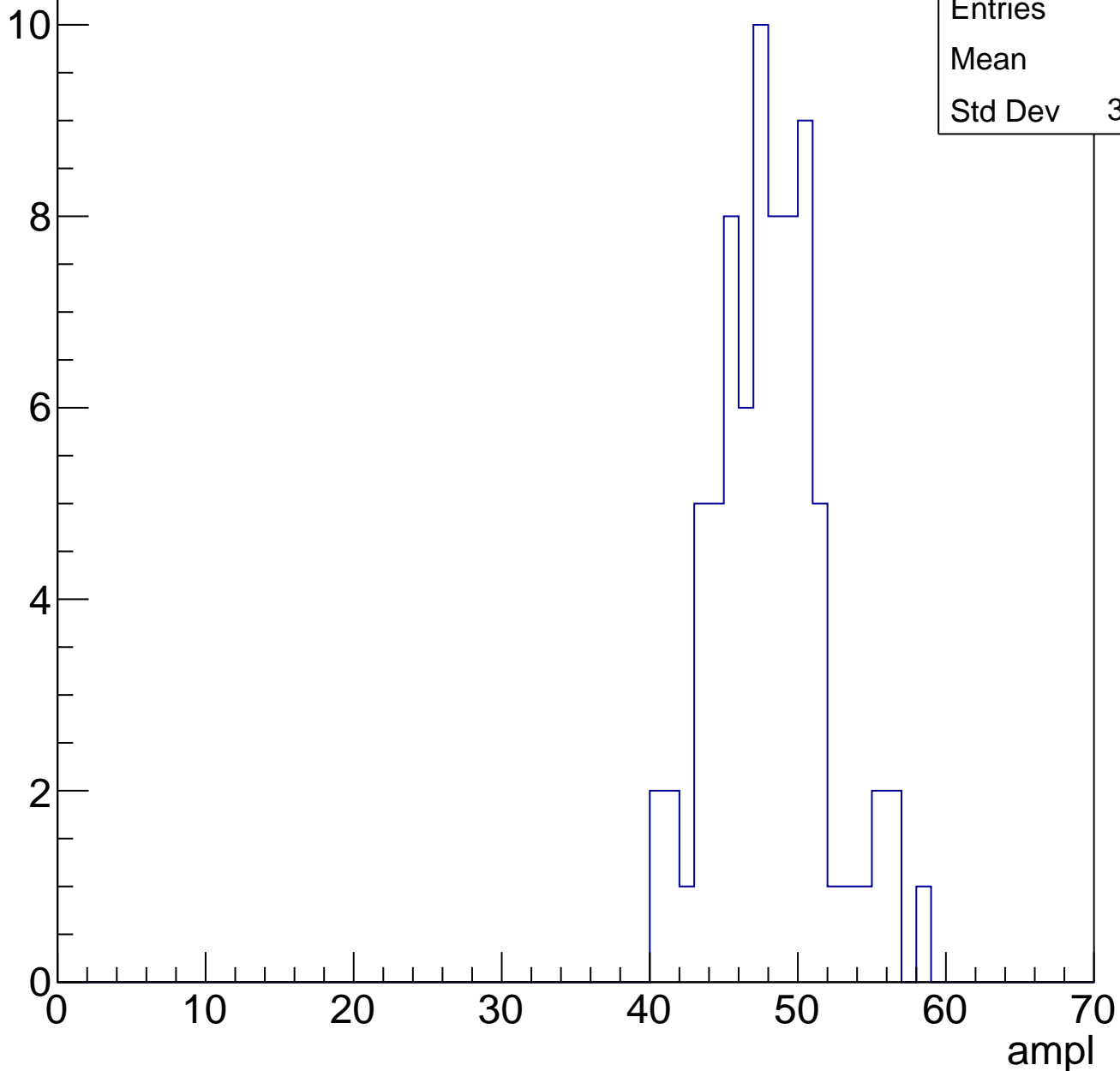


B1L102S, U2-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	47.6
Std Dev	3.697

Entry

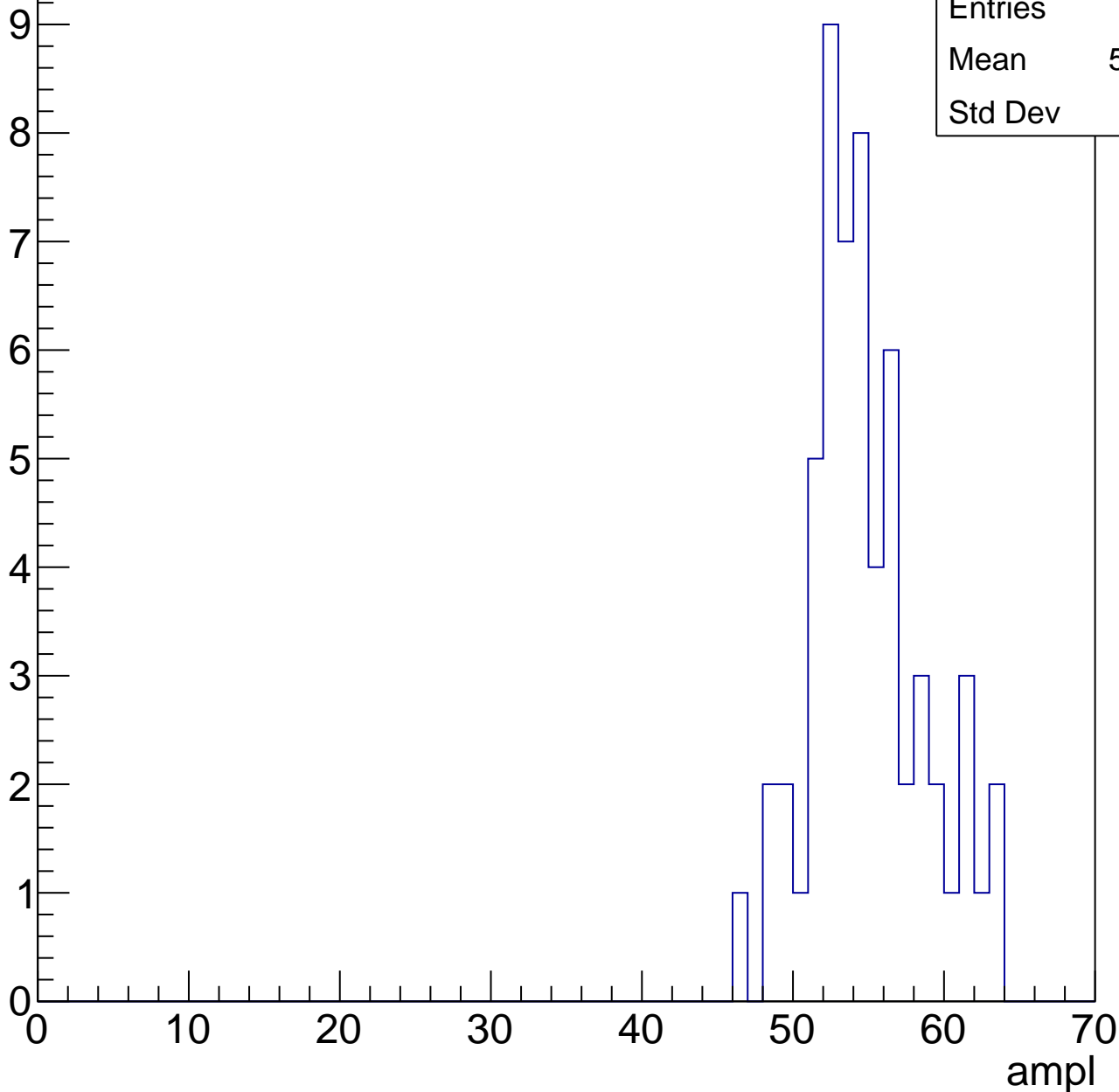


B1L102S, U2-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.39
Std Dev	3.76

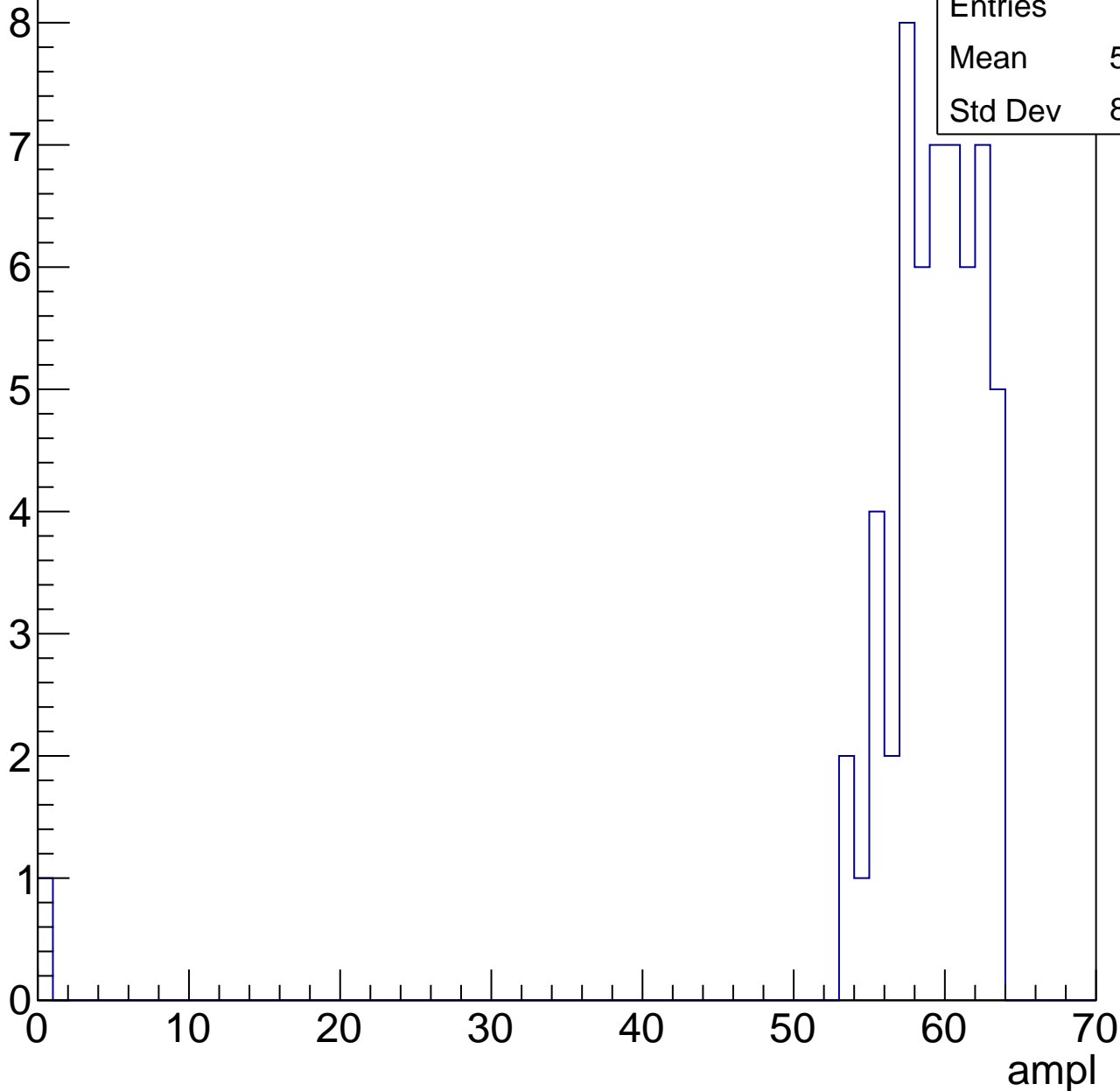


B1L102S, U2-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

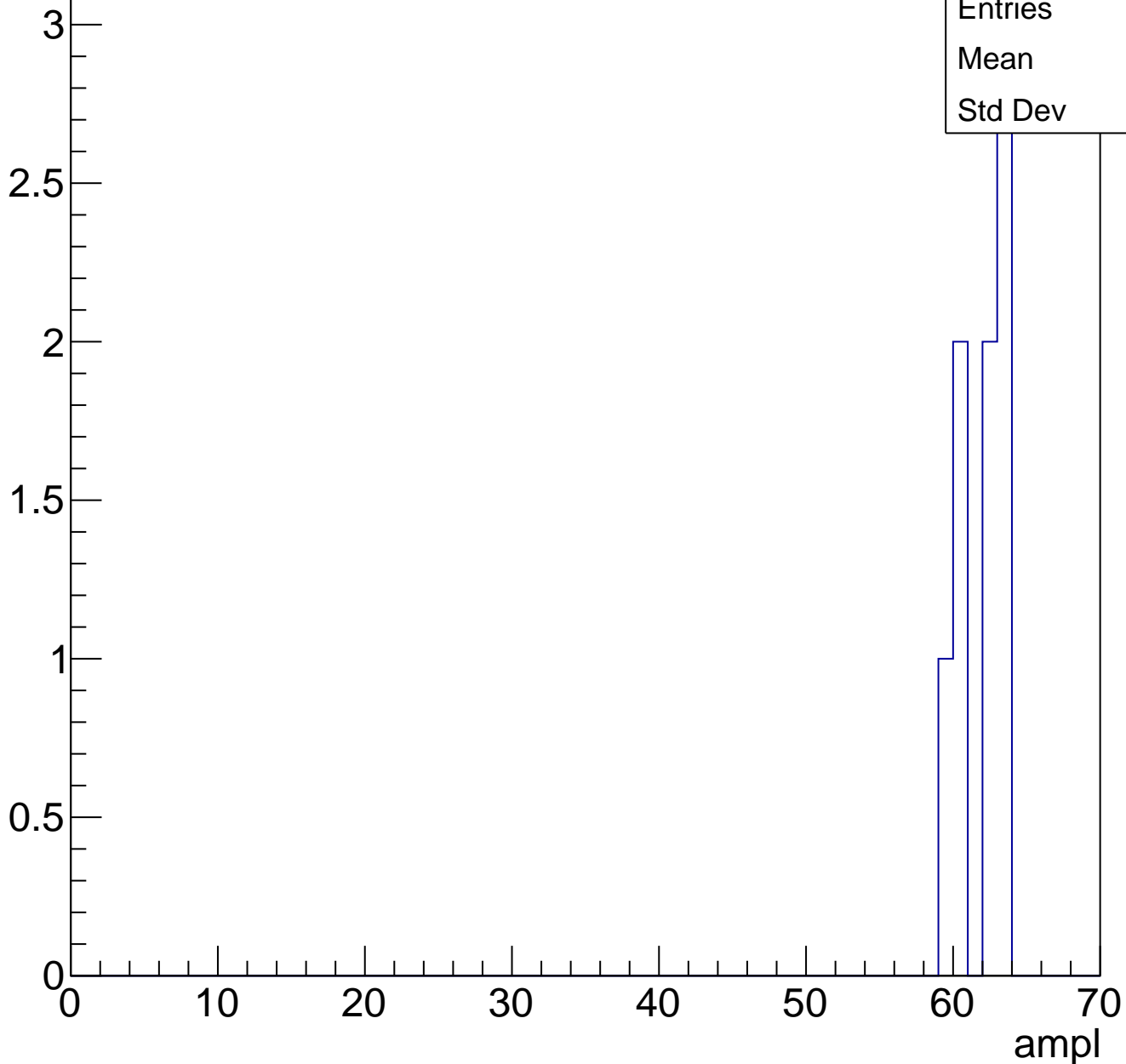
Entries	56
Mean	57.93
Std Dev	8.246



B1L102S, U2-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

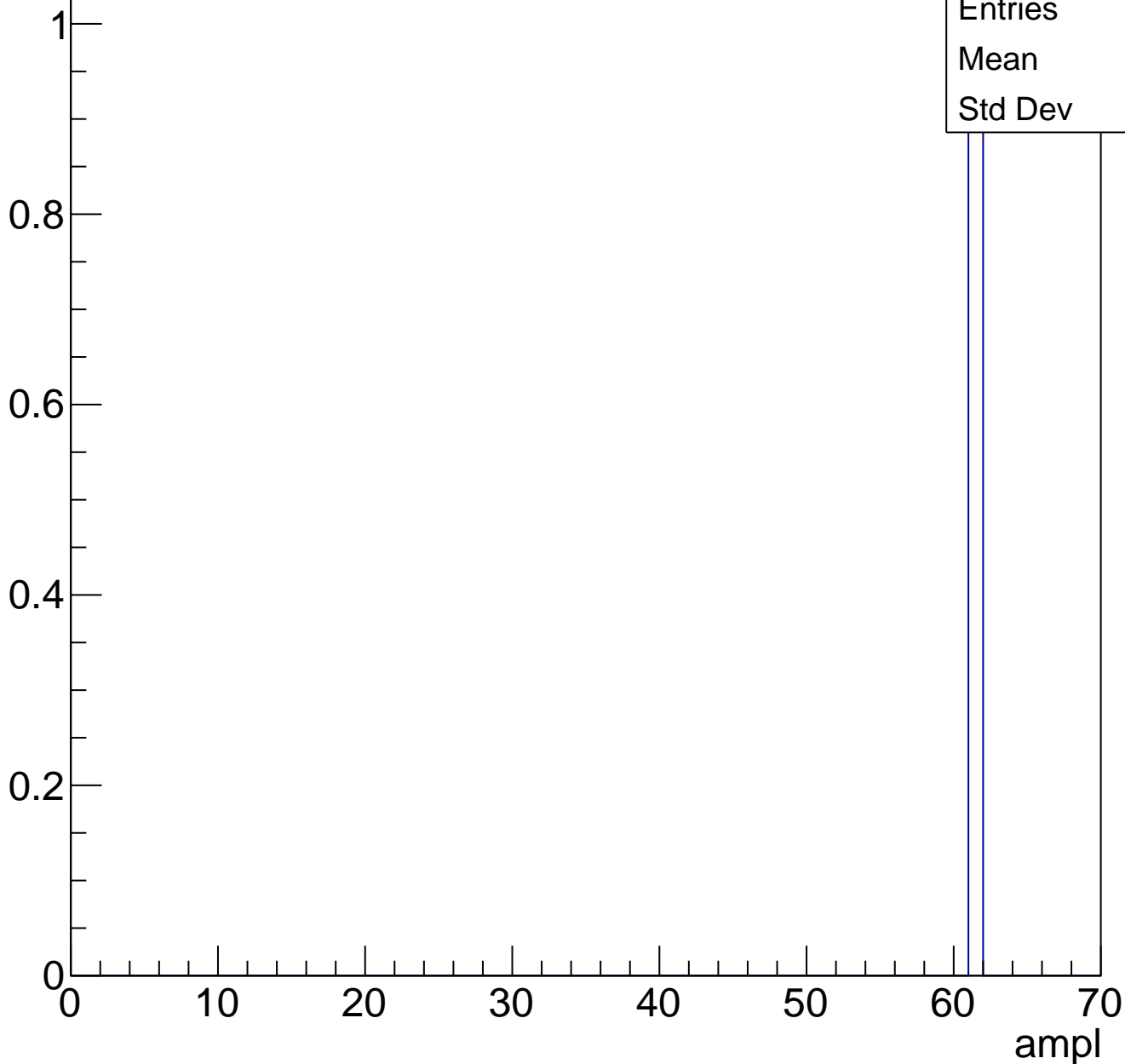
Entry



B1L102S, U2-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch29, adc0

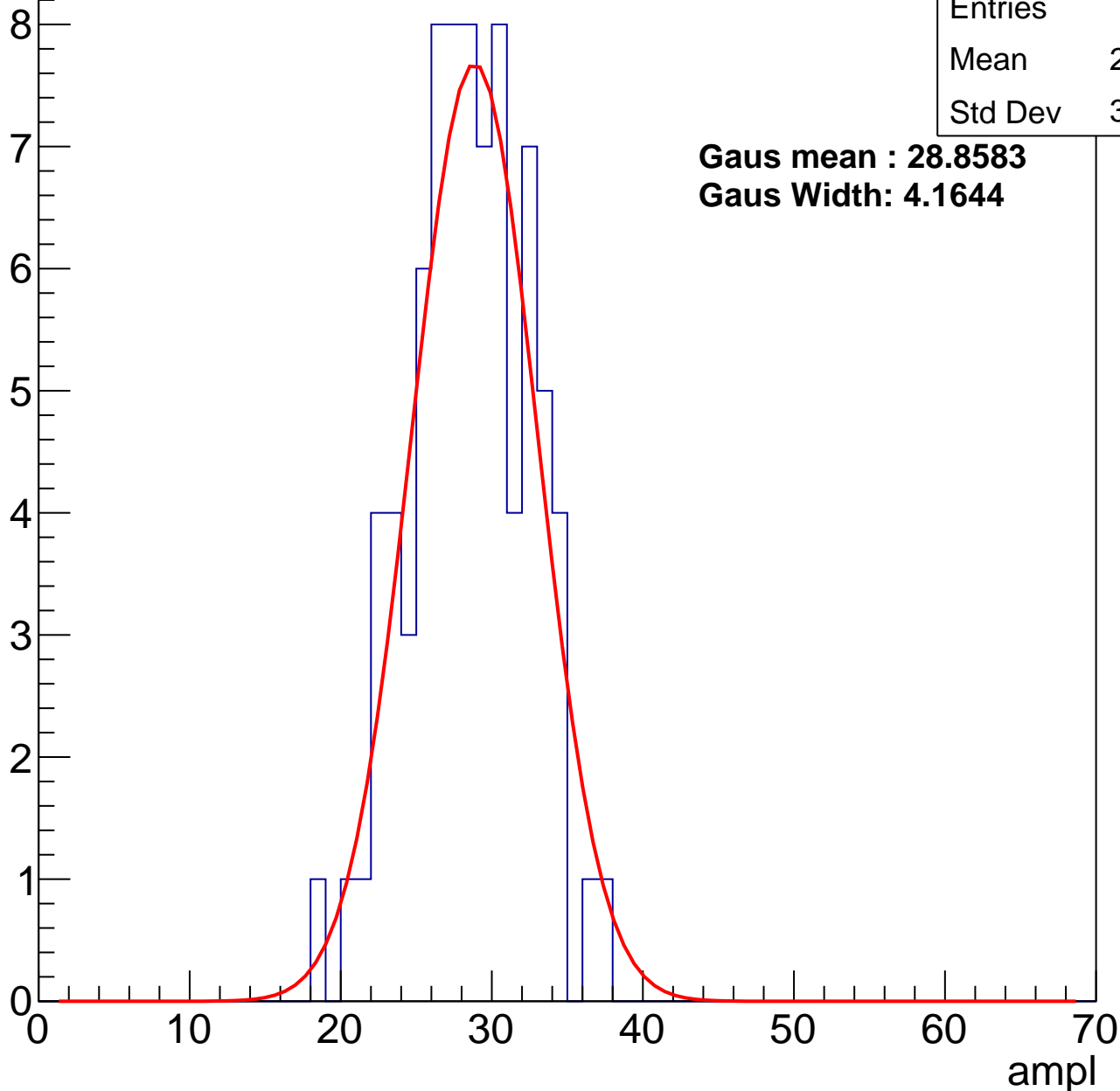
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.07
Std Dev	3.848

Gaus mean : 28.8583

Gaus Width: 4.1644



B1L102S, U2-ch29, adc1

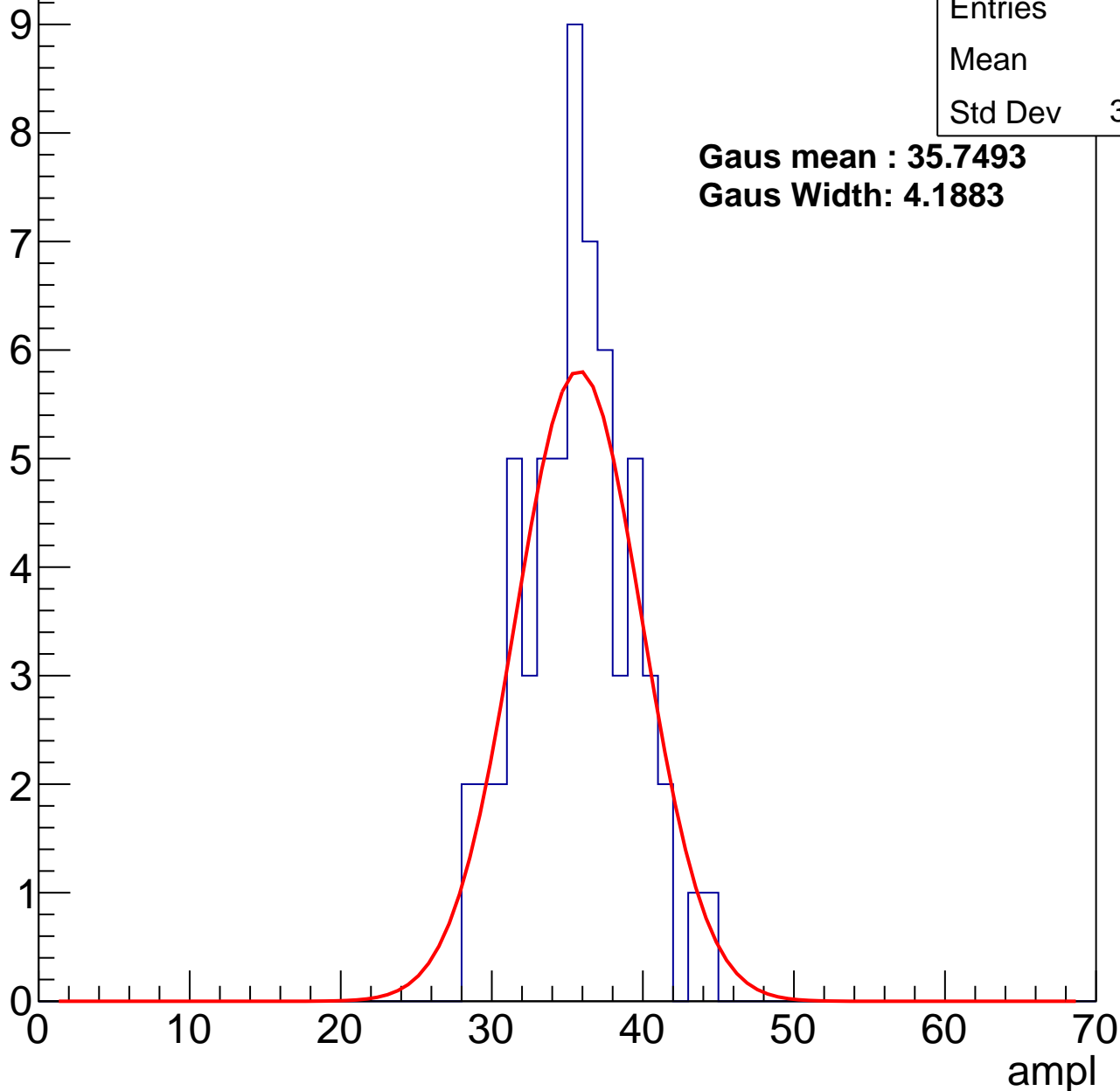
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.2
Std Dev	3.575

Gaus mean : 35.7493

Gaus Width: 4.1883

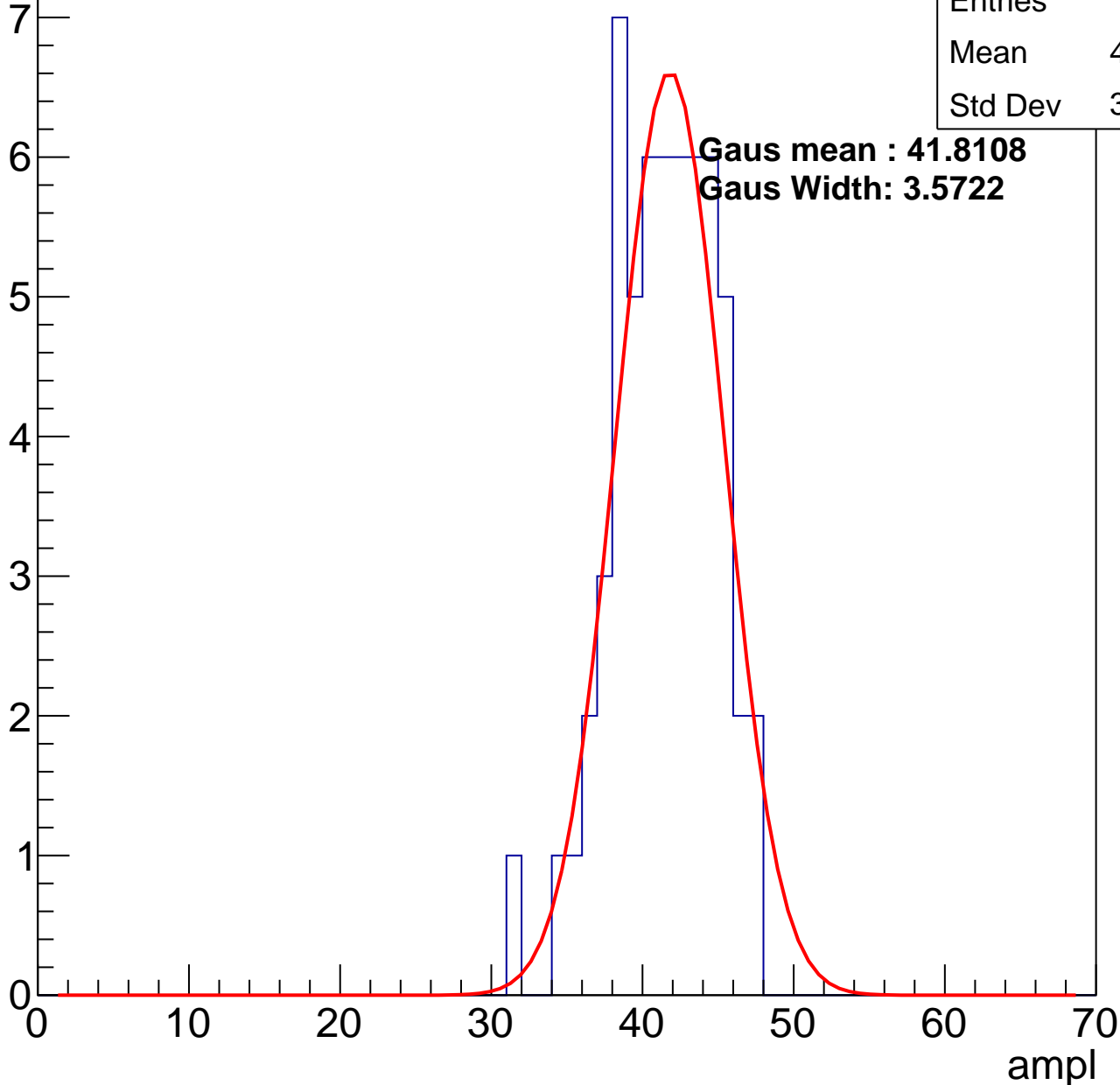


B1L102S, U2-ch29, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	40.93
Std Dev	3.339

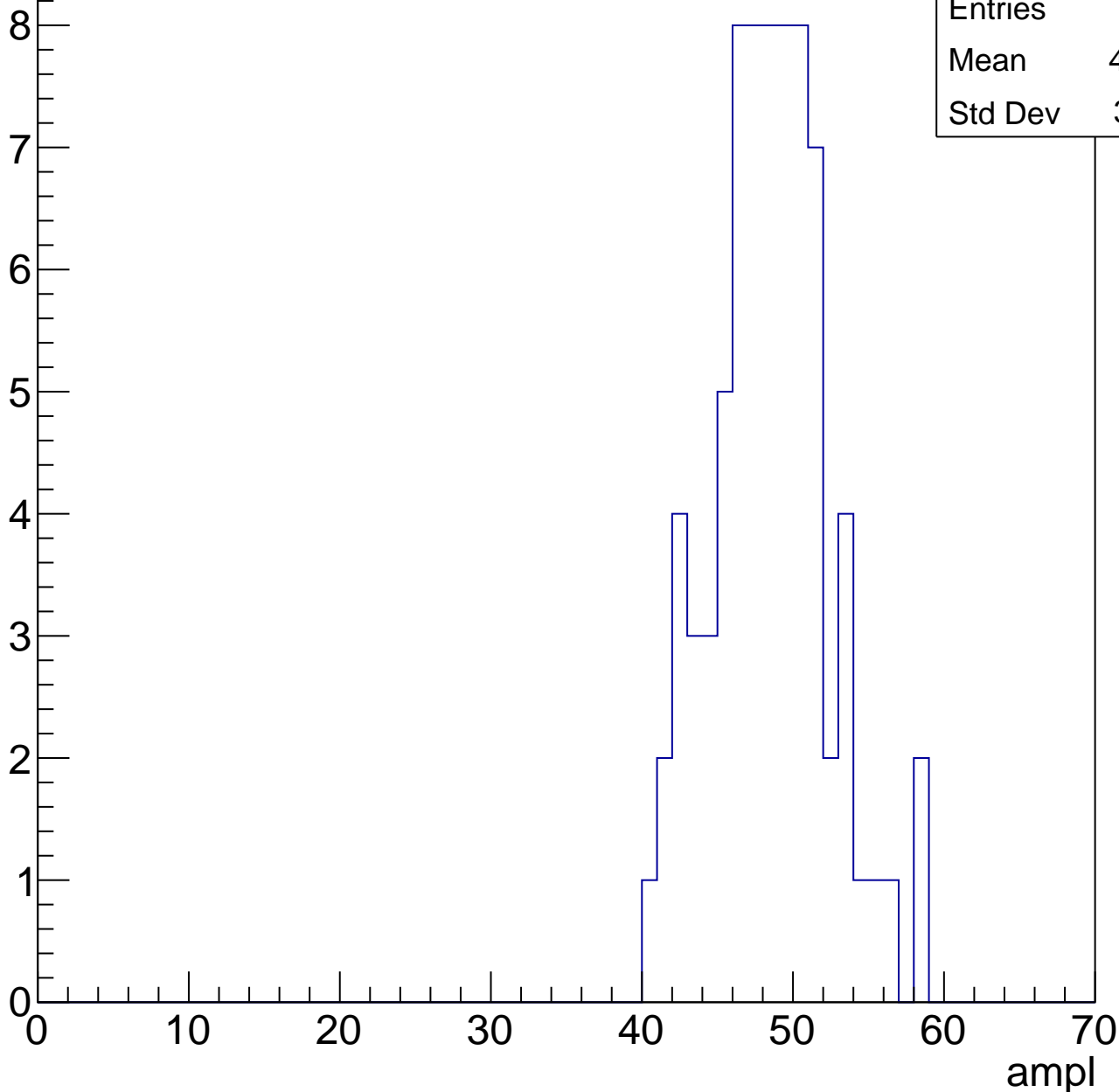


B1L102S, U2-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

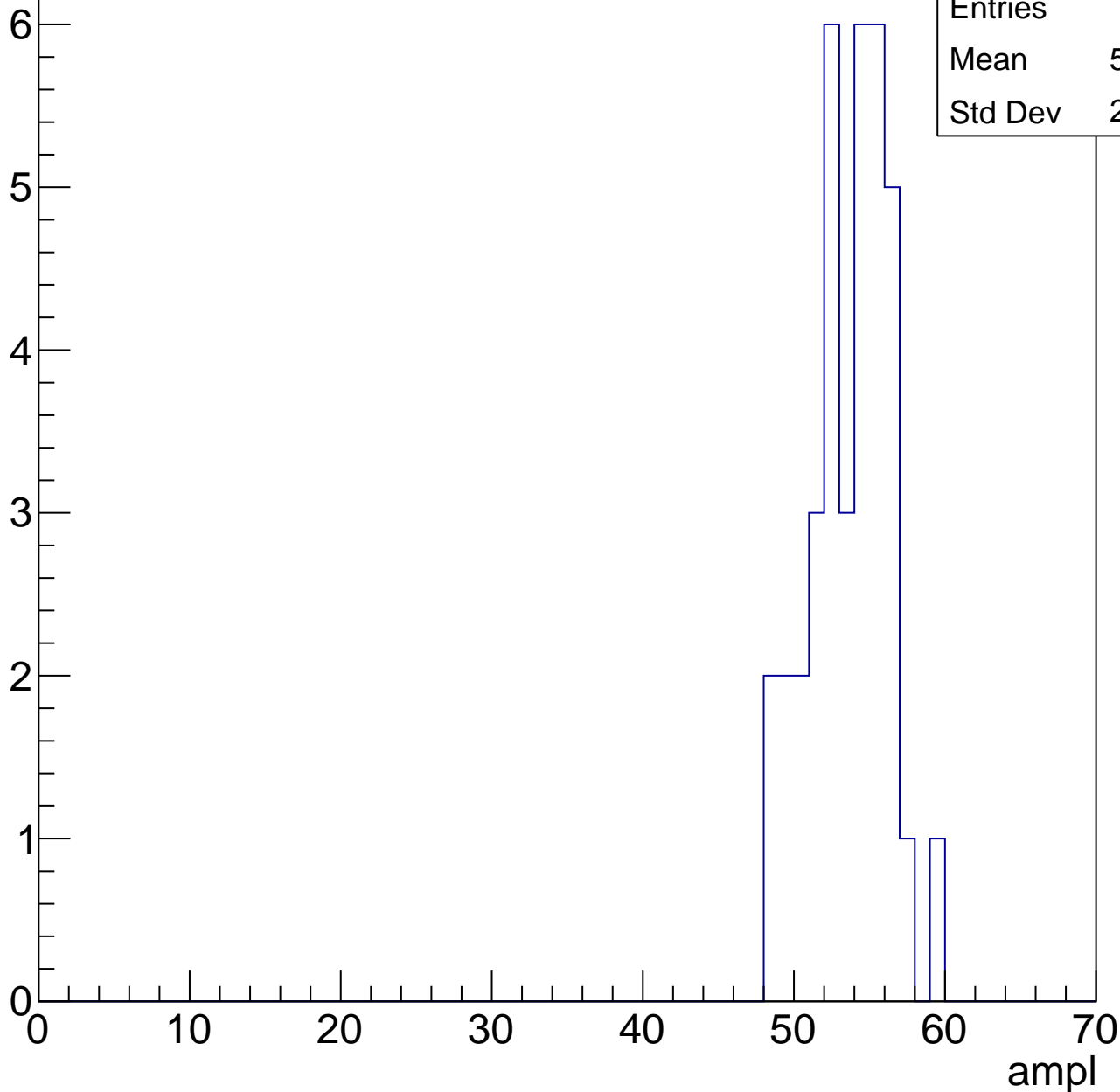
Entries	76
Mean	48.03
Std Dev	3.801



B1L102S, U2-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

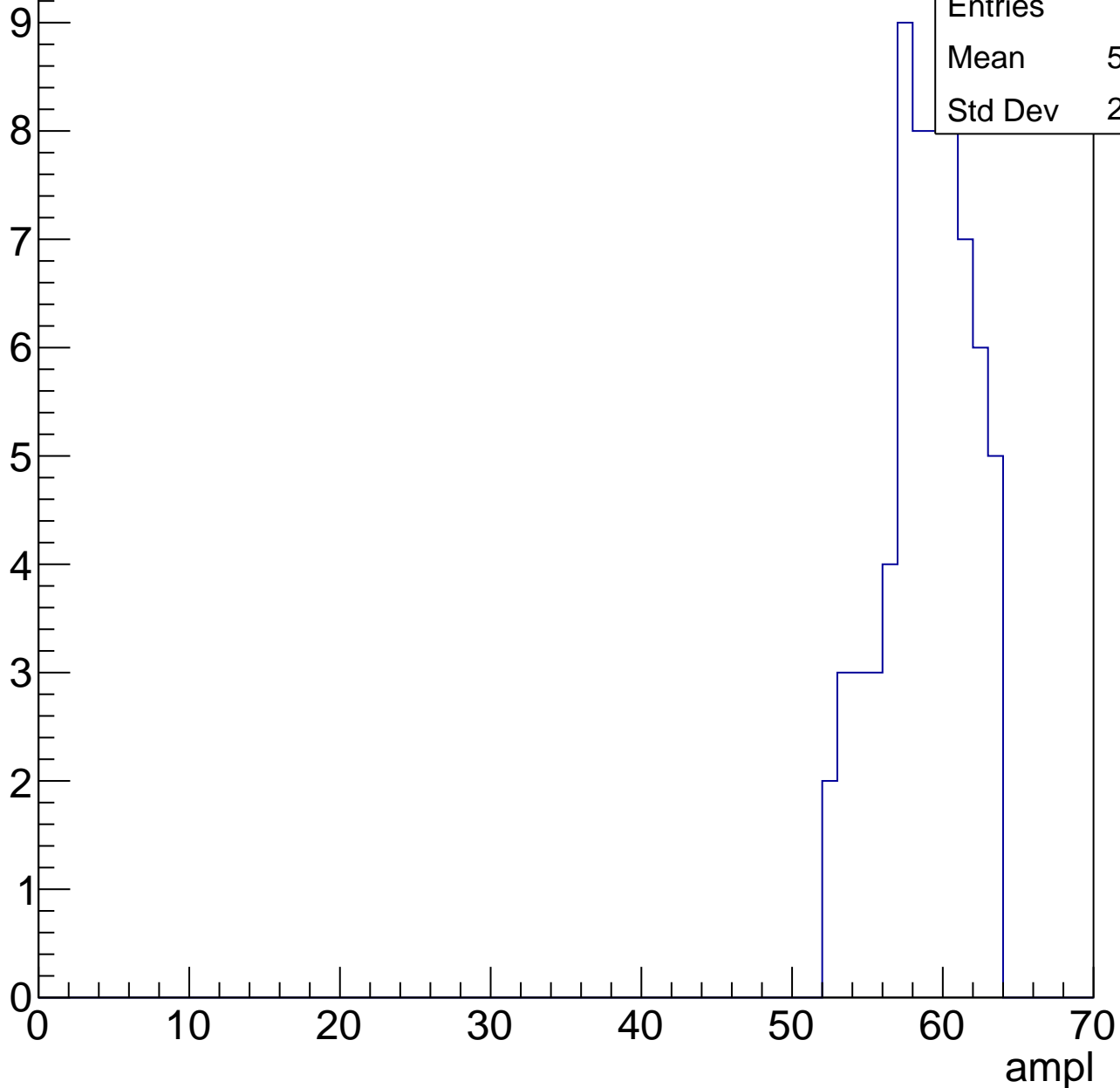


Entries	37
Mean	53.19
Std Dev	2.566

B1L102S, U2-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



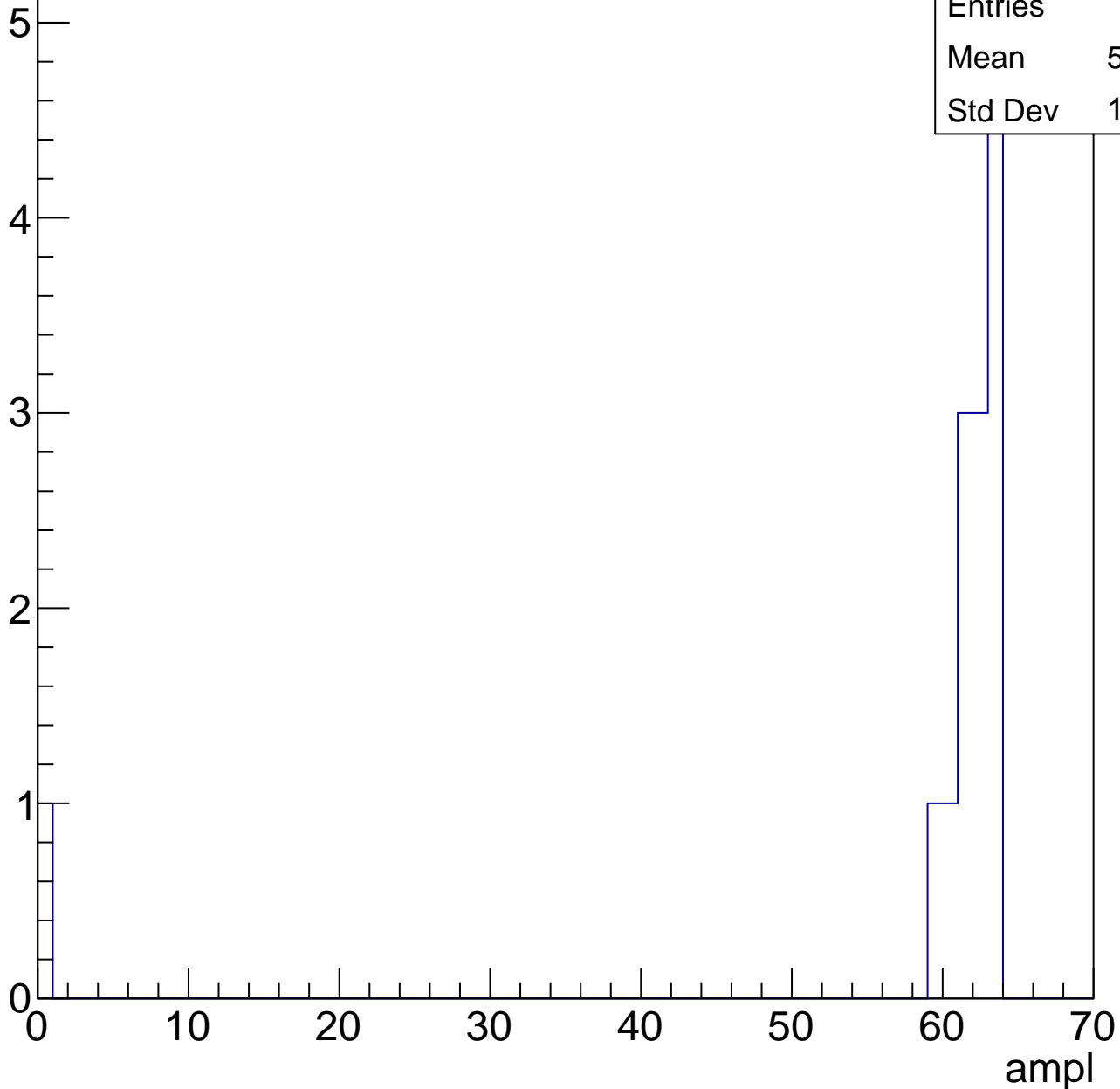
Entries	67
Mean	58.46
Std Dev	2.893

B1L102S, U2-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.36
Std Dev	15.95



B1L102S, U2-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch30, adc0

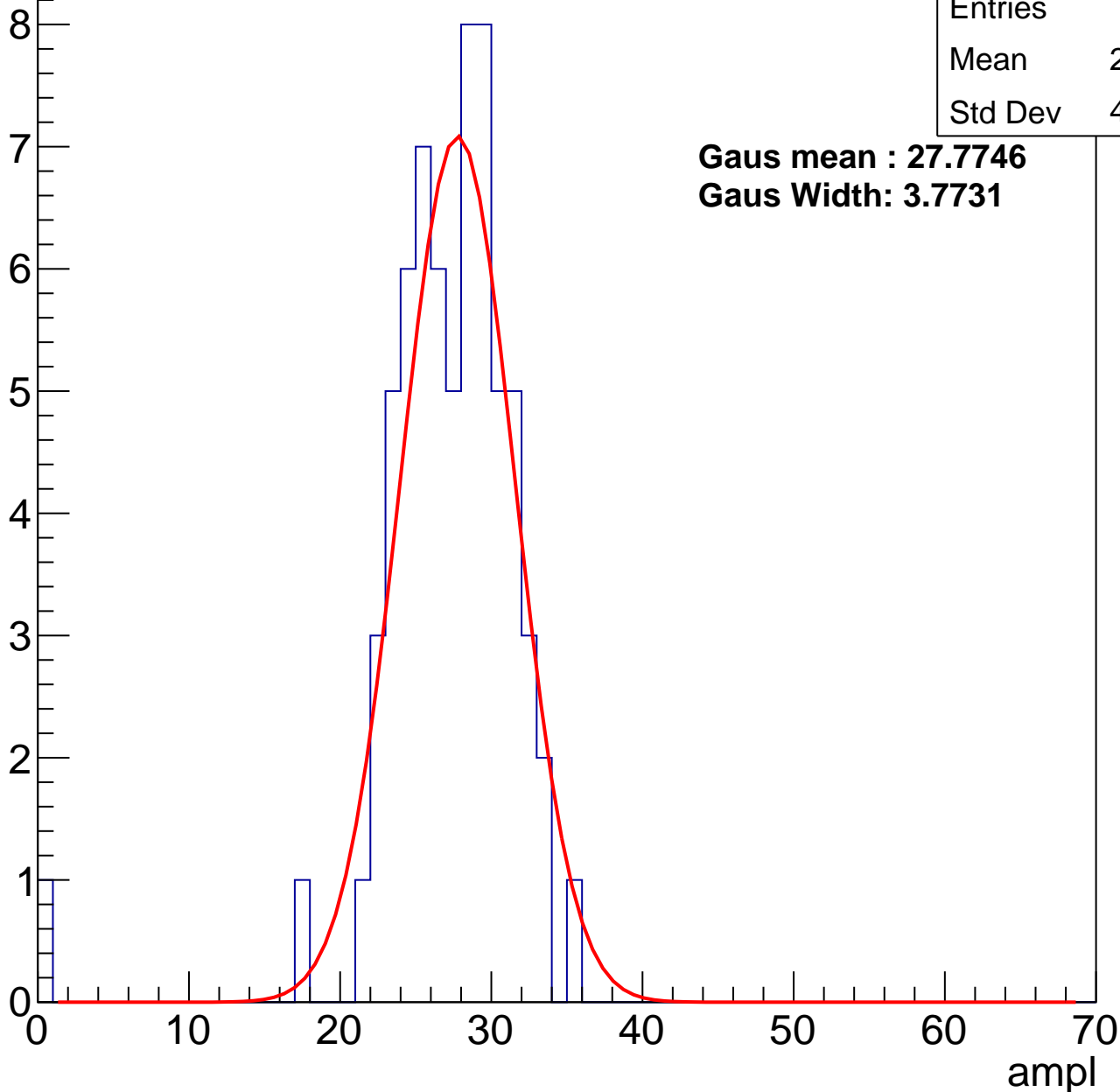
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.67
Std Dev	4.695

Gaus mean : 27.7746

Gaus Width: 3.7731



B1L102S, U2-ch30, adc1

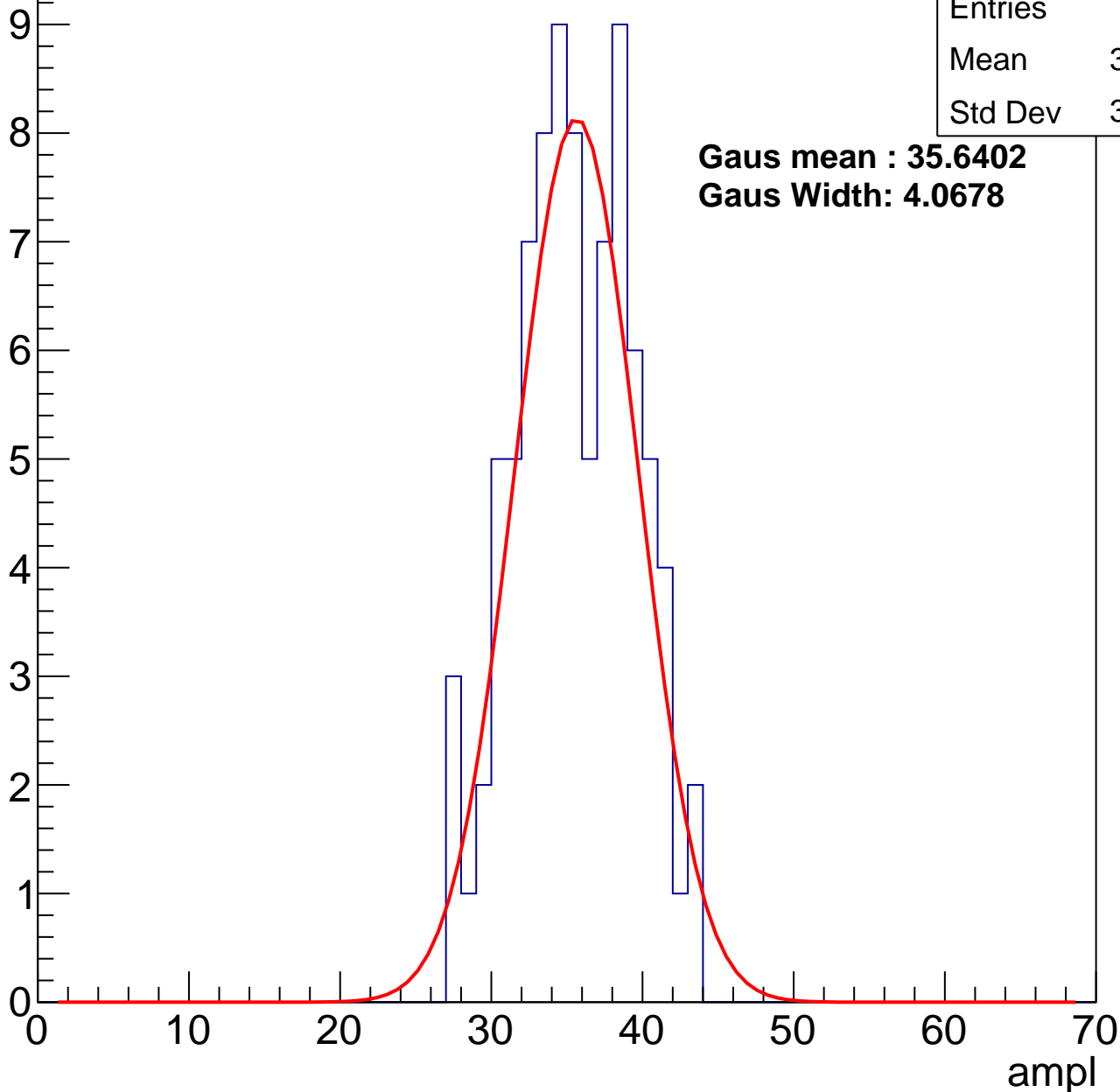
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	35.09
Std Dev	3.832

Gaus mean : 35.6402

Gaus Width: 4.0678



B1L102S, U2-ch30, adc2

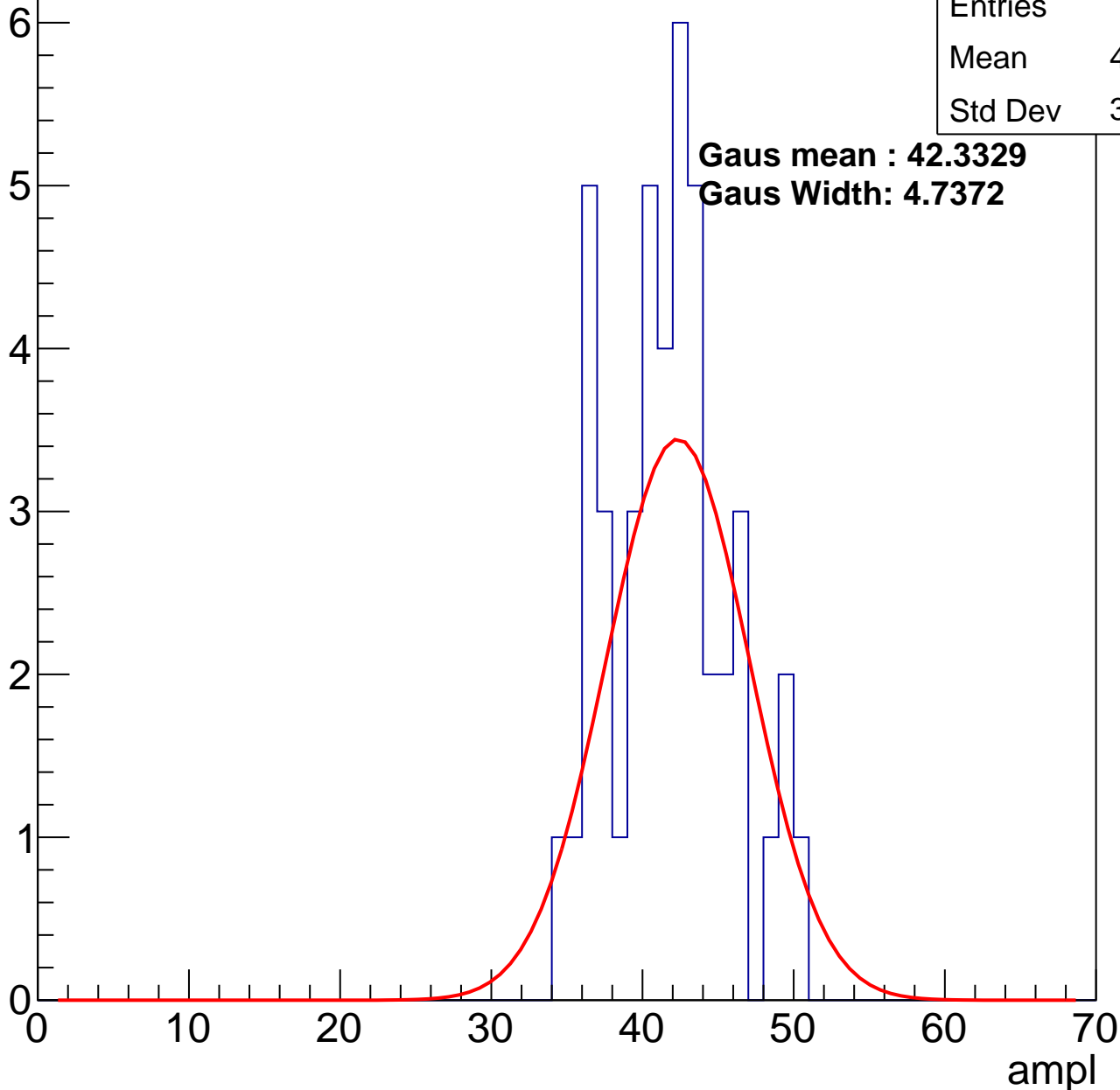
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	41.29
Std Dev	3.914

Gaus mean : 42.3329

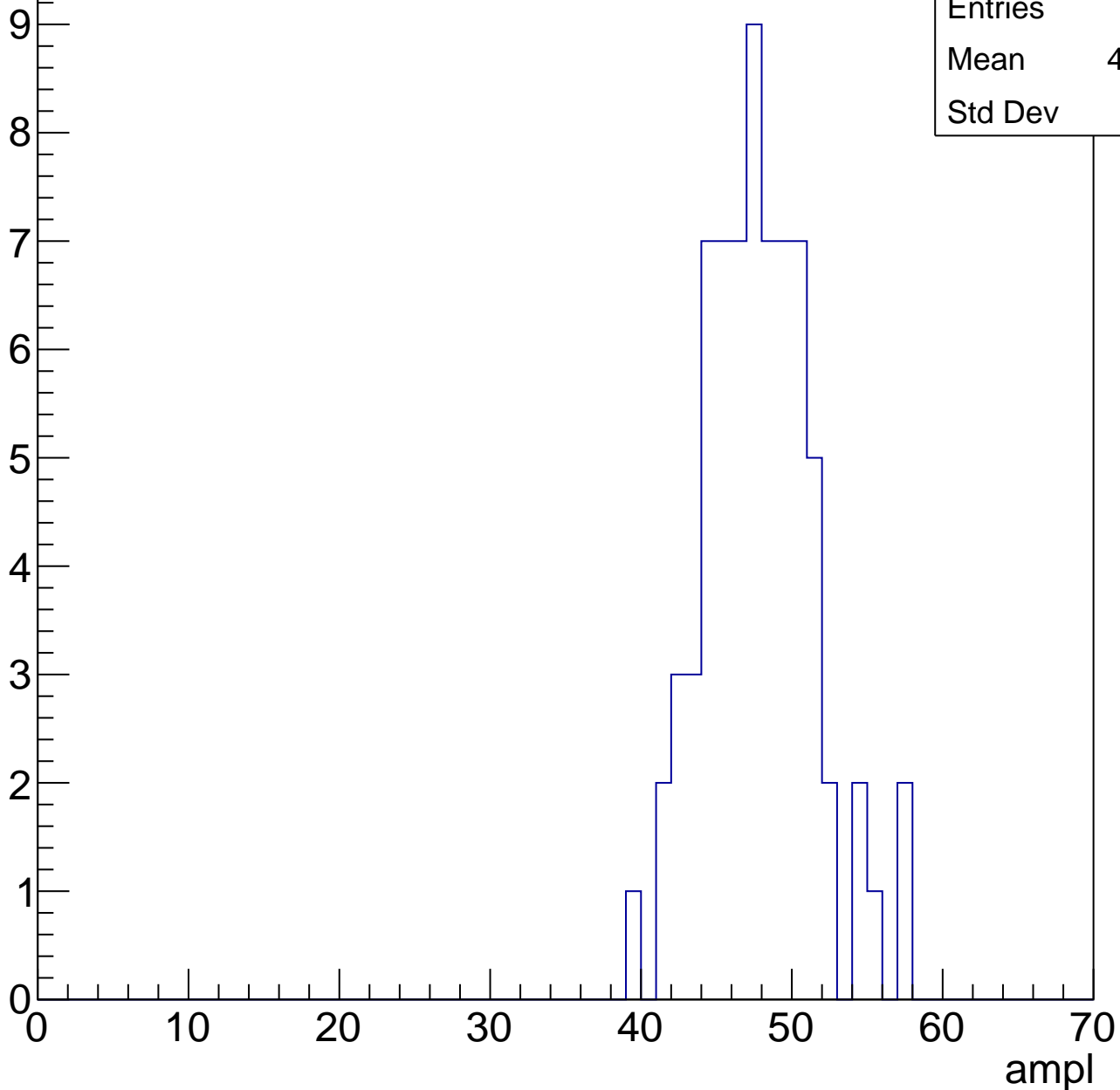
Gaus Width: 4.7372



B1L102S, U2-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	72
Mean	47.35
Std Dev	3.61

B1L102S, U2-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	53.91
Std Dev	3.484

Entry

10

8

6

4

2

0

0

10

20

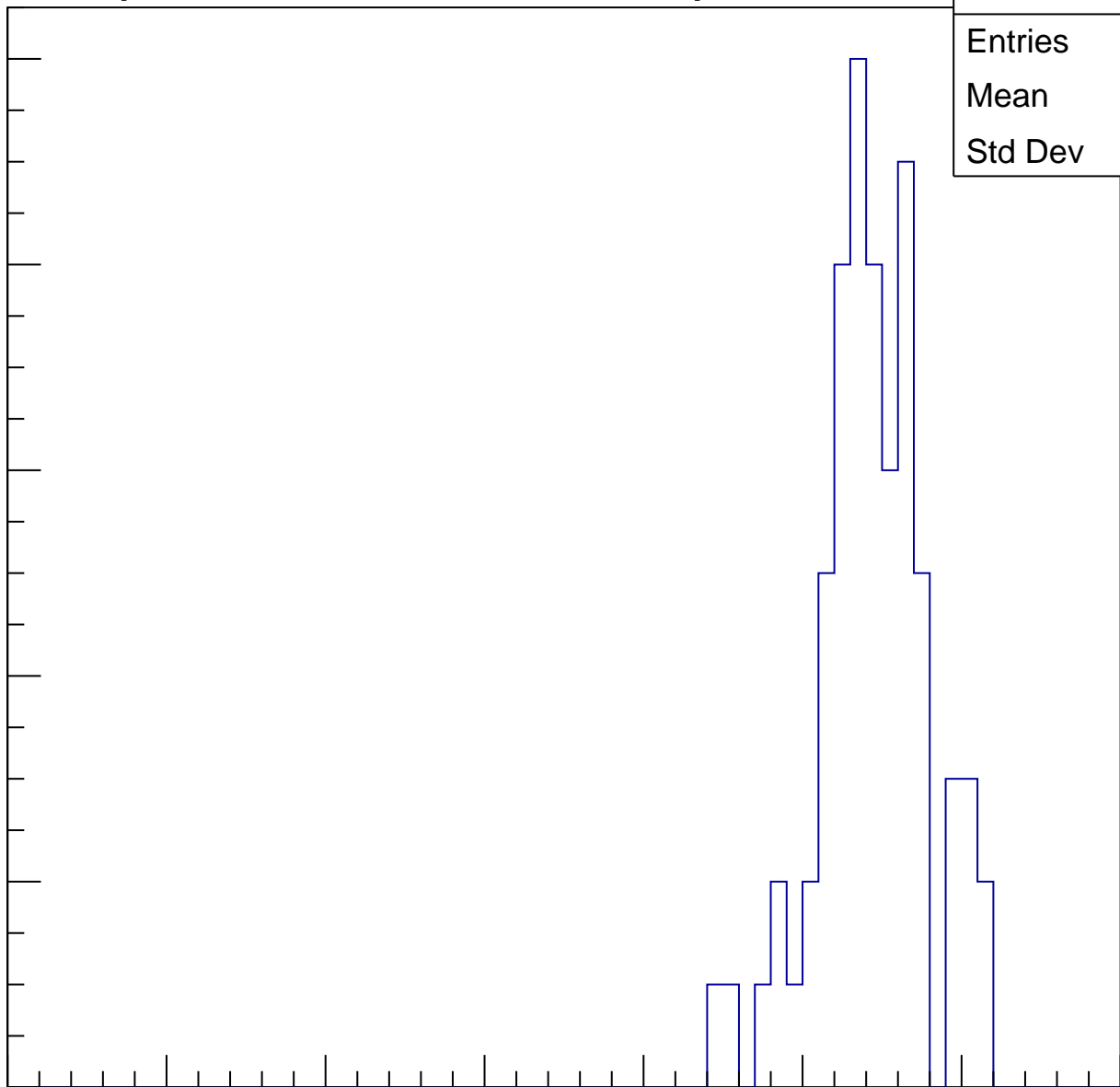
30

40

50

60

ampl

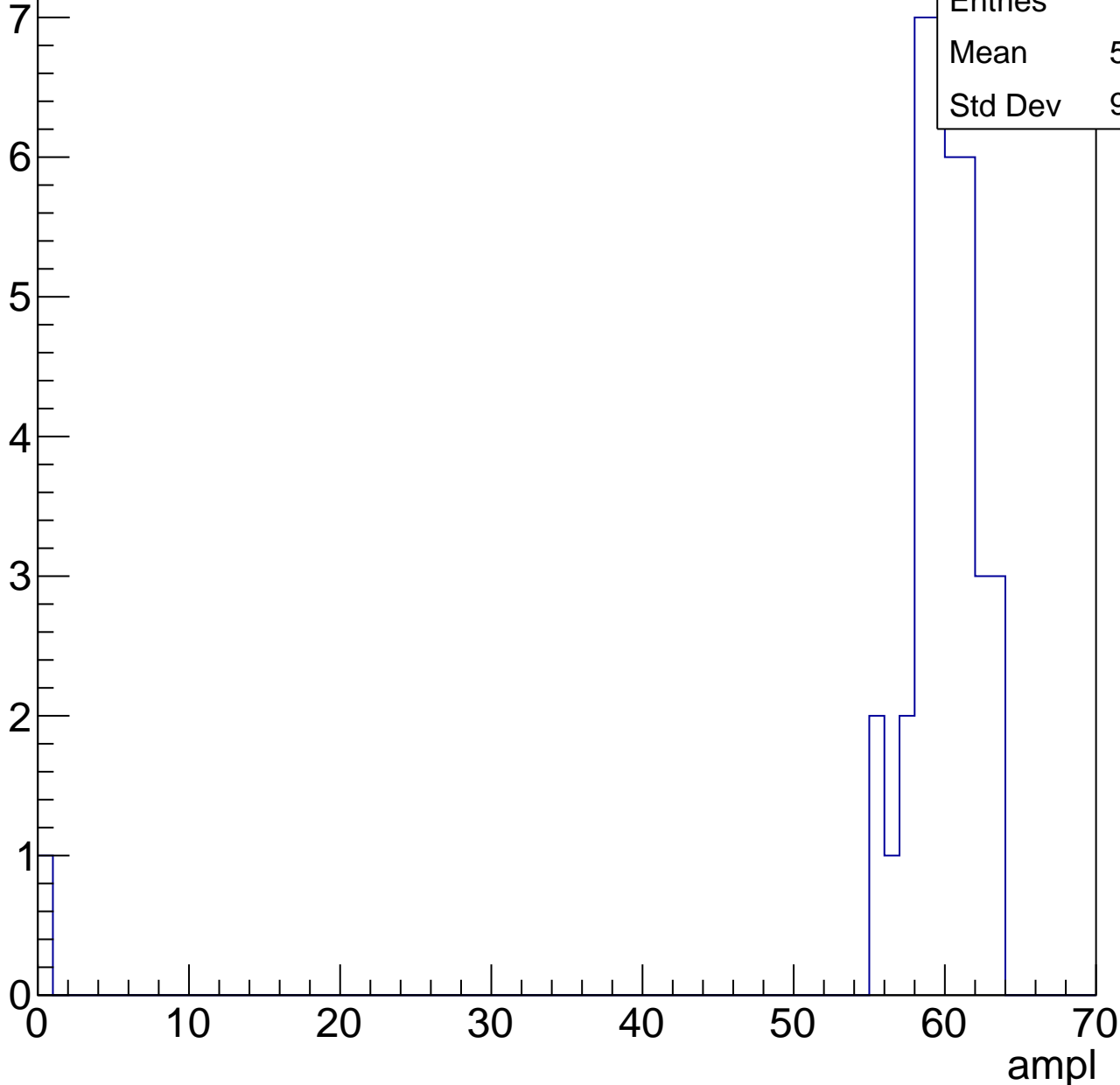


B1L102S, U2-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	57.89
Std Dev	9.727

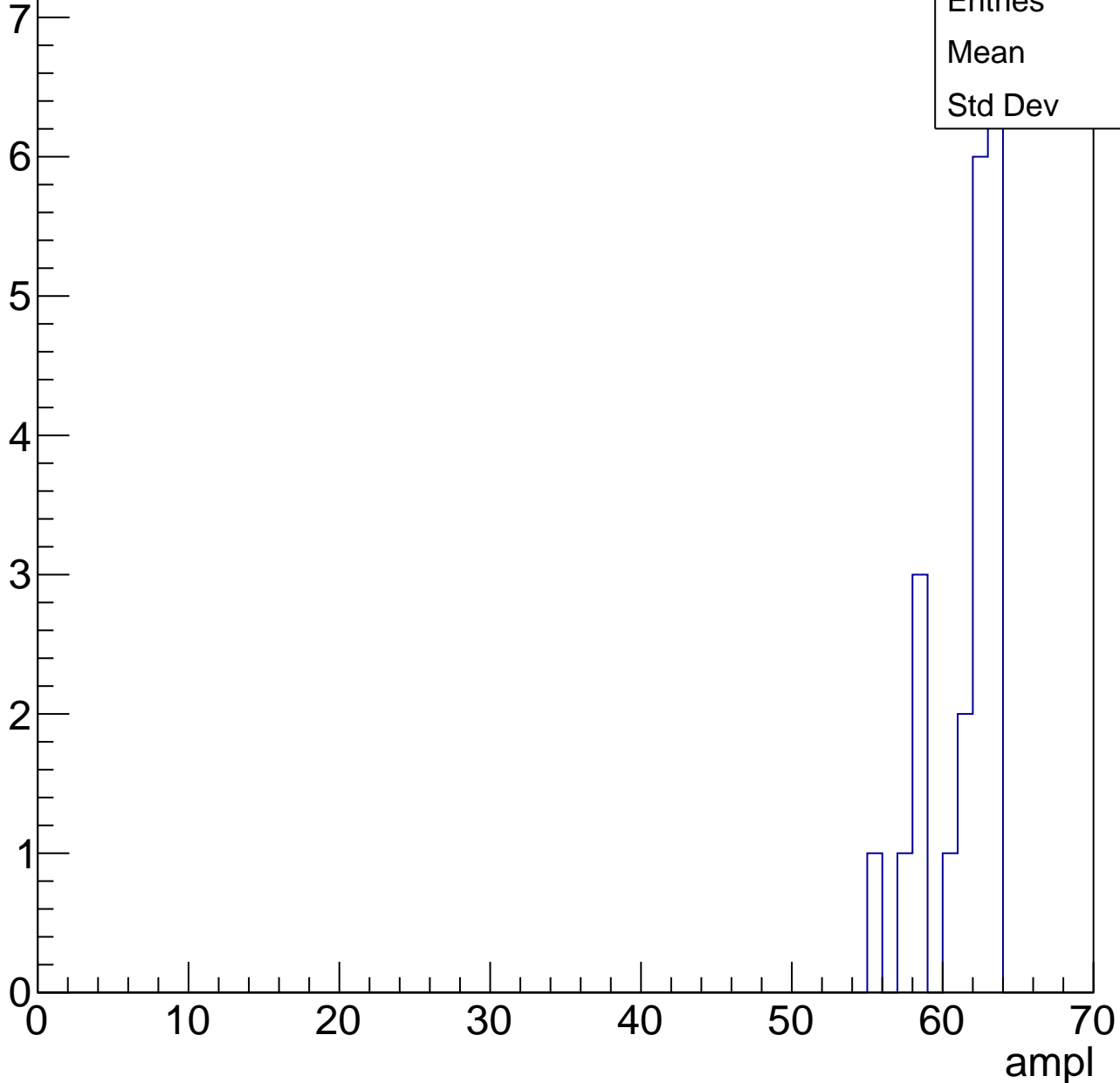


B1L102S, U2-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61
Std Dev	2.33



B1L102S, U2-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U2-ch31, adc0

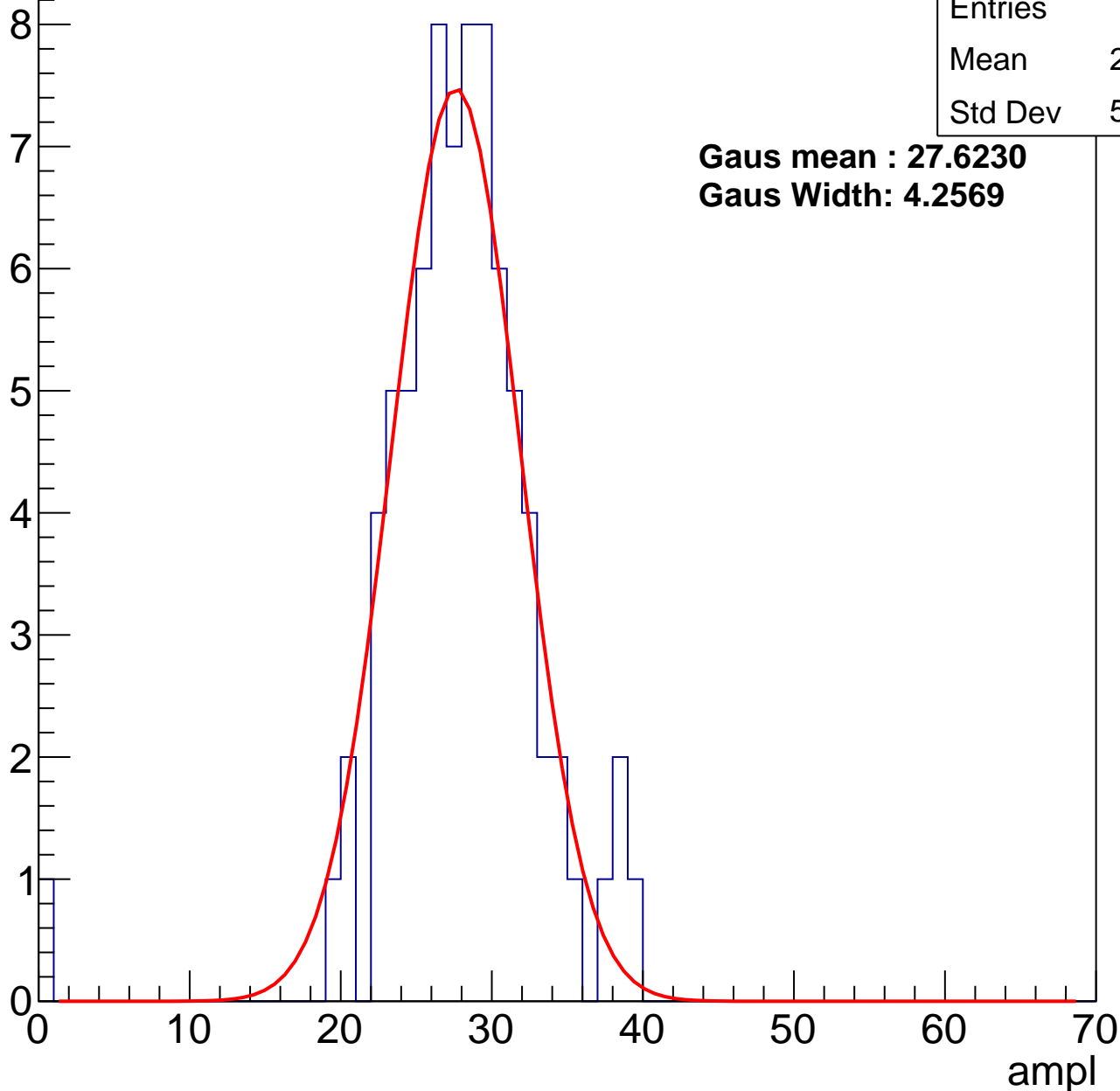
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.46
Std Dev	5.187

Gaus mean : 27.6230

Gaus Width: 4.2569



B1L102S, U2-ch31, adc1

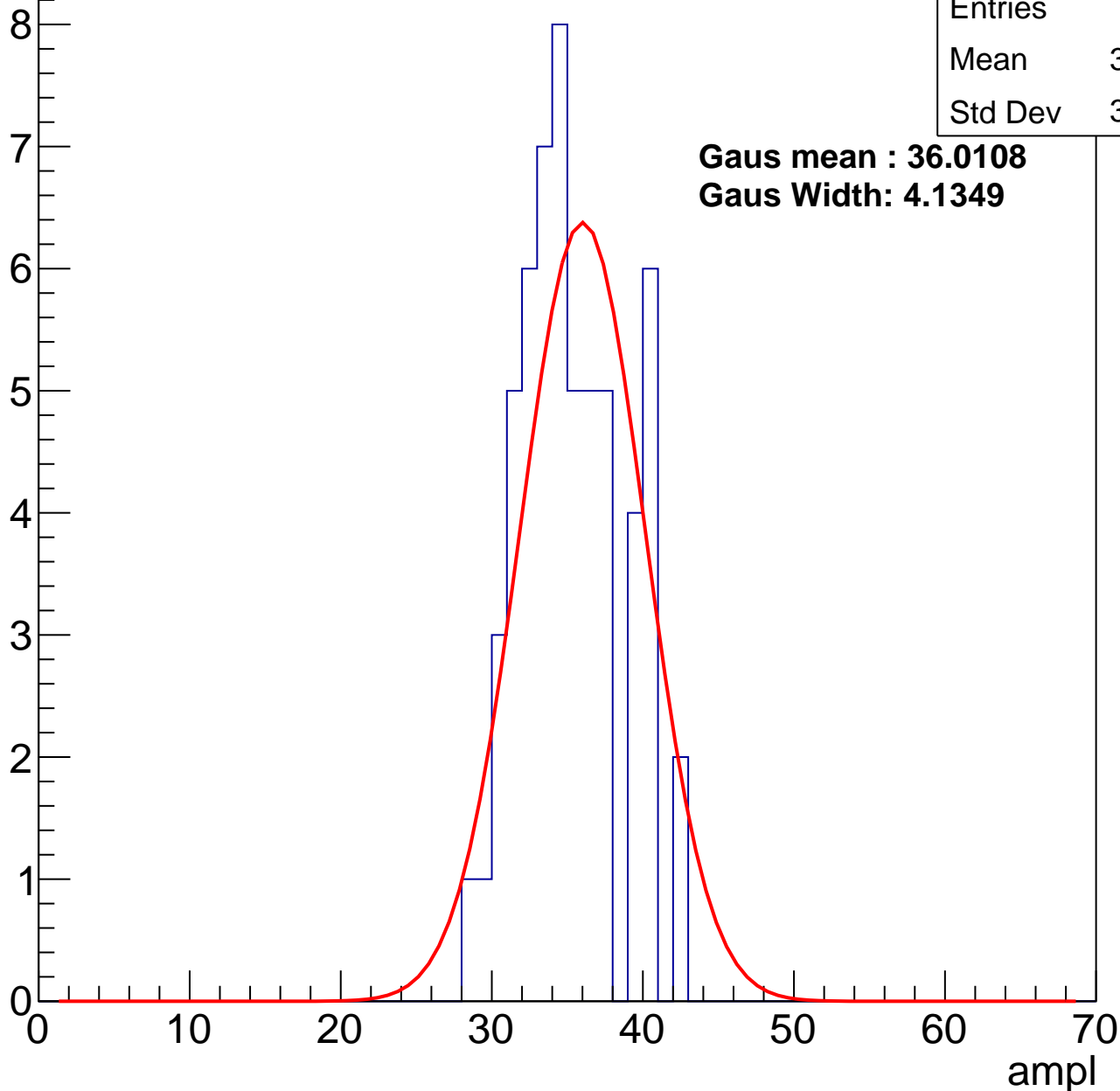
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.78
Std Dev	3.384

Gaus mean : 36.0108

Gaus Width: 4.1349



B1L102S, U2-ch31, adc2

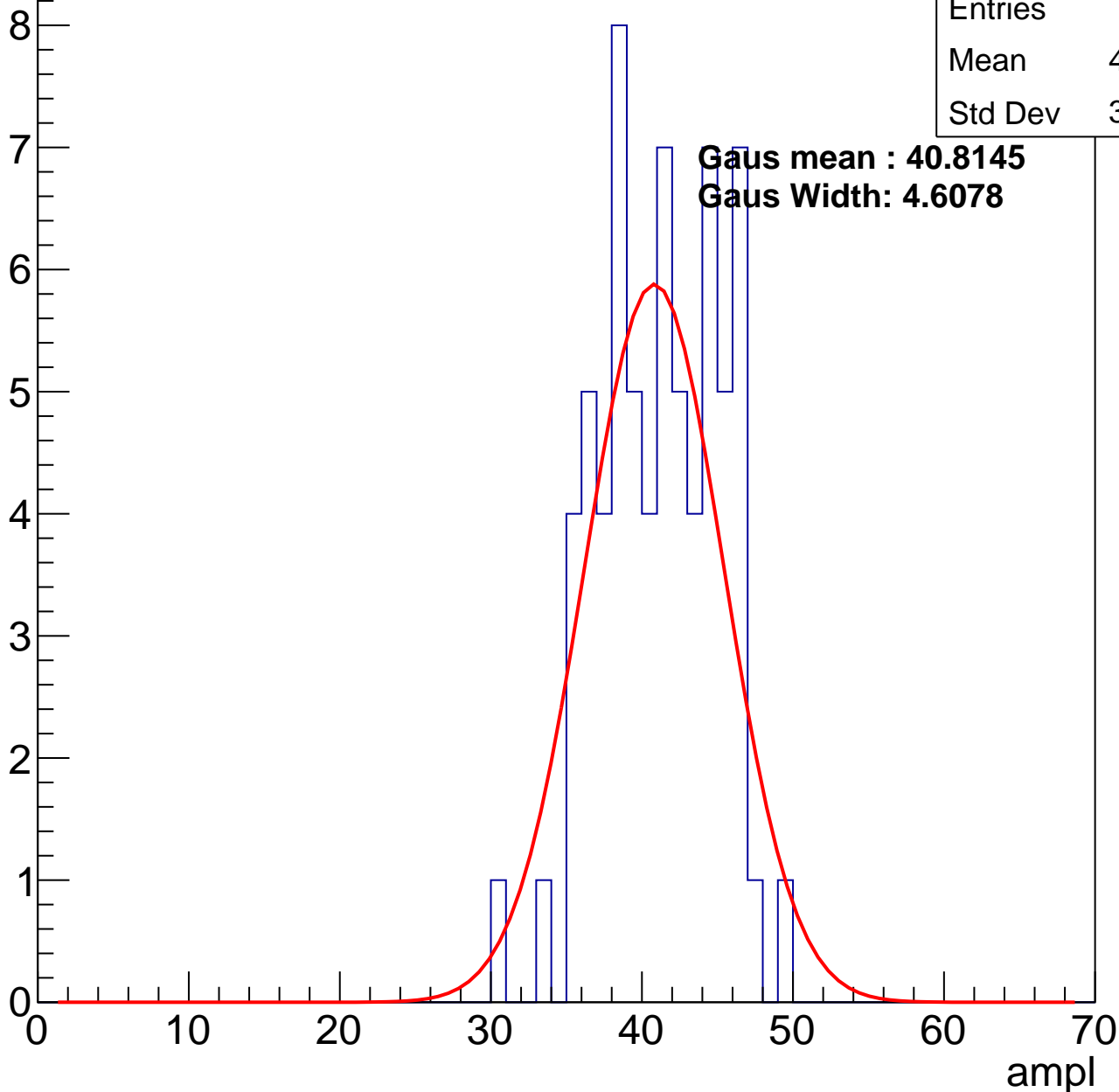
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.72
Std Dev	3.897

Gaus mean : 40.8145

Gaus Width: 4.6078



B1L102S, U2-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	47.41
Std Dev	3.553

Entry

10

8

6

4

2

0

0

10

20

30

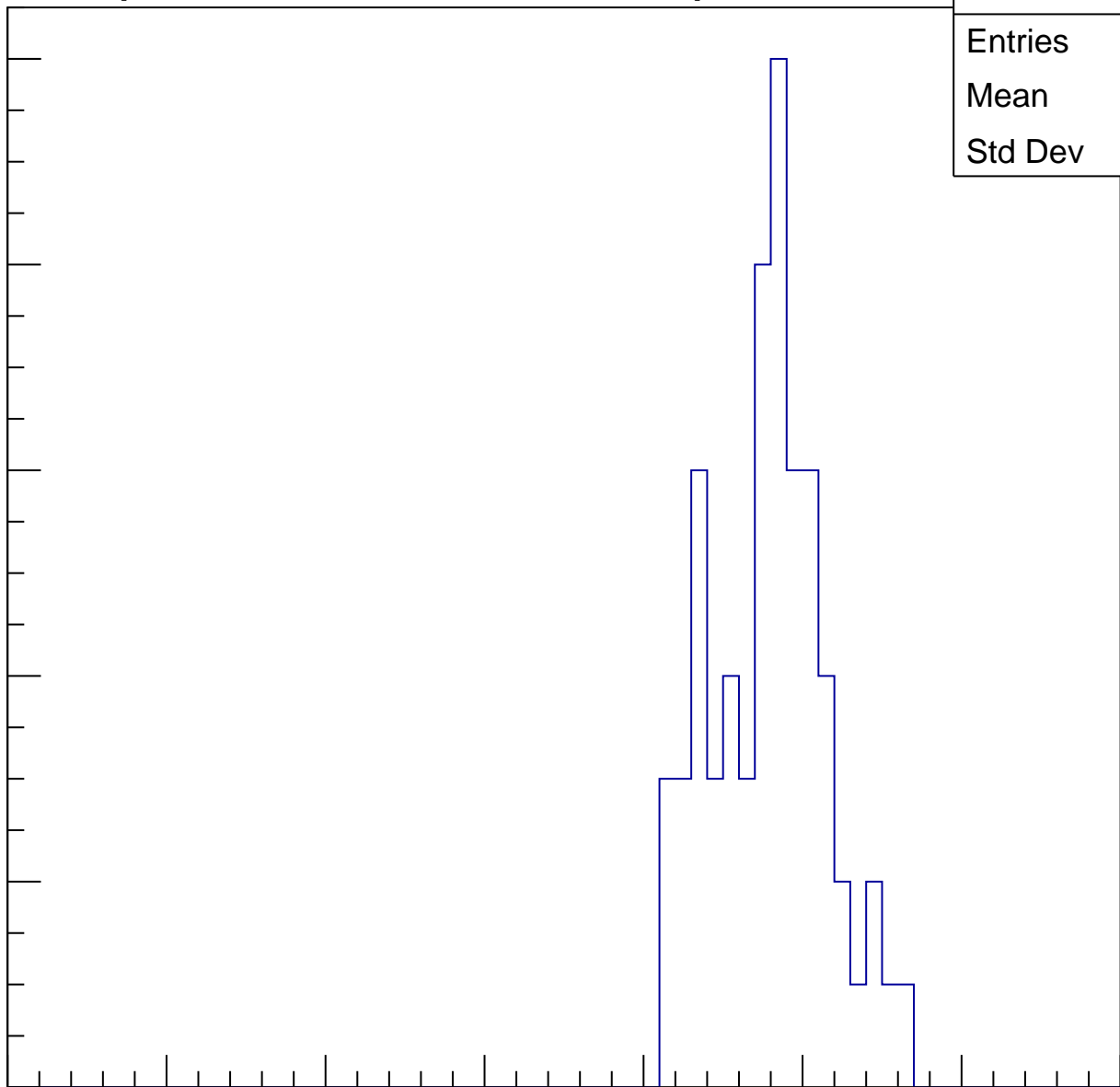
40

50

60

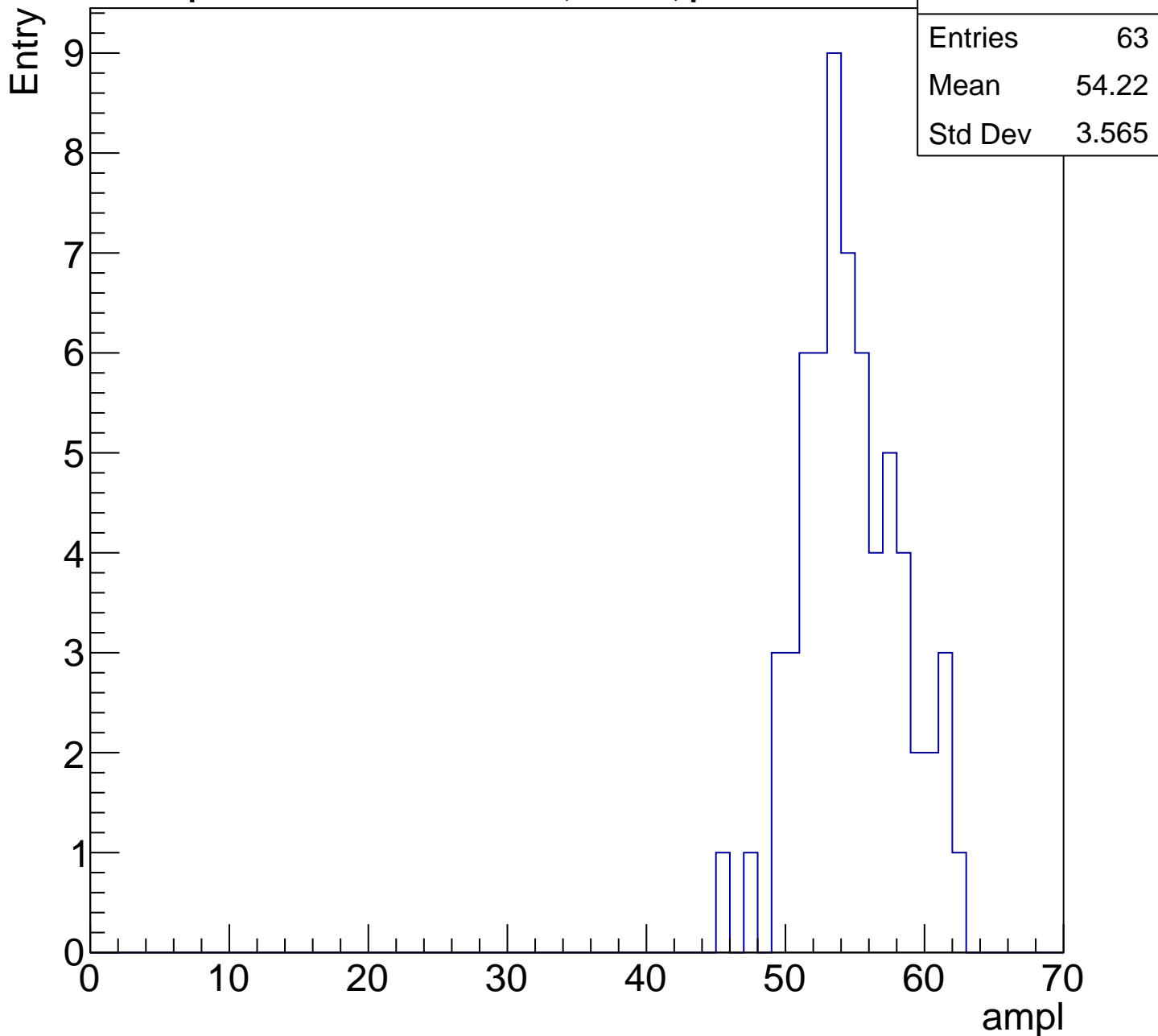
70

ampl



B1L102S, U2-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

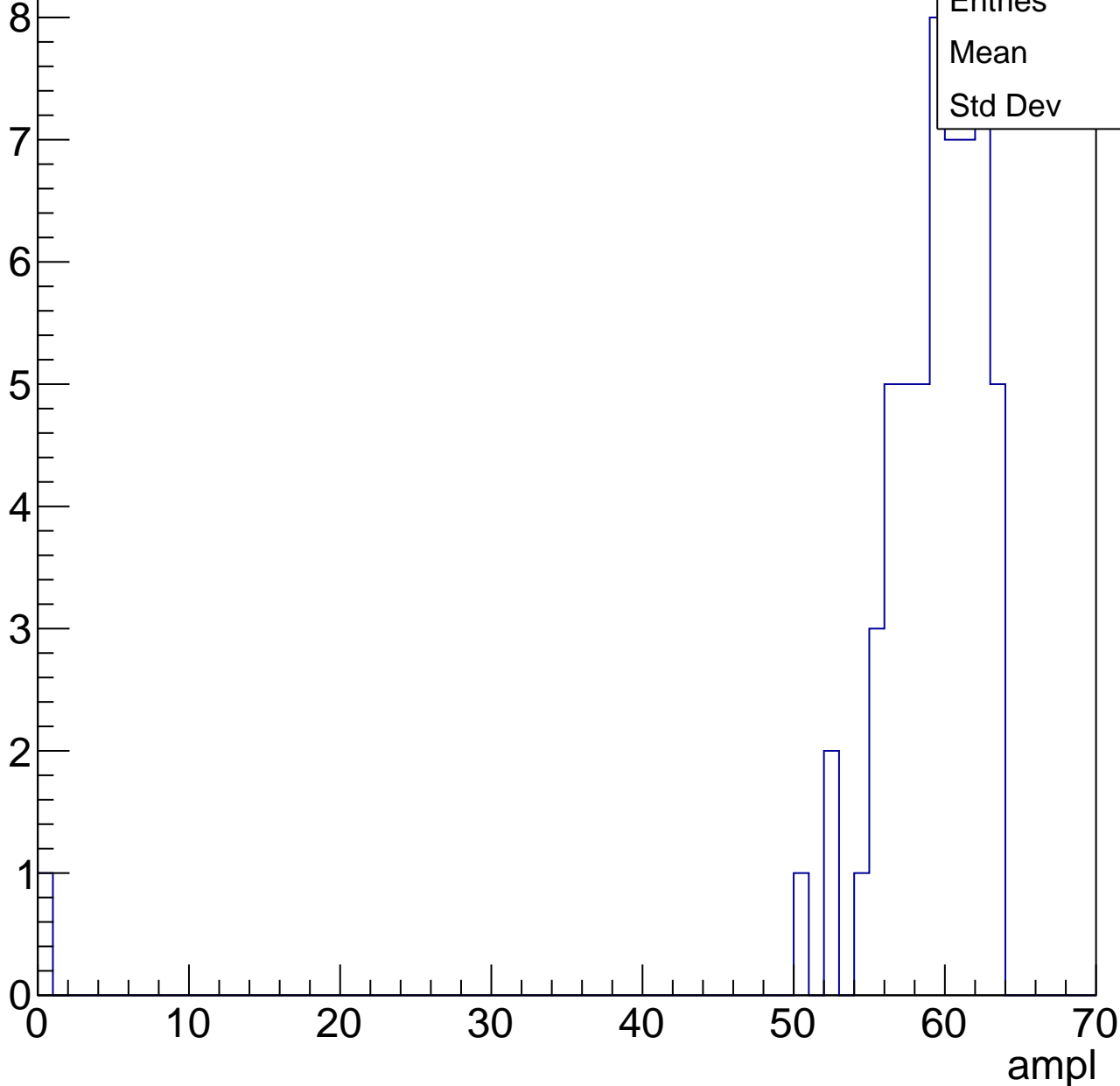


B1L102S, U2-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

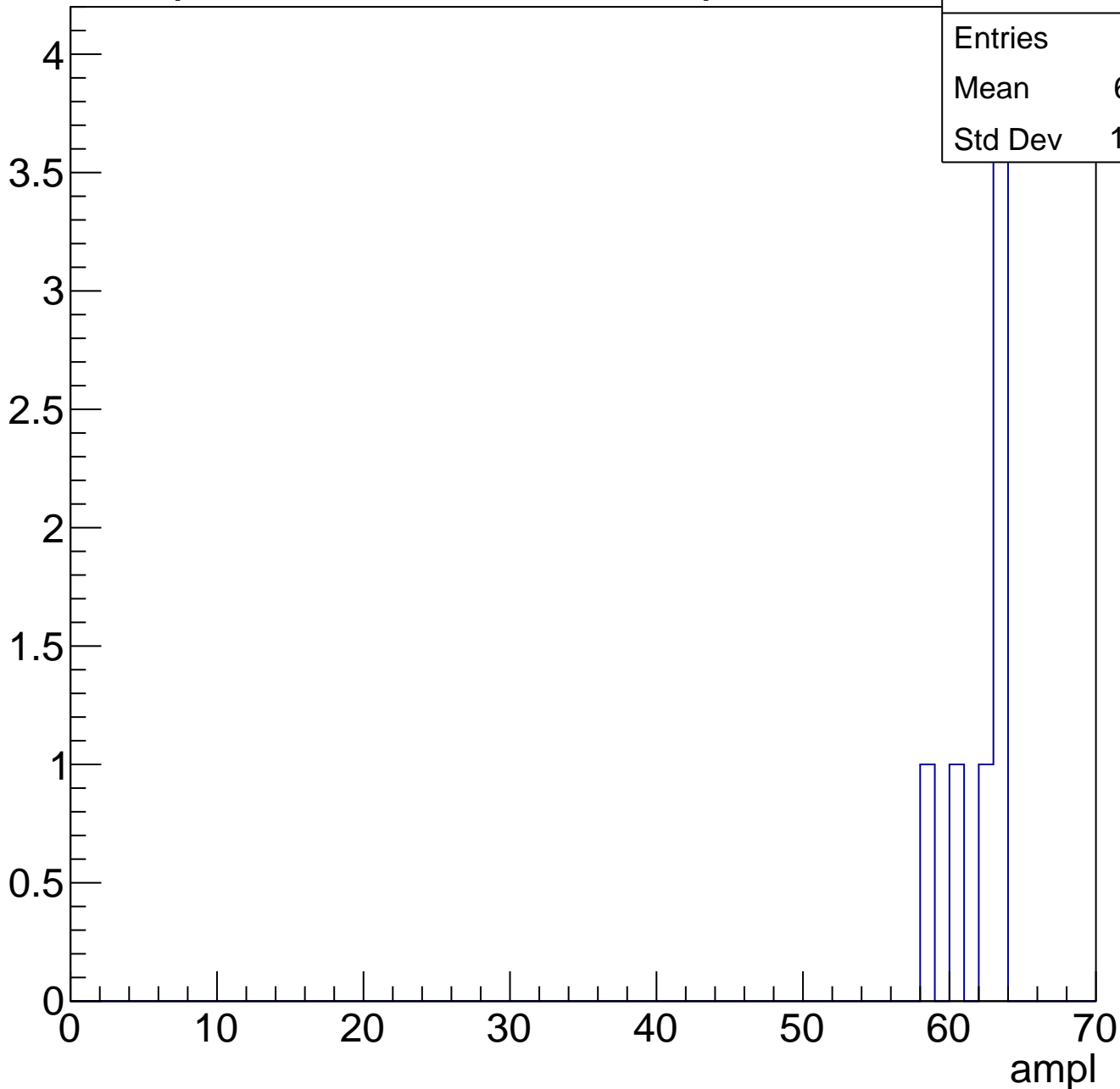
Entries	58
Mean	57.9
Std Dev	8.22



B1L102S, U2-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch32, adc0

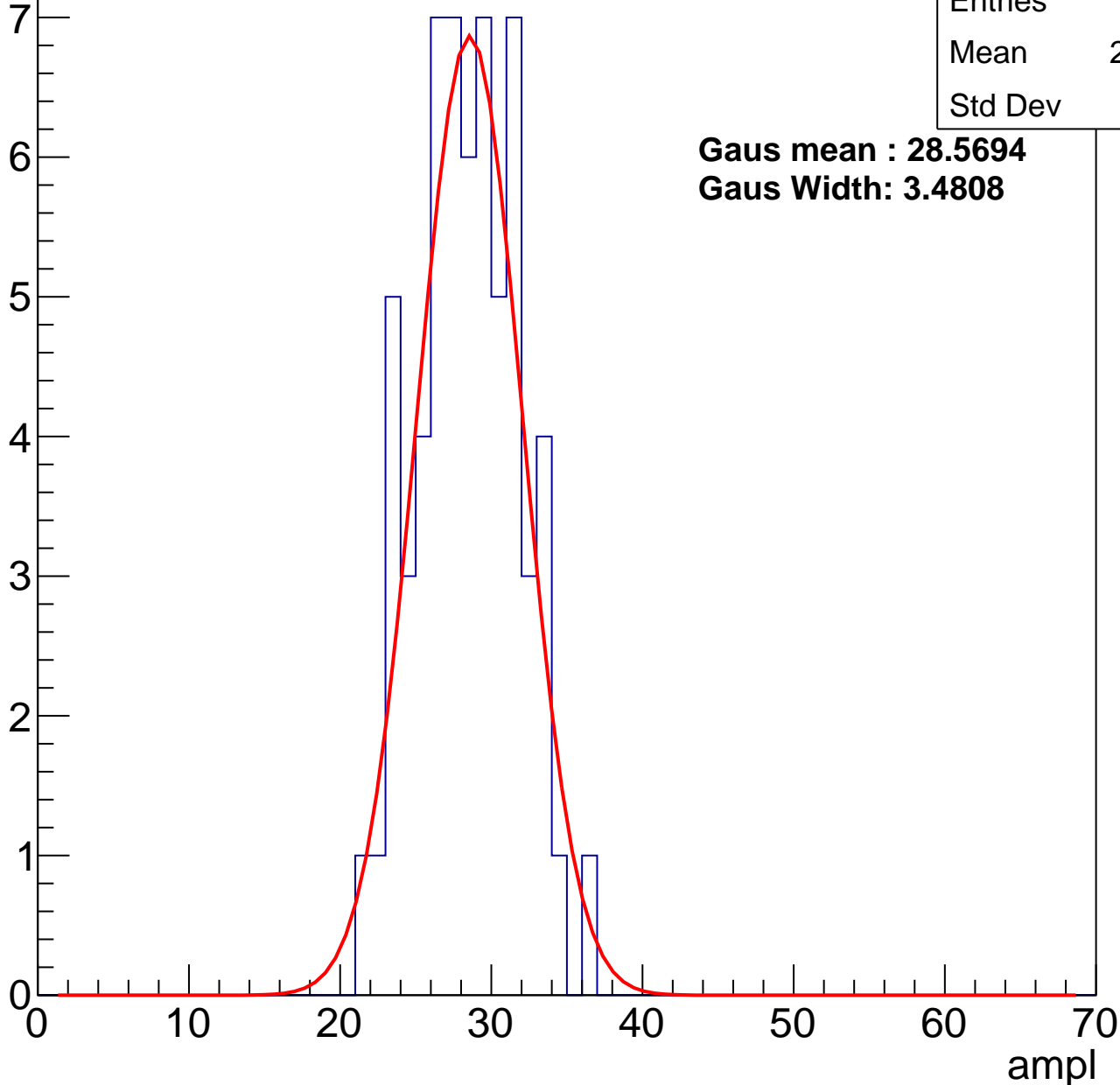
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.02
Std Dev	3.28

Gaus mean : 28.5694

Gaus Width: 3.4808



B1L102S, U2-ch32, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	35.09
Std Dev	3.648

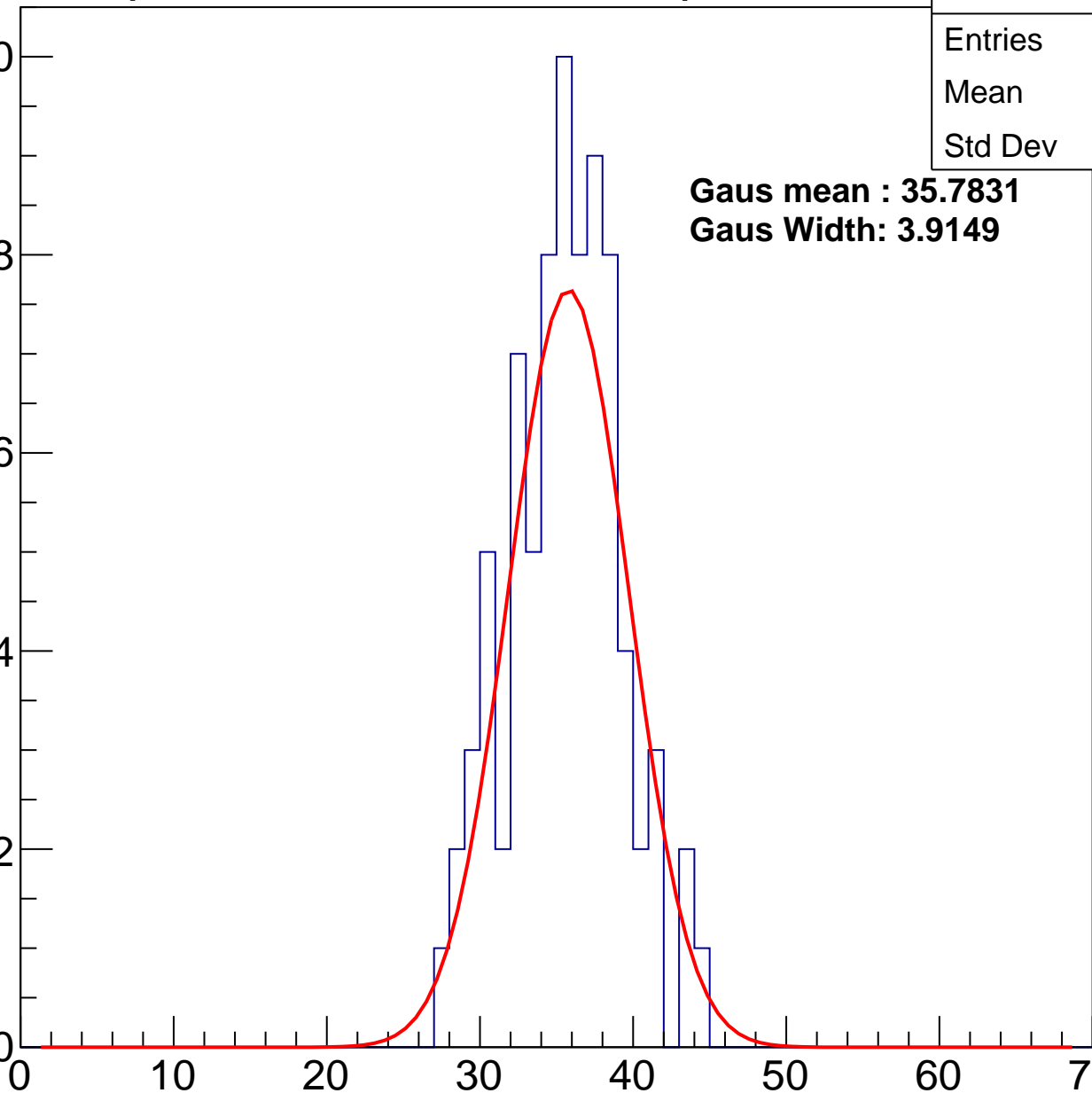
Gaus mean : 35.7831

Gaus Width: 3.9149

Entry

10
8
6
4
2
0

ampl



B1L102S, U2-ch32, adc2

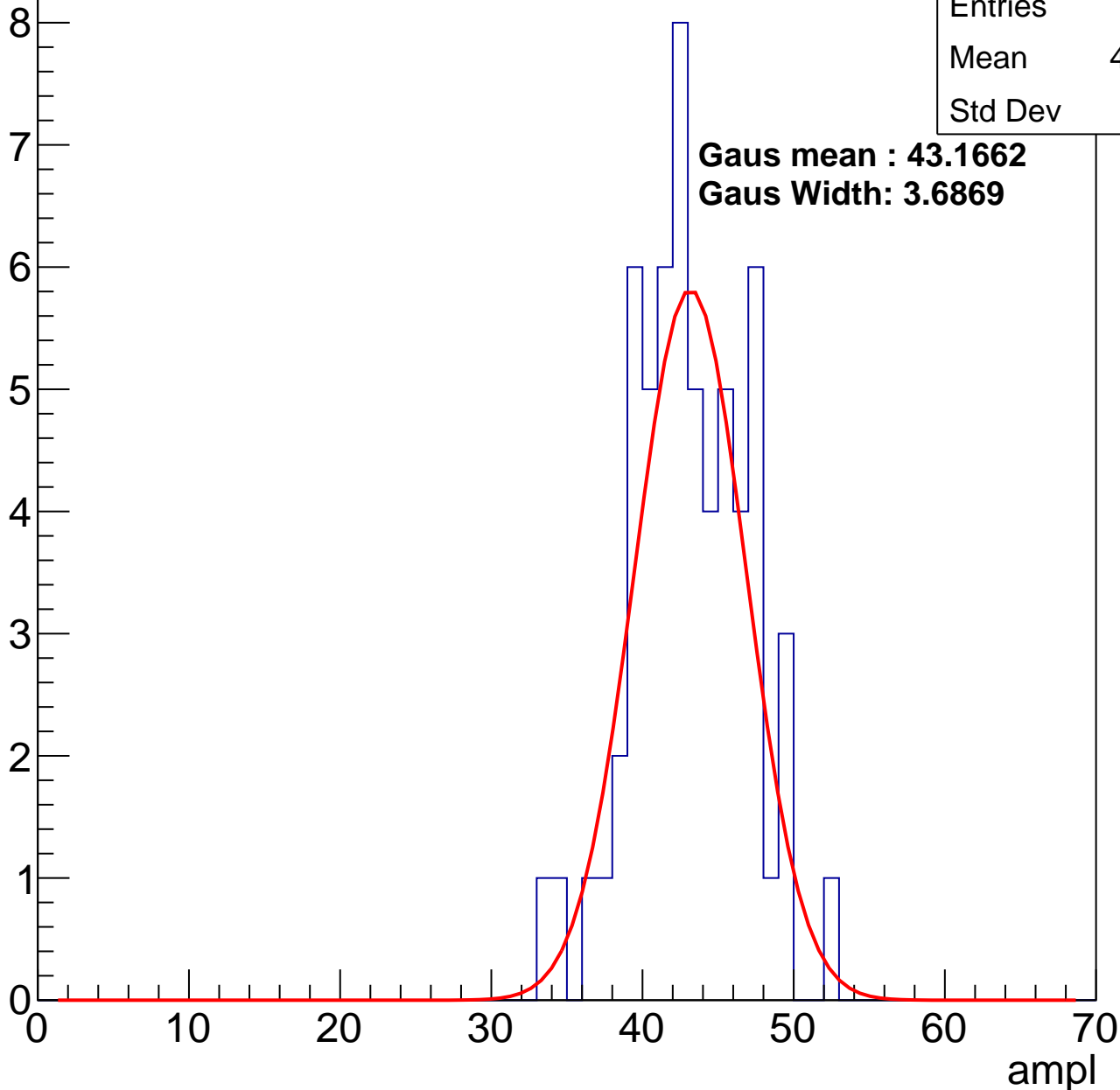
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.68
Std Dev	3.77

Gaus mean : 43.1662

Gaus Width: 3.6869

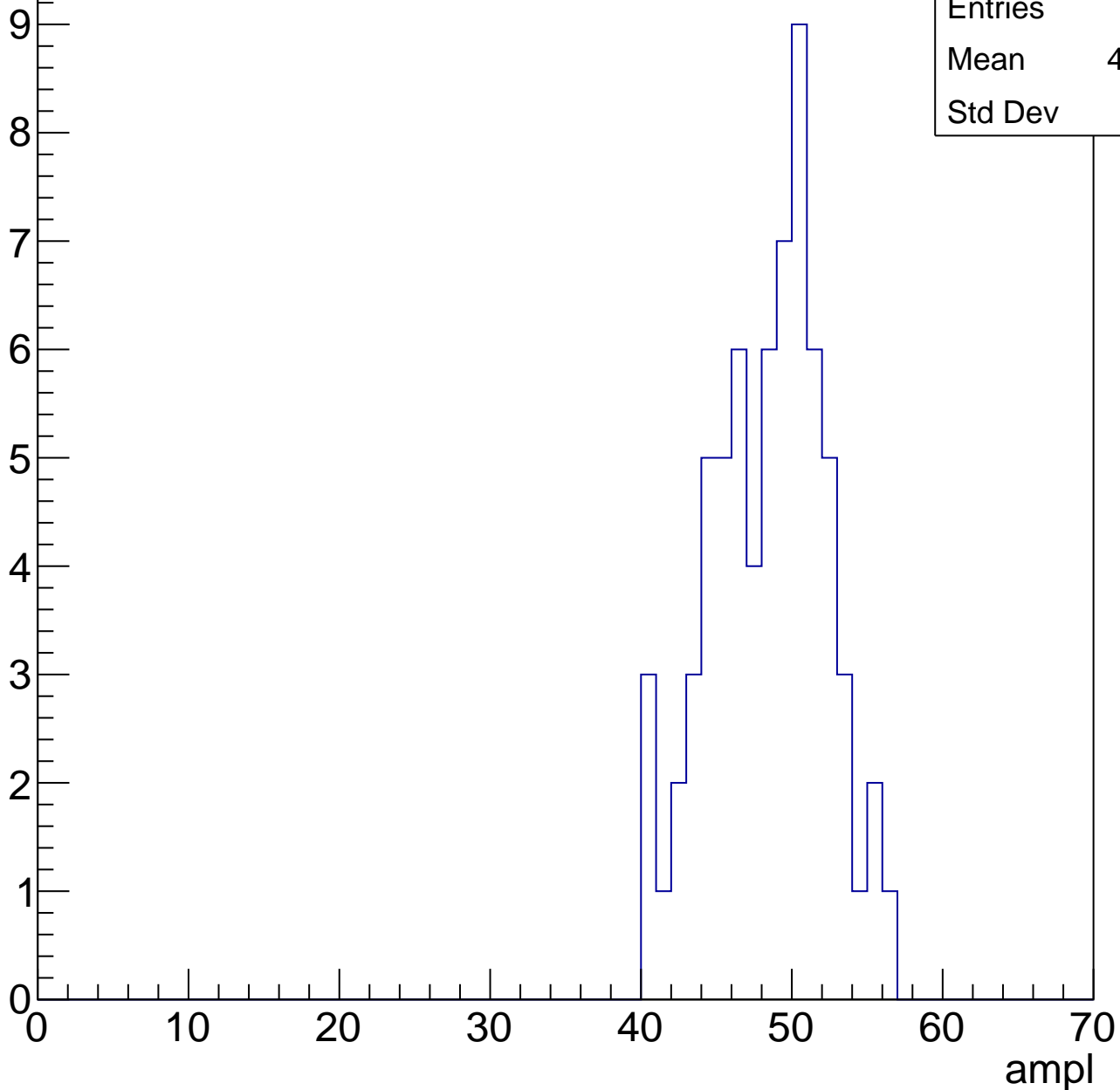


B1L102S, U2-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.96
Std Dev	3.79

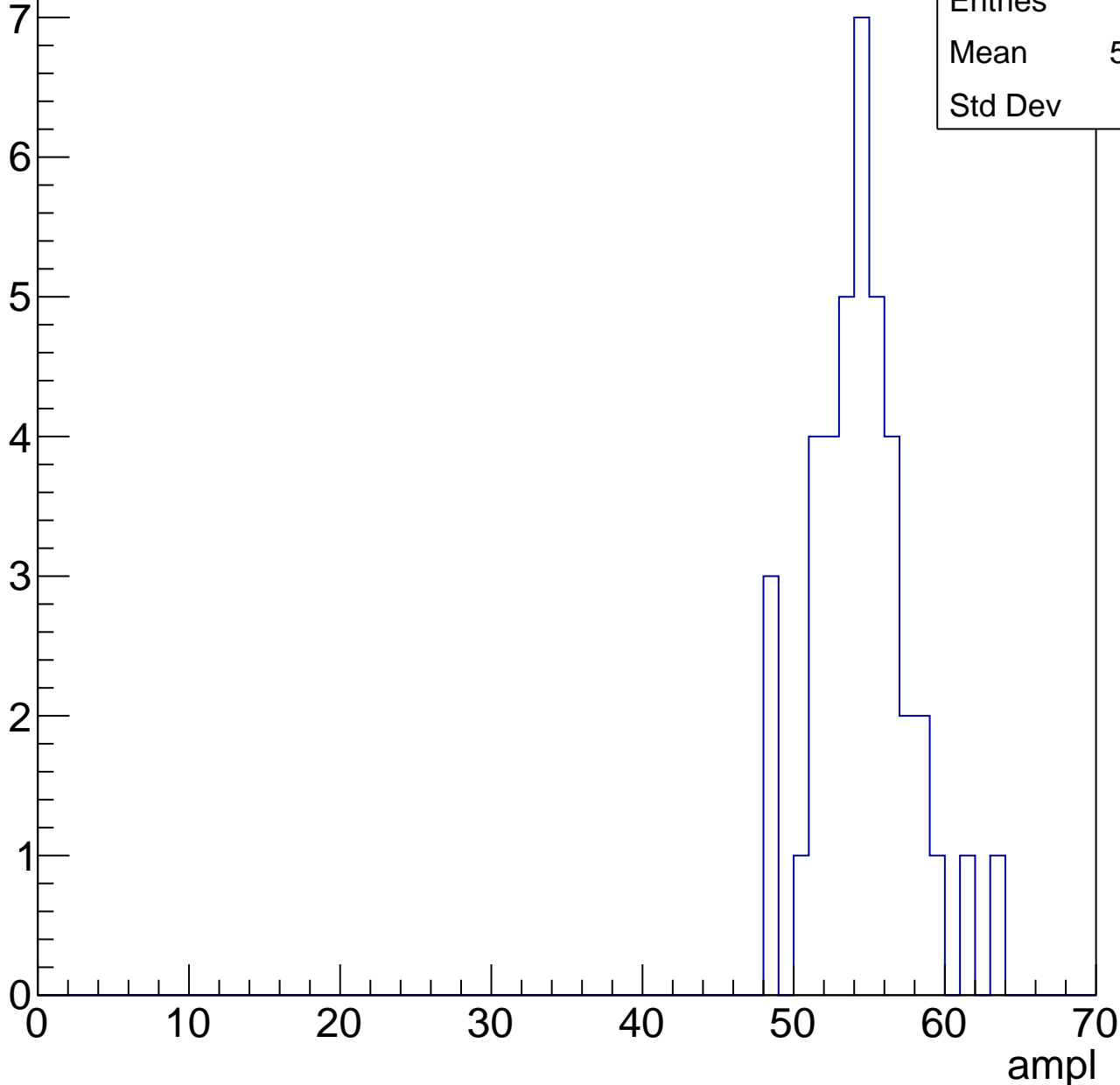


B1L102S, U2-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

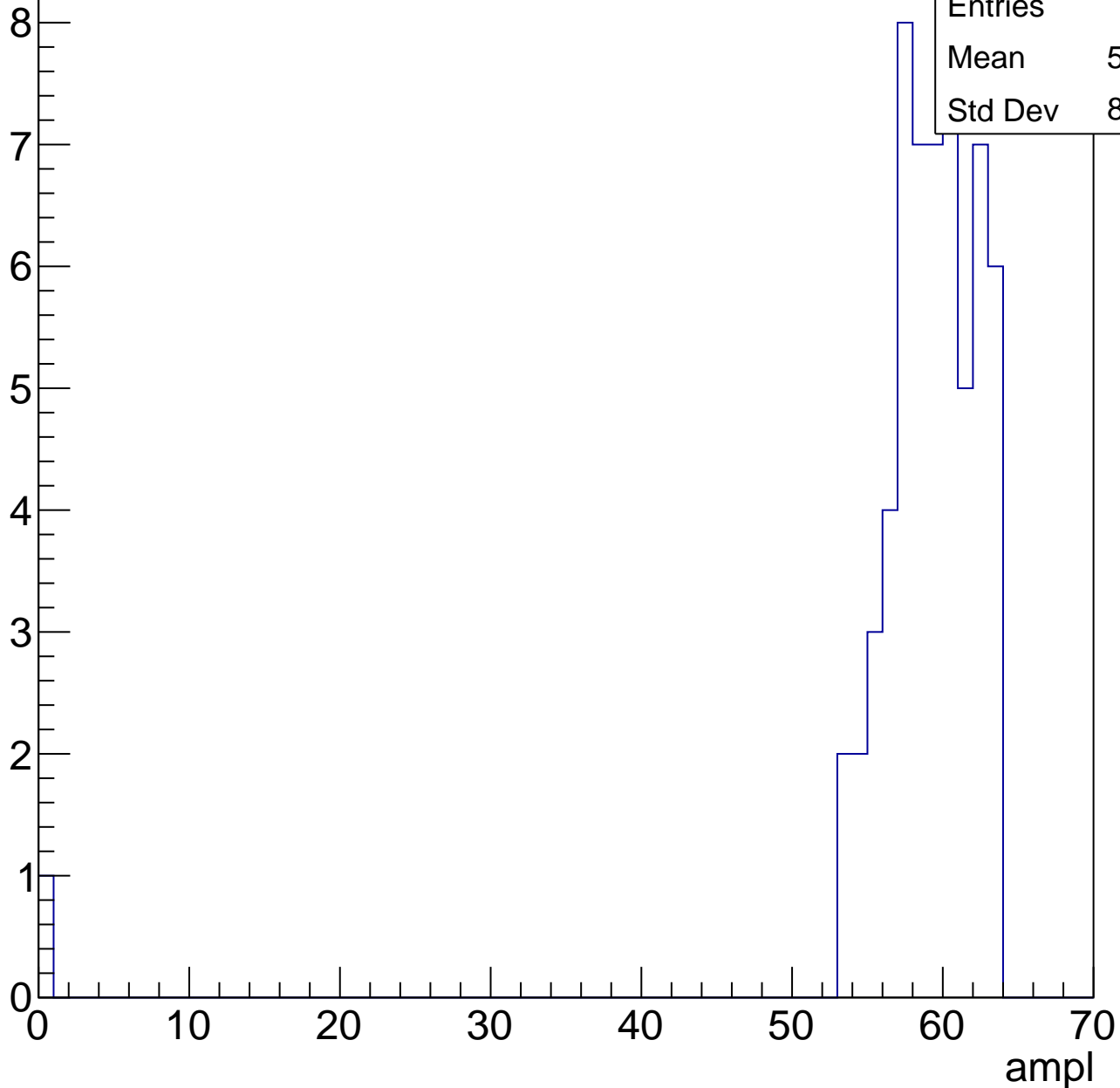
Entries	40
Mean	54.02
Std Dev	3.19



B1L102S, U2-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

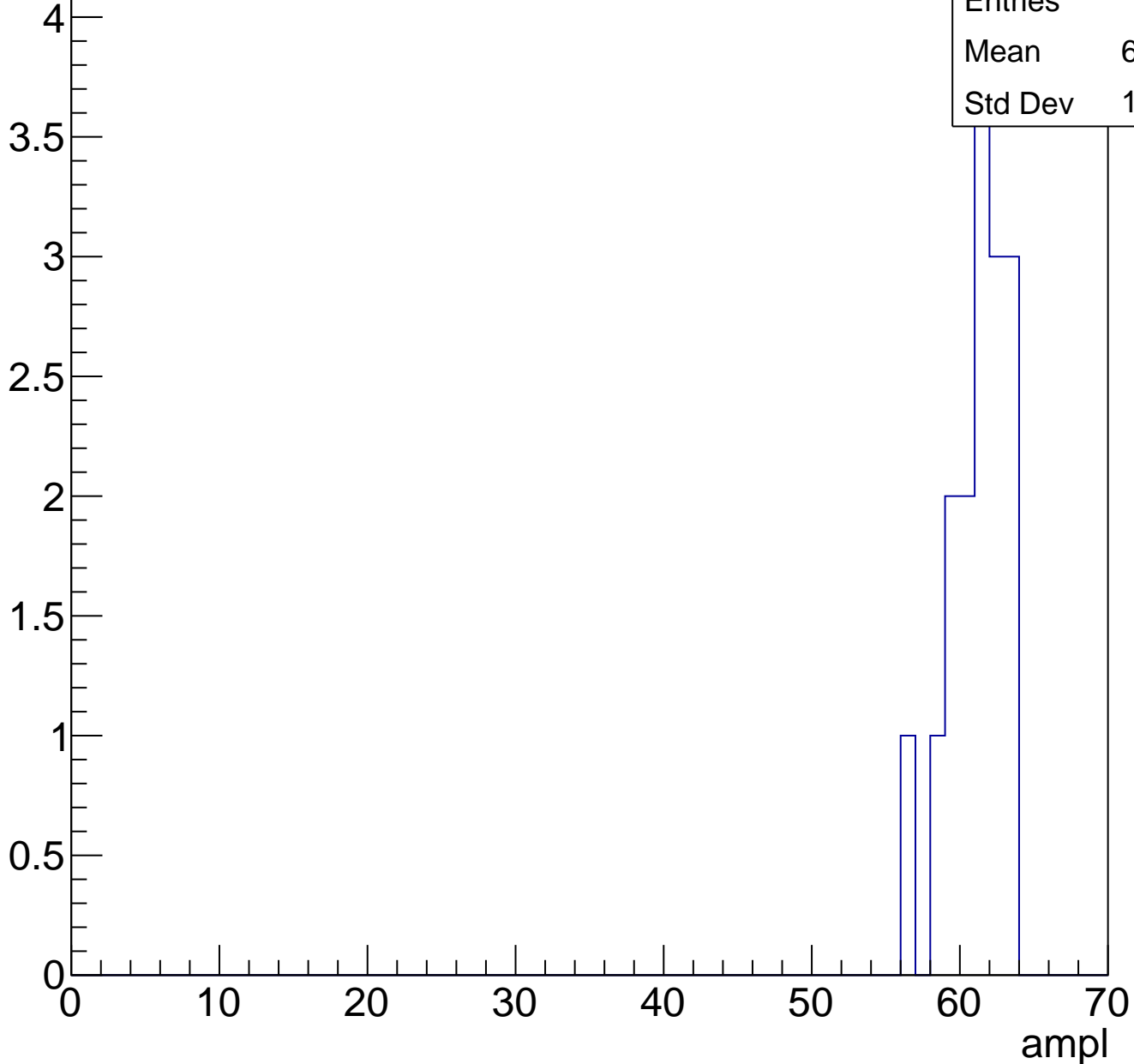
Entry



B1L102S, U2-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch33, adc0

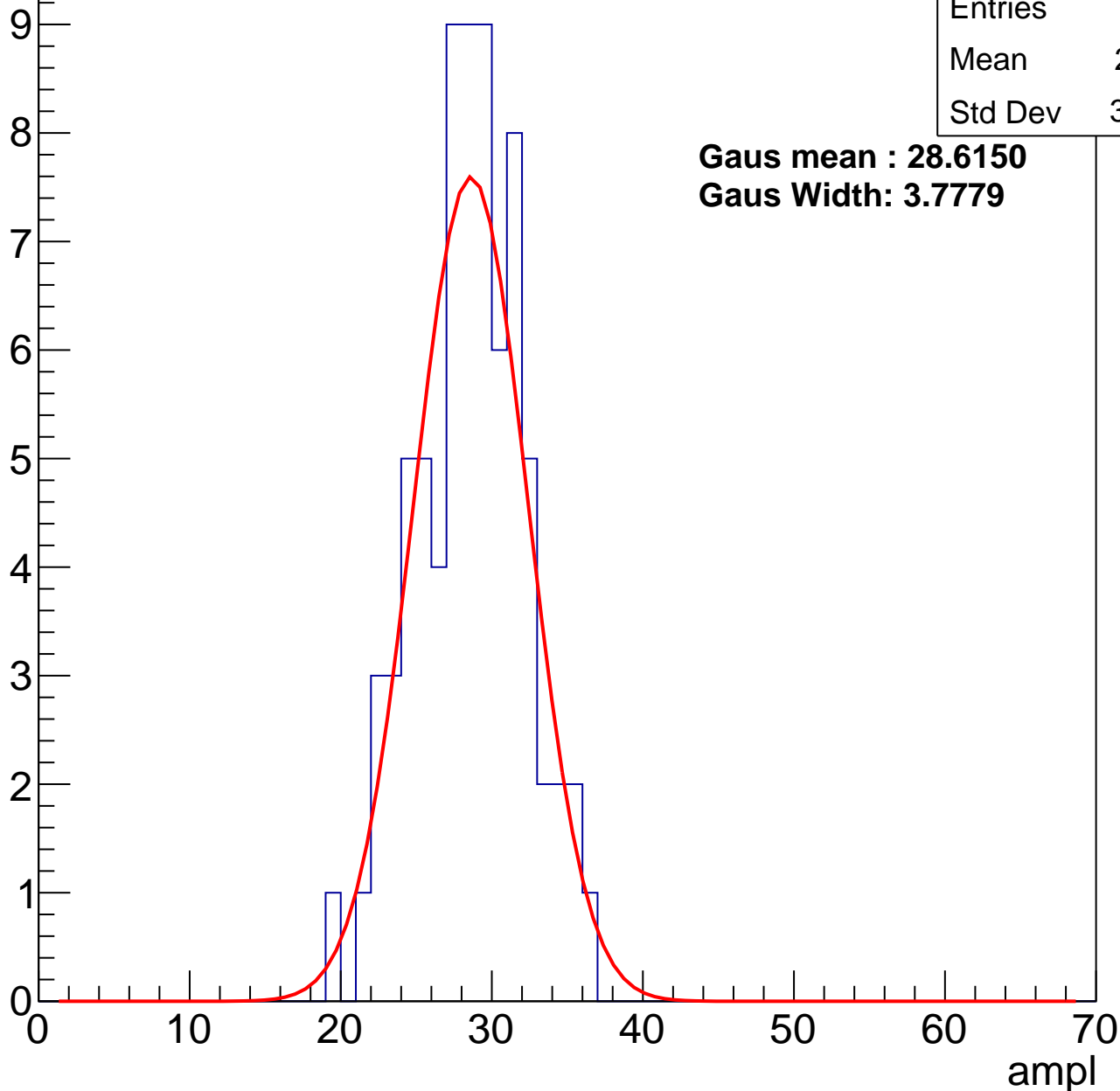
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.11
Std Dev	3.524

Gaus mean : 28.6150

Gaus Width: 3.7779



B1L102S, U2-ch33, adc1

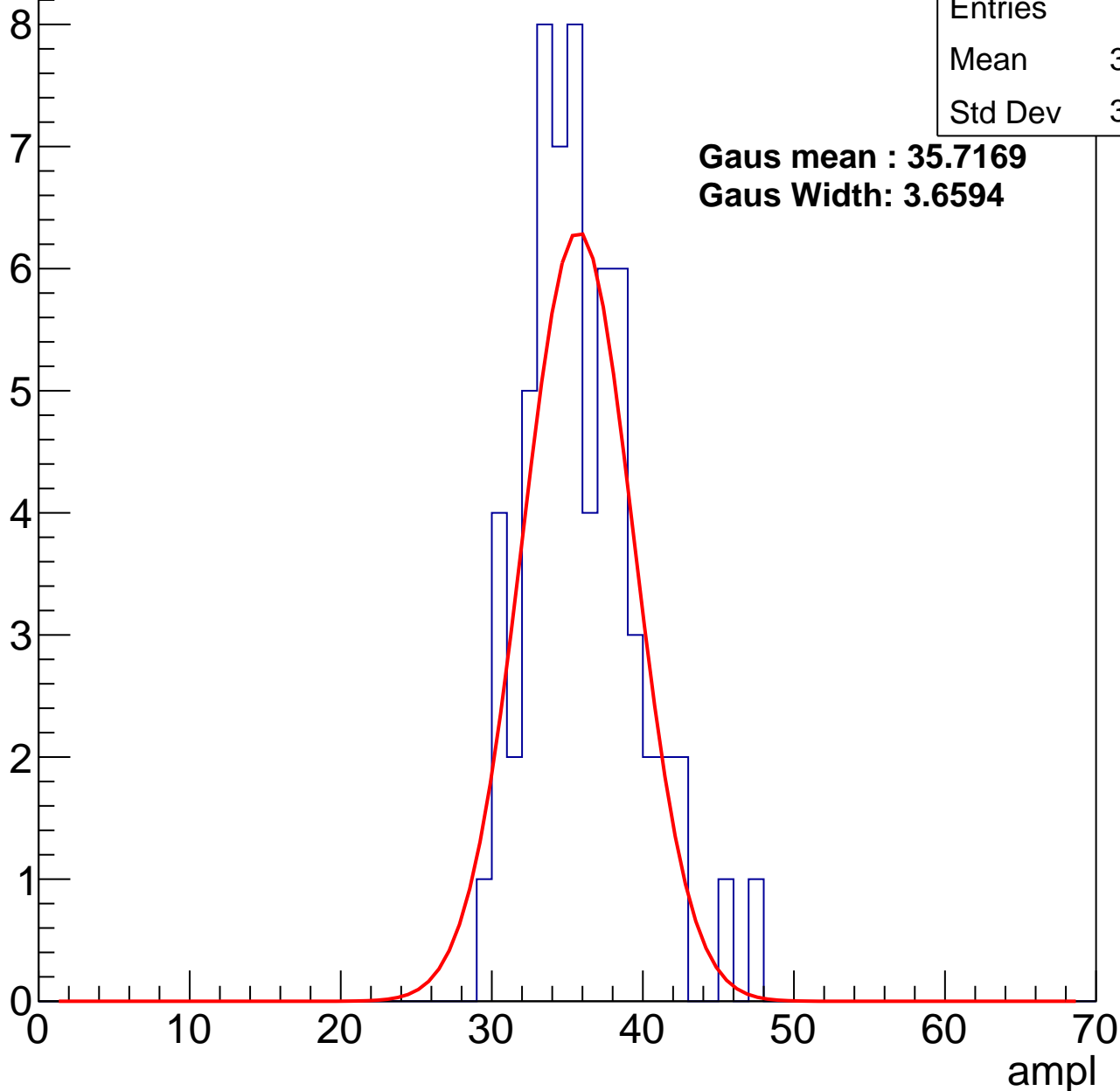
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.52
Std Dev	3.658

Gaus mean : 35.7169

Gaus Width: 3.6594



B1L102S, U2-ch33, adc2

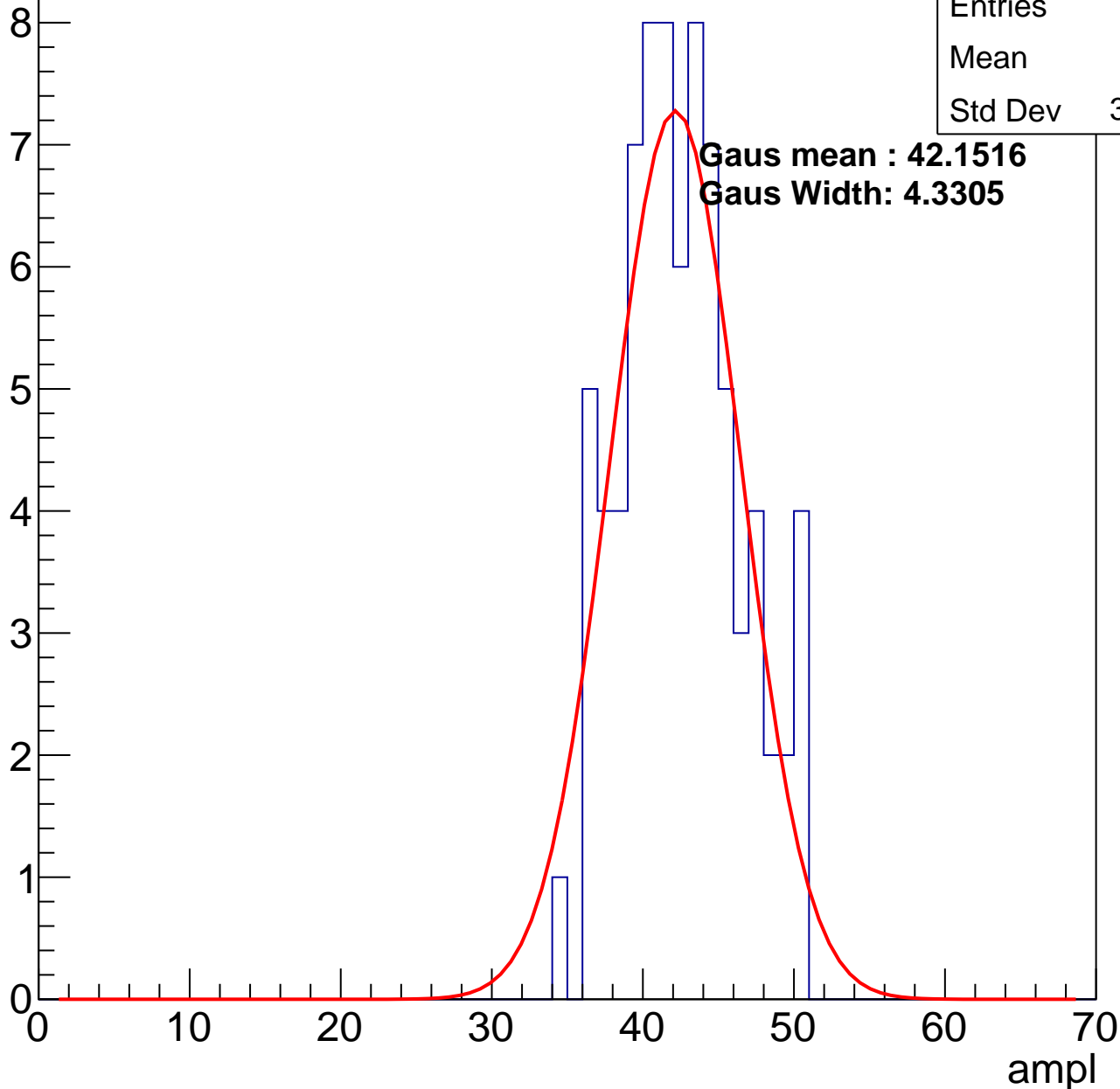
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.1
Std Dev	3.865

Gaus mean : 42.1516

Gaus Width: 4.3305

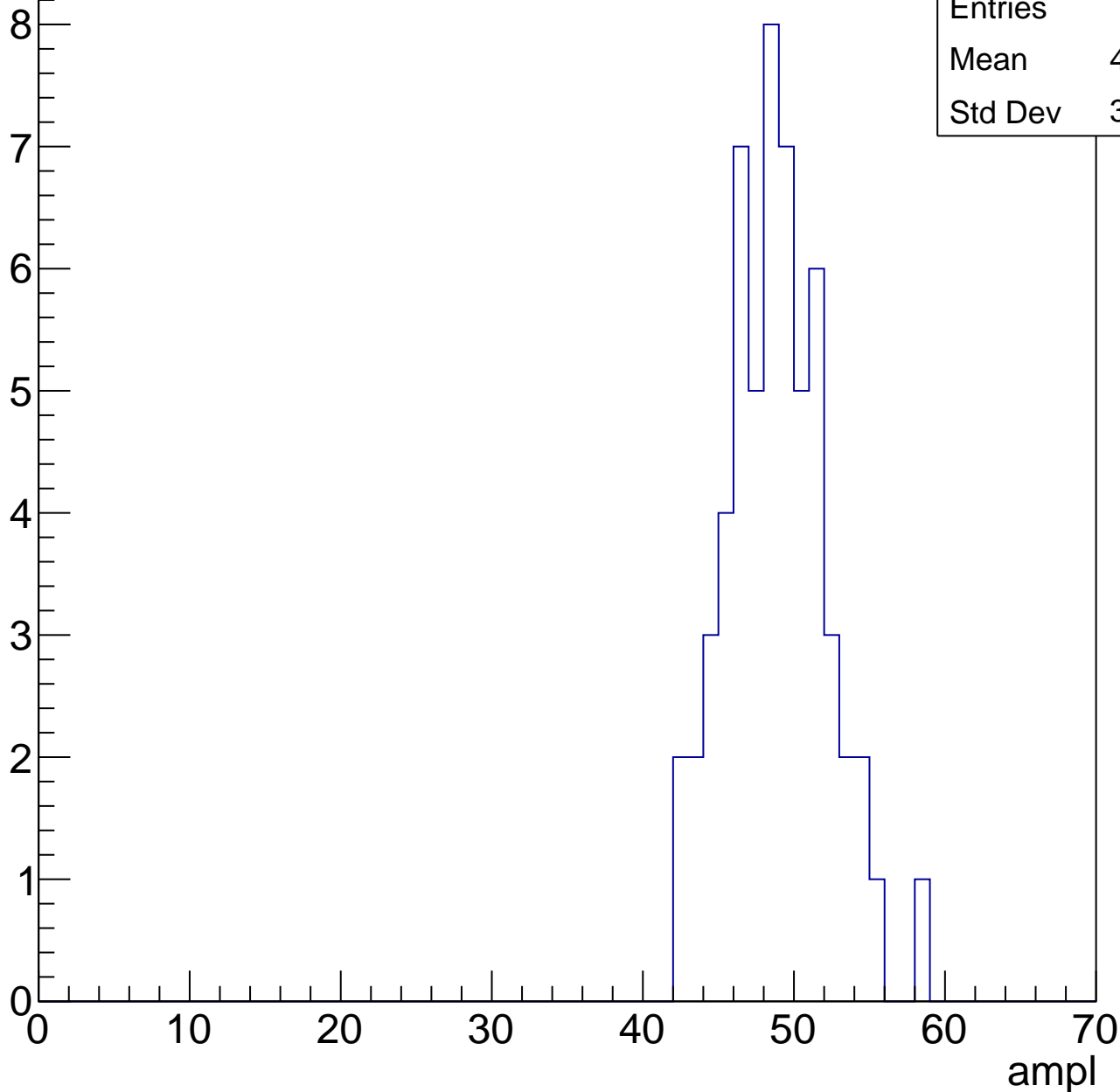


B1L102S, U2-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

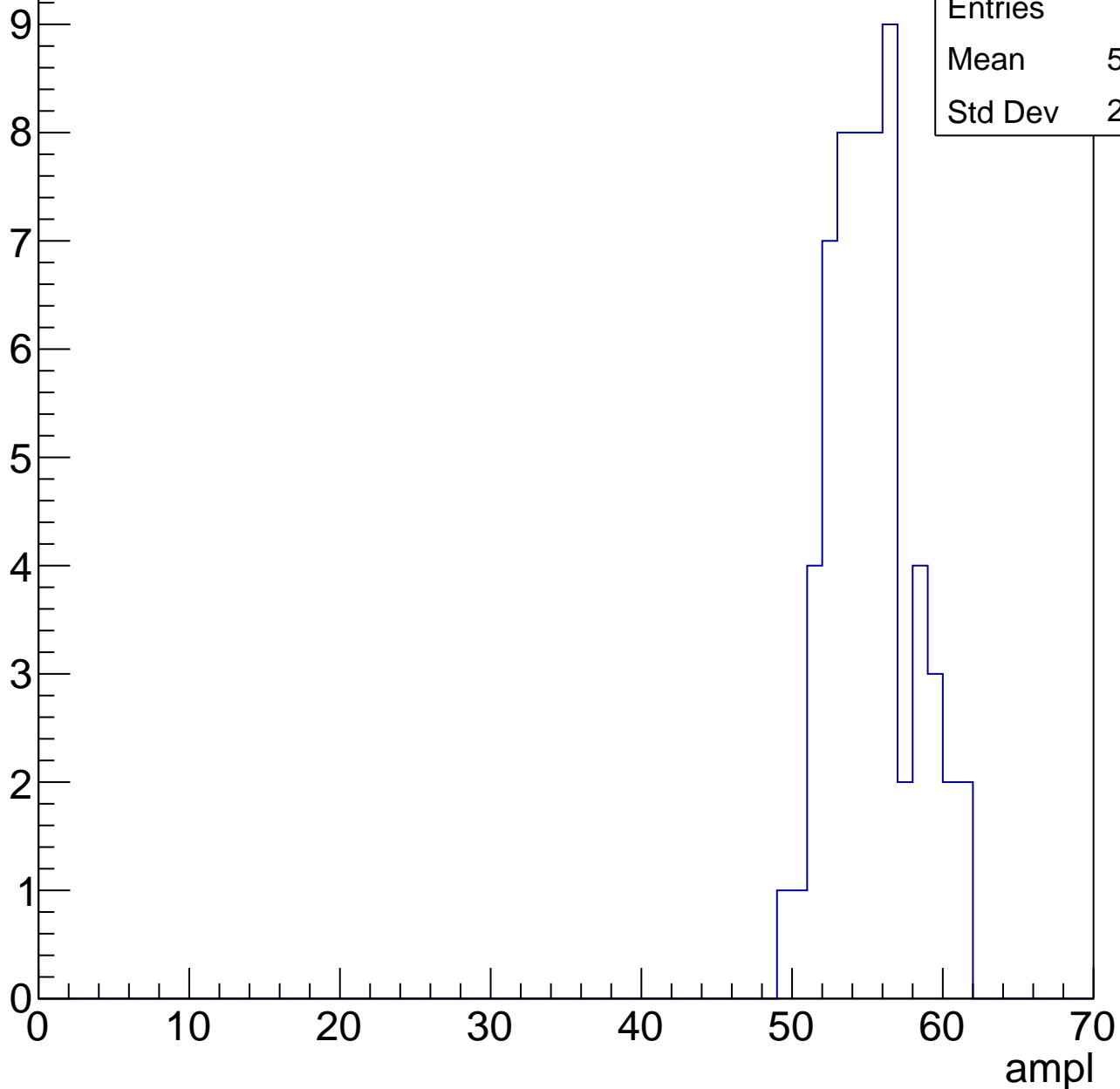
Entries	58
Mean	48.36
Std Dev	3.299



B1L102S, U2-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

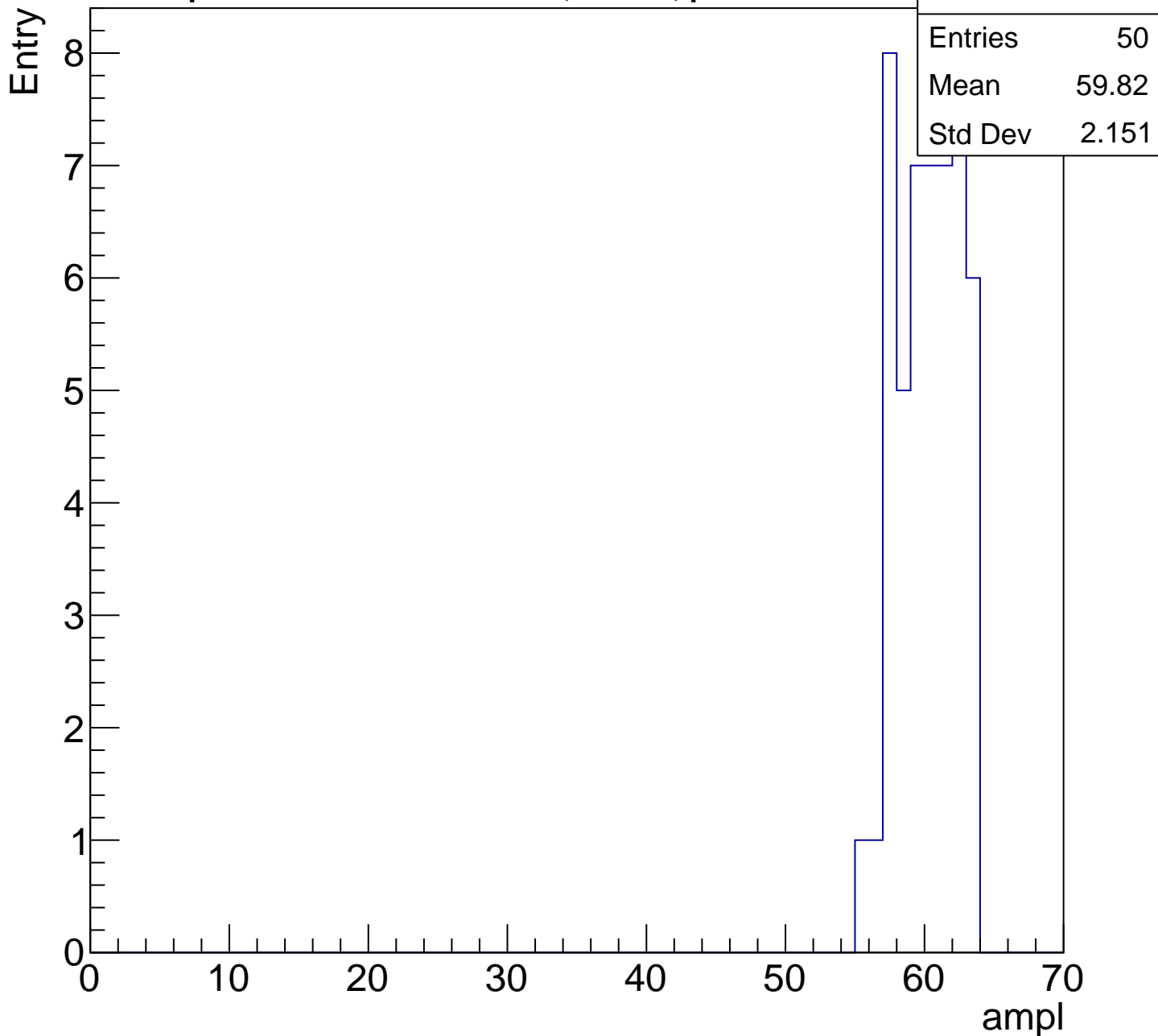
Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.82
Std Dev	2.151

ampl

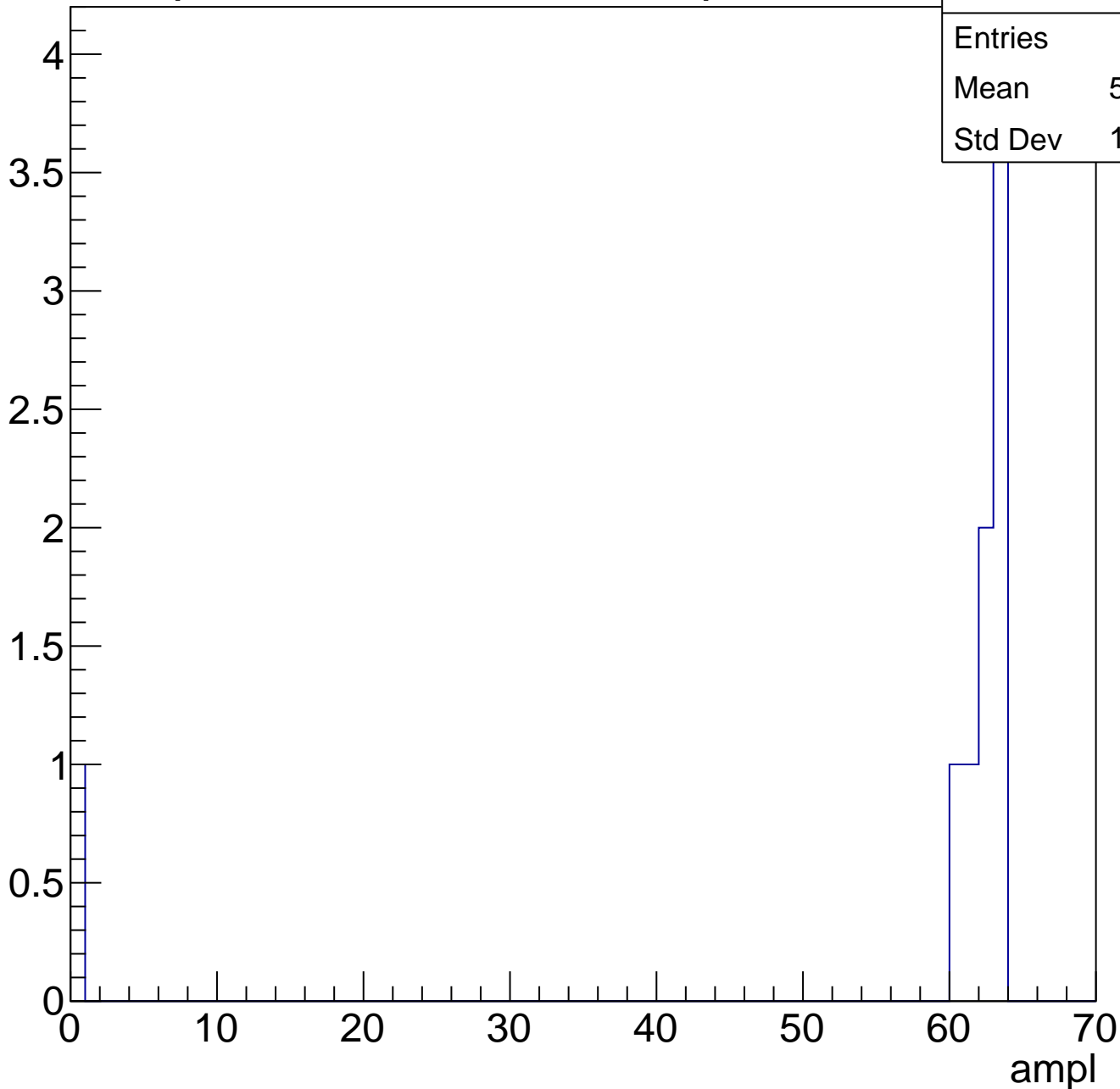
0 10 20 30 40 50 60 70



B1L102S, U2-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch34, adc0

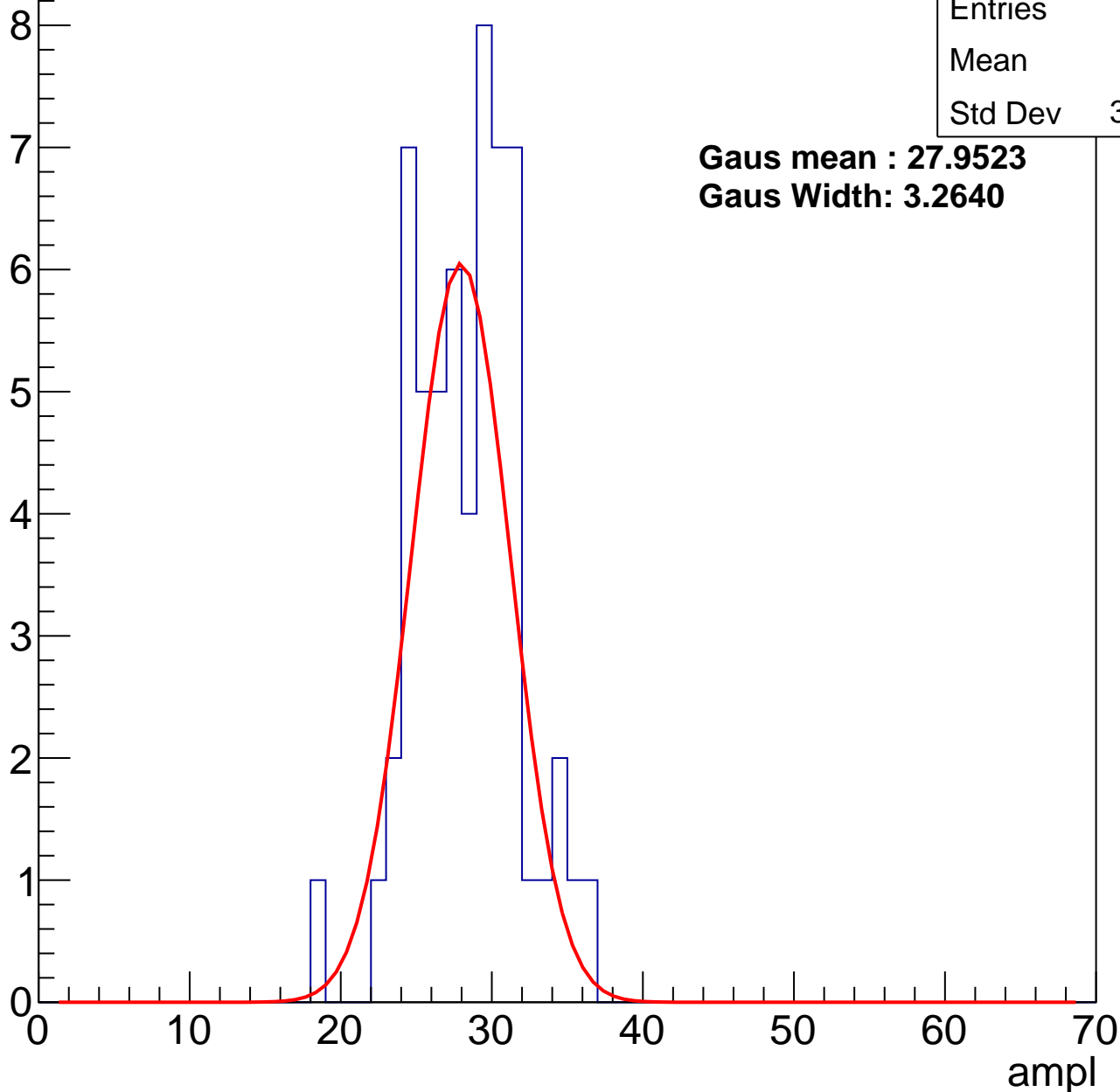
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.9
Std Dev	3.428

Gaus mean : 27.9523

Gaus Width: 3.2640



B1L102S, U2-ch34, adc1

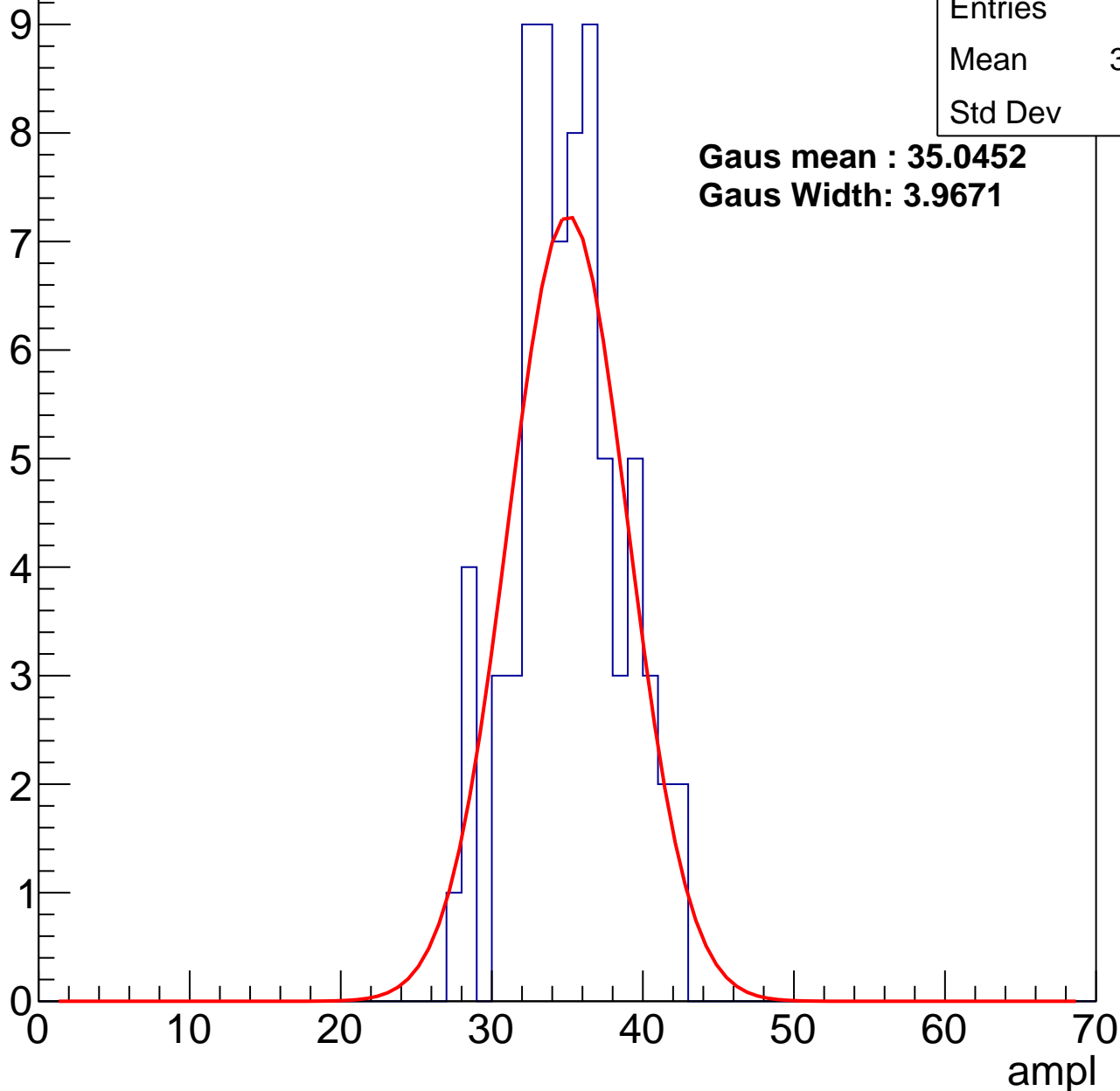
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.64
Std Dev	3.47

Gaus mean : 35.0452

Gaus Width: 3.9671



B1L102S, U2-ch34, adc2

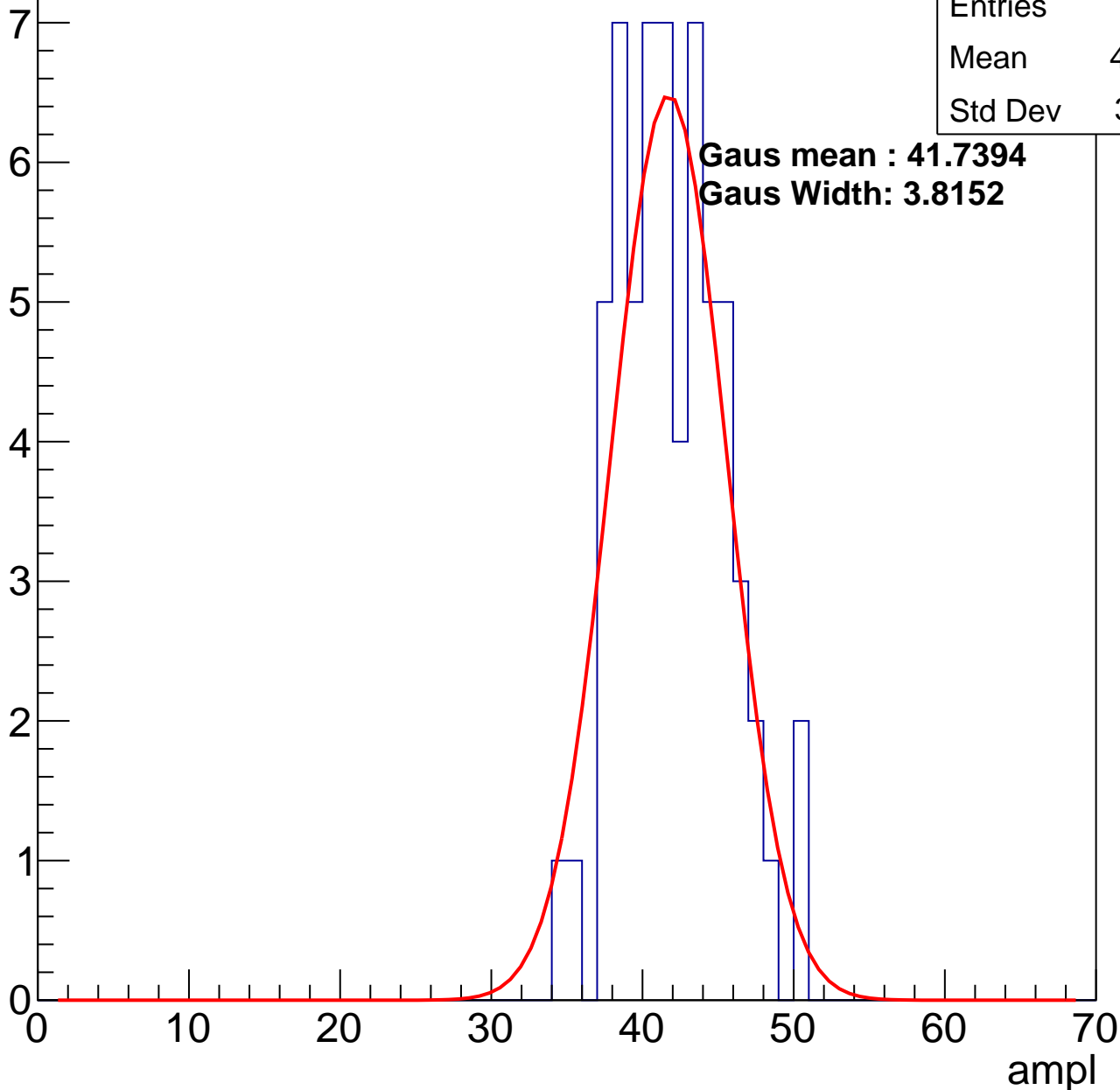
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.55
Std Dev	3.481

Gaus mean : 41.7394

Gaus Width: 3.8152

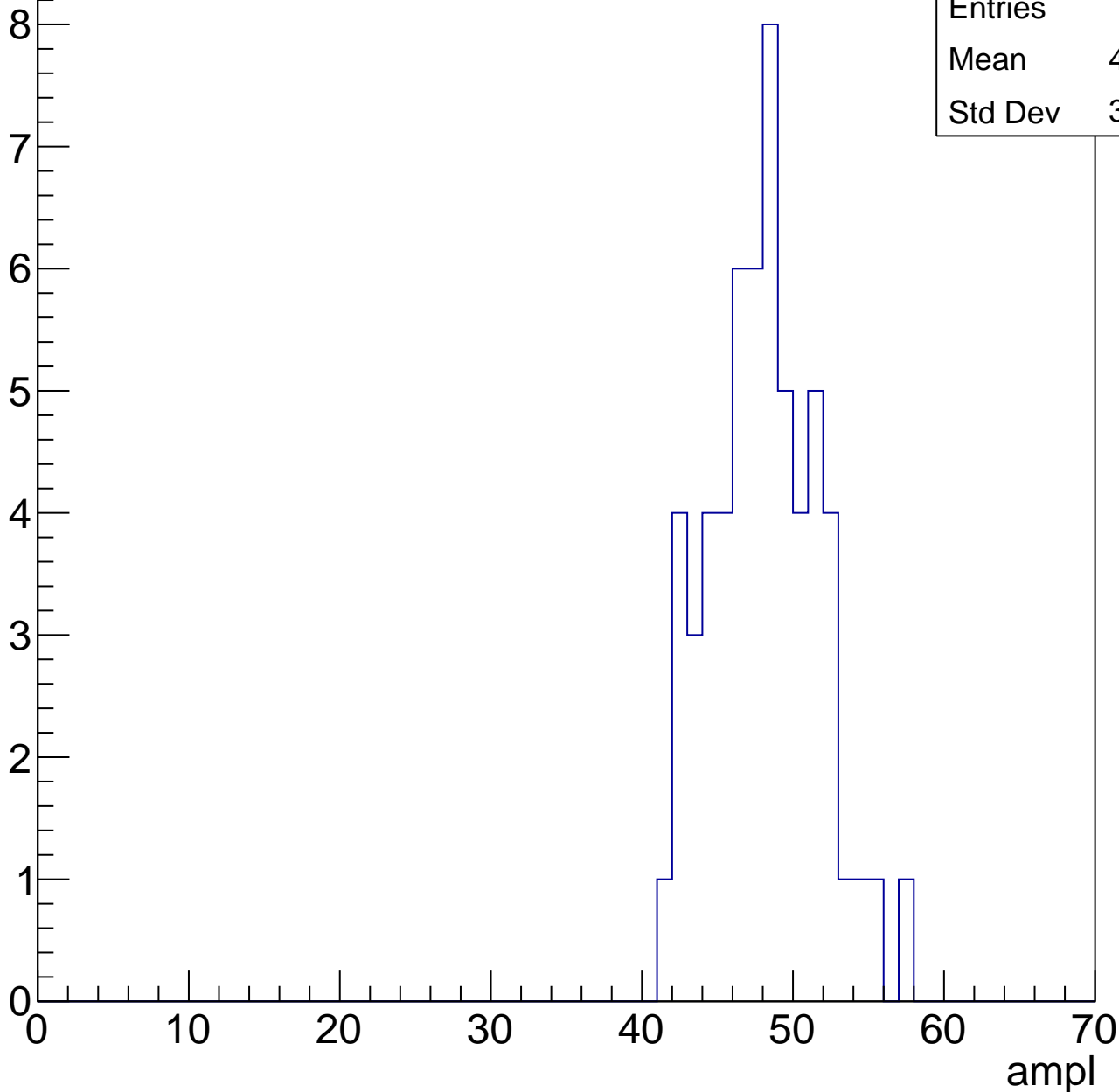


B1L102S, U2-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

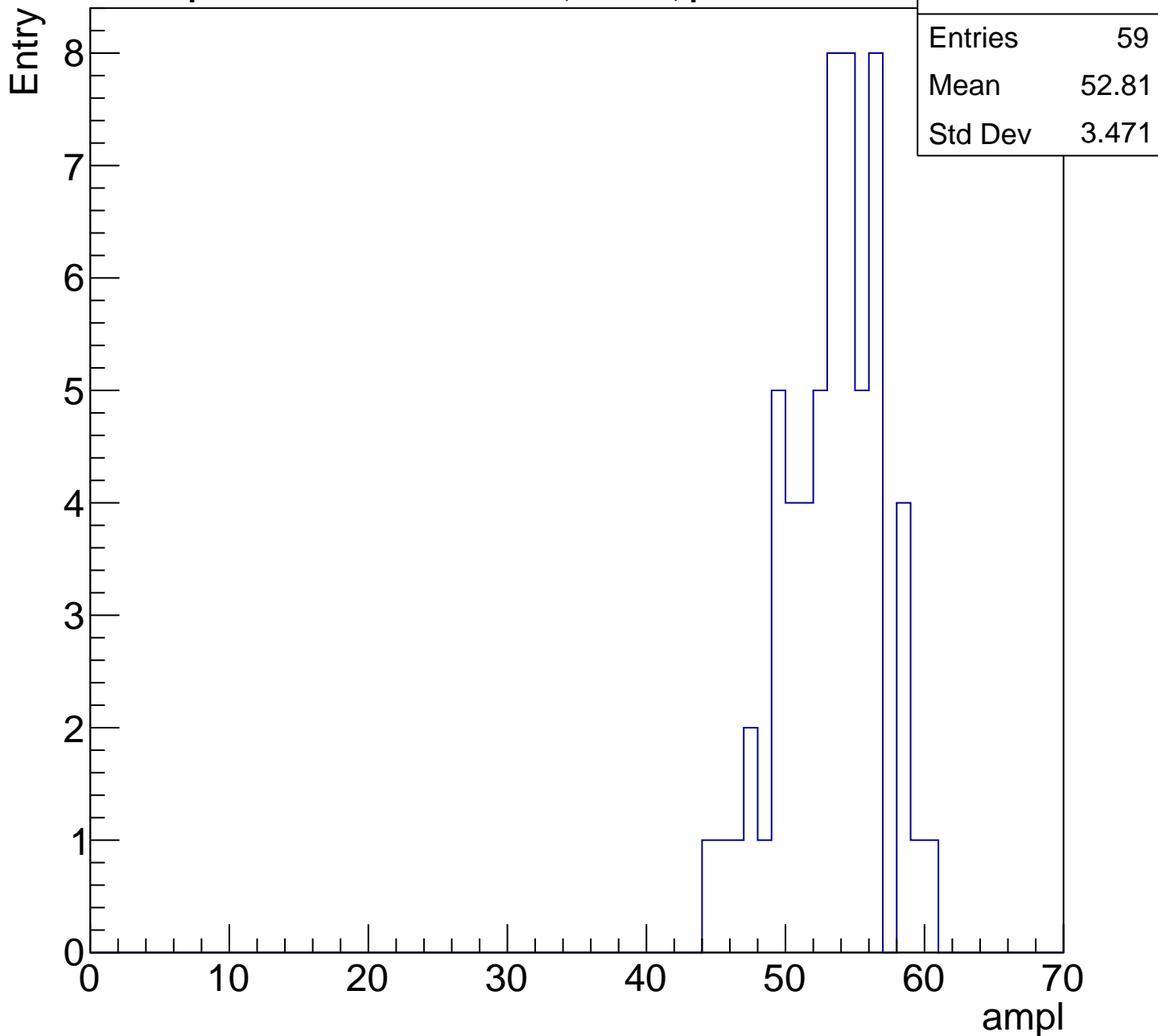
Entry

Entries	58
Mean	47.64
Std Dev	3.502



B1L102S, U2-ch34, adc4

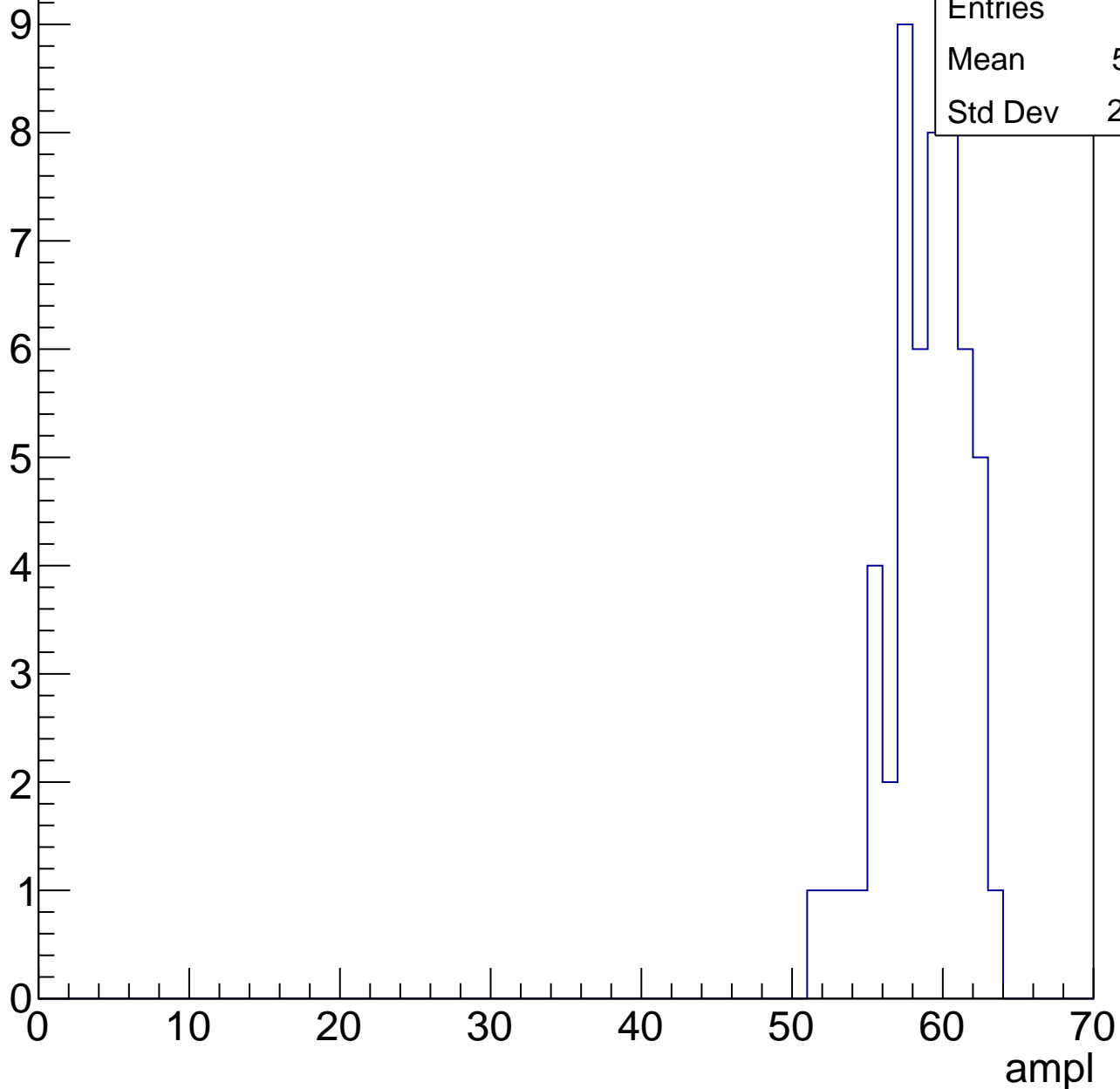
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



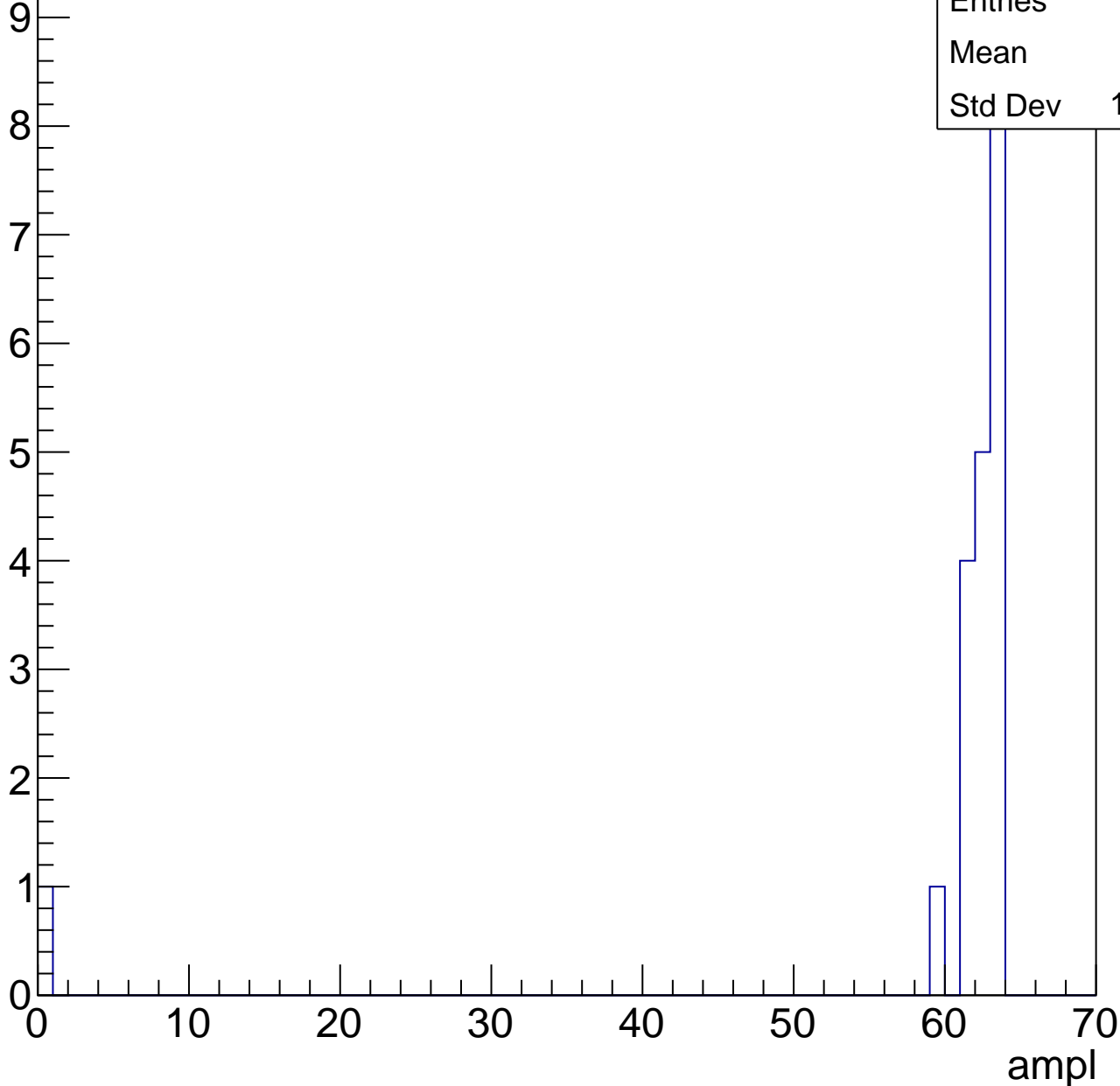
Entries	54
Mean	58.41
Std Dev	2.628

B1L102S, U2-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	59
Std Dev	13.58



B1L102S, U2-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch35, adc0

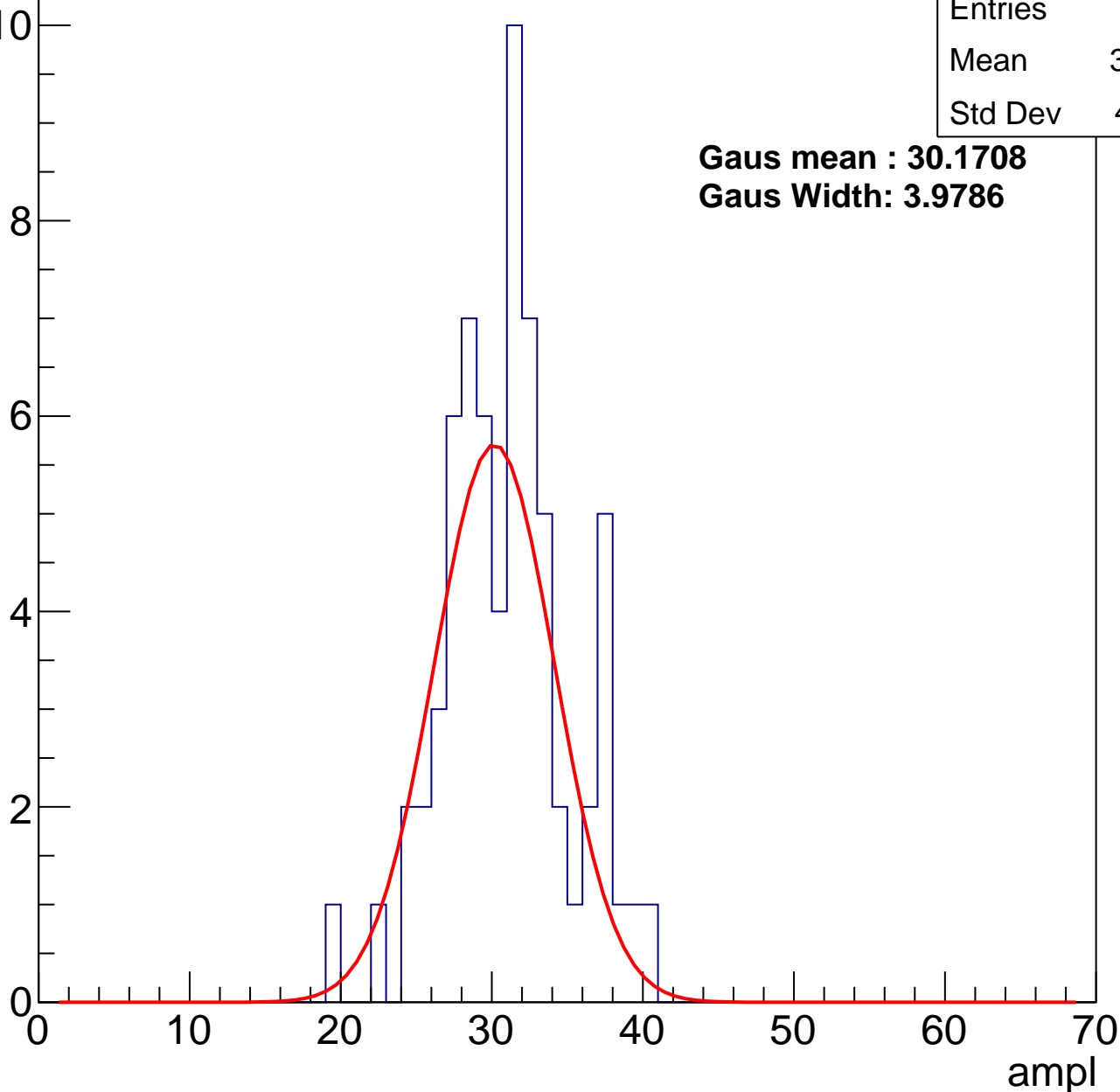
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.52
Std Dev	4.101

Gaus mean : 30.1708

Gaus Width: 3.9786



B1L102S, U2-ch35, adc1

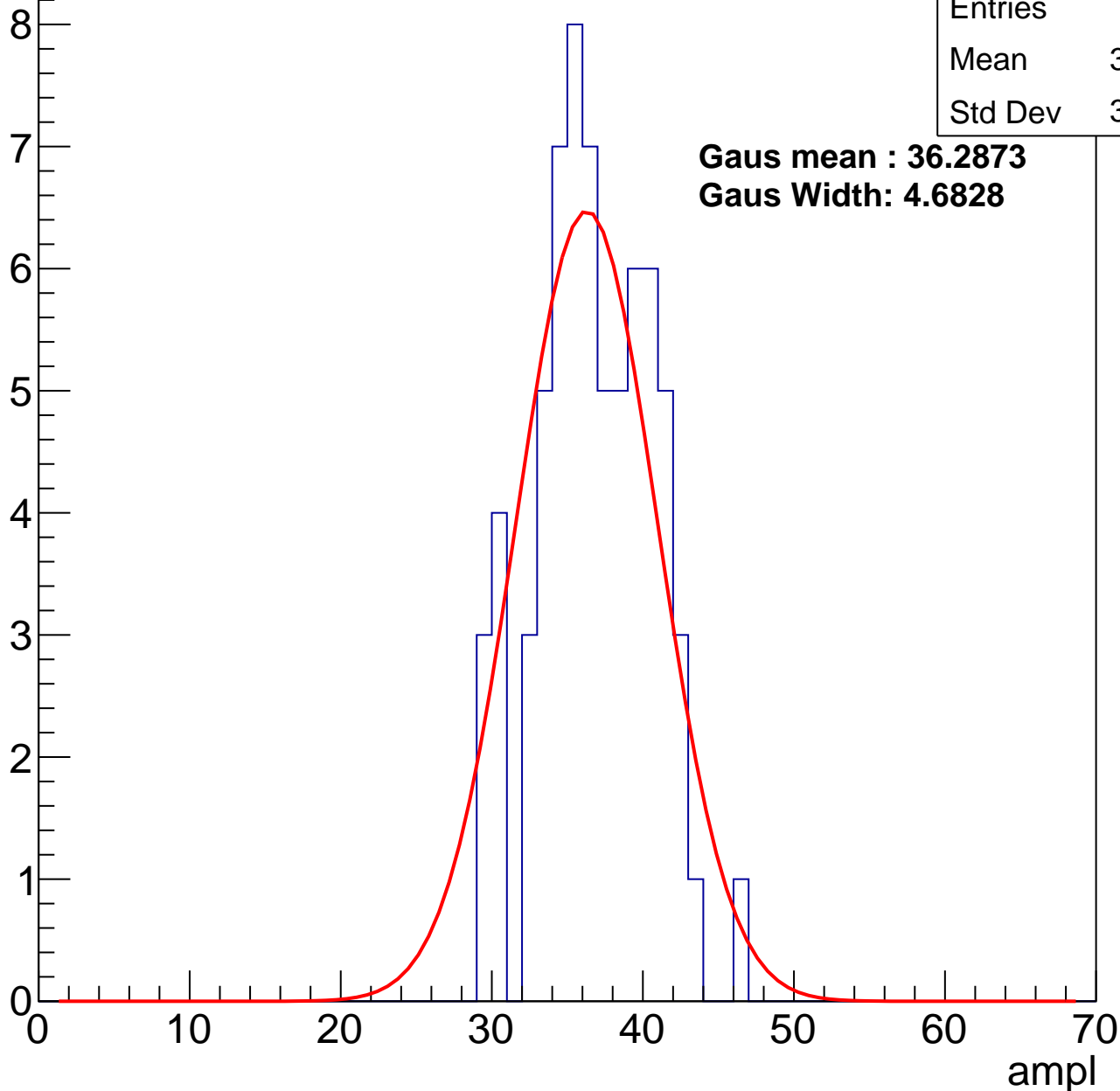
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.33
Std Dev	3.748

Gaus mean : 36.2873

Gaus Width: 4.6828



B1L102S, U2-ch35, adc2

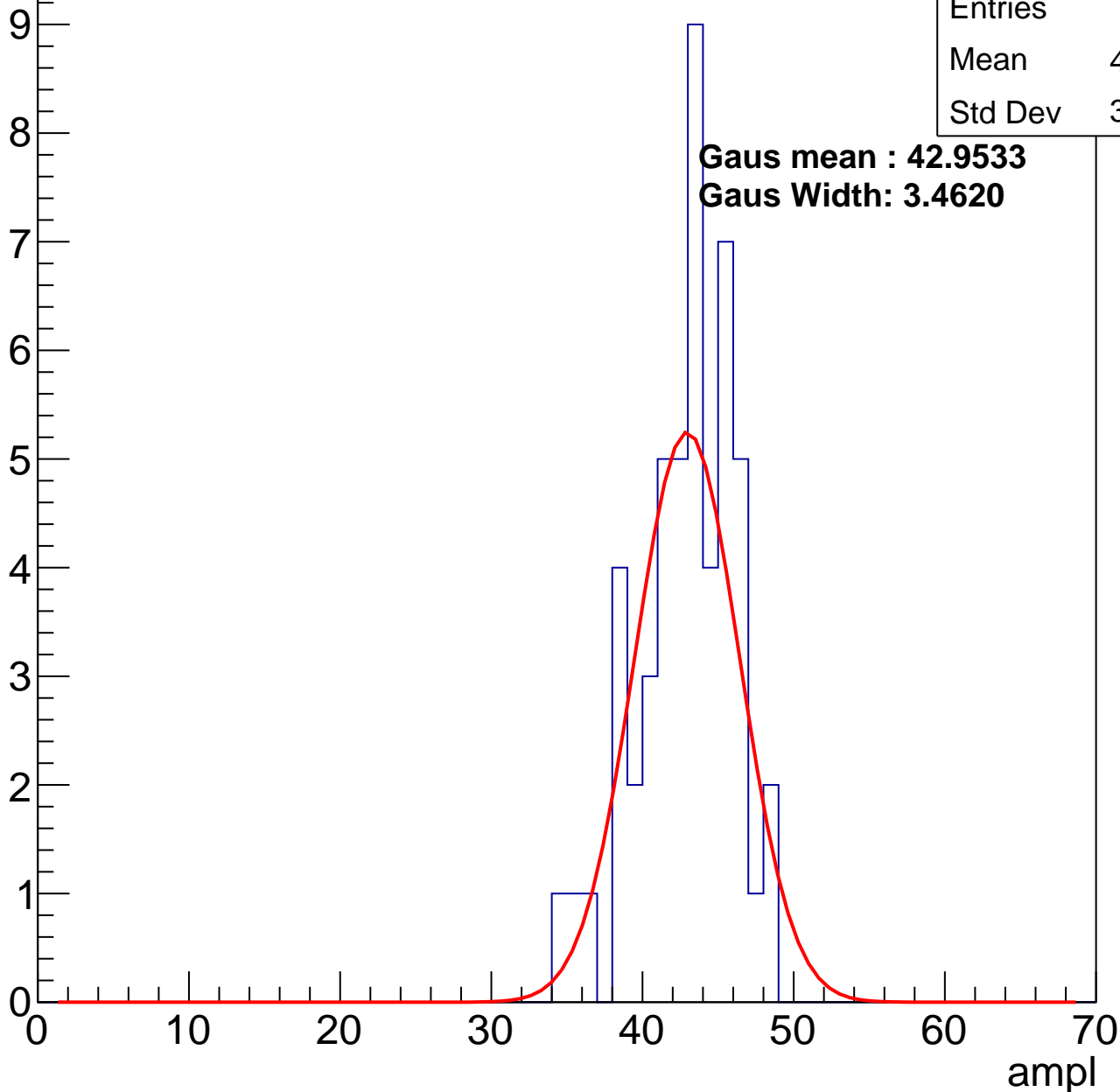
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	42.42
Std Dev	3.175

Gaus mean : 42.9533

Gaus Width: 3.4620

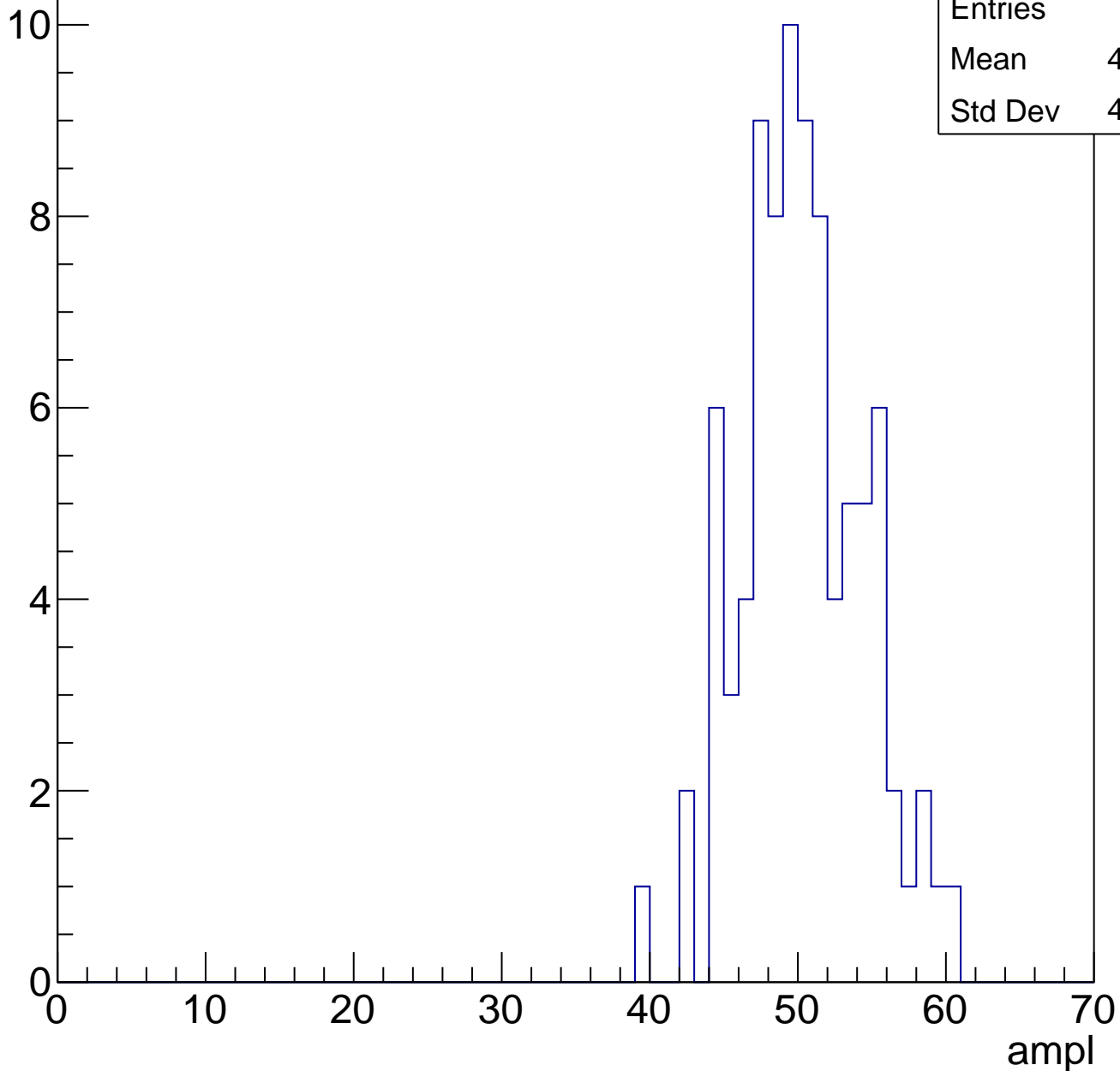


B1L102S, U2-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	49.86
Std Dev	4.089

Entry

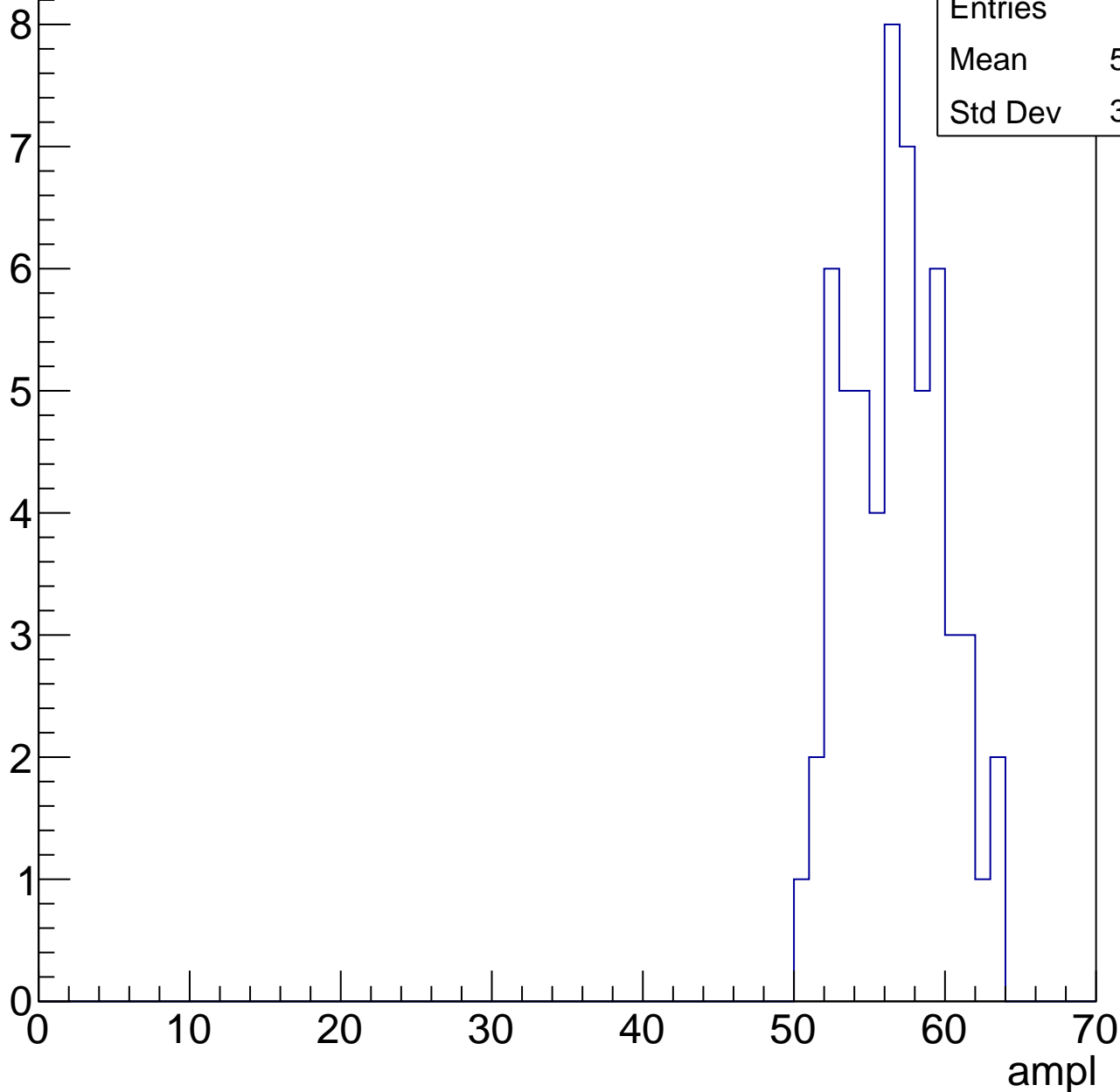


B1L102S, U2-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.22
Std Dev	3.179

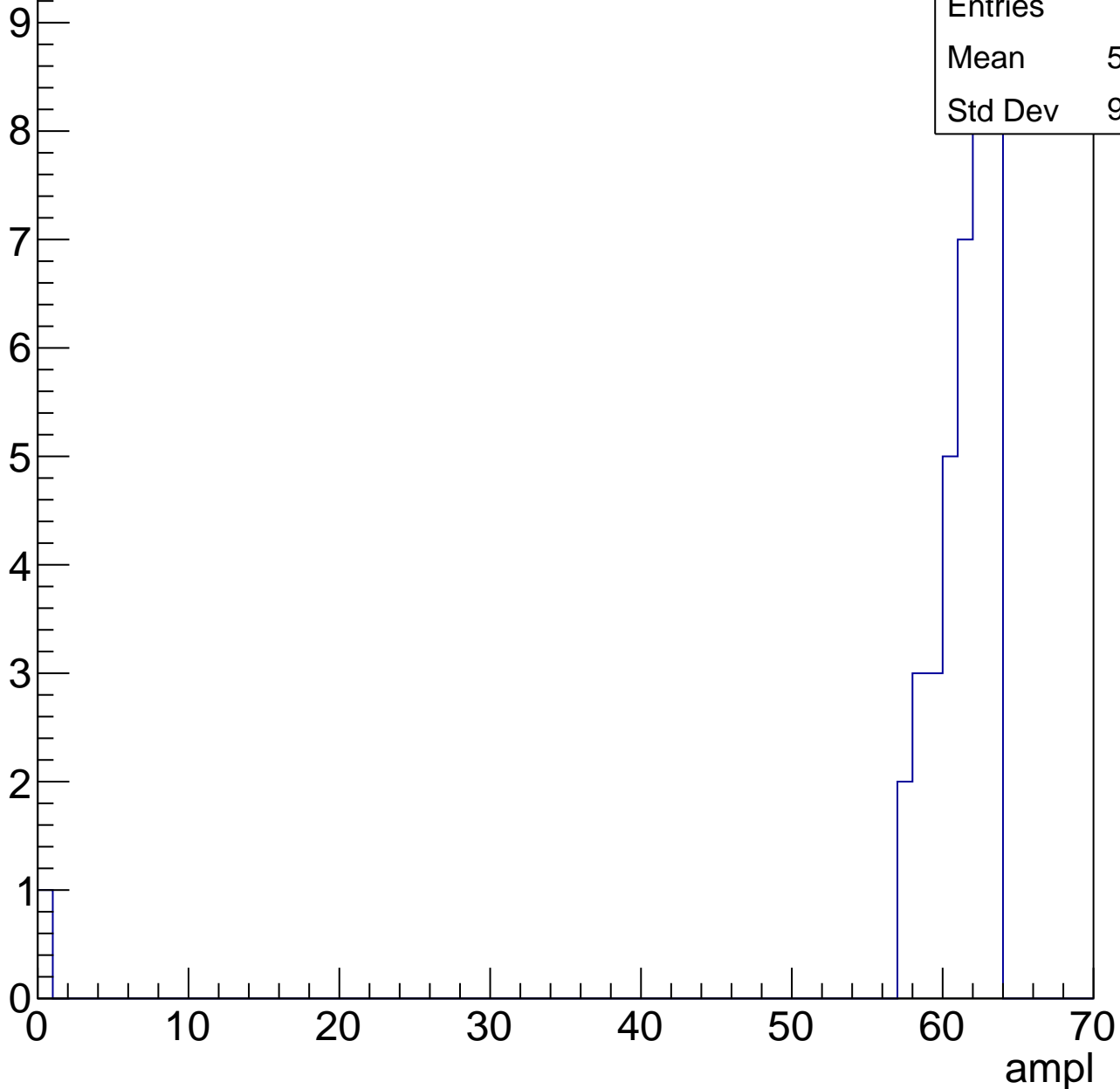


B1L102S, U2-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.32
Std Dev	9.908



B1L102S, U2-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch36, adc0

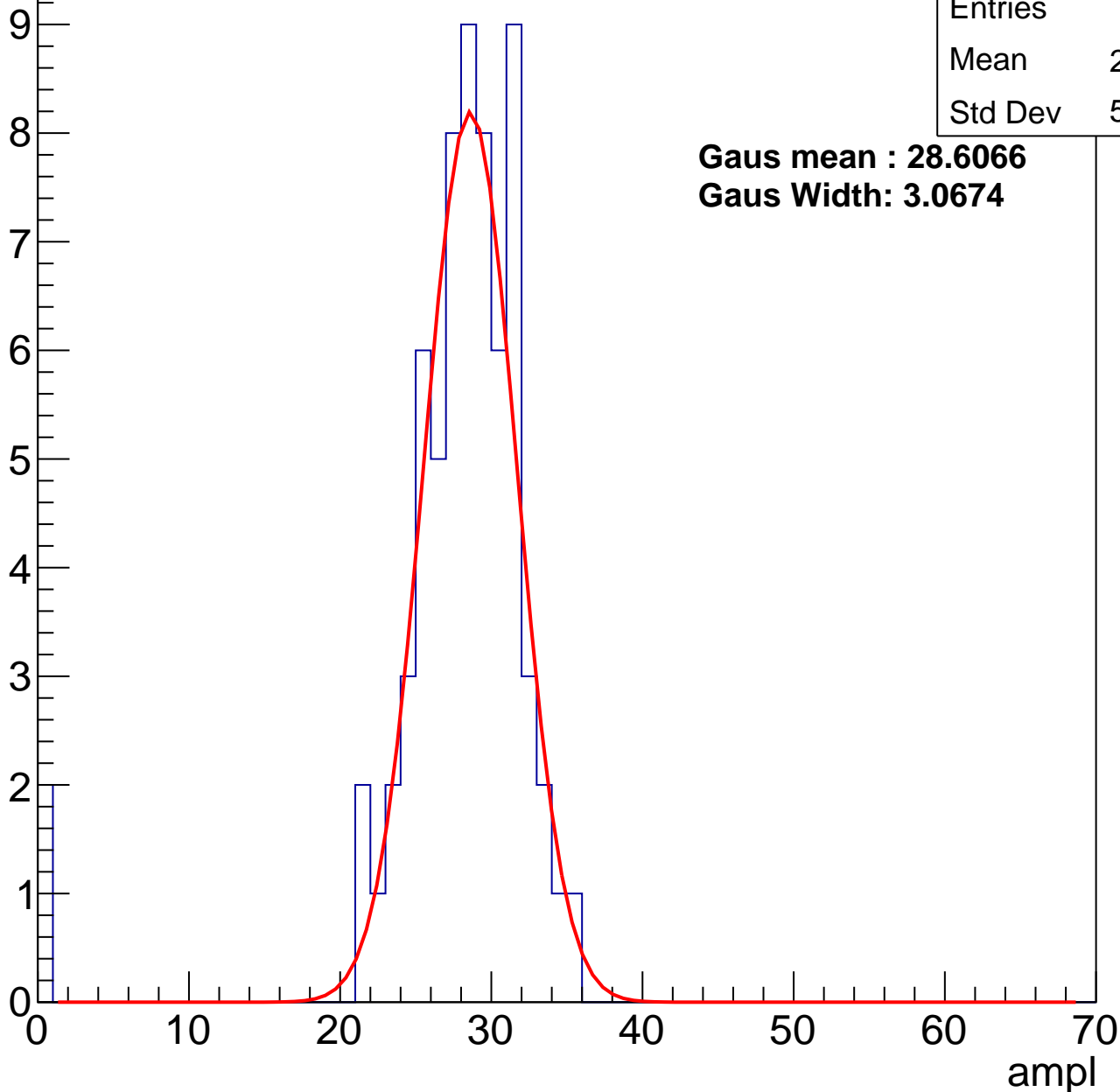
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.24
Std Dev	5.608

Gaus mean : 28.6066

Gaus Width: 3.0674



B1L102S, U2-ch36, adc1

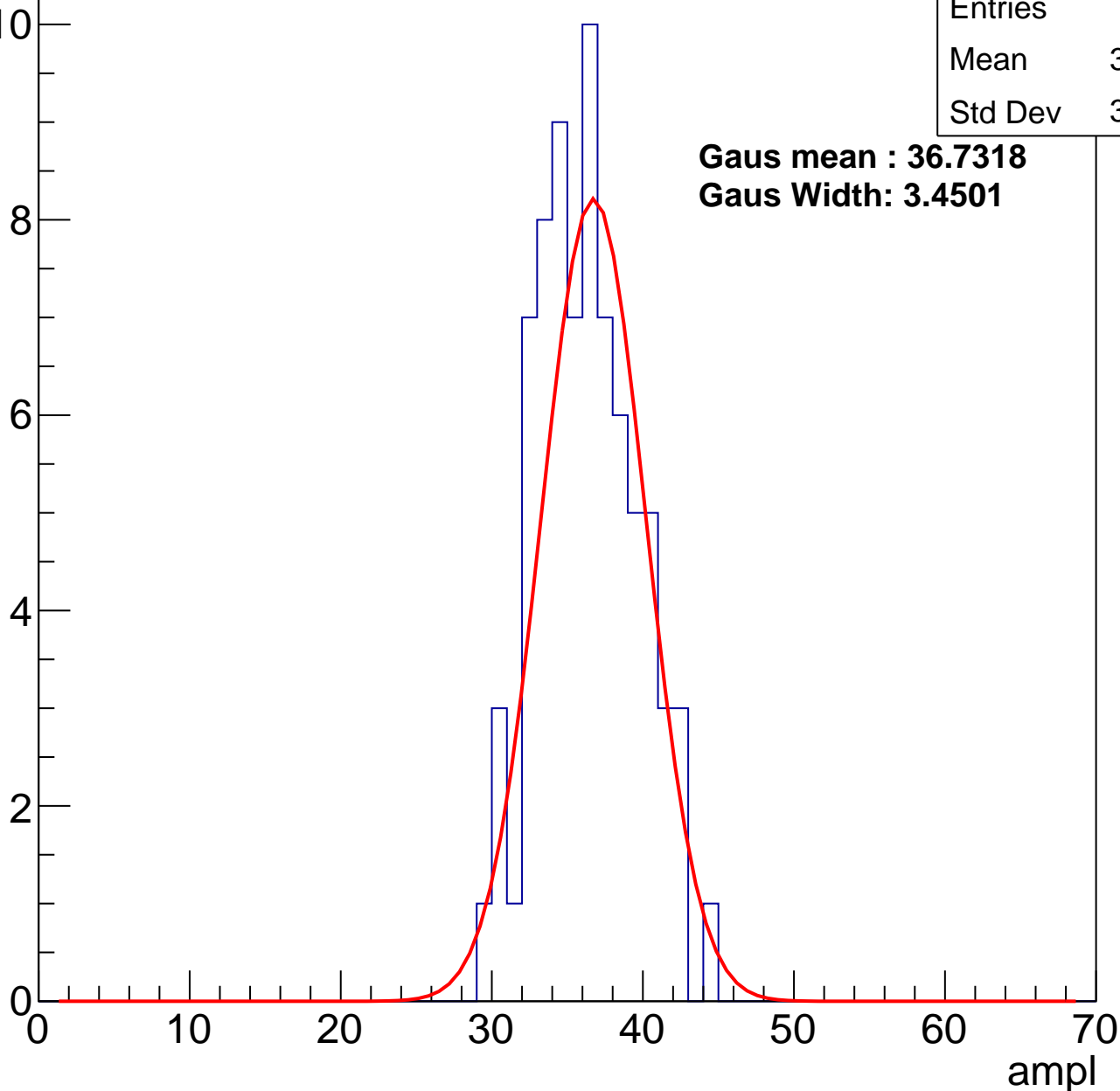
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.84
Std Dev	3.265

Gaus mean : 36.7318

Gaus Width: 3.4501



B1L102S, U2-ch36, adc2

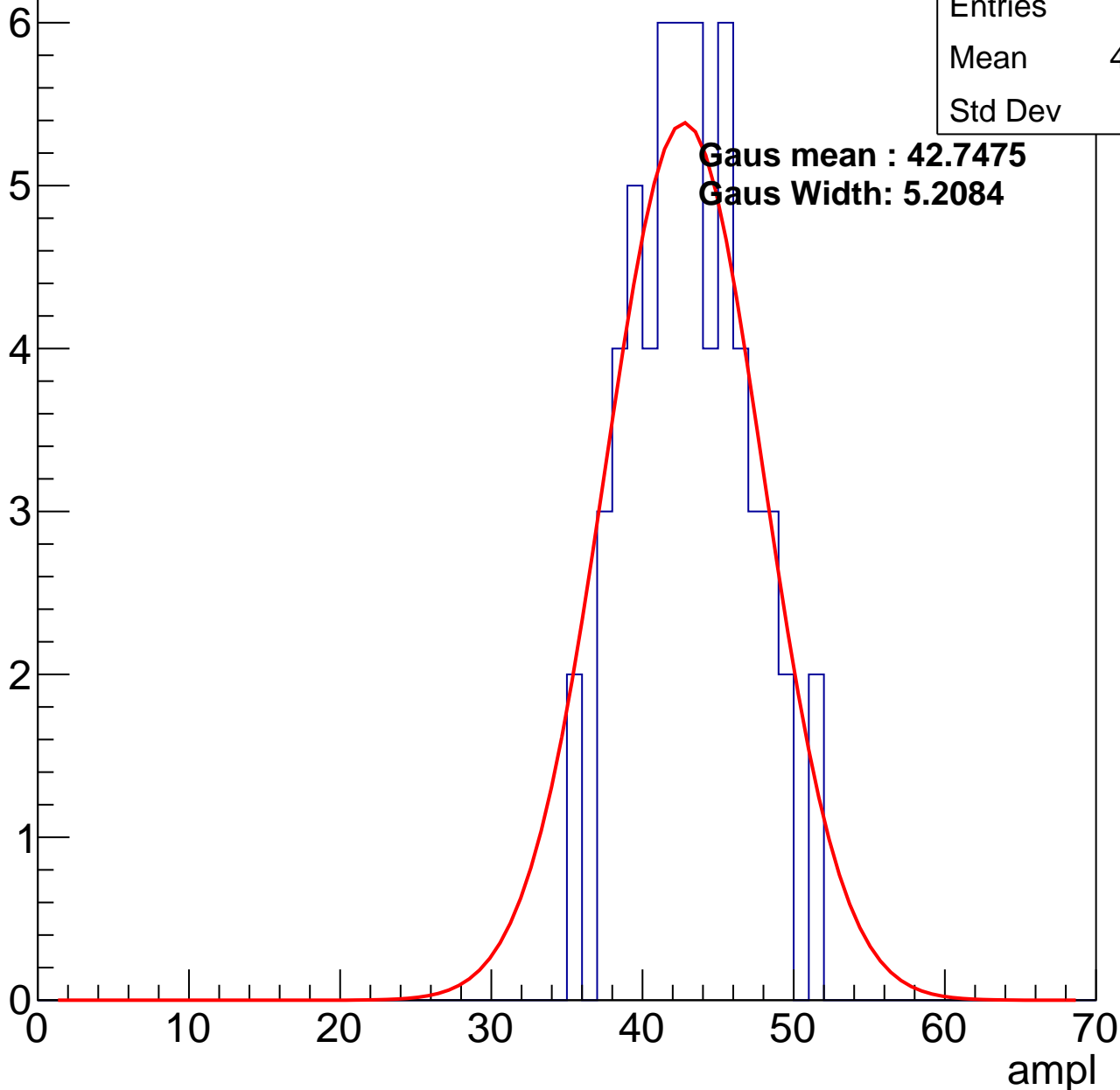
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.65
Std Dev	3.79

Gaus mean : 42.7475

Gaus Width: 5.2084

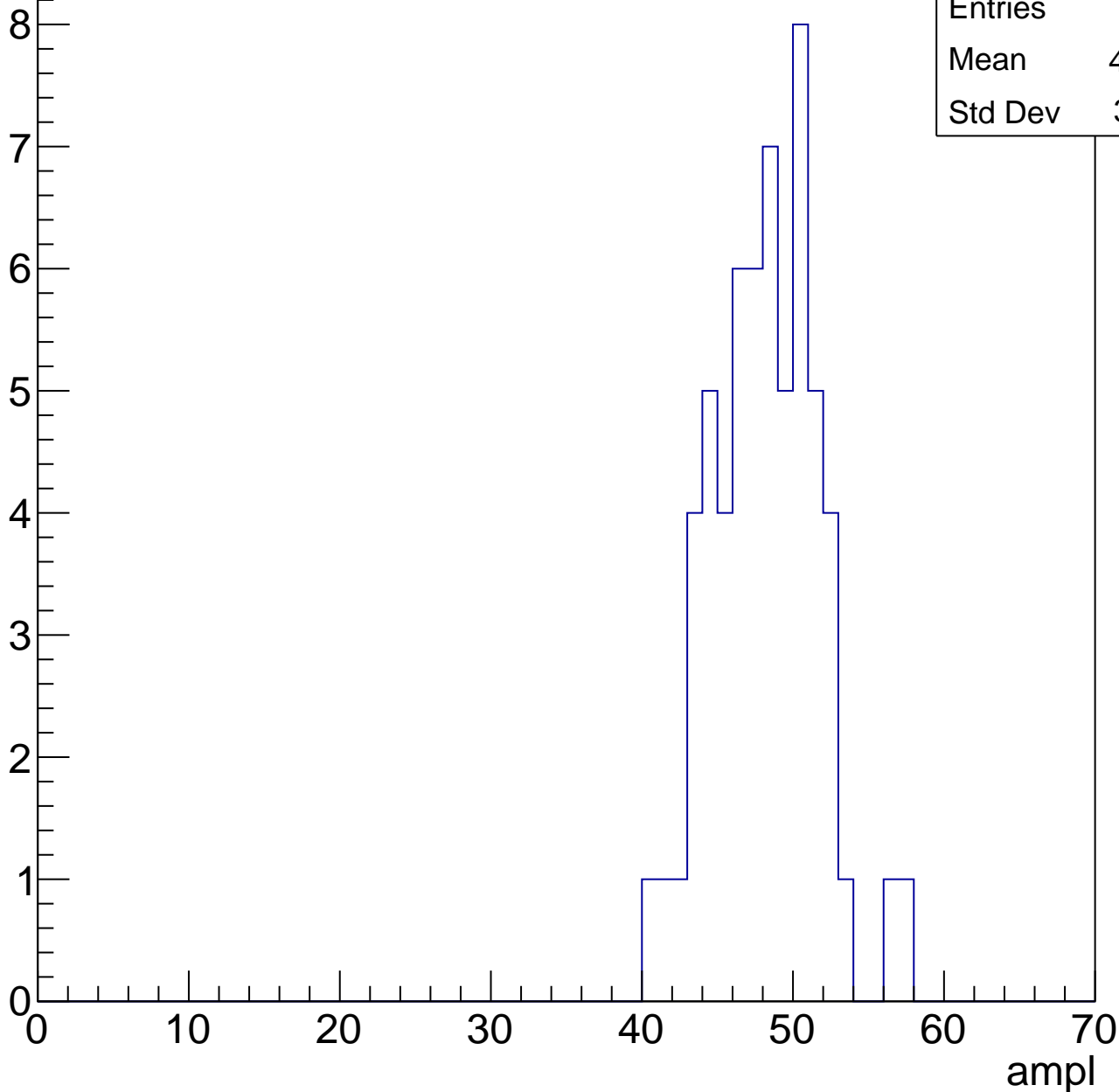


B1L102S, U2-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.72
Std Dev	3.431

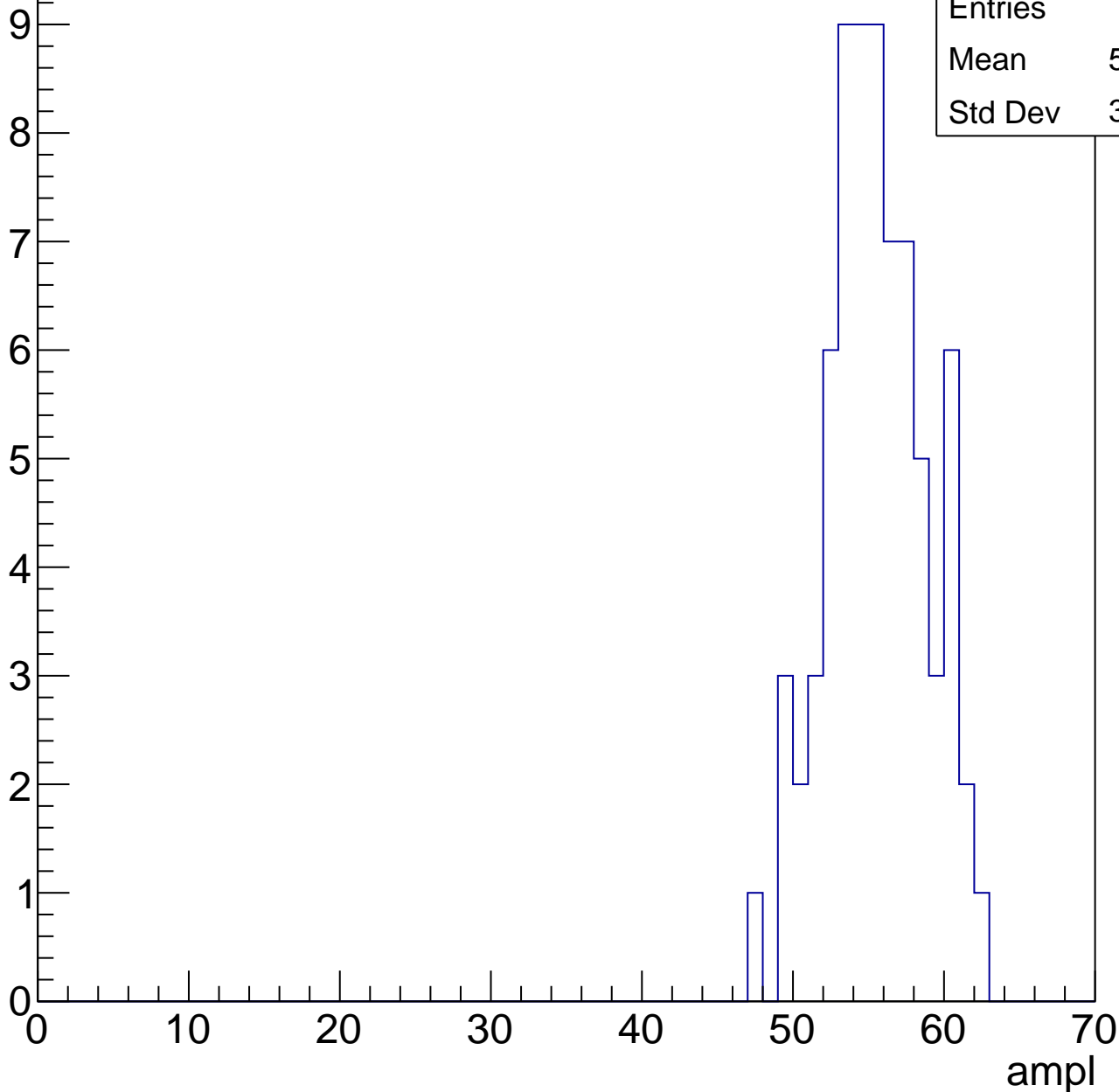


B1L102S, U2-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

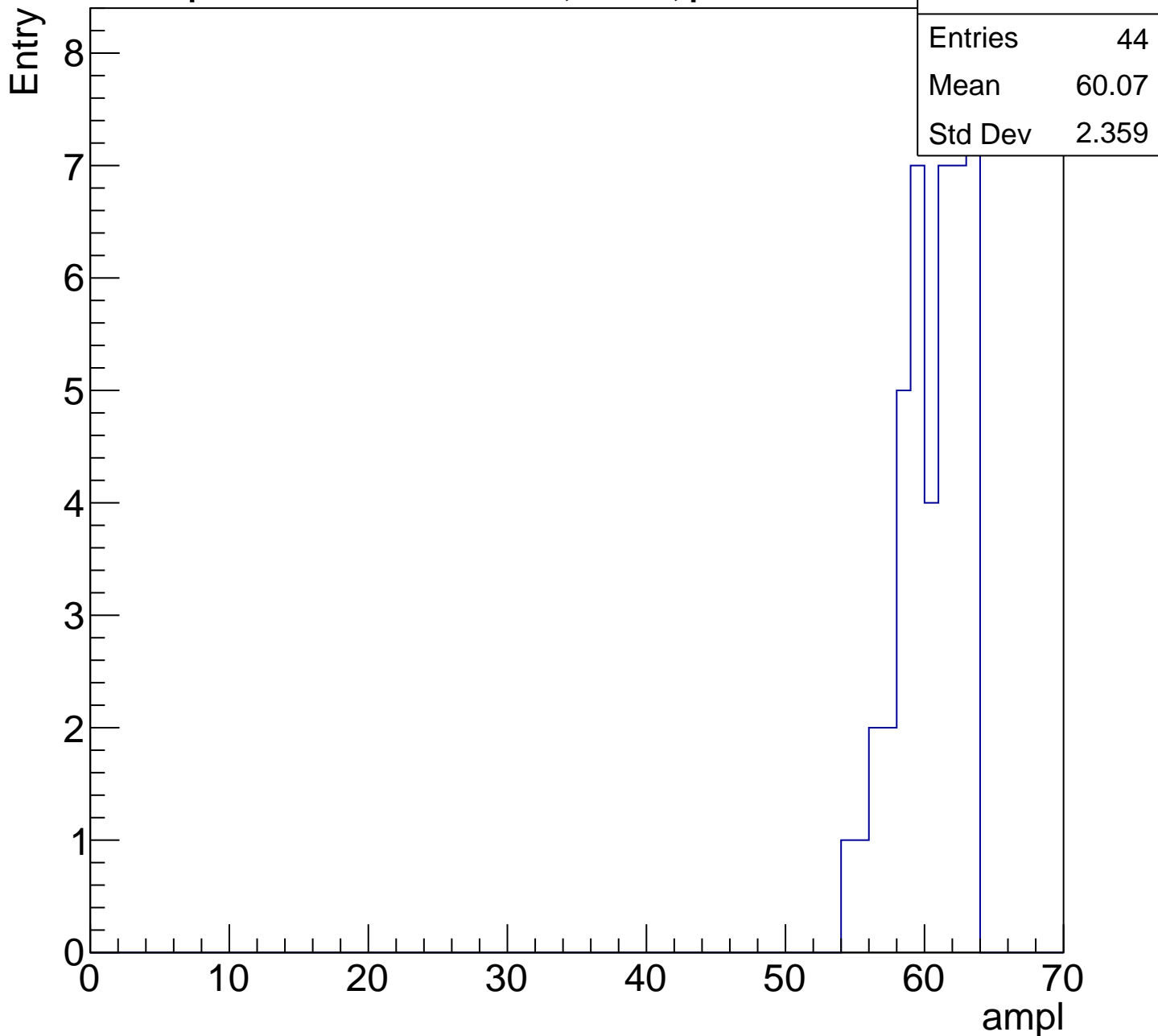
Entry

Entries	73
Mean	55.05
Std Dev	3.243



B1L102S, U2-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch37, adc0

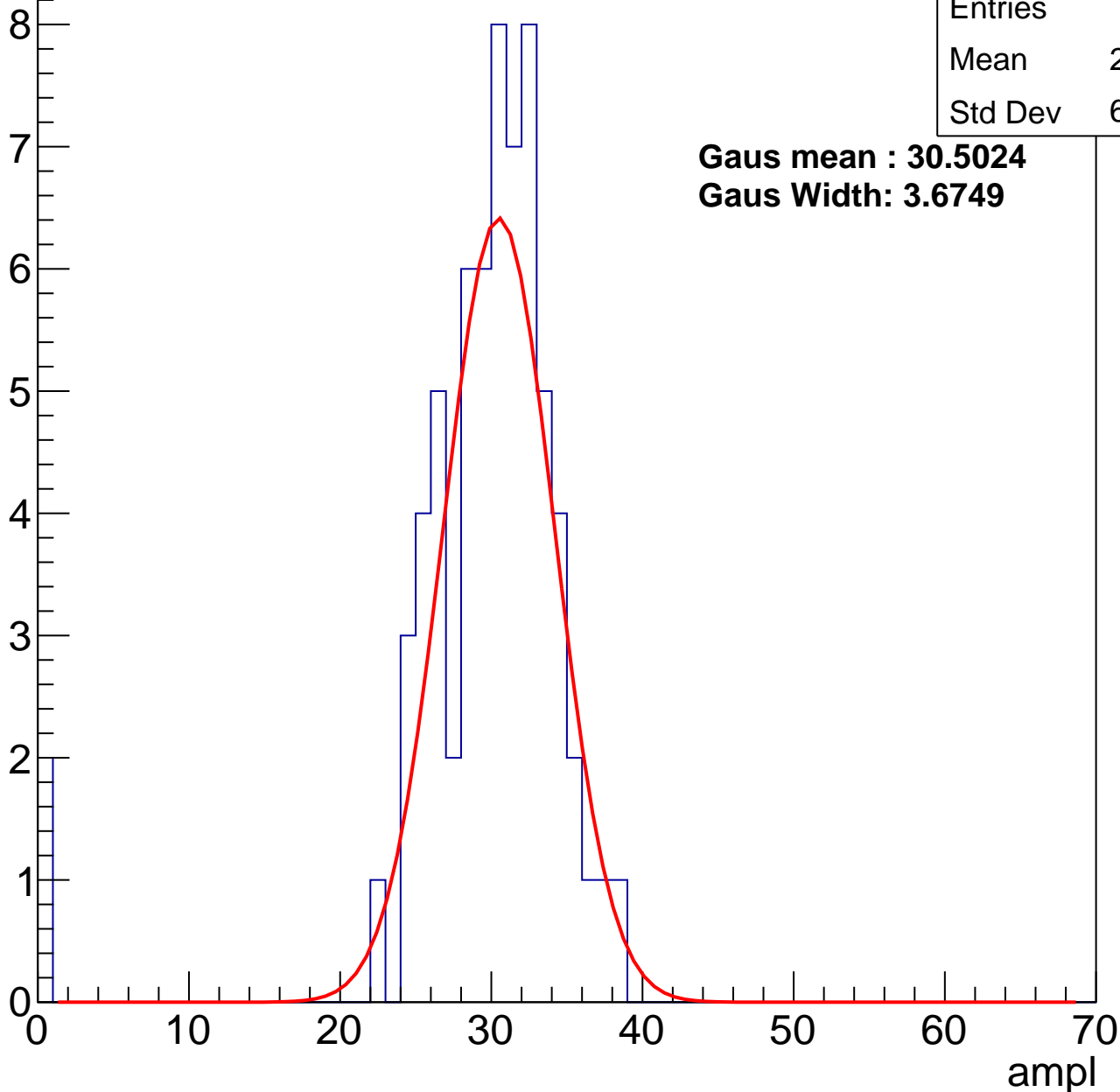
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.02
Std Dev	6.134

Gaus mean : 30.5024

Gaus Width: 3.6749



B1L102S, U2-ch37, adc1

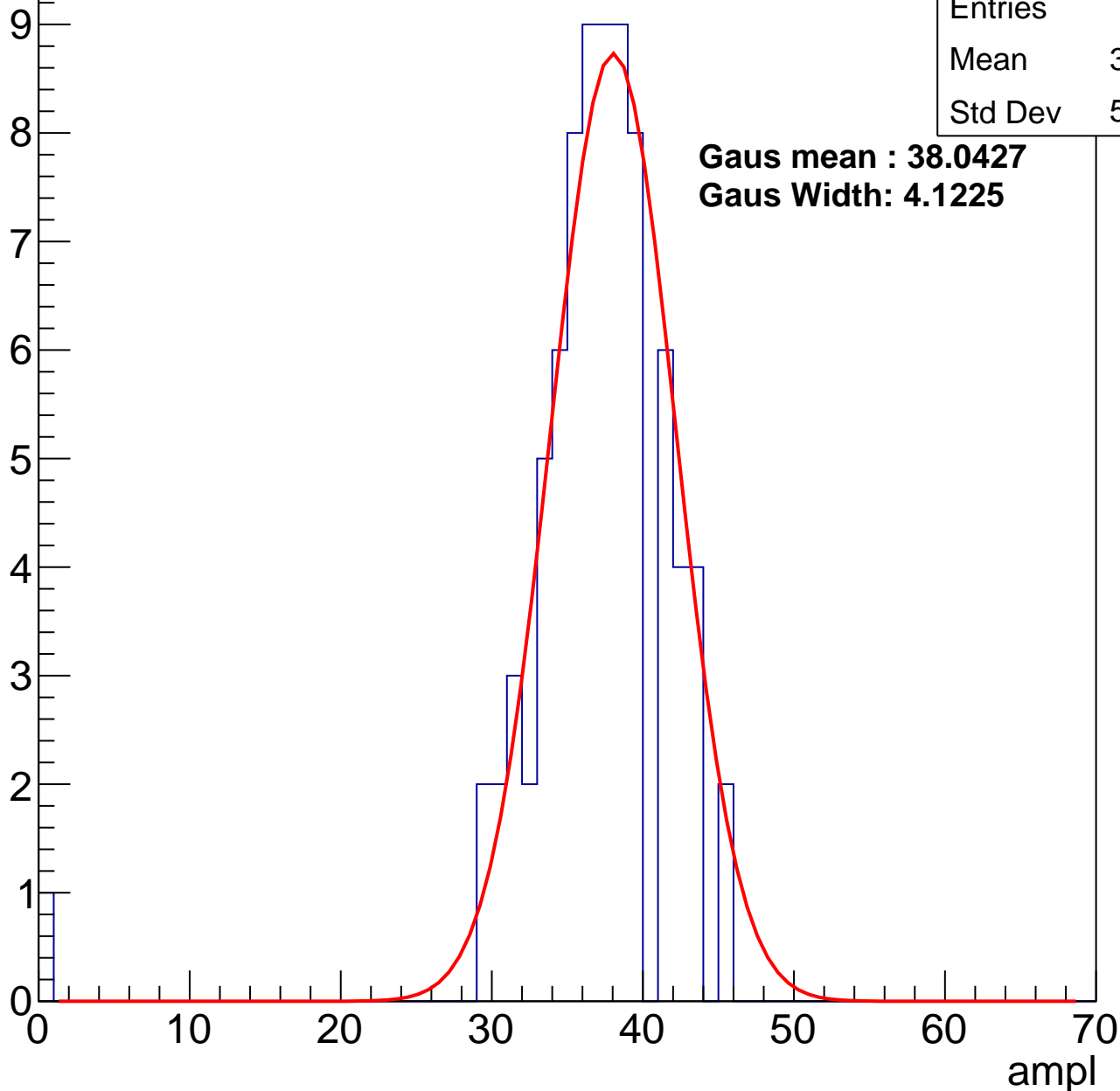
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	36.39
Std Dev	5.485

Gaus mean : 38.0427

Gaus Width: 4.1225



B1L102S, U2-ch37, adc2

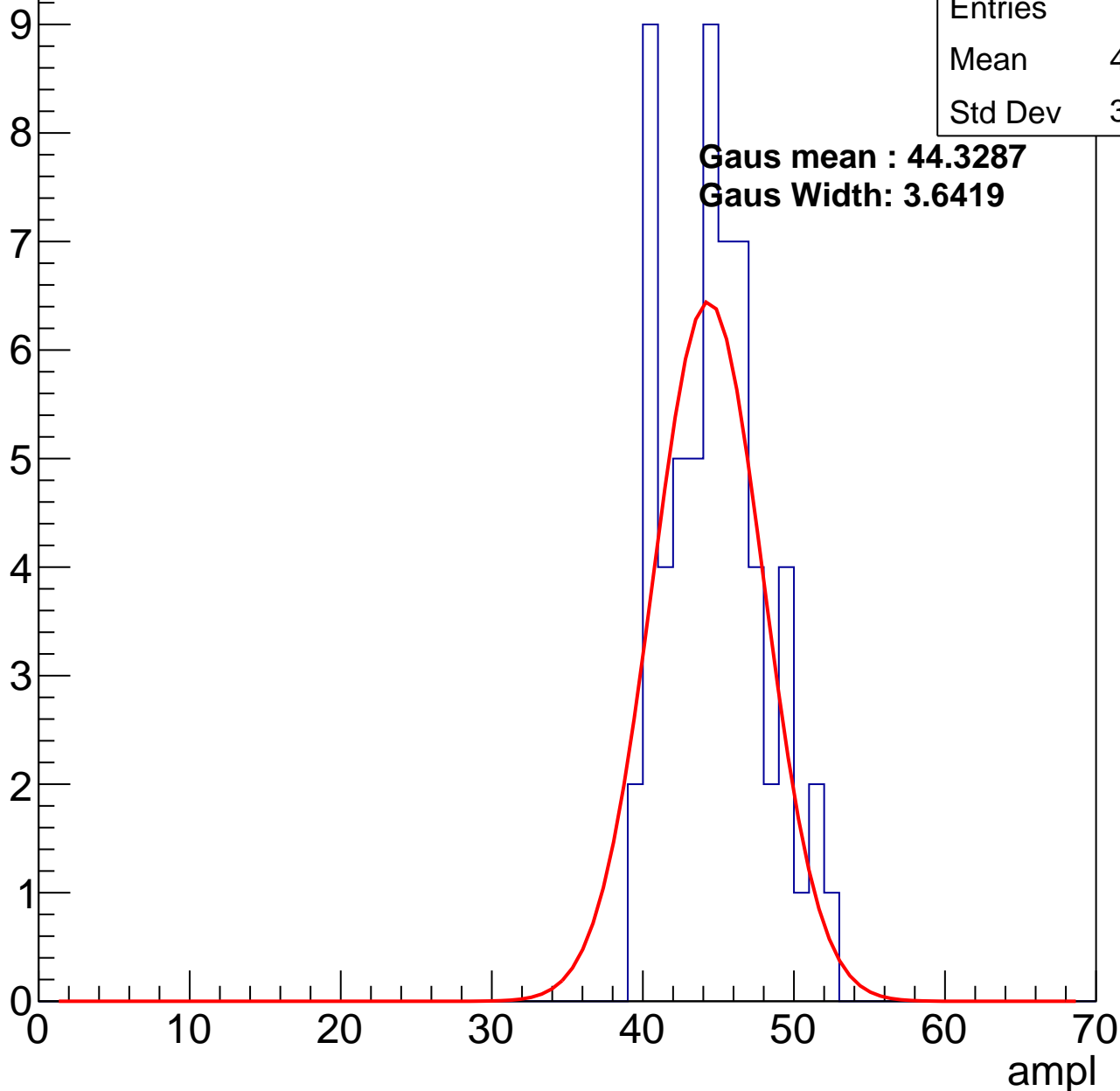
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	44.26
Std Dev	3.243

Gaus mean : 44.3287

Gaus Width: 3.6419

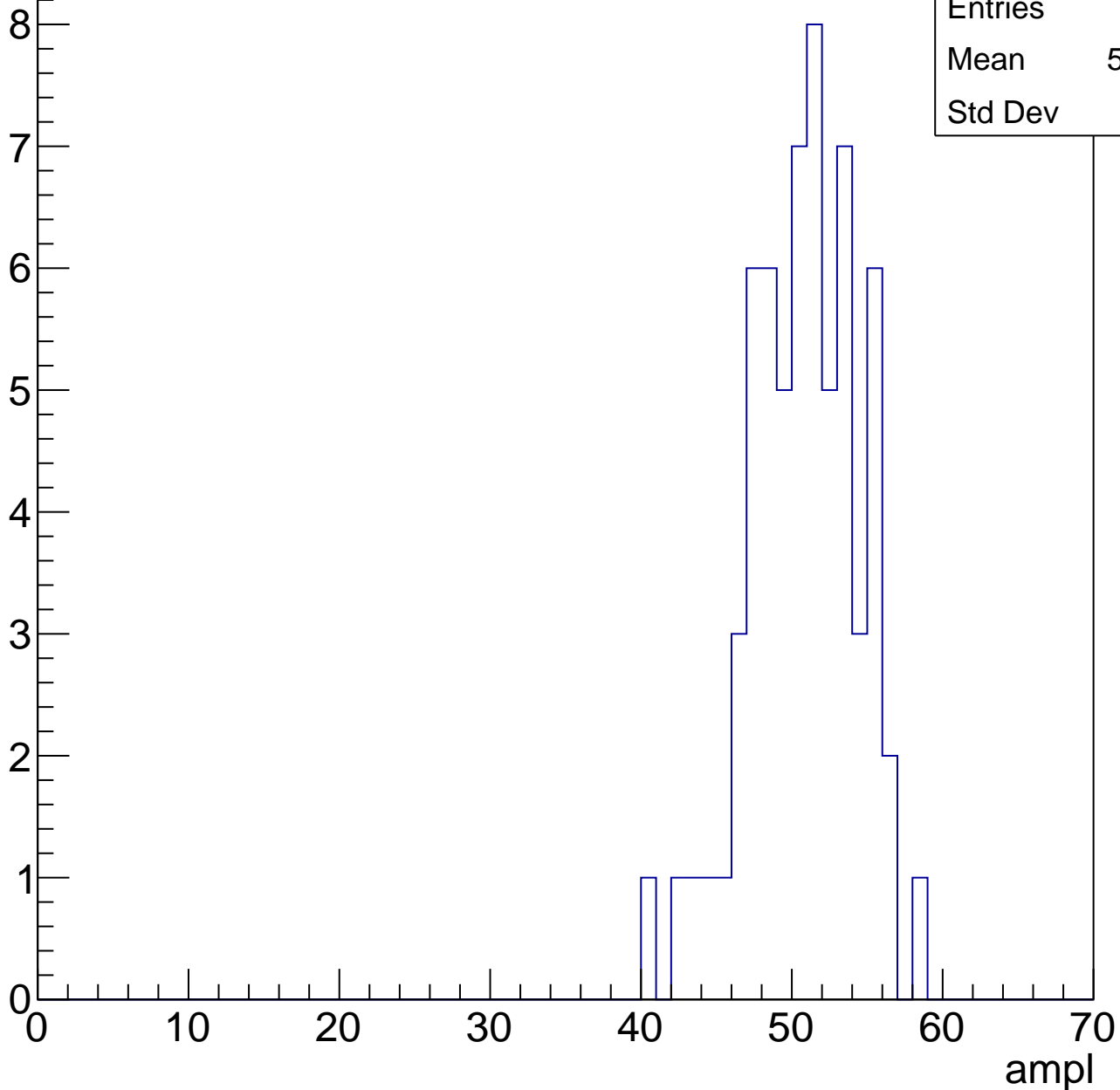


B1L102S, U2-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	50.28
Std Dev	3.59

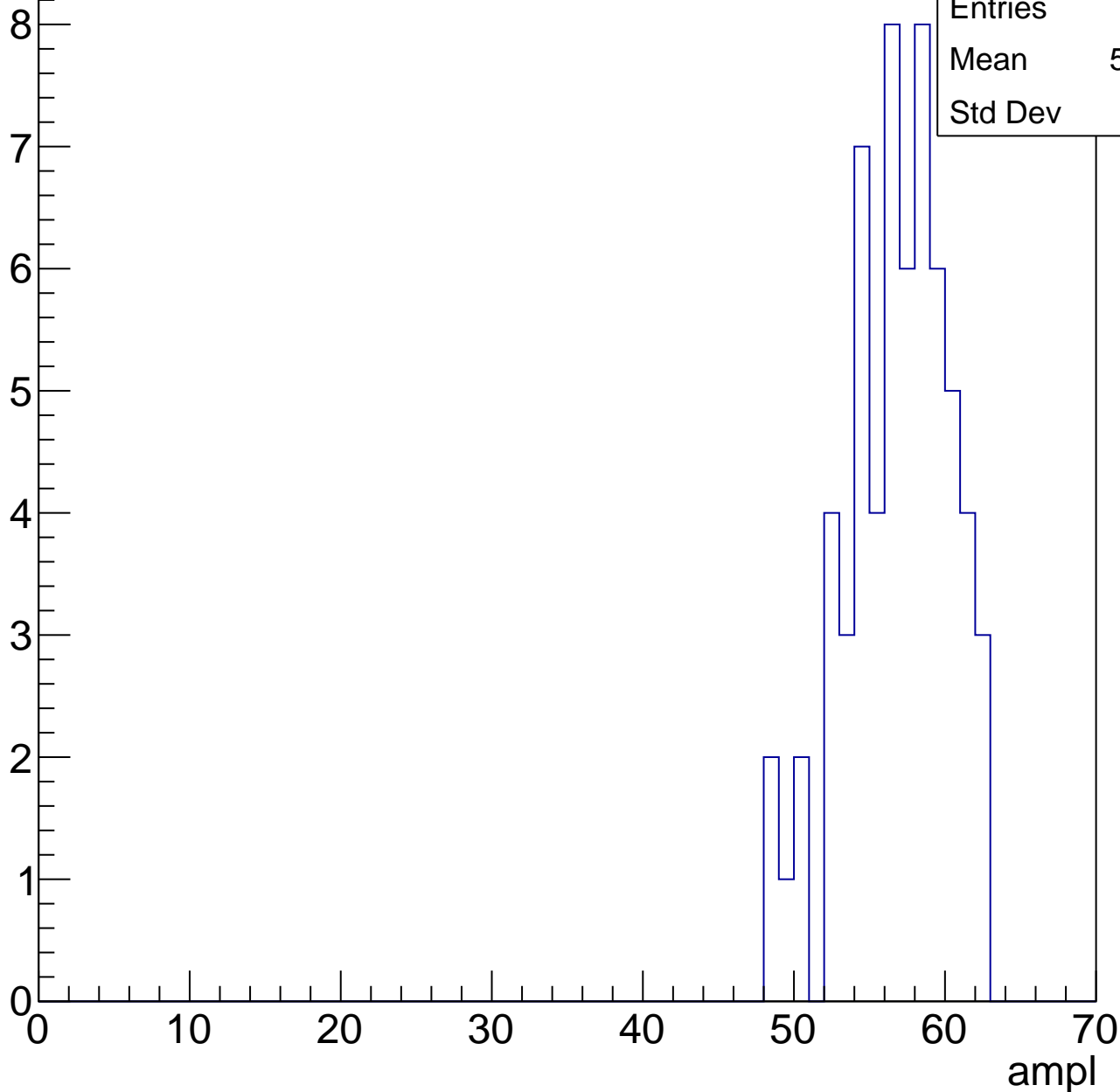


B1L102S, U2-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	56.32
Std Dev	3.44

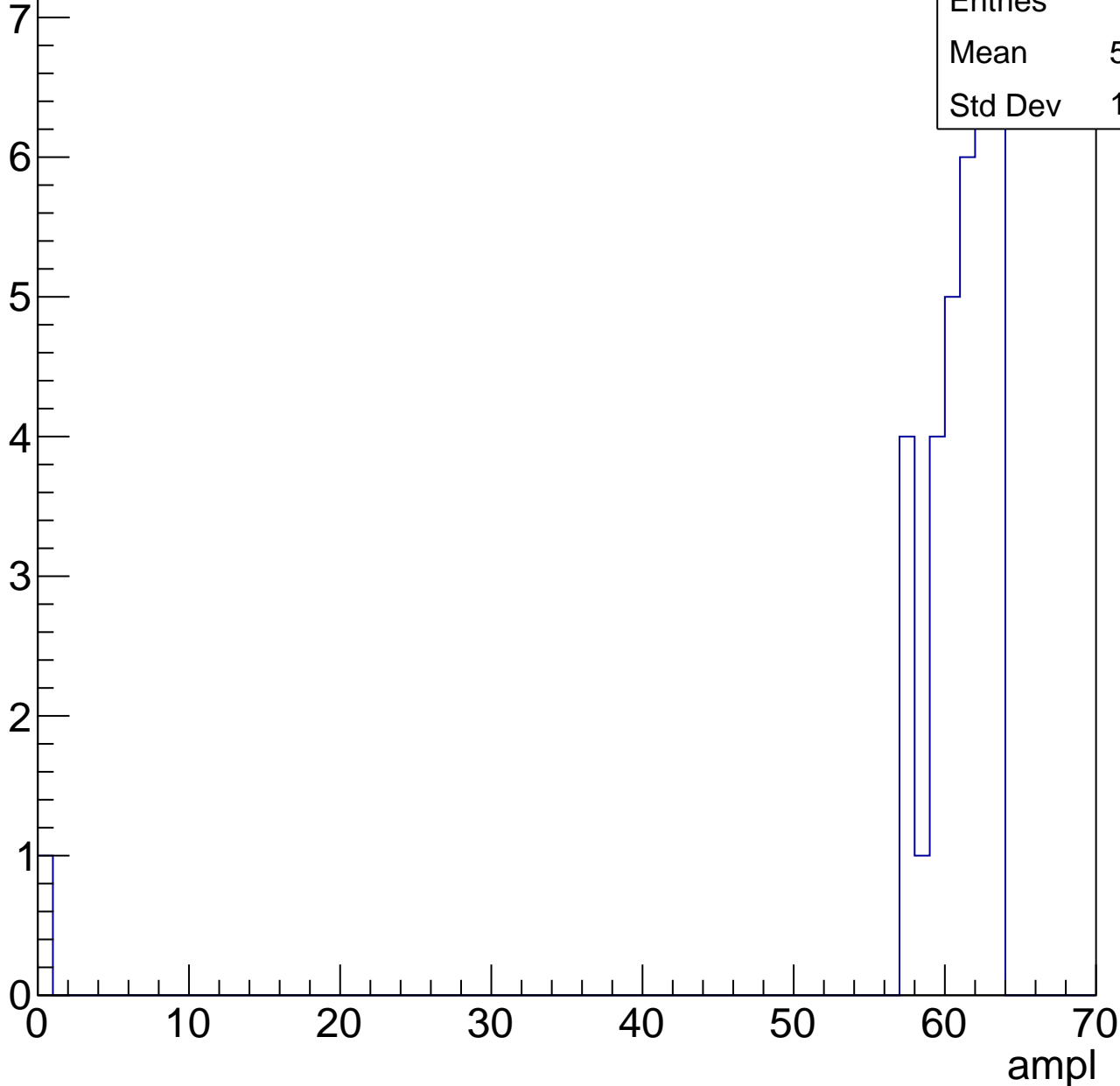


B1L102S, U2-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	58.94
Std Dev	10.28



B1L102S, U2-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch38, adc0

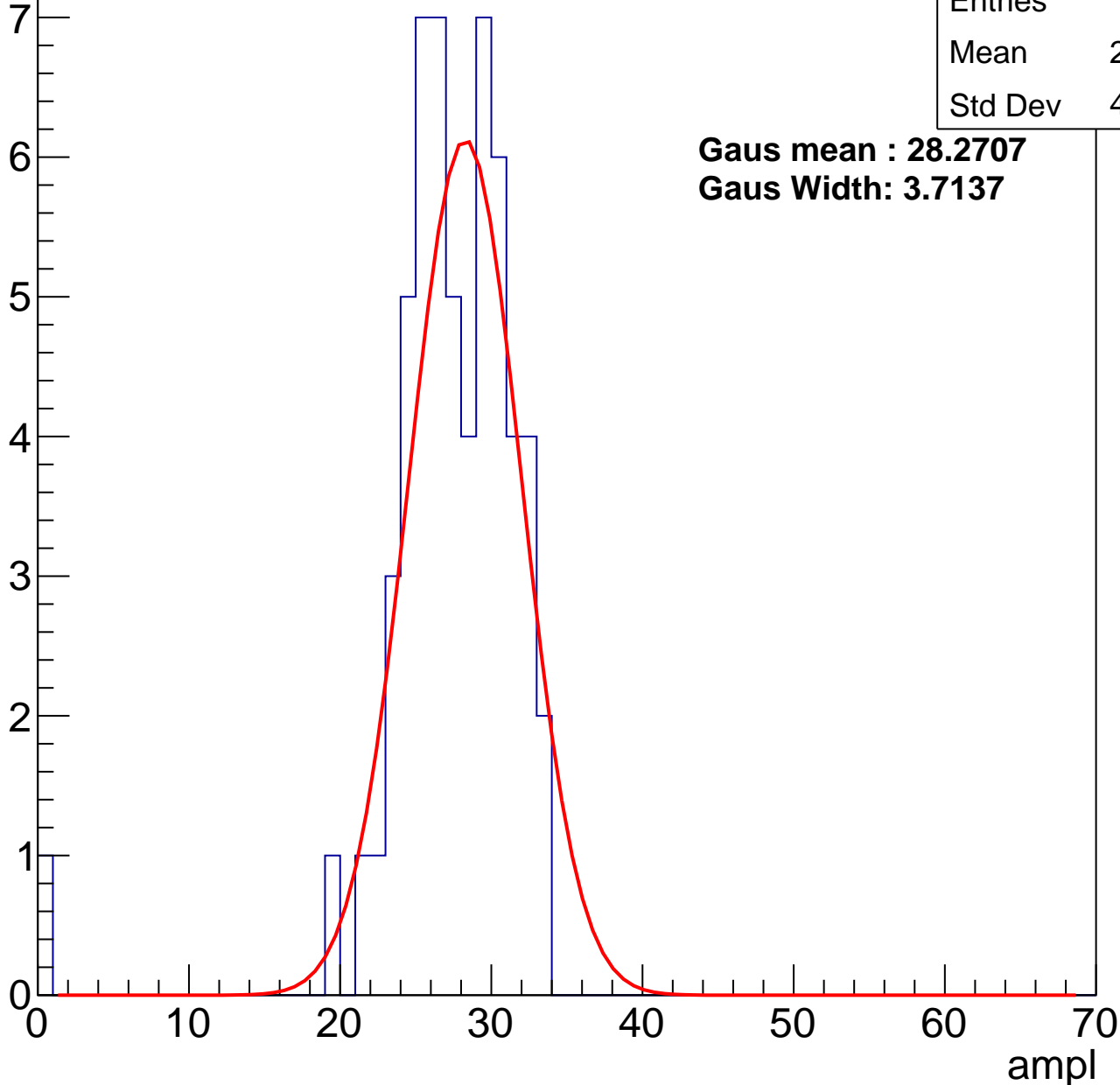
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	26.83
Std Dev	4.738

Gaus mean : 28.2707

Gaus Width: 3.7137



B1L102S, U2-ch38, adc1

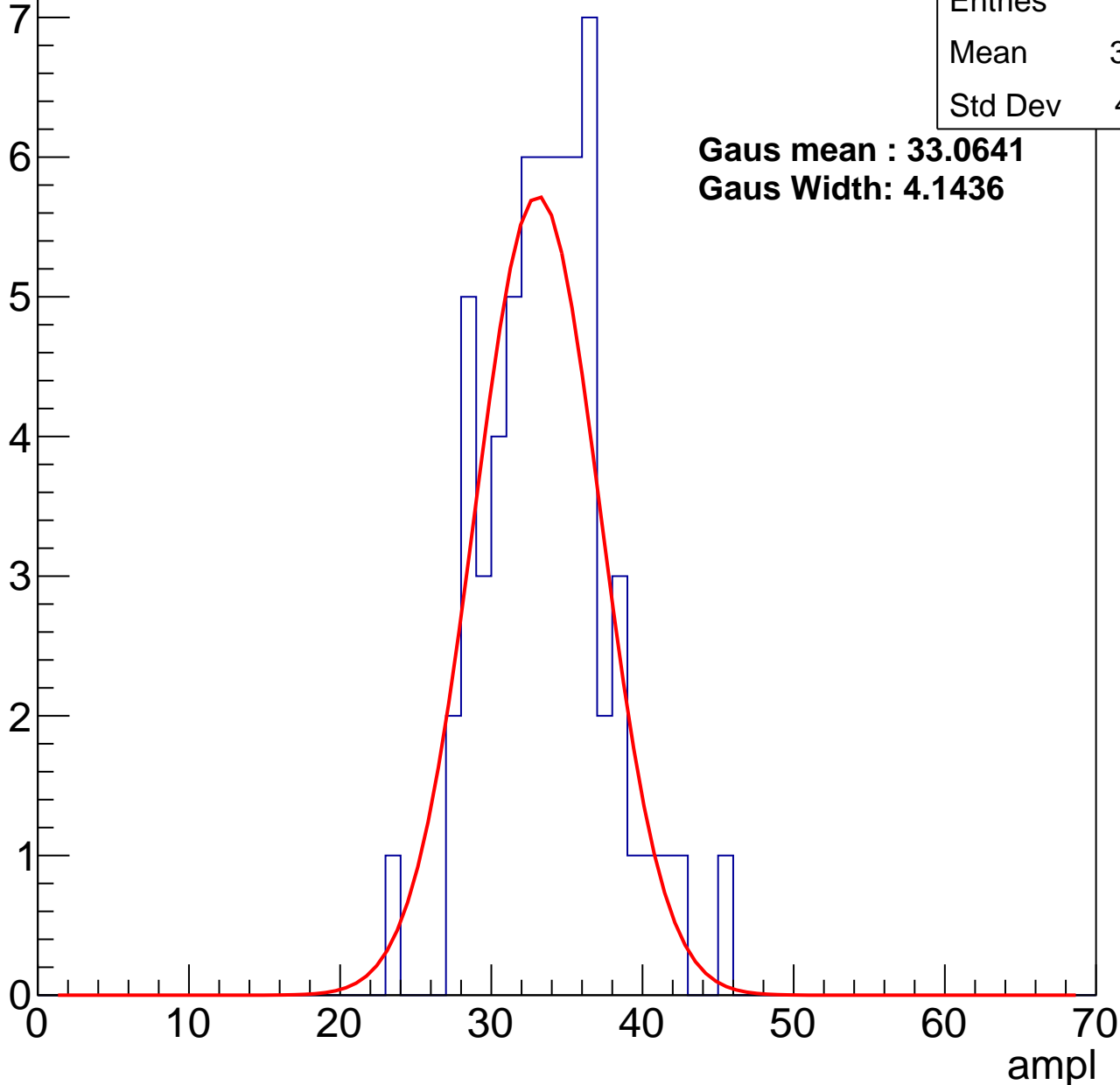
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	33.28
Std Dev	4.001

Gaus mean : 33.0641

Gaus Width: 4.1436



B1L102S, U2-ch38, adc2

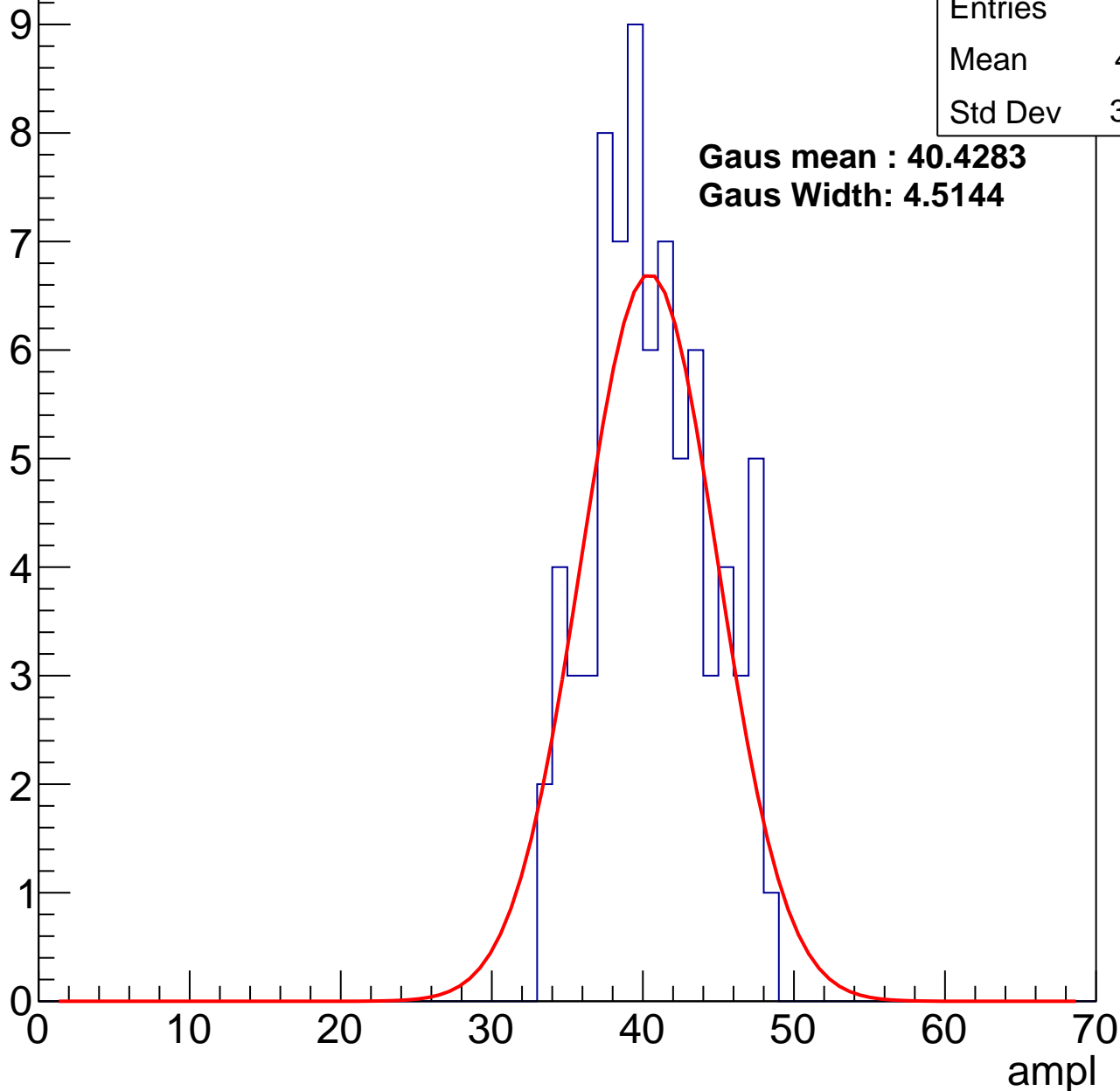
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	40.21
Std Dev	3.833

Gaus mean : 40.4283

Gaus Width: 4.5144

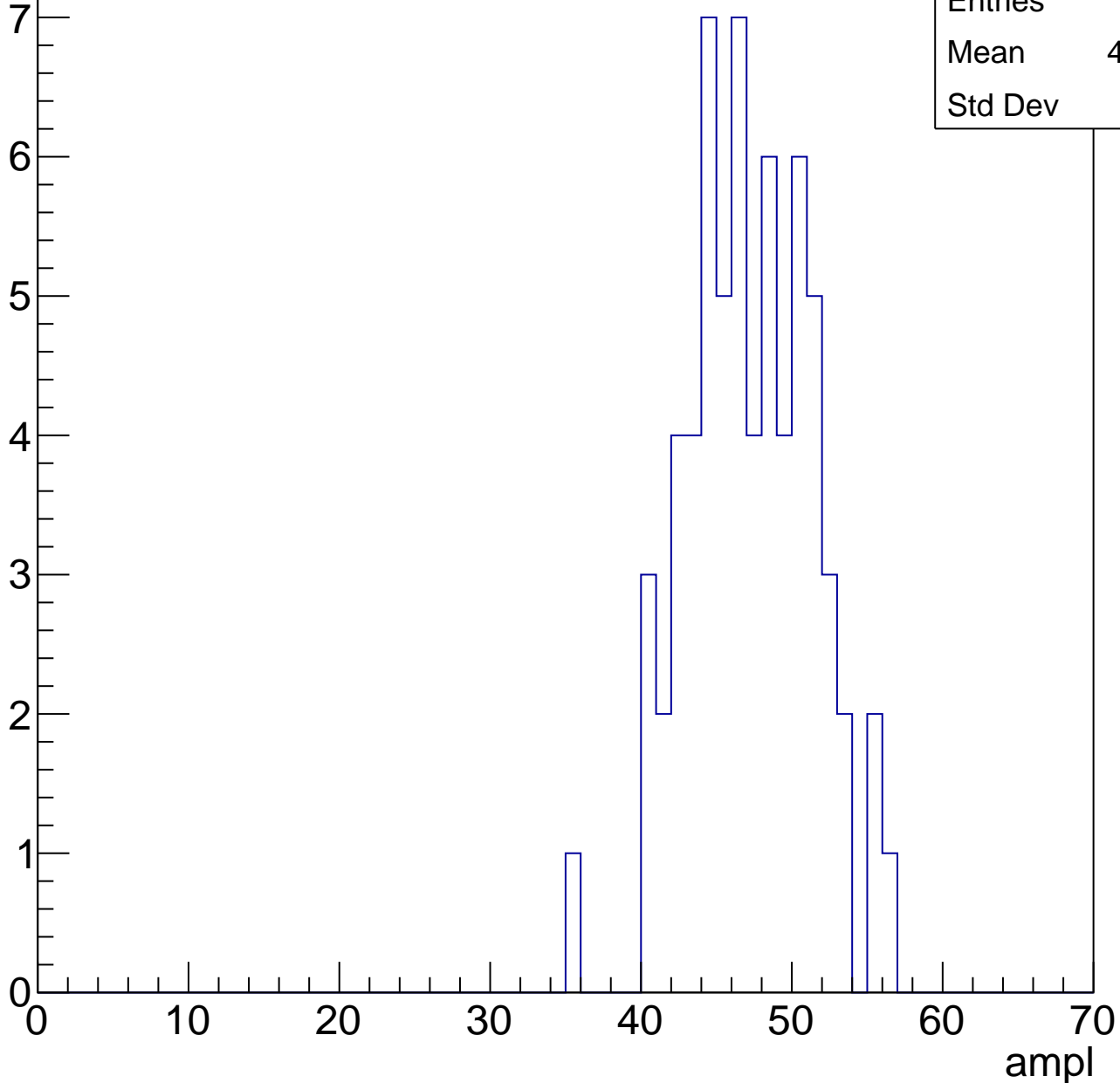


B1L102S, U2-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.77
Std Dev	4.13

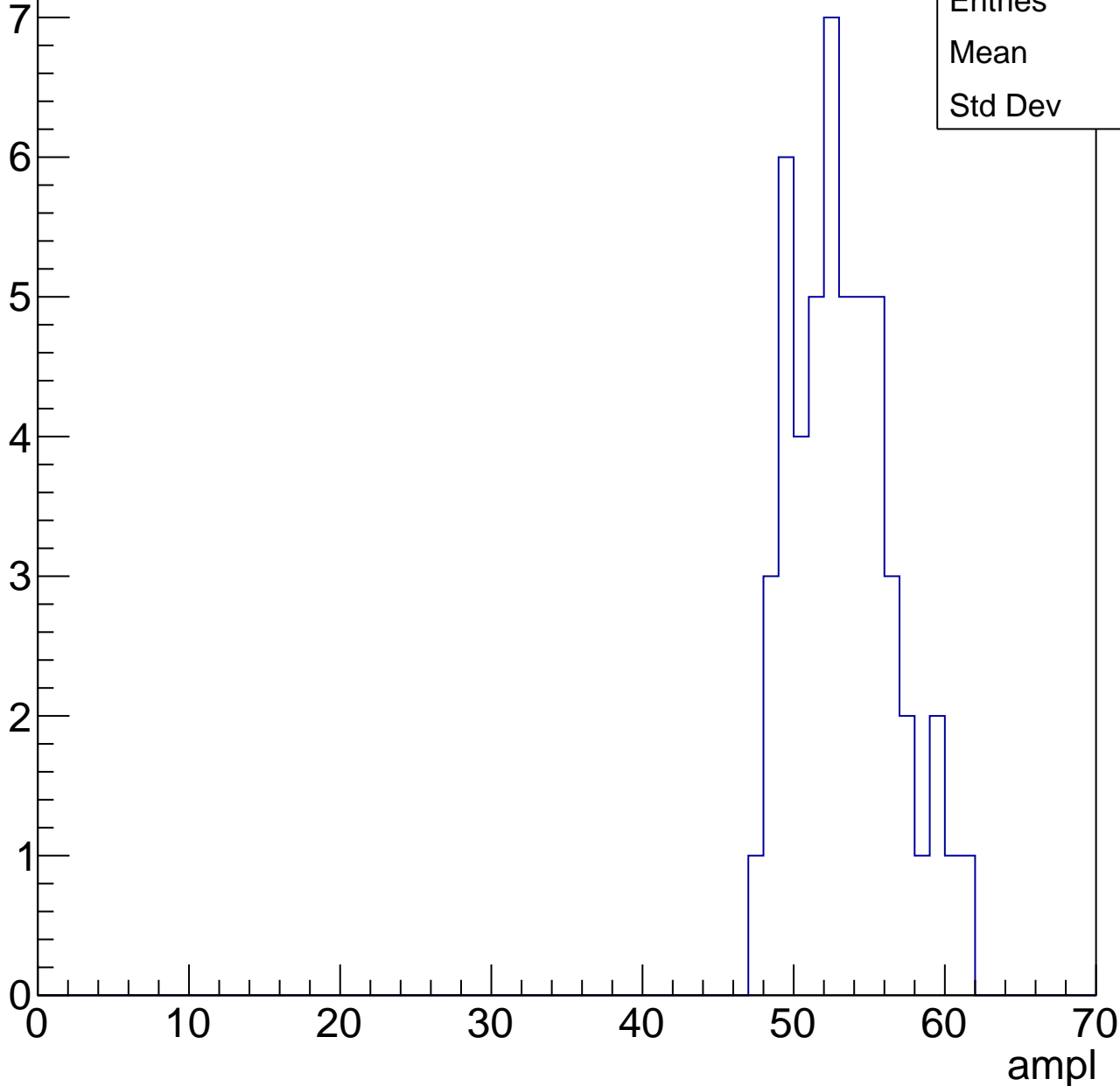


B1L102S, U2-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	52.8
Std Dev	3.32



B1L102S, U2-ch38, adc5

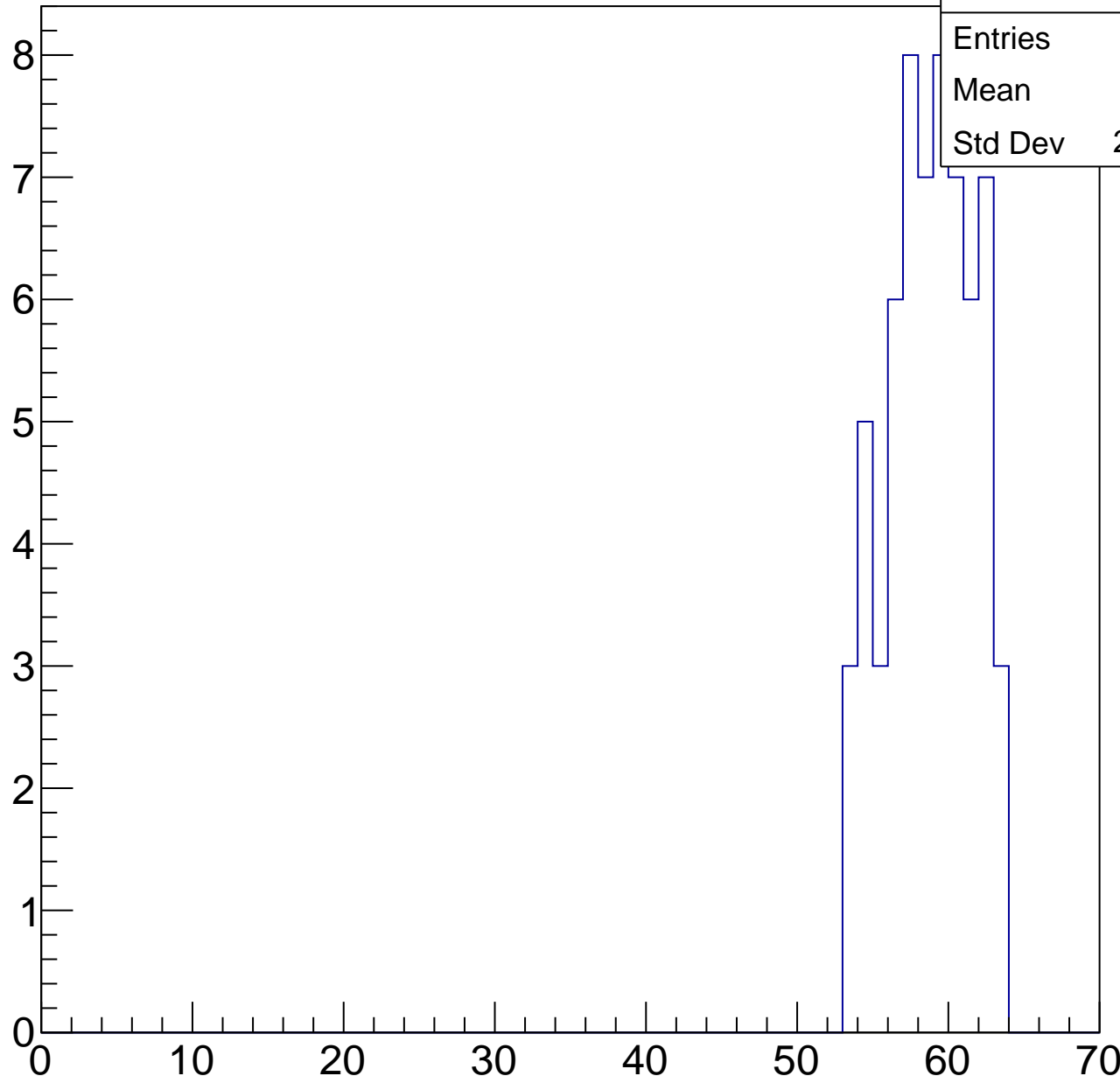
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.3
Std Dev	2.775

ampl

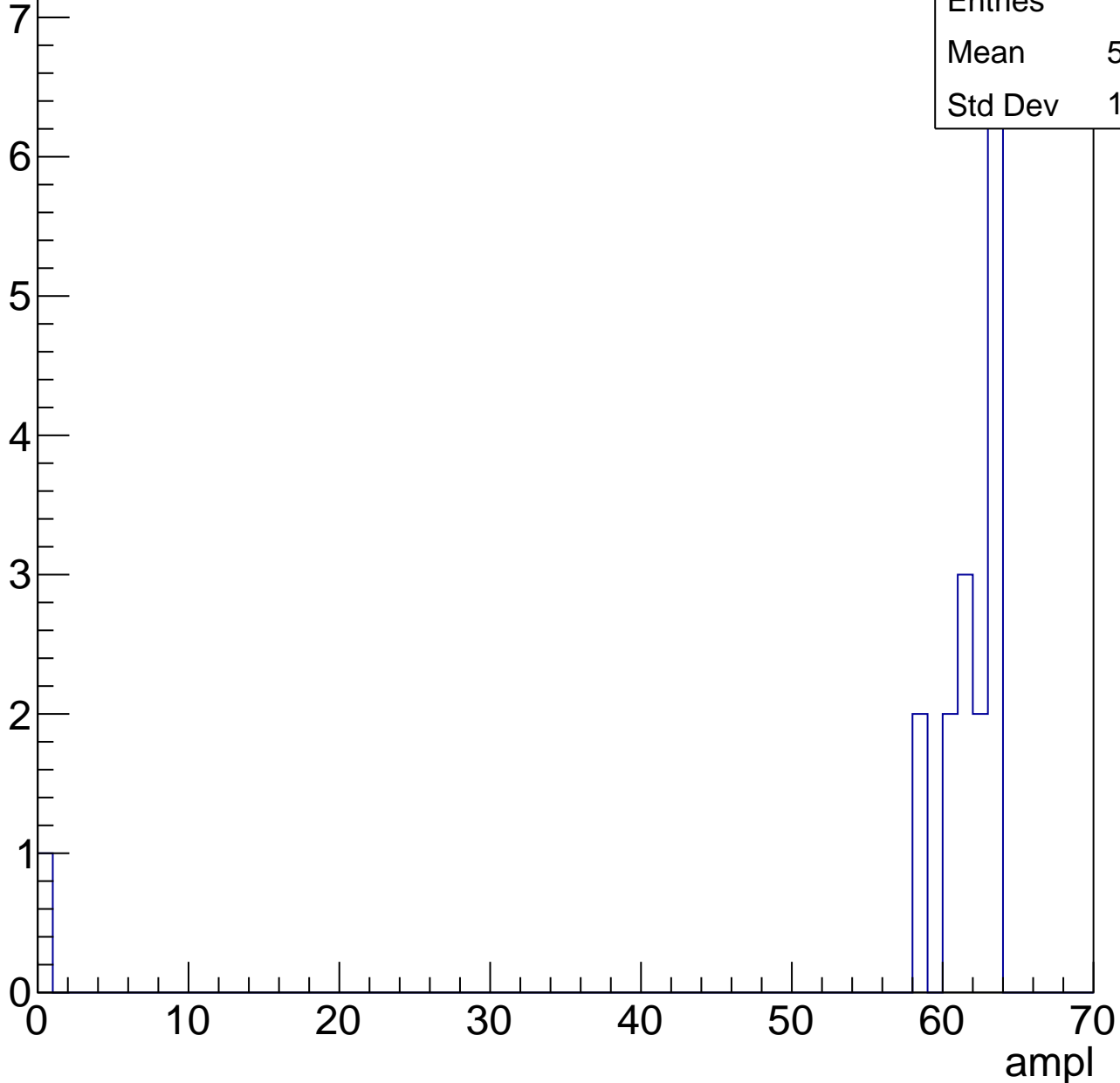


B1L102S, U2-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

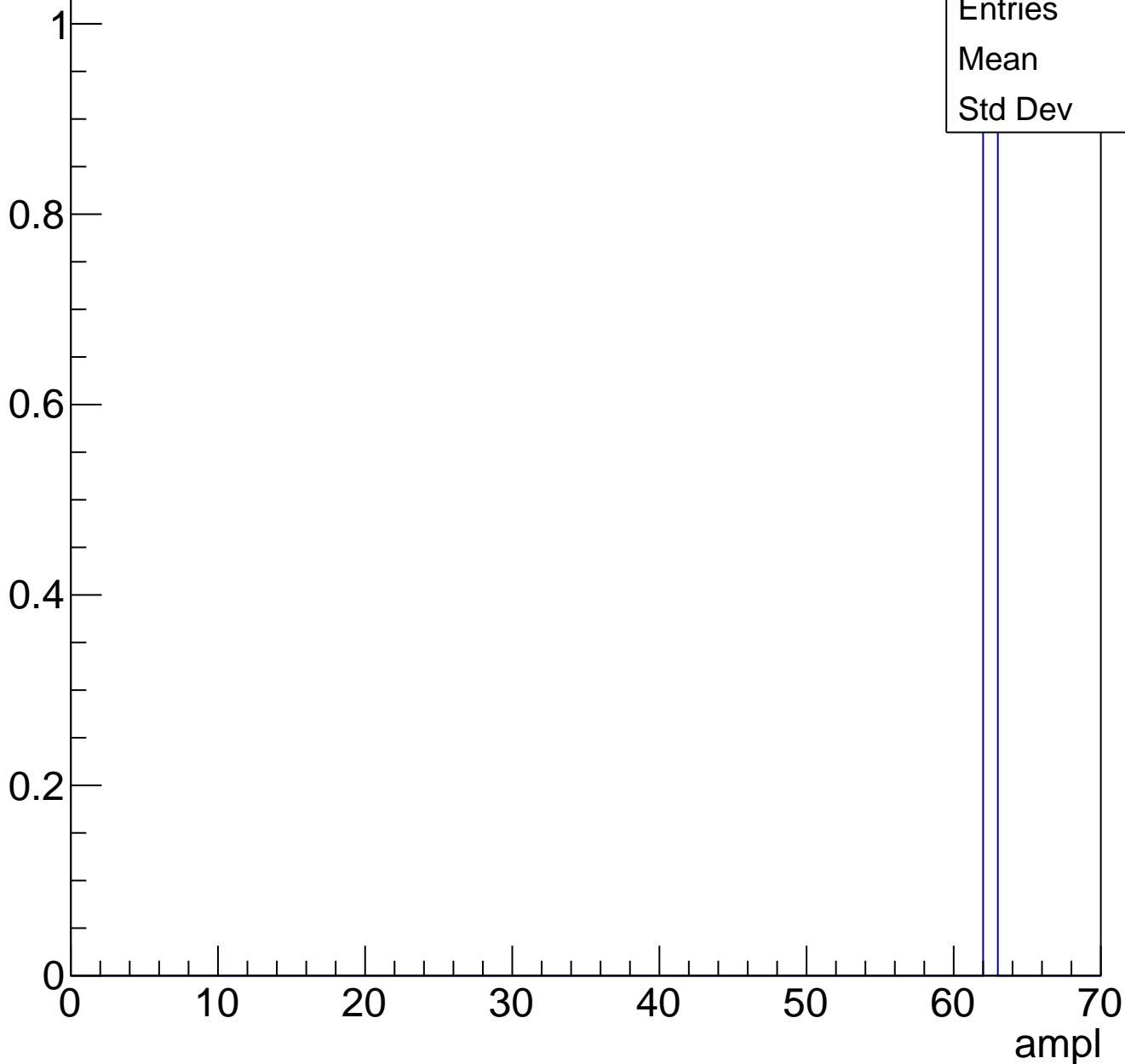
Entries	17
Mean	57.88
Std Dev	14.56



B1L102S, U2-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch39, adc0

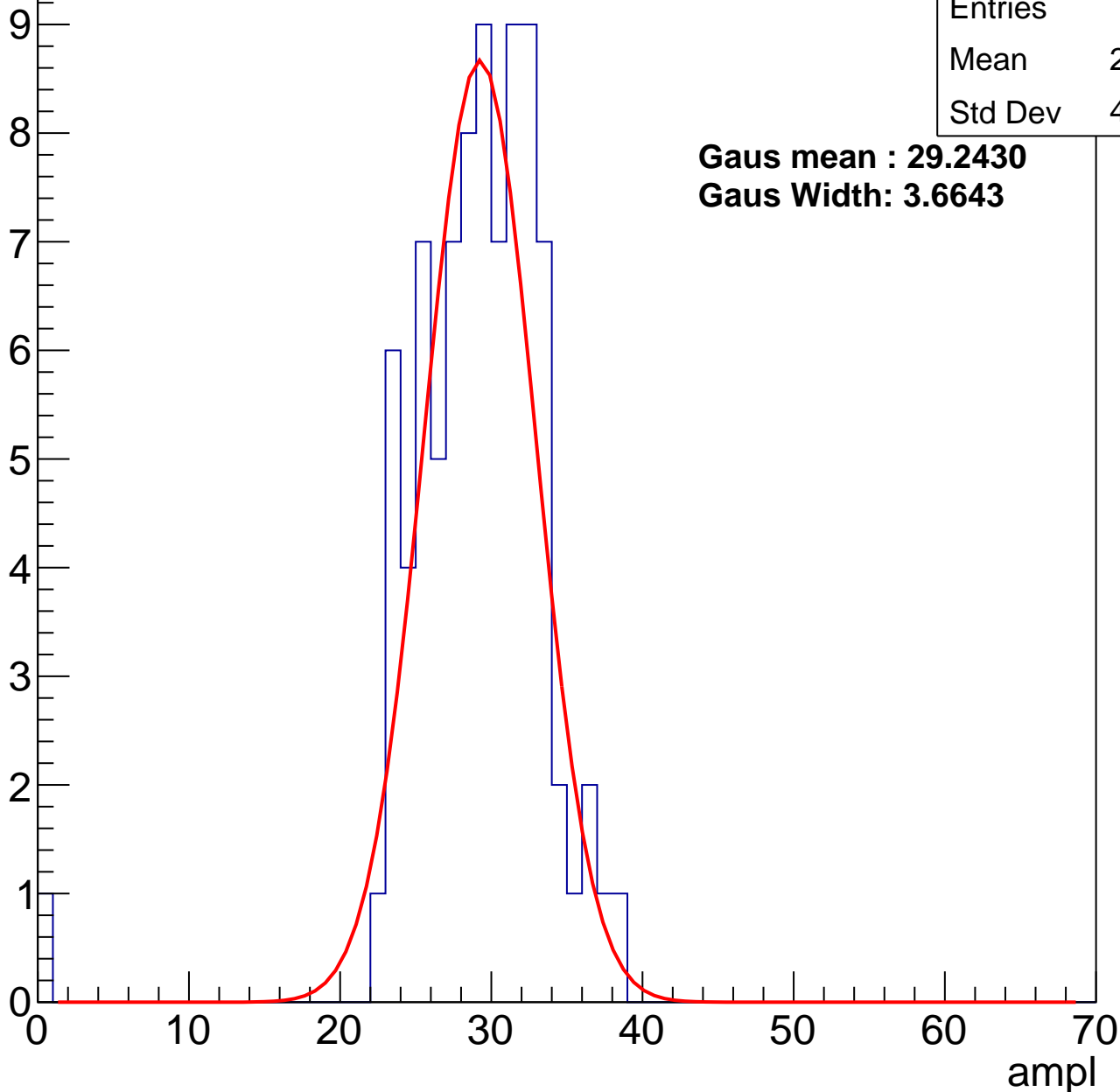
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	28.66
Std Dev	4.739

Gaus mean : 29.2430

Gaus Width: 3.6643



B1L102S, U2-ch39, adc1

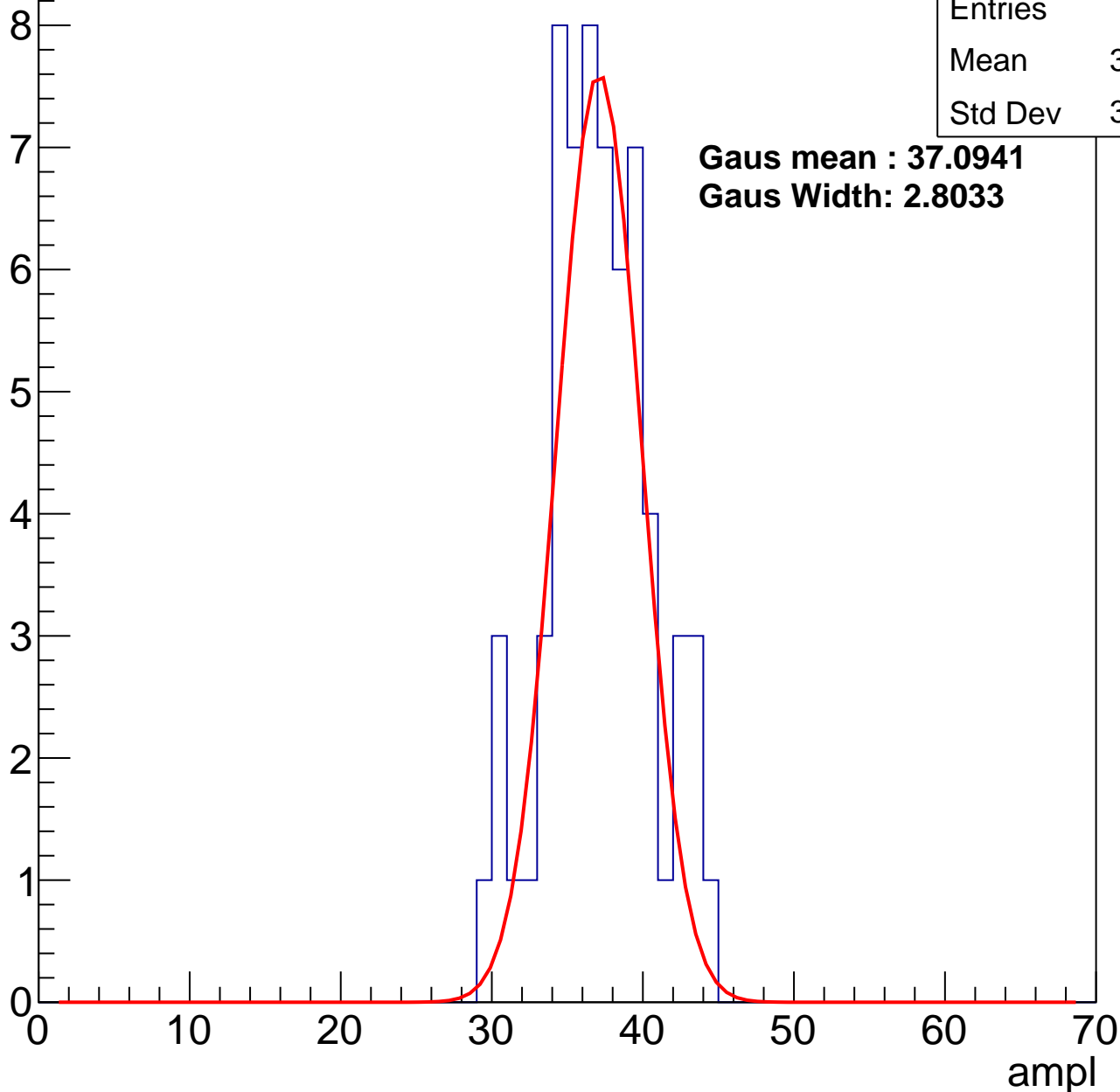
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.66
Std Dev	3.397

Gaus mean : 37.0941

Gaus Width: 2.8033



B1L102S, U2-ch39, adc2

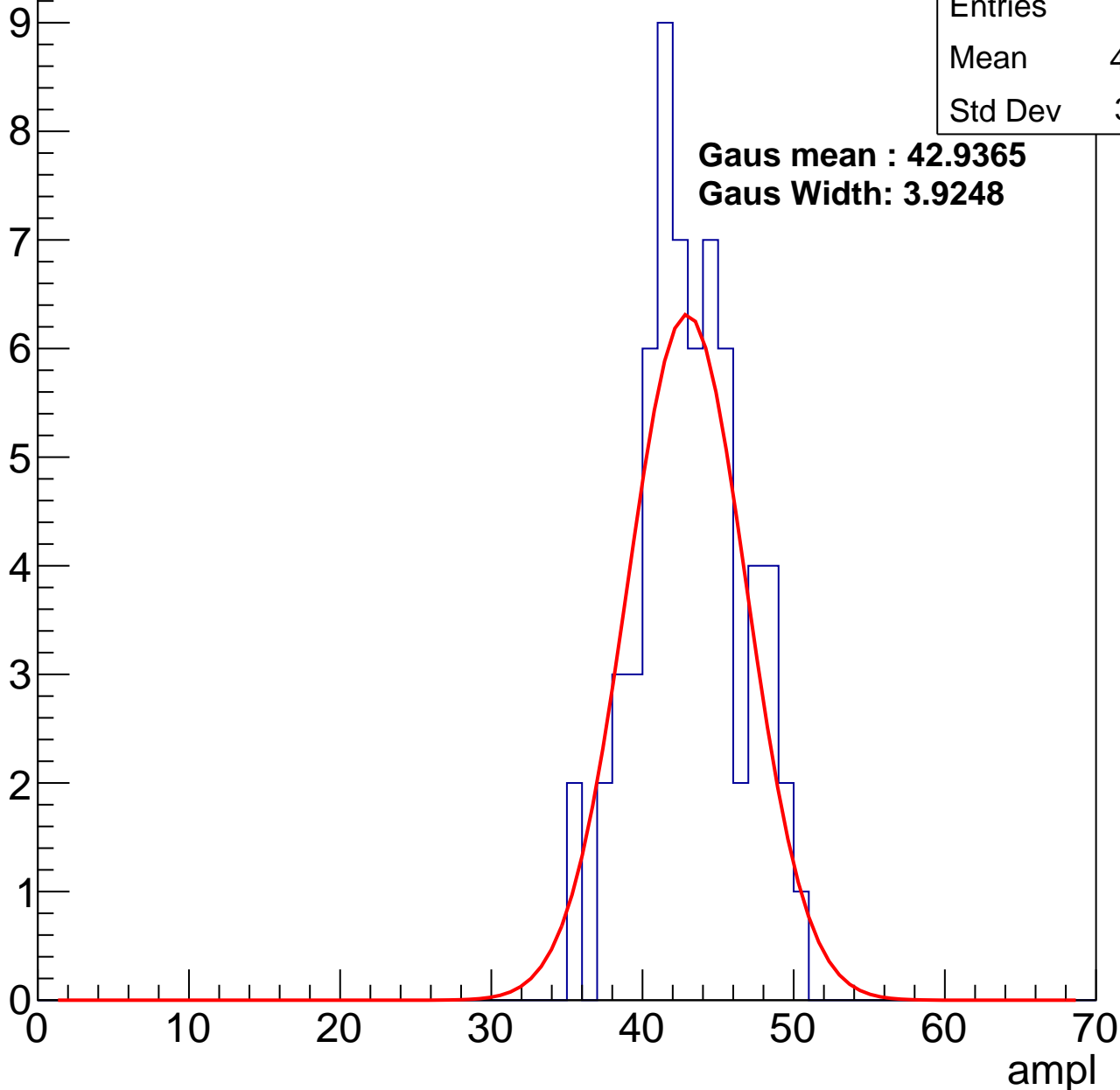
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.72
Std Dev	3.421

Gaus mean : 42.9365

Gaus Width: 3.9248

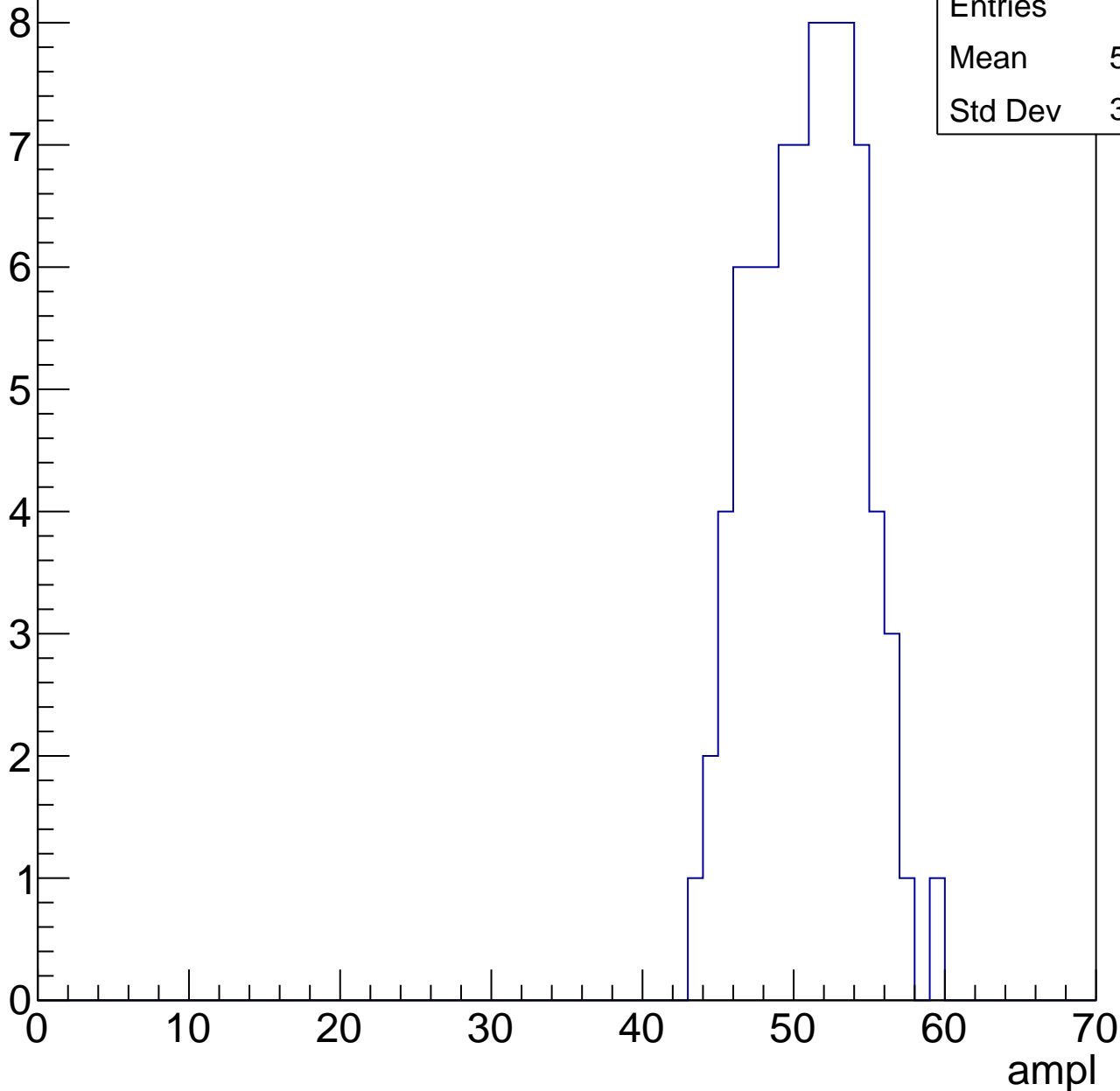


B1L102S, U2-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	50.38
Std Dev	3.473

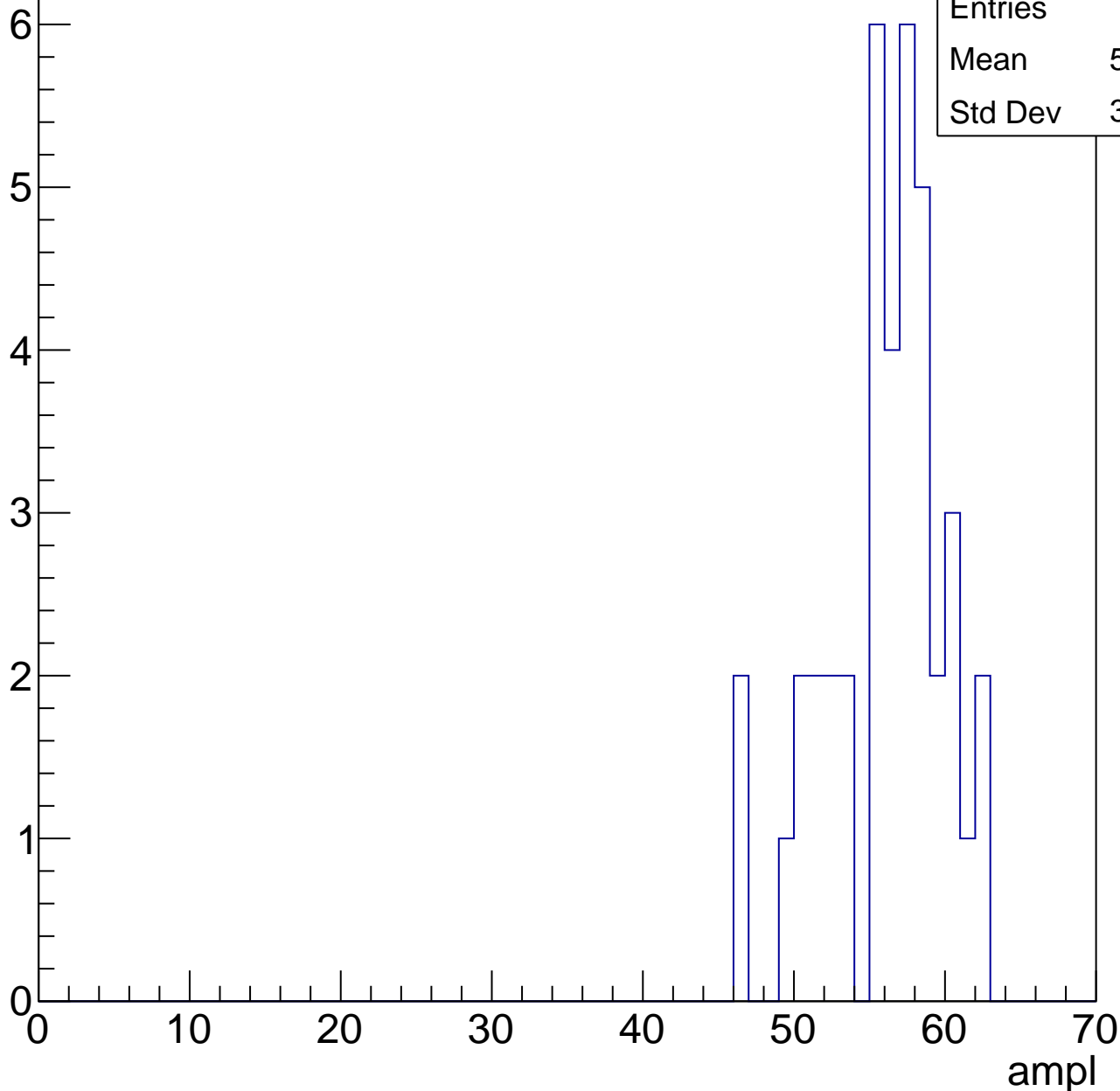


B1L102S, U2-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	55.55
Std Dev	3.898

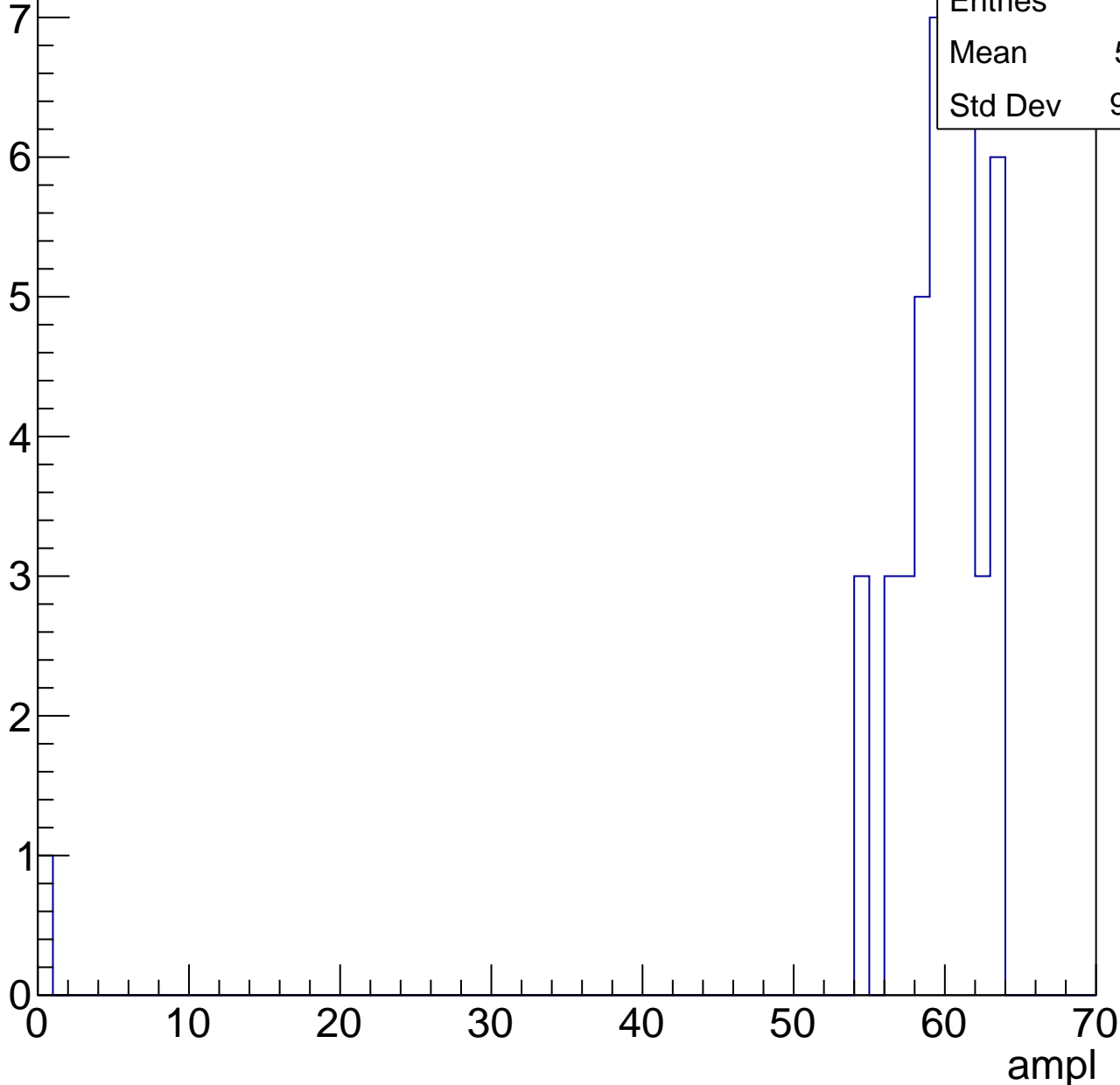


B1L102S, U2-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.11
Std Dev	9.095

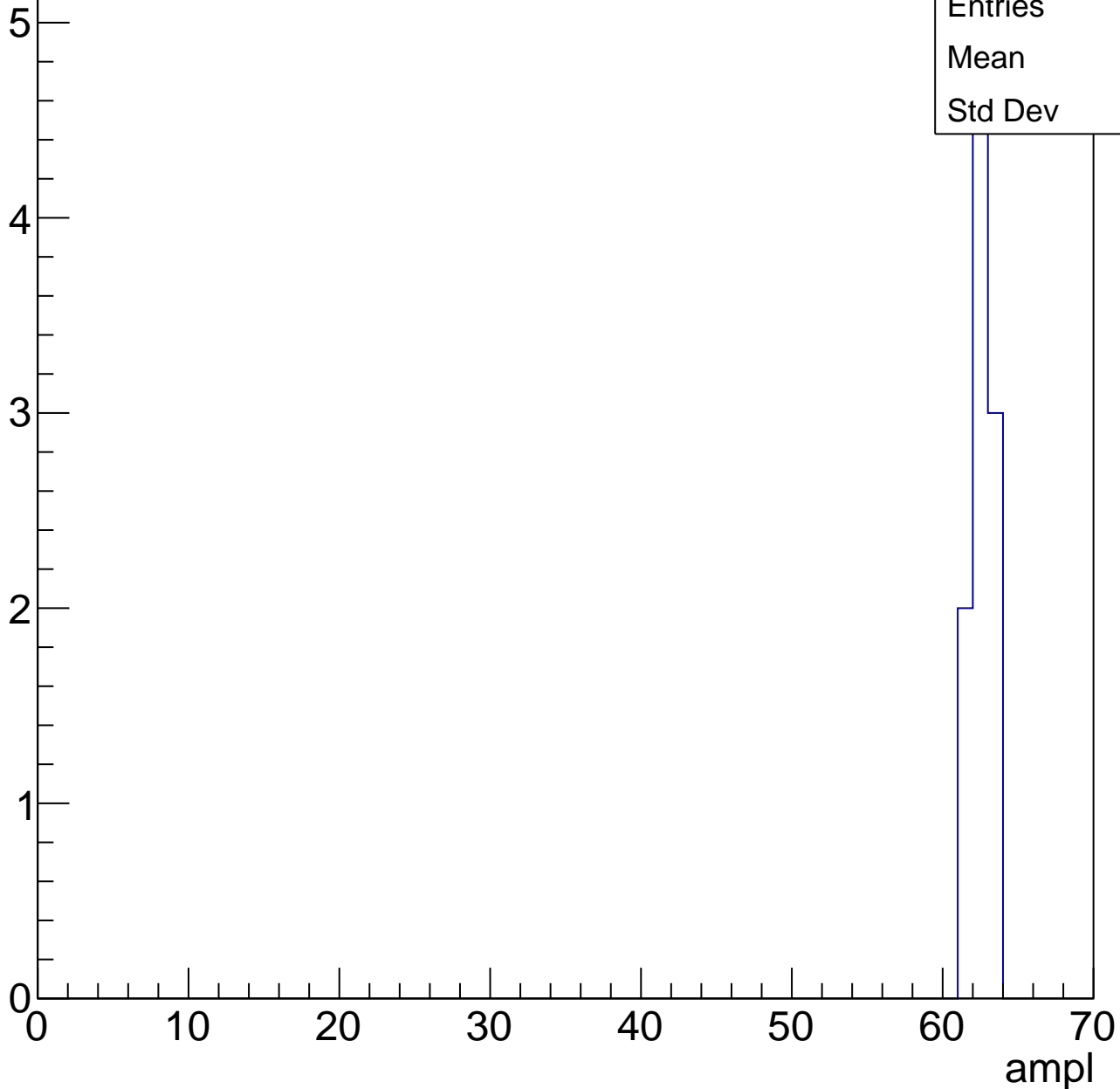


B1L102S, U2-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

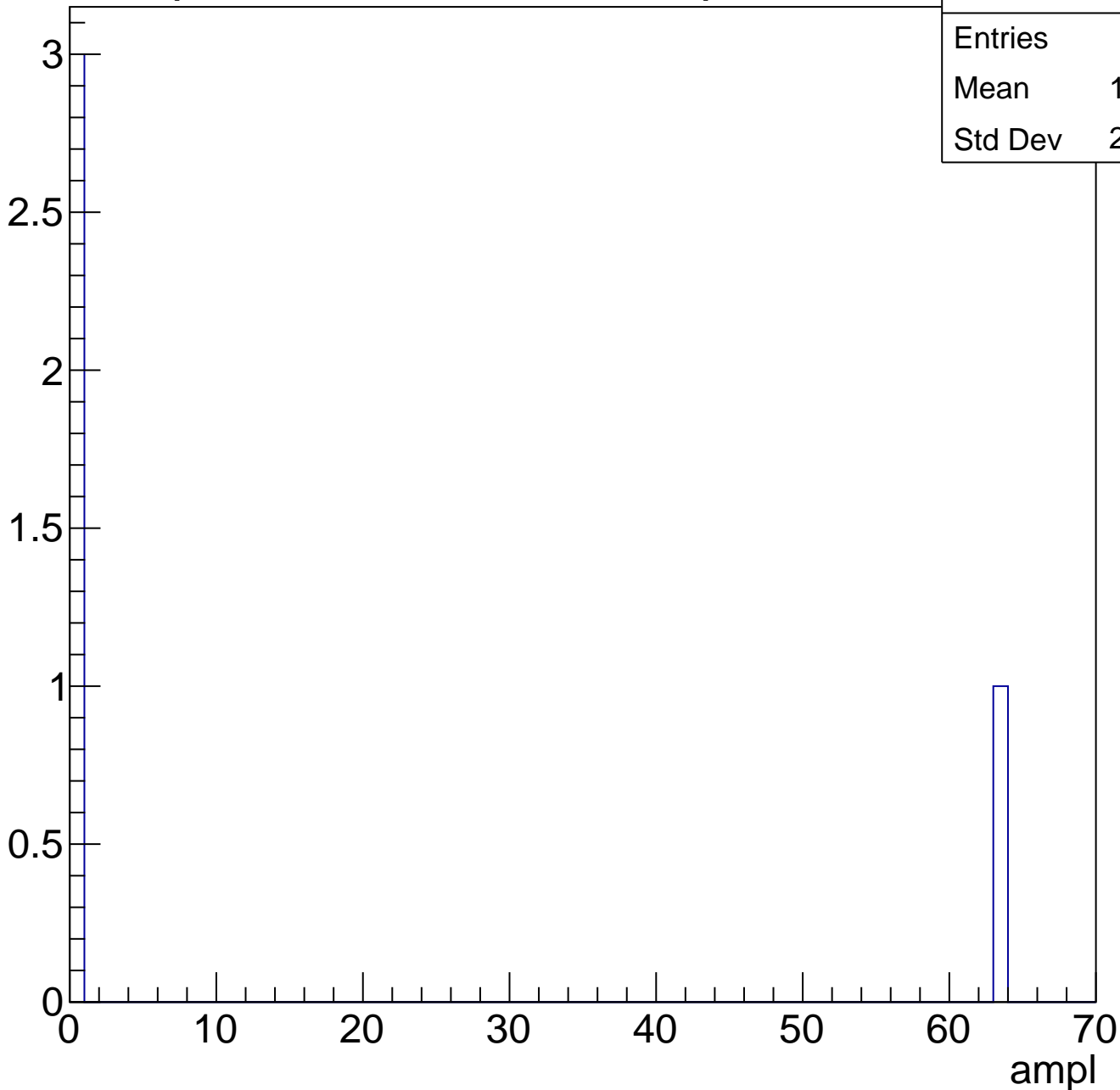
Entries	10
Mean	62.1
Std Dev	0.7



B1L102S, U2-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch40, adc0

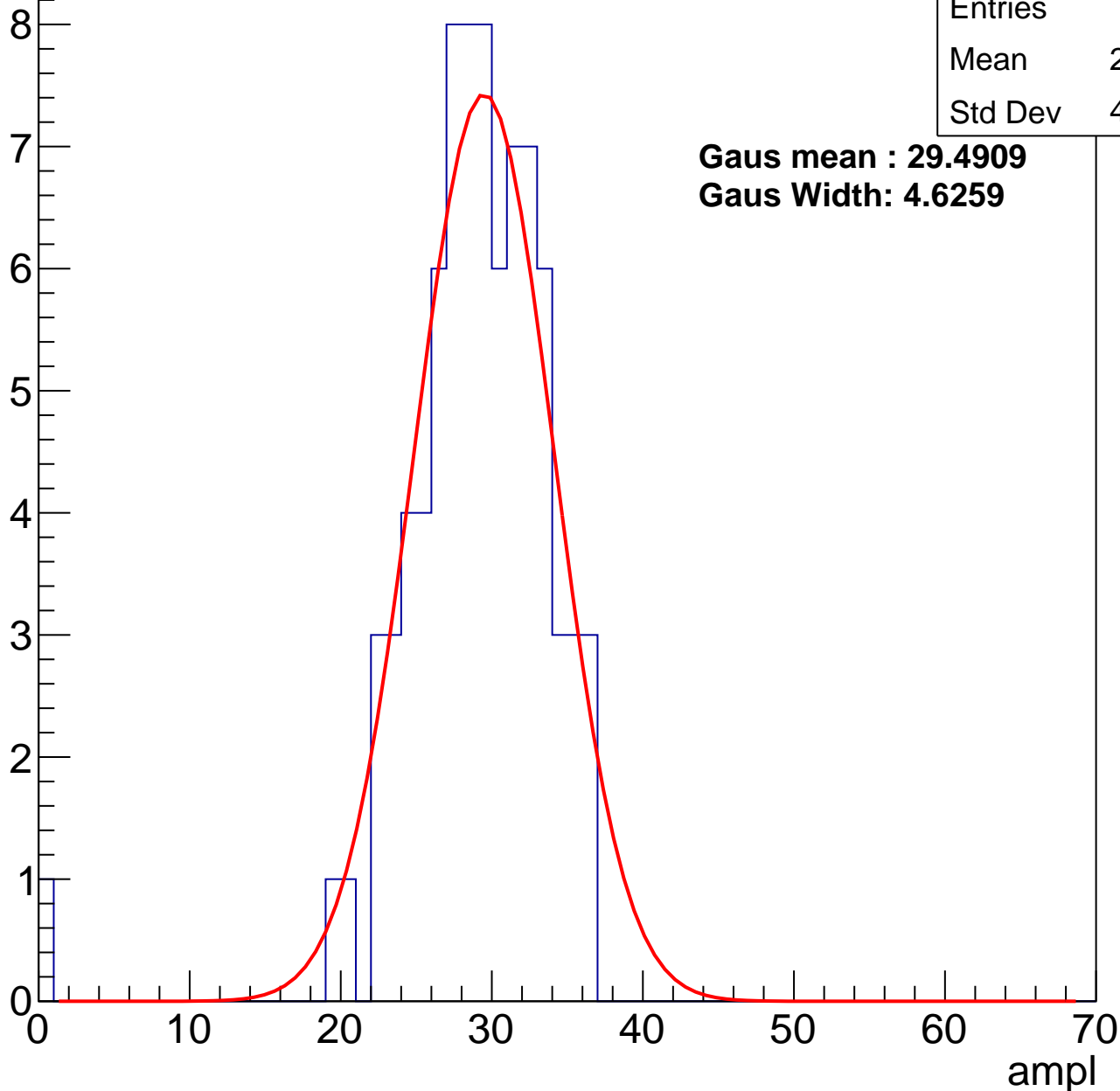
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	28.44
Std Dev	4.978

Gaus mean : 29.4909

Gaus Width: 4.6259



B1L102S, U2-ch40, adc1

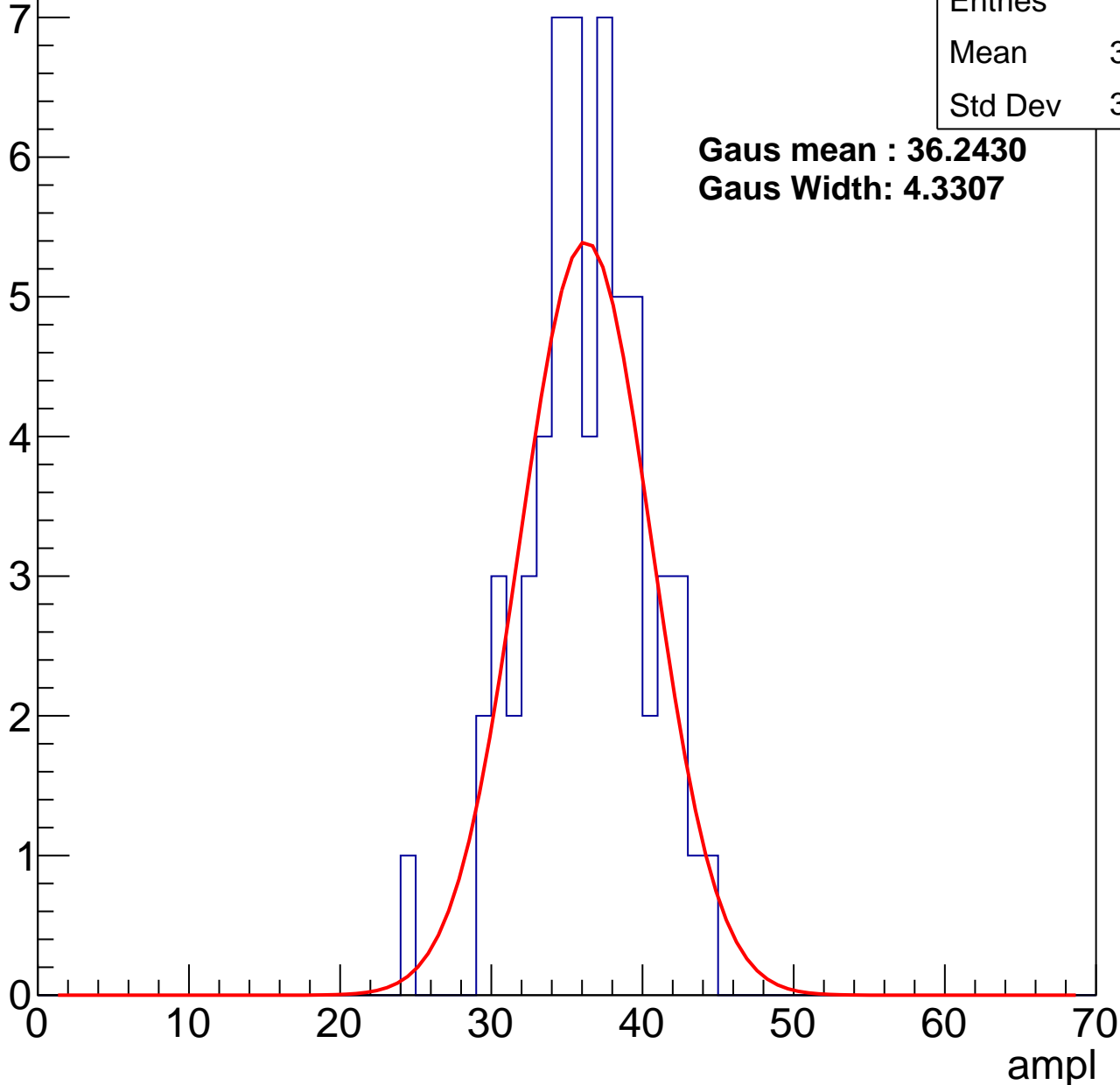
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.82
Std Dev	3.909

Gaus mean : 36.2430

Gaus Width: 4.3307



B1L102S, U2-ch40, adc2

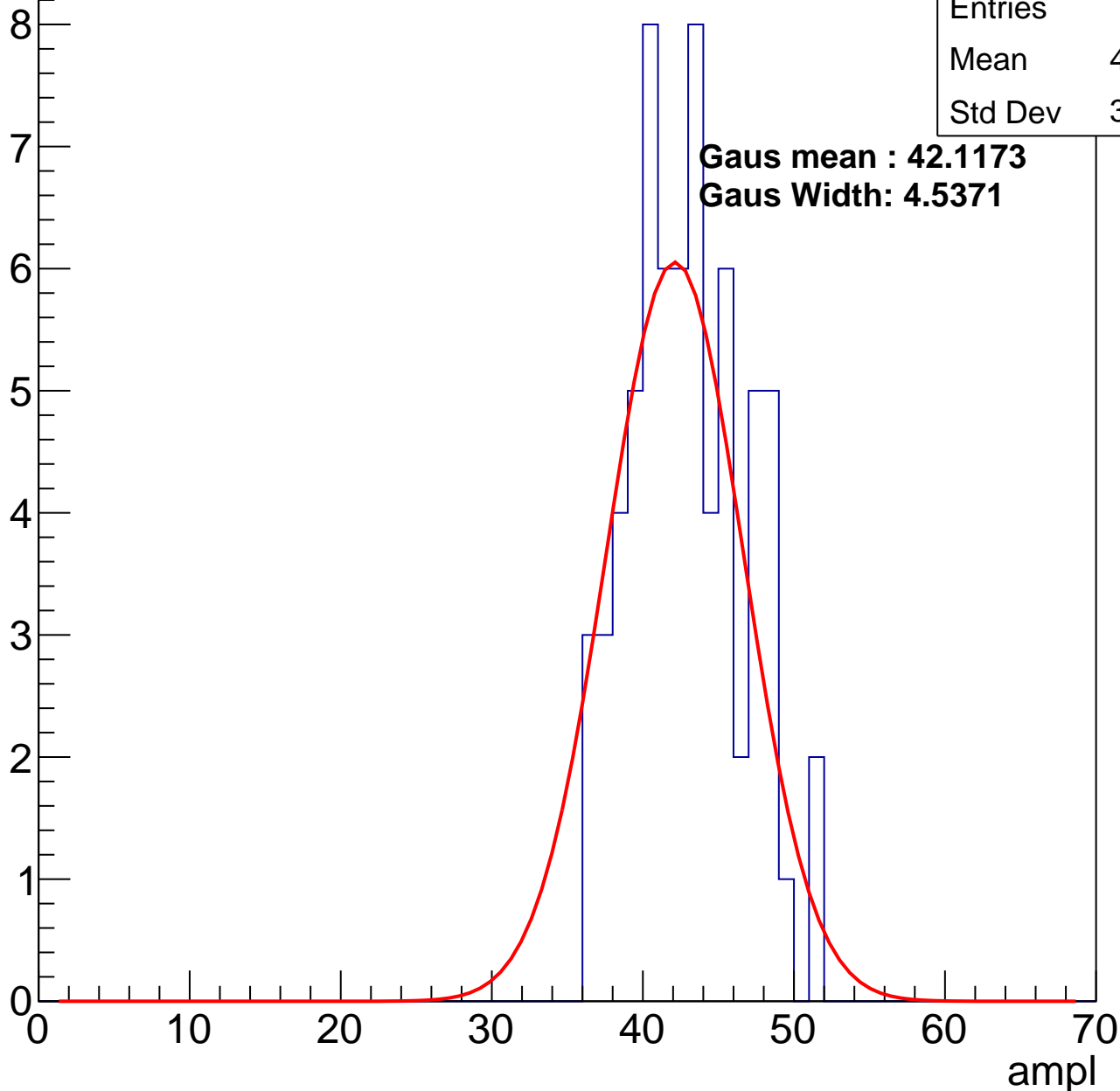
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.53
Std Dev	3.712

Gaus mean : 42.1173

Gaus Width: 4.5371

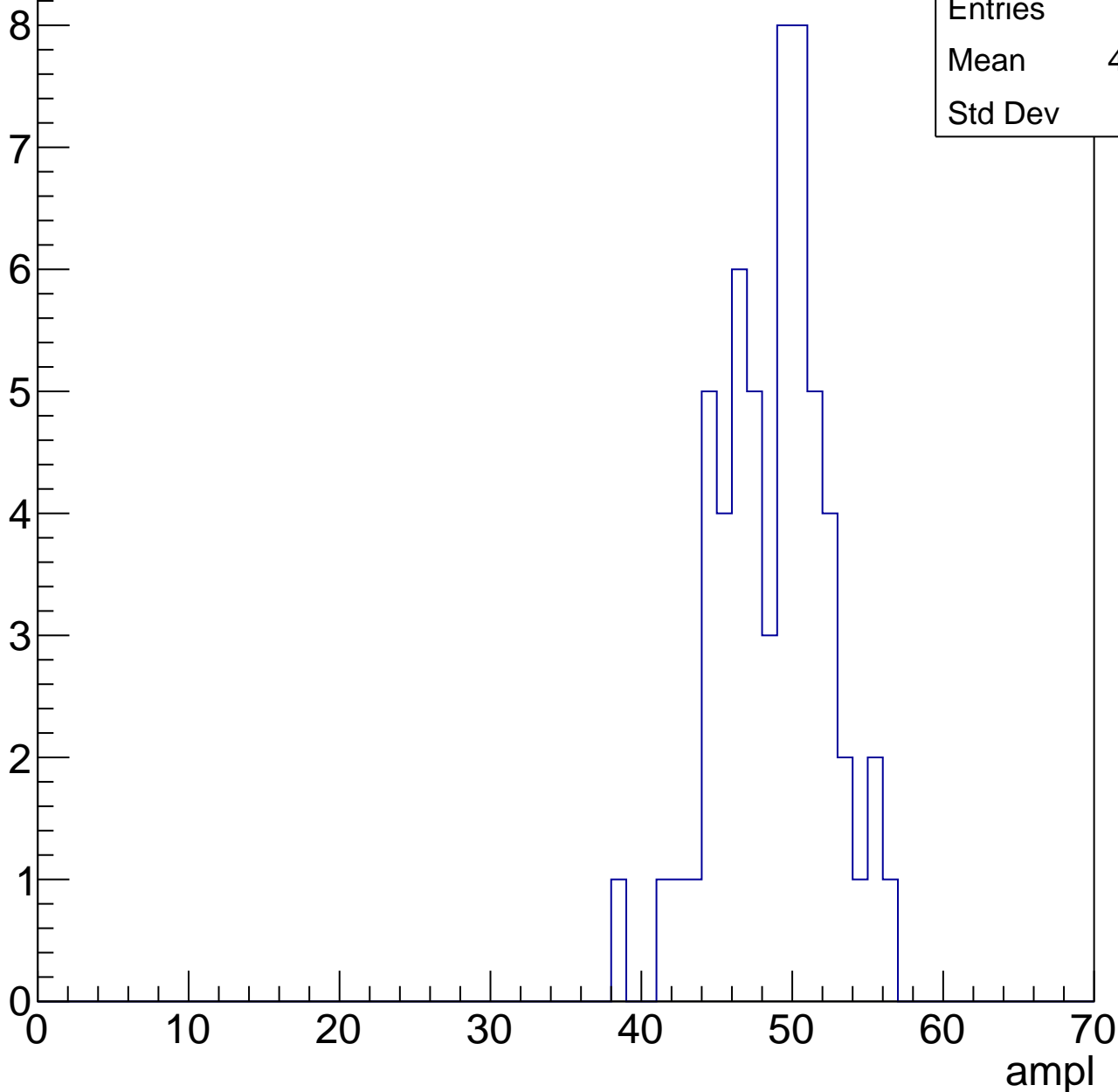


B1L102S, U2-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.28
Std Dev	3.59

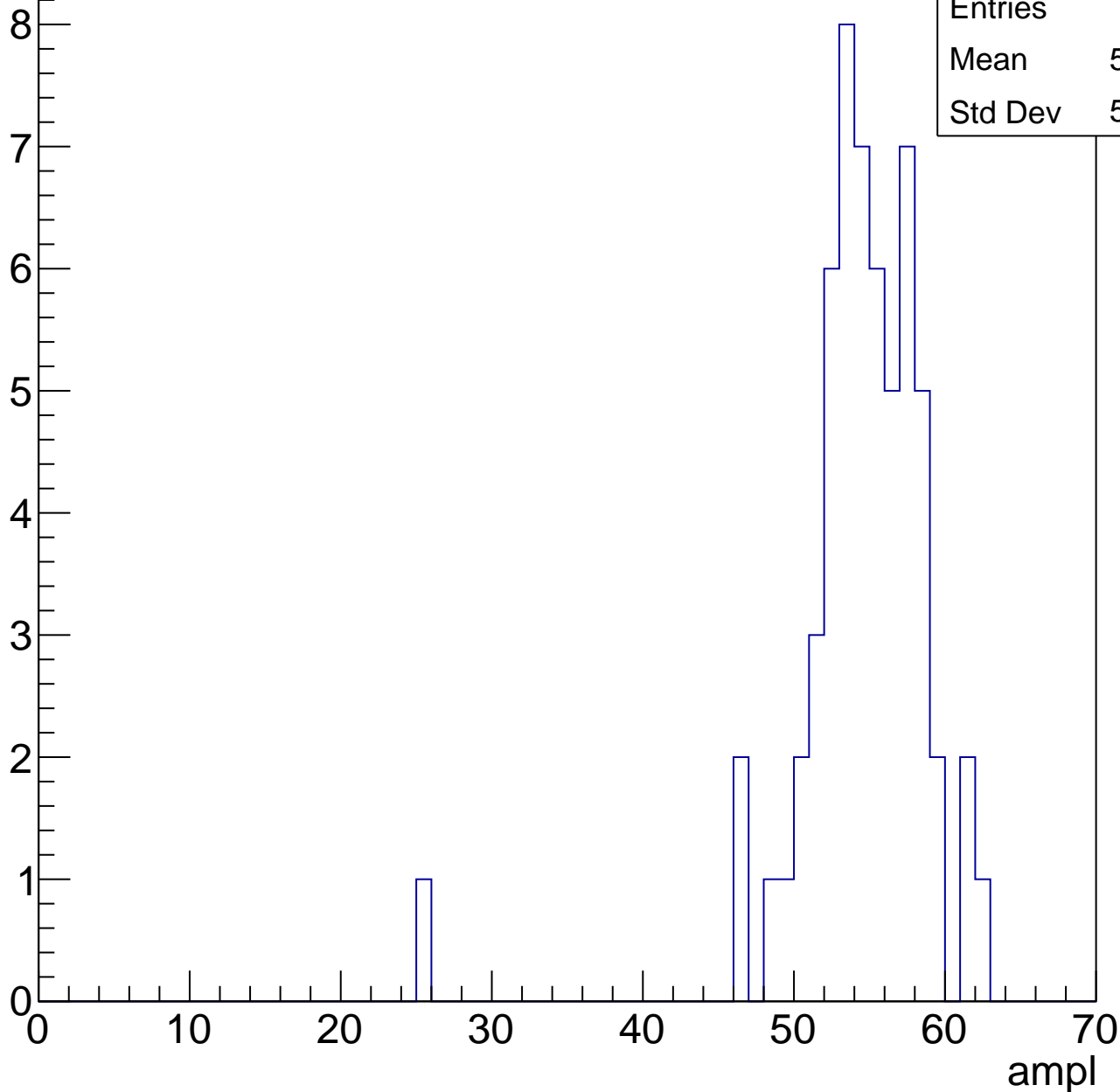


B1L102S, U2-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.93
Std Dev	5.045

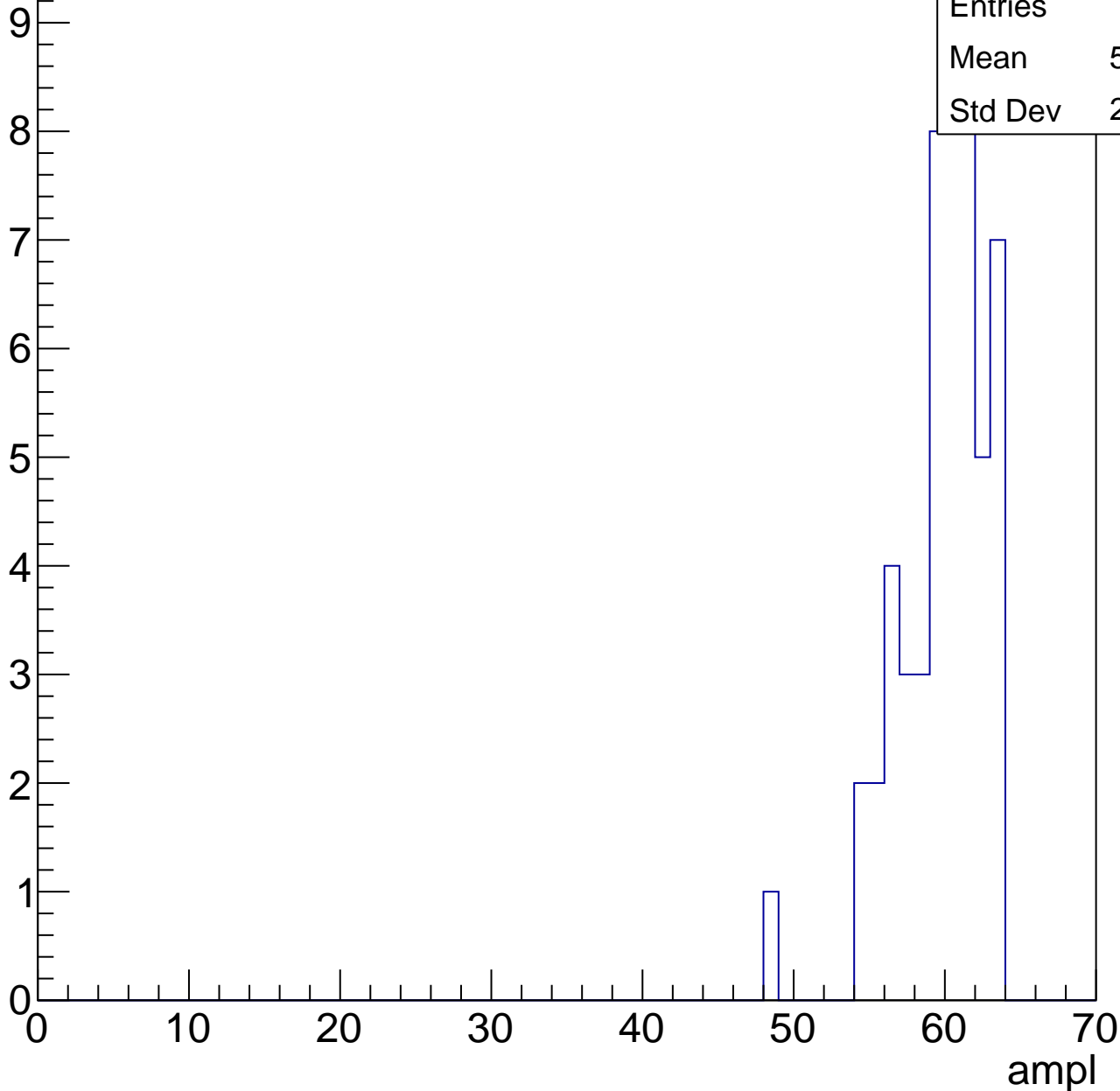


B1L102S, U2-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

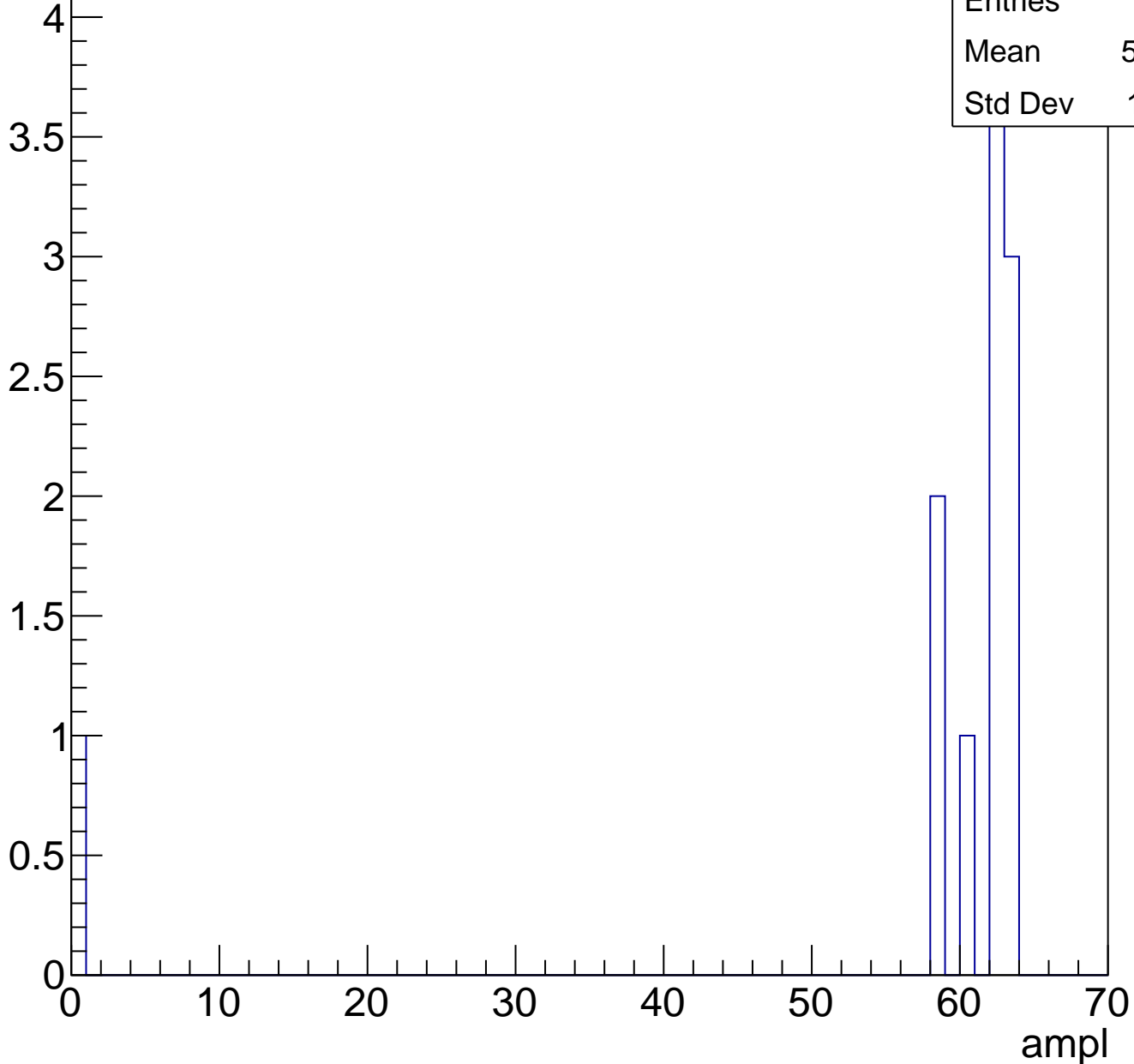
Entries	52
Mean	59.35
Std Dev	2.928



B1L102S, U2-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch41, adc0

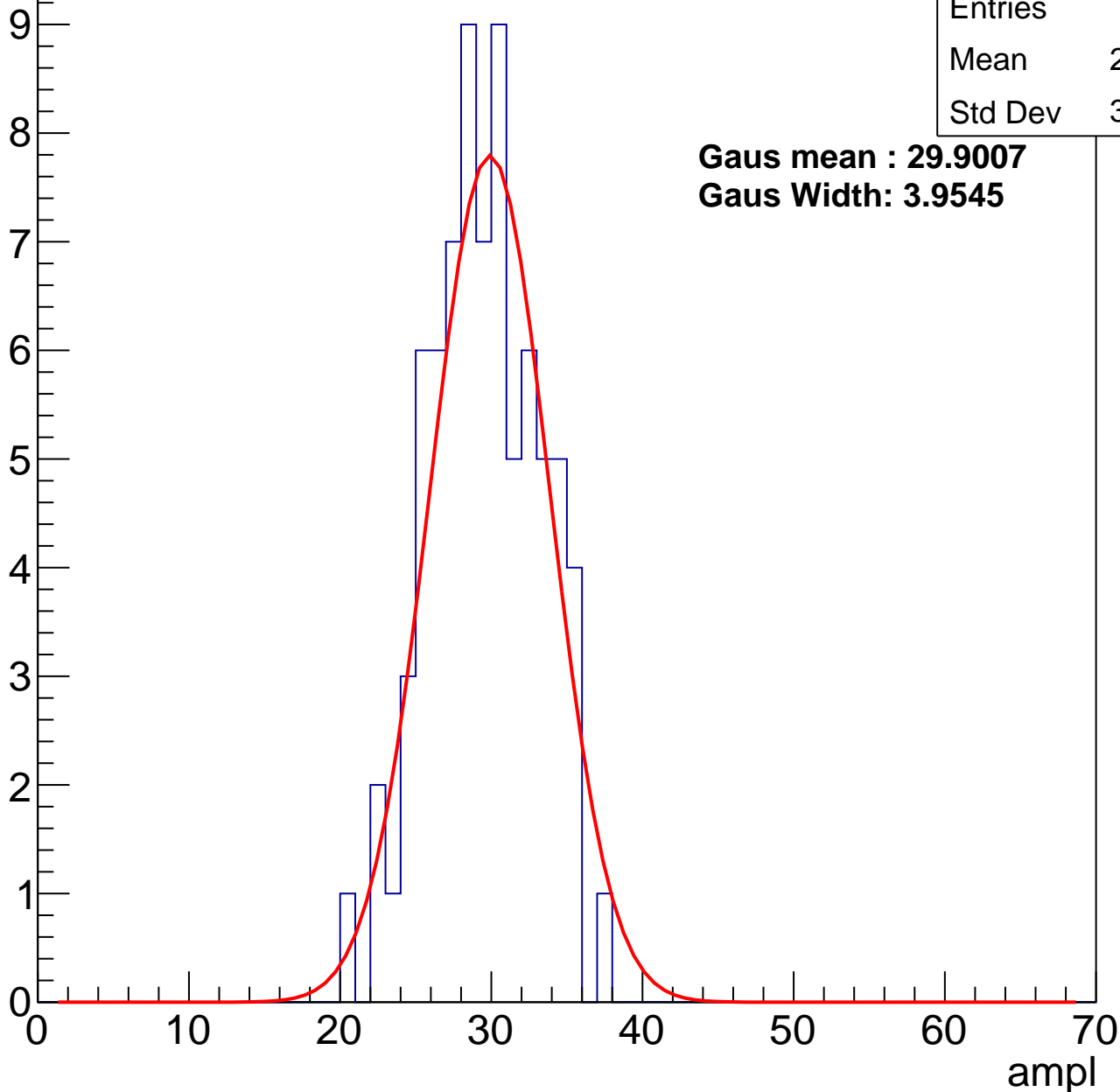
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.06
Std Dev	3.554

Gaus mean : 29.9007

Gaus Width: 3.9545



B1L102S, U2-ch41, adc1

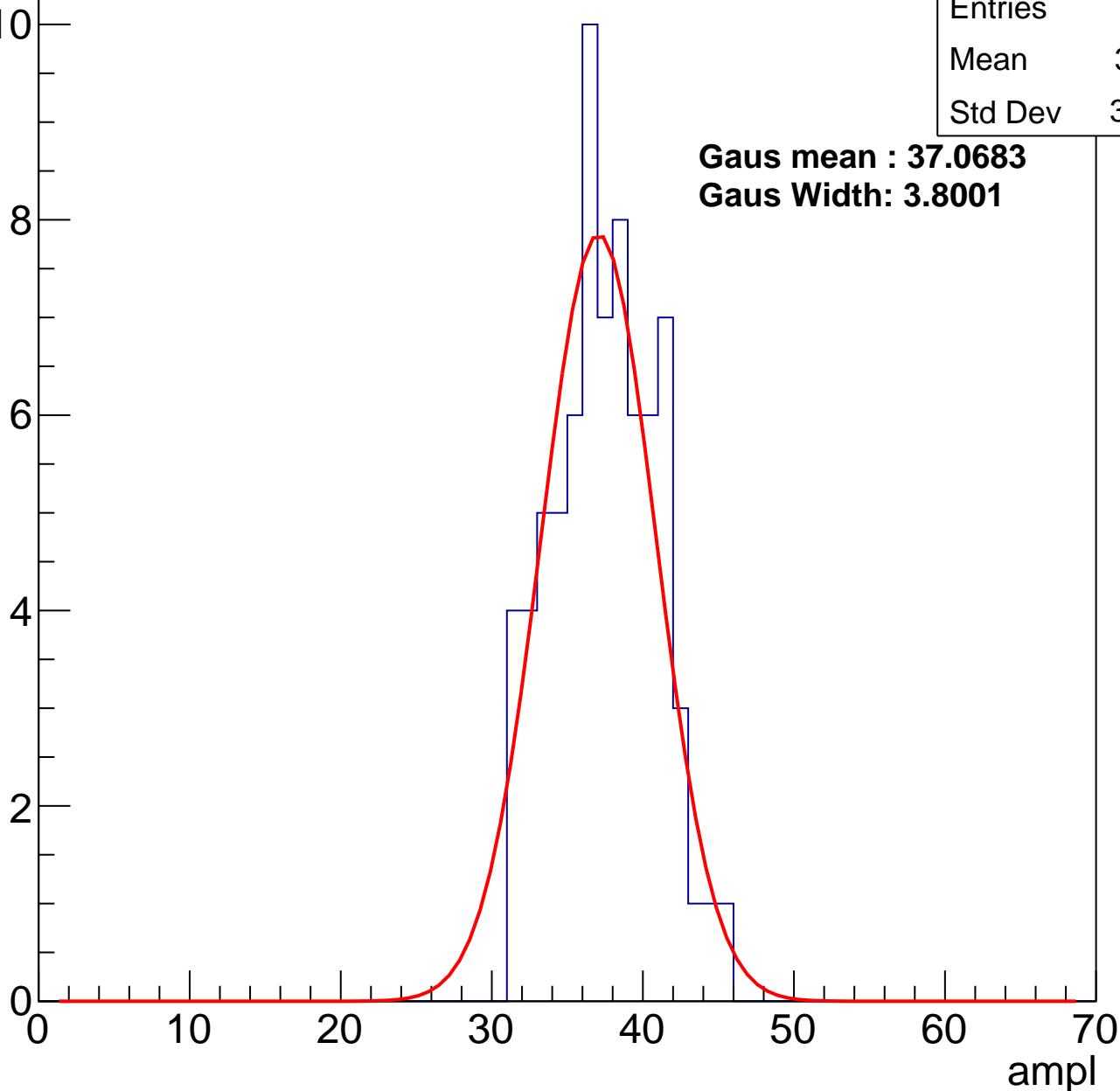
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	37.01
Std Dev	3.339

Gaus mean : 37.0683

Gaus Width: 3.8001



B1L102S, U2-ch41, adc2

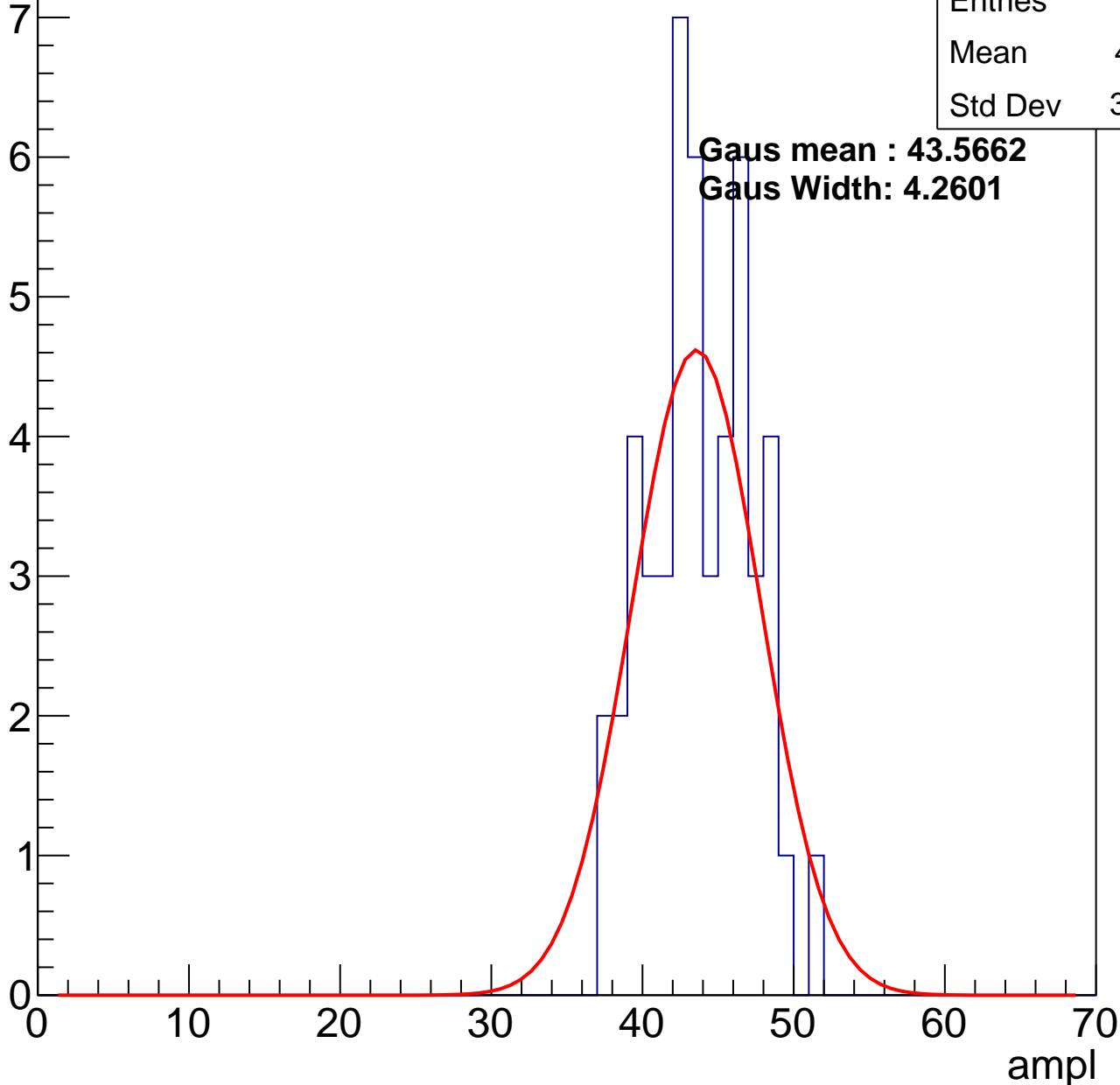
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	43.31
Std Dev	3.345

Gaus mean : 43.5662

Gaus Width: 4.2601

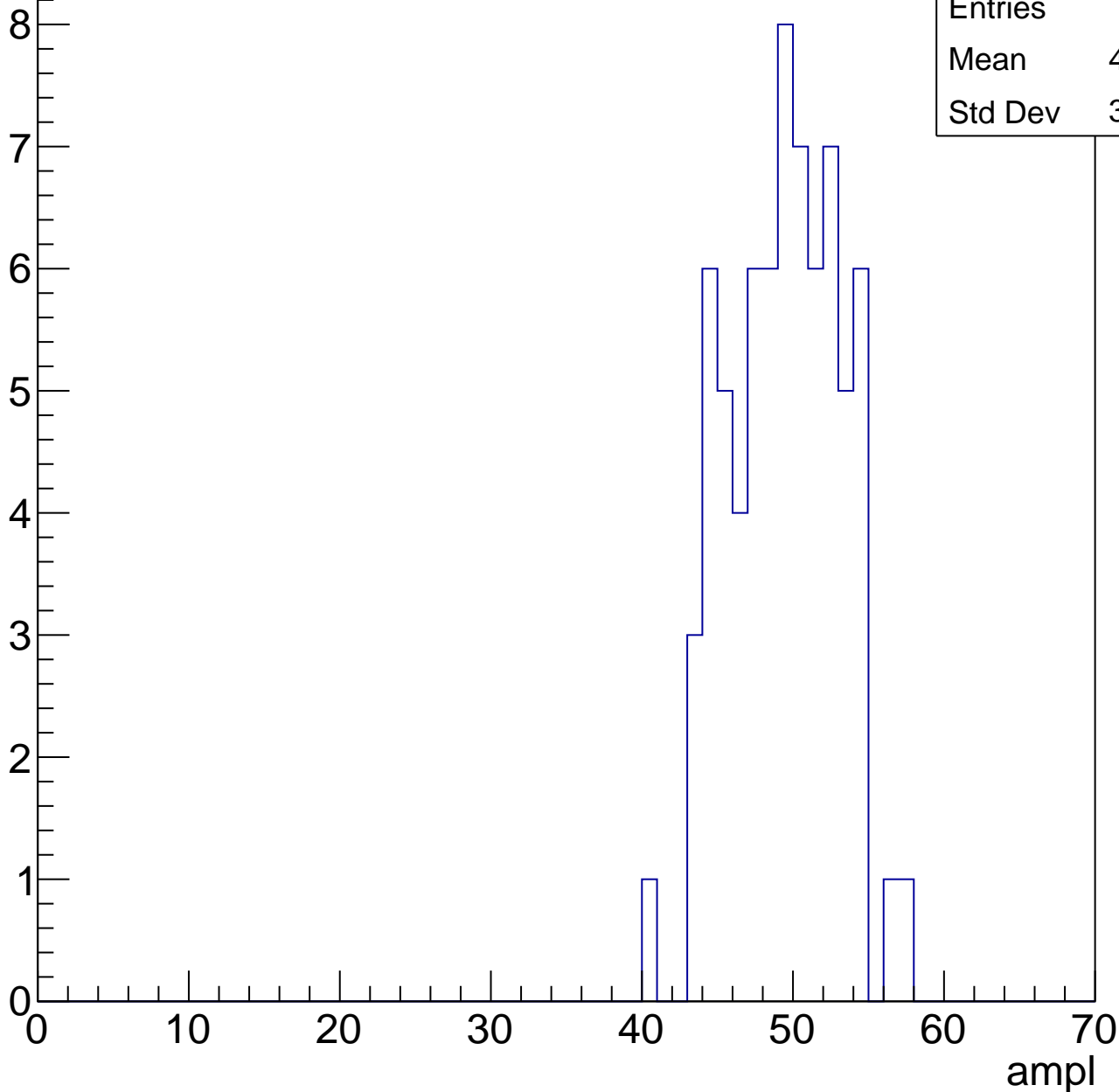


B1L102S, U2-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

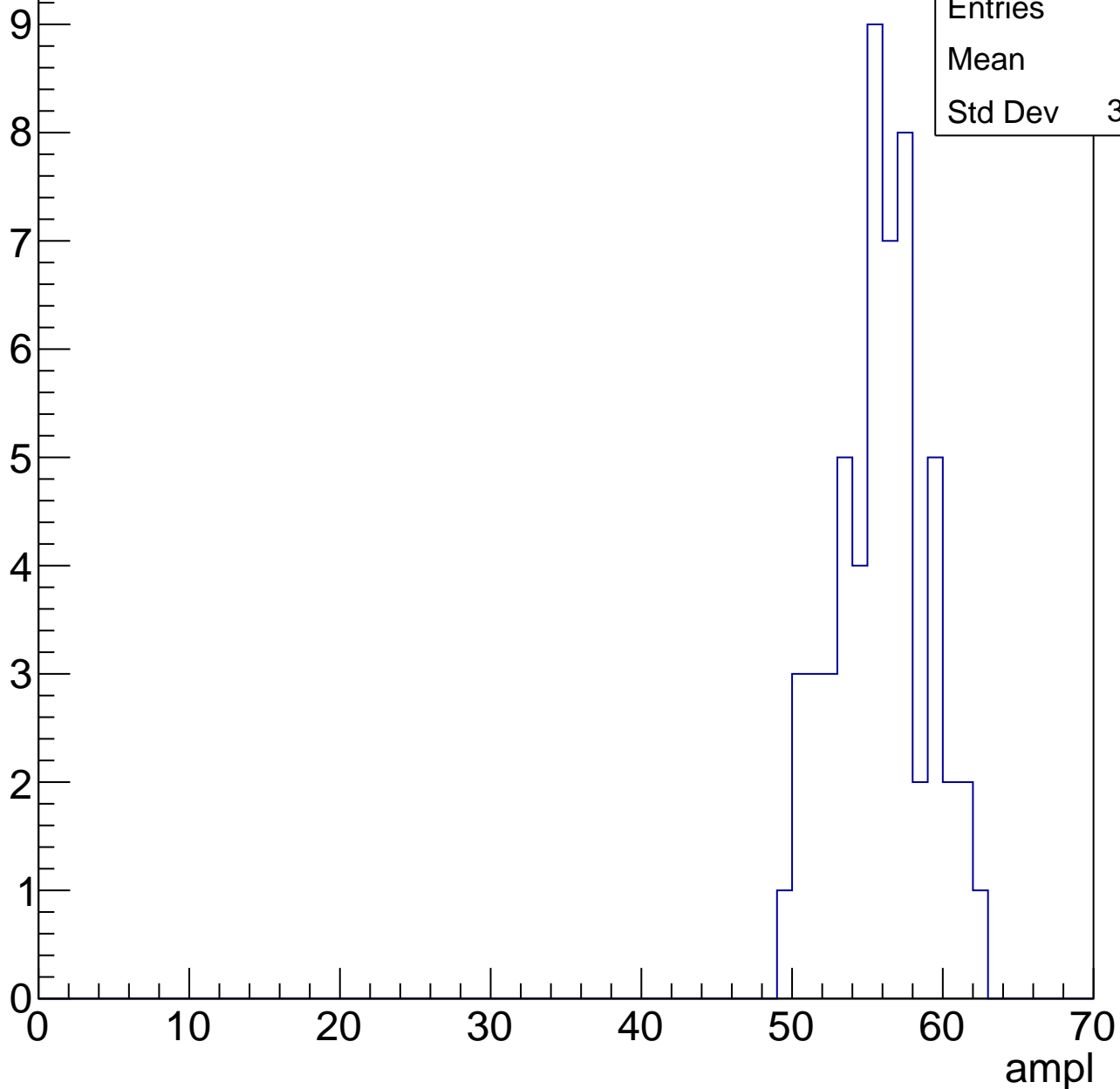
Entries	72
Mean	48.97
Std Dev	3.578



B1L102S, U2-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

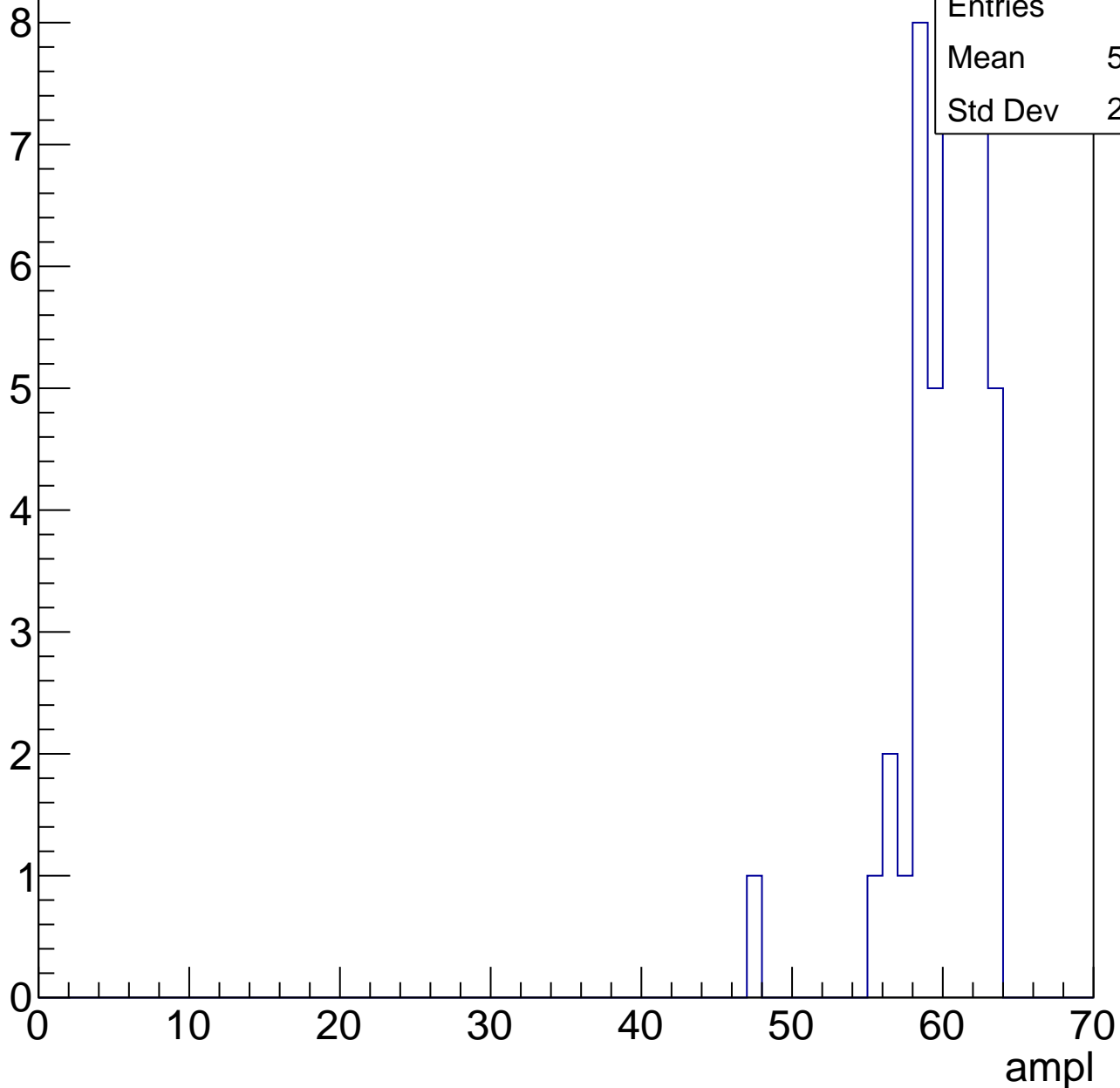
Entry



B1L102S, U2-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

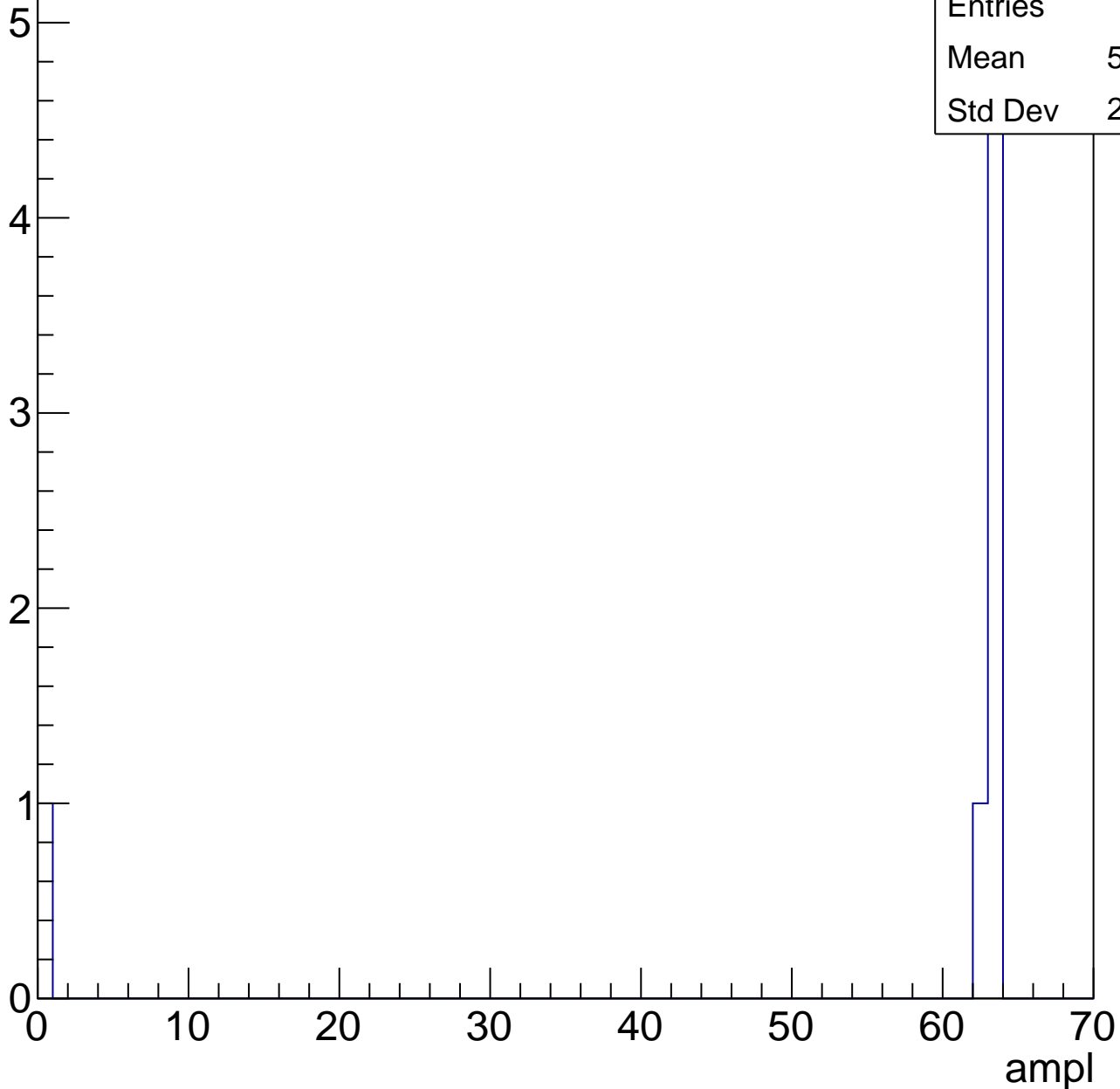


B1L102S, U2-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	53.86
Std Dev	21.99



B1L102S, U2-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch42, adc0

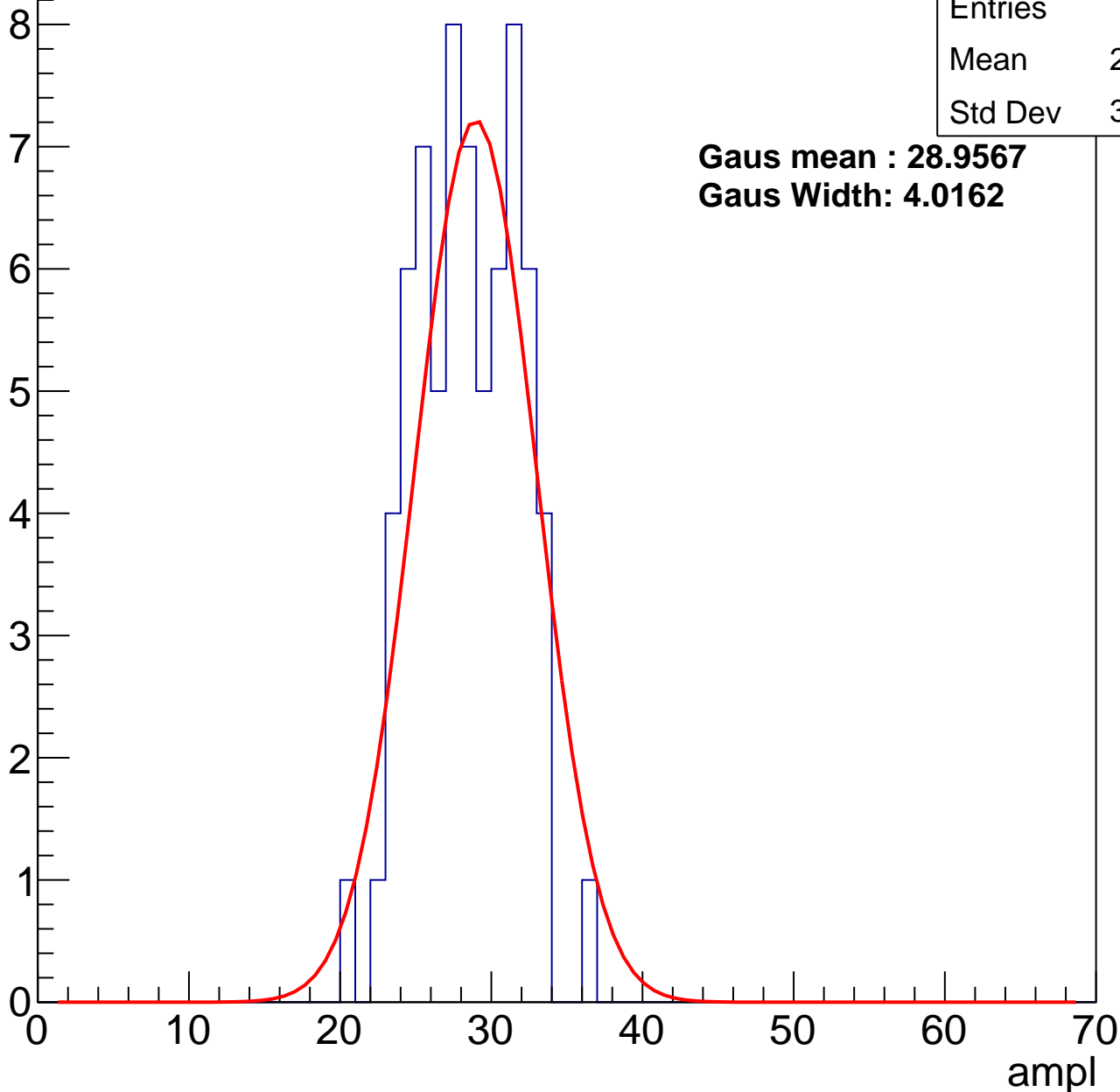
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.94
Std Dev	3.292

Gaus mean : 28.9567

Gaus Width: 4.0162



B1L102S, U2-ch42, adc1

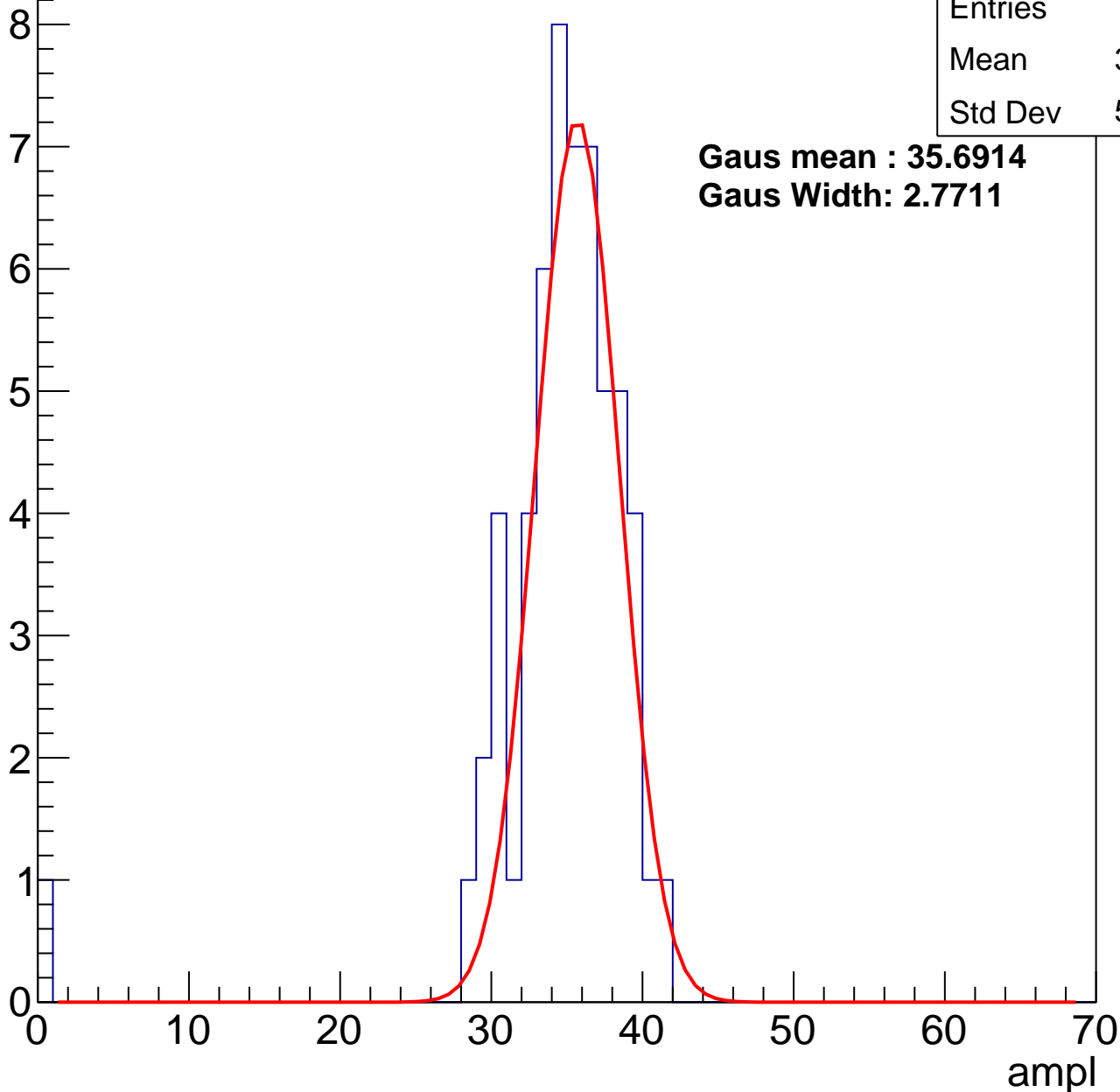
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.11
Std Dev	5.431

Gaus mean : 35.6914

Gaus Width: 2.7711



B1L102S, U2-ch42, adc2

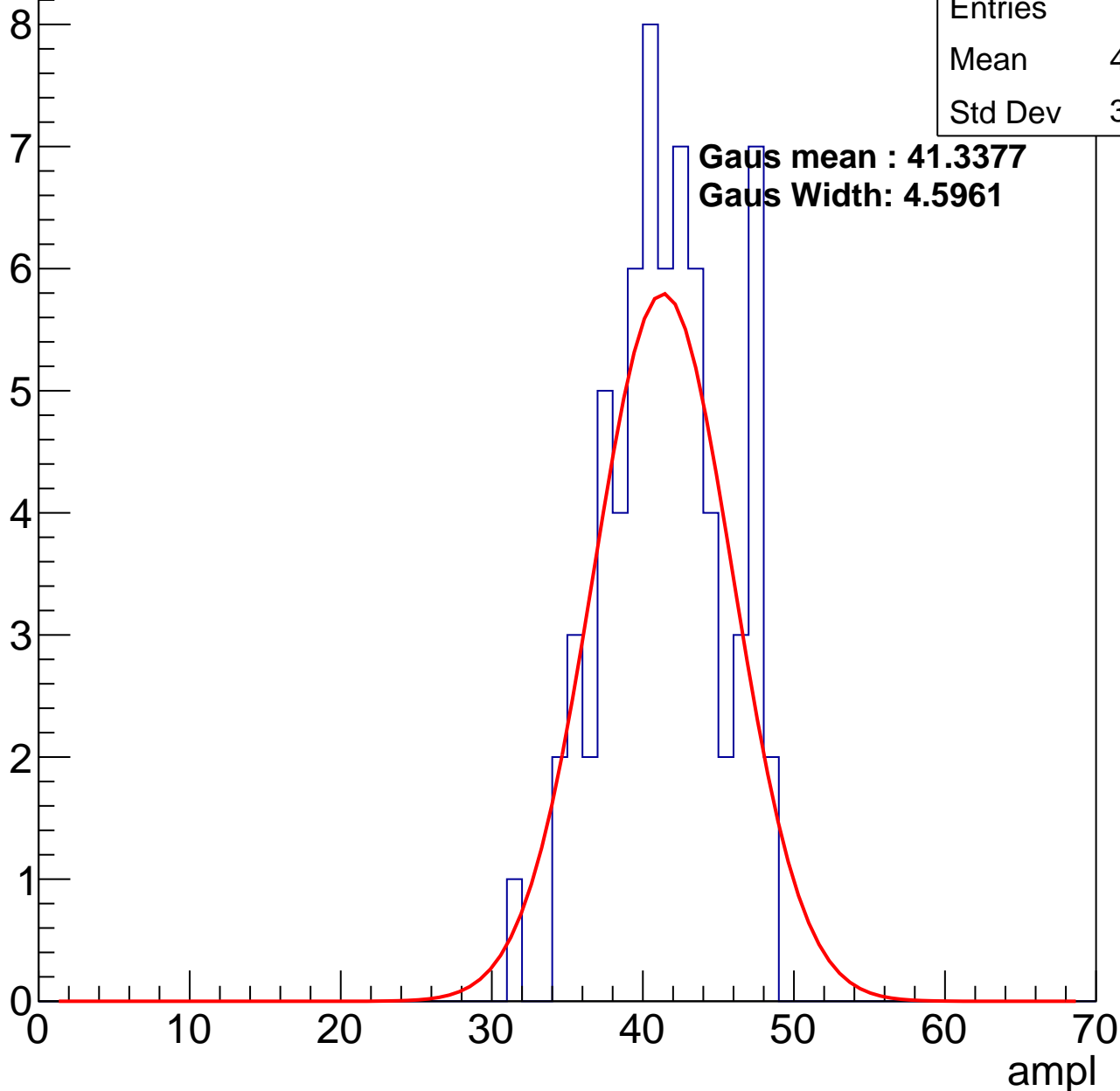
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.09
Std Dev	3.887

Gaus mean : 41.3377

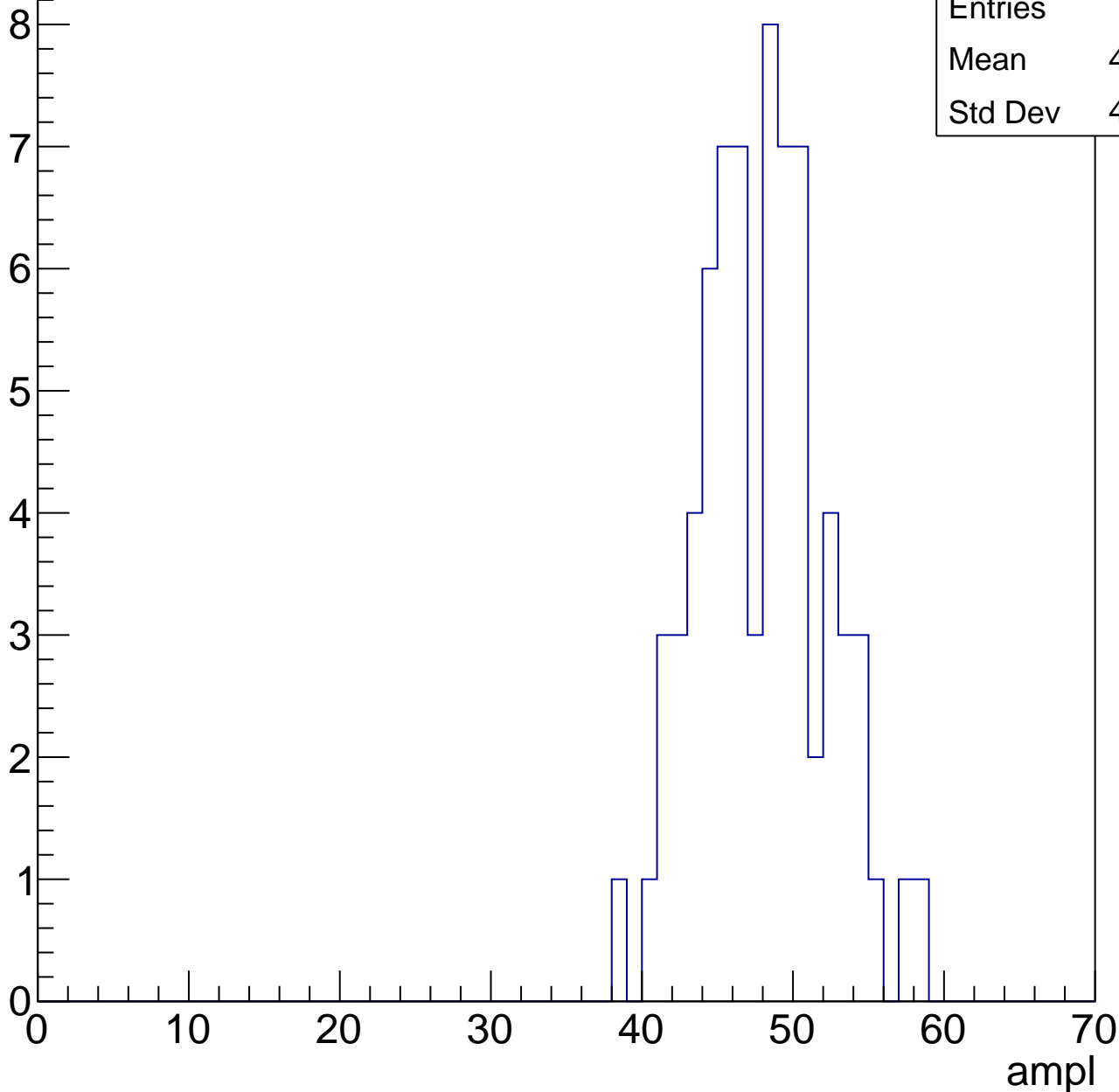
Gaus Width: 4.5961



B1L102S, U2-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

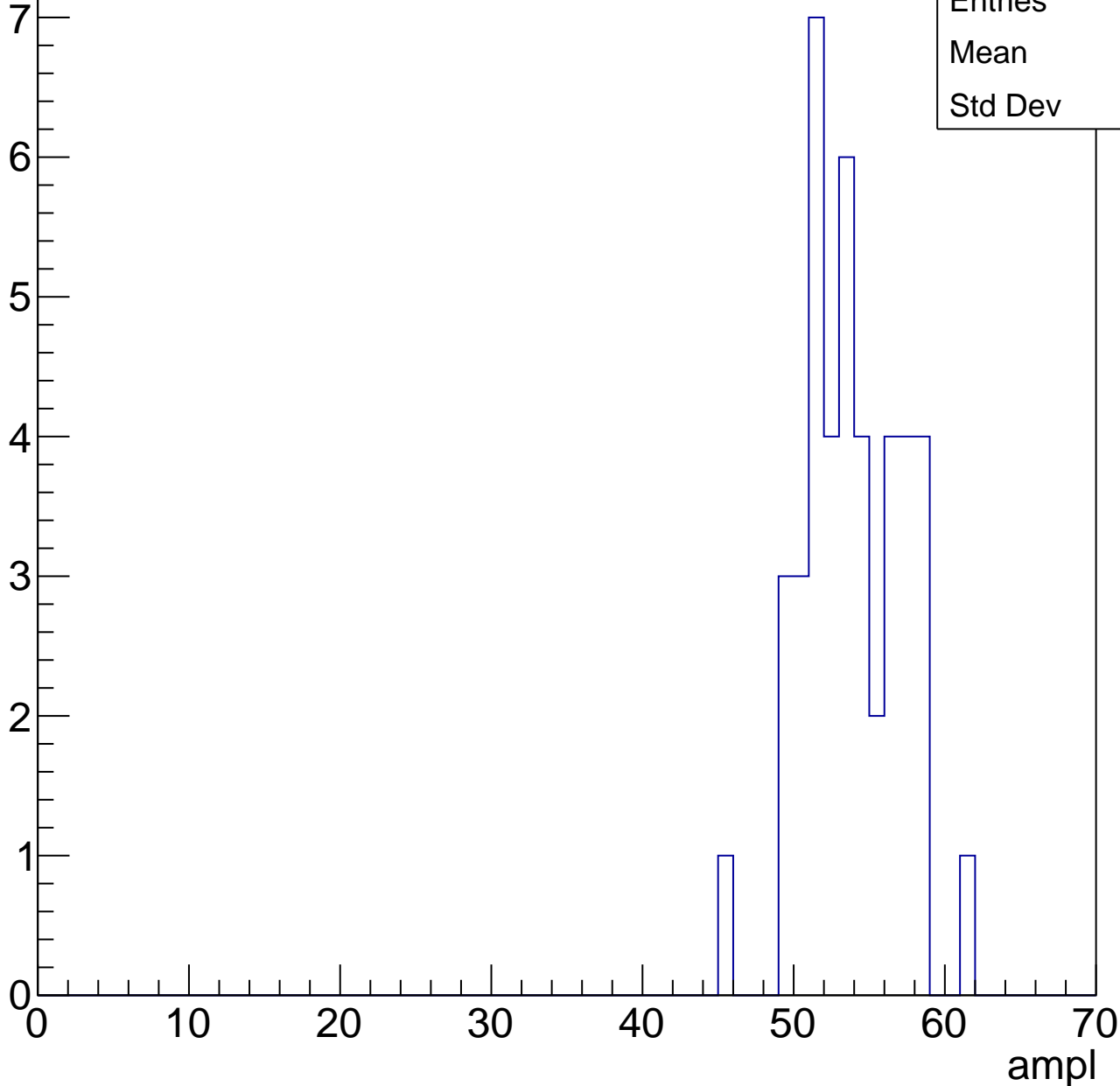


B1L102S, U2-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	53.4
Std Dev	3.2

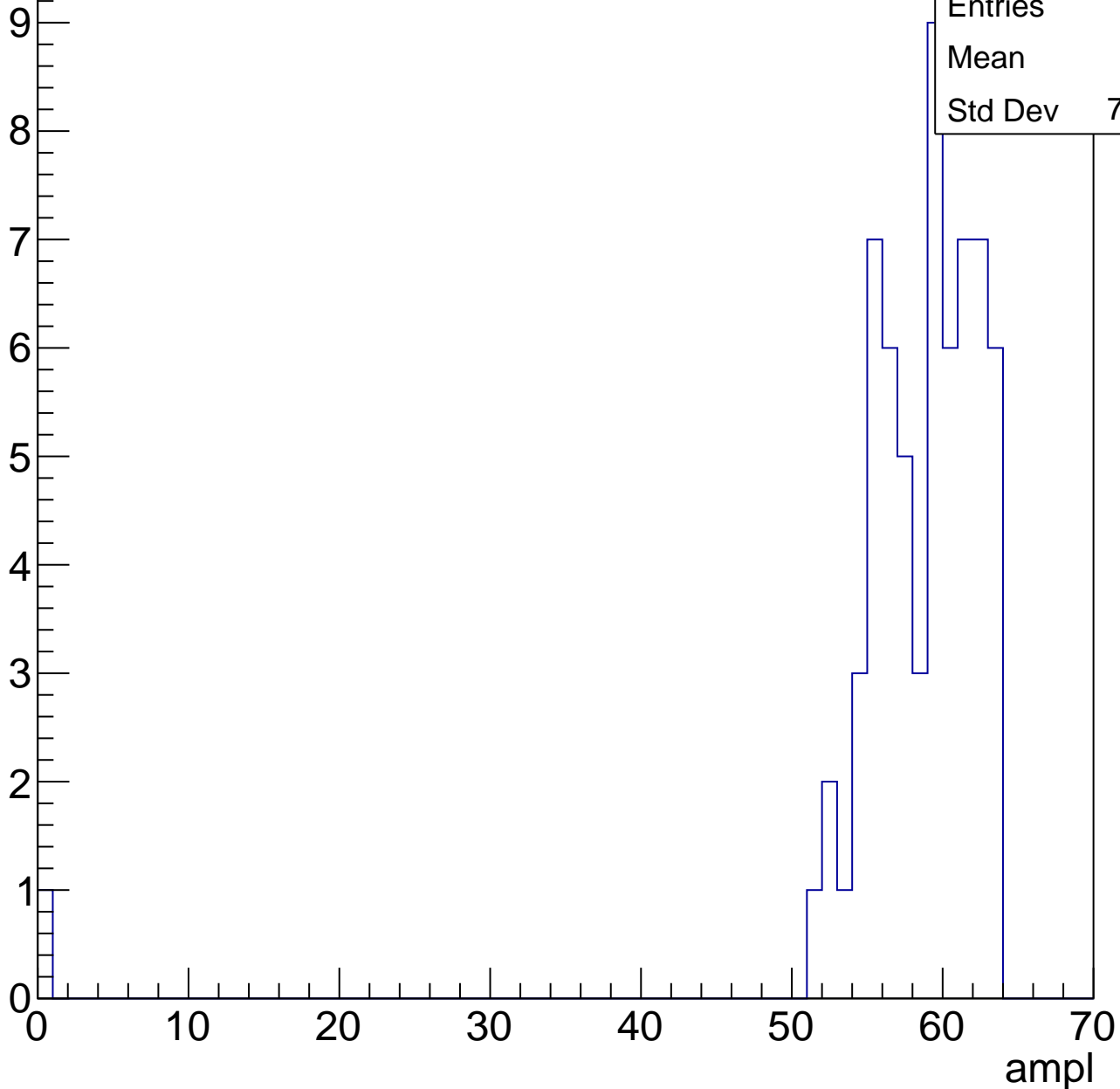


B1L102S, U2-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

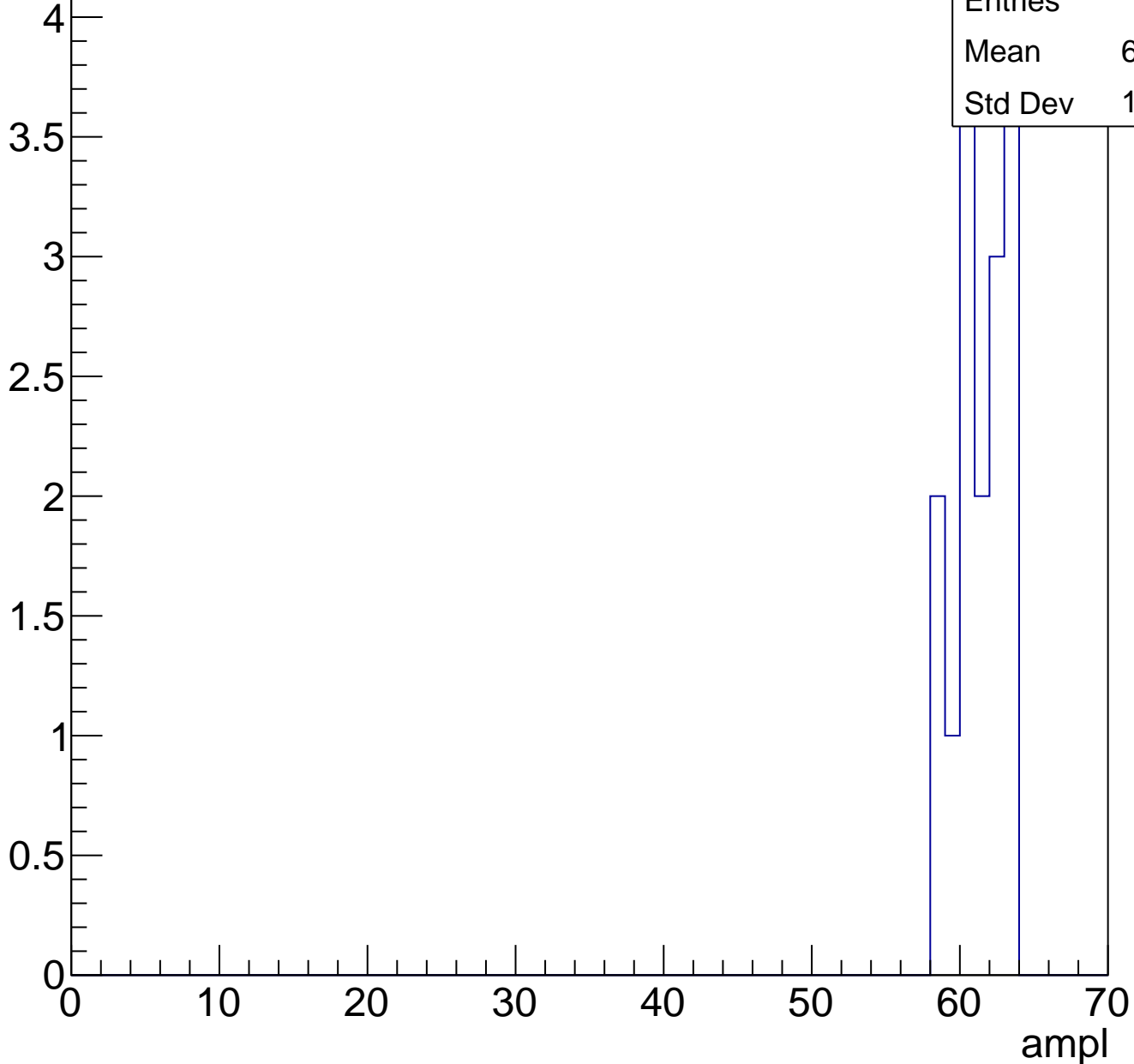
Entries	64
Mean	57.5
Std Dev	7.898



B1L102S, U2-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch43, adc0

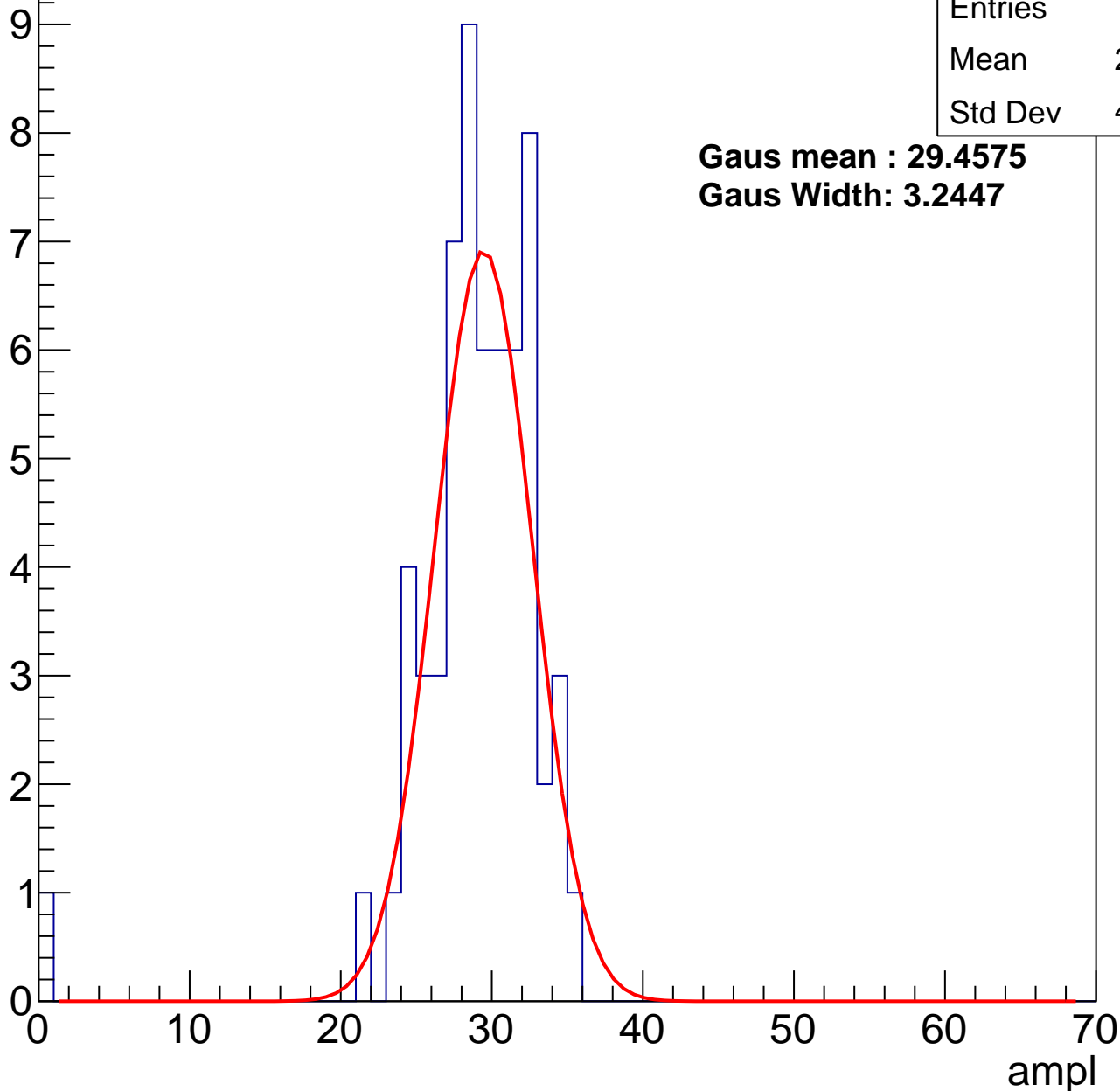
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.41
Std Dev	4.751

Gaus mean : 29.4575

Gaus Width: 3.2447



B1L102S, U2-ch43, adc1

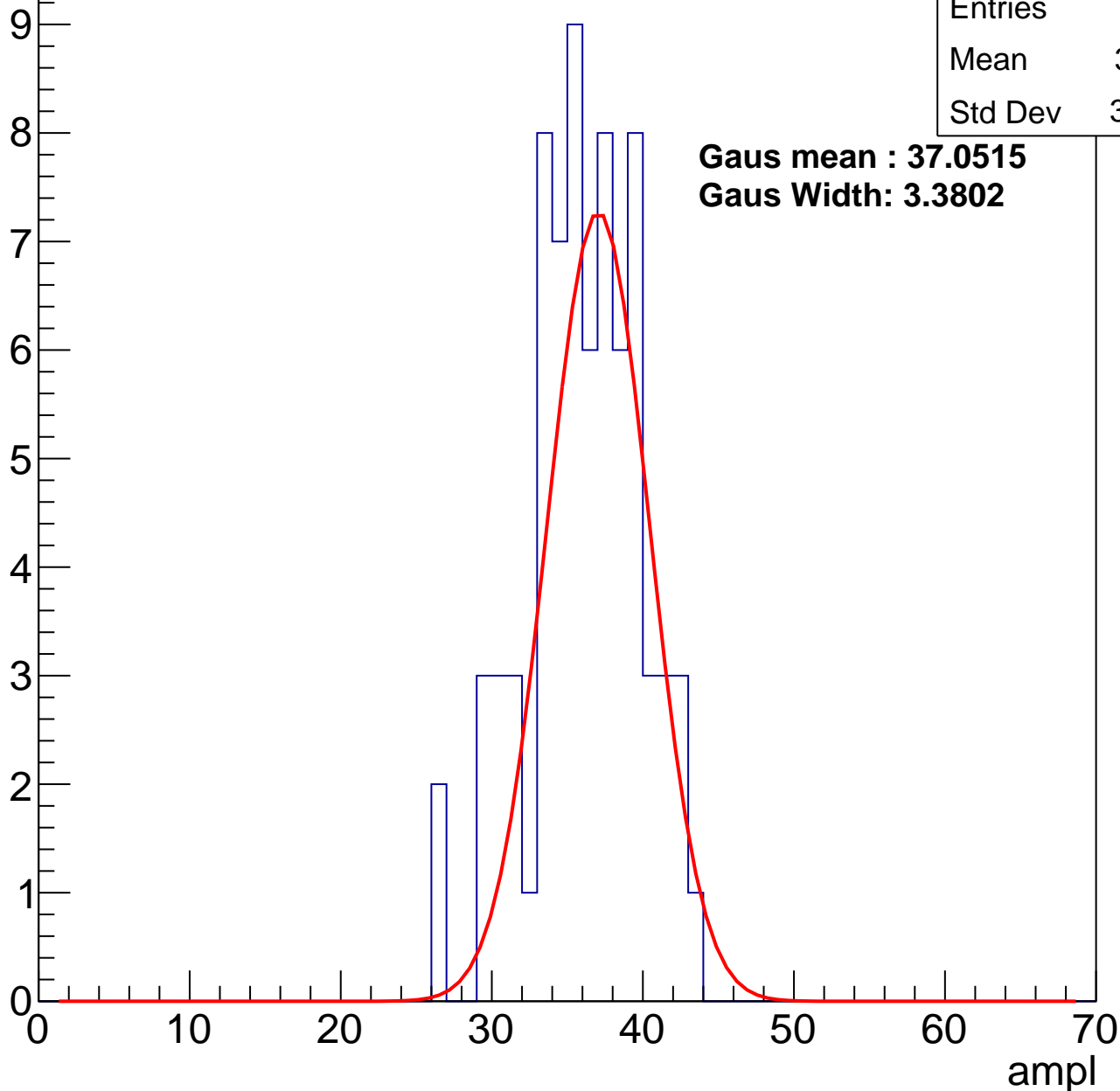
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.61
Std Dev	3.727

Gaus mean : 37.0515

Gaus Width: 3.3802



B1L102S, U2-ch43, adc2

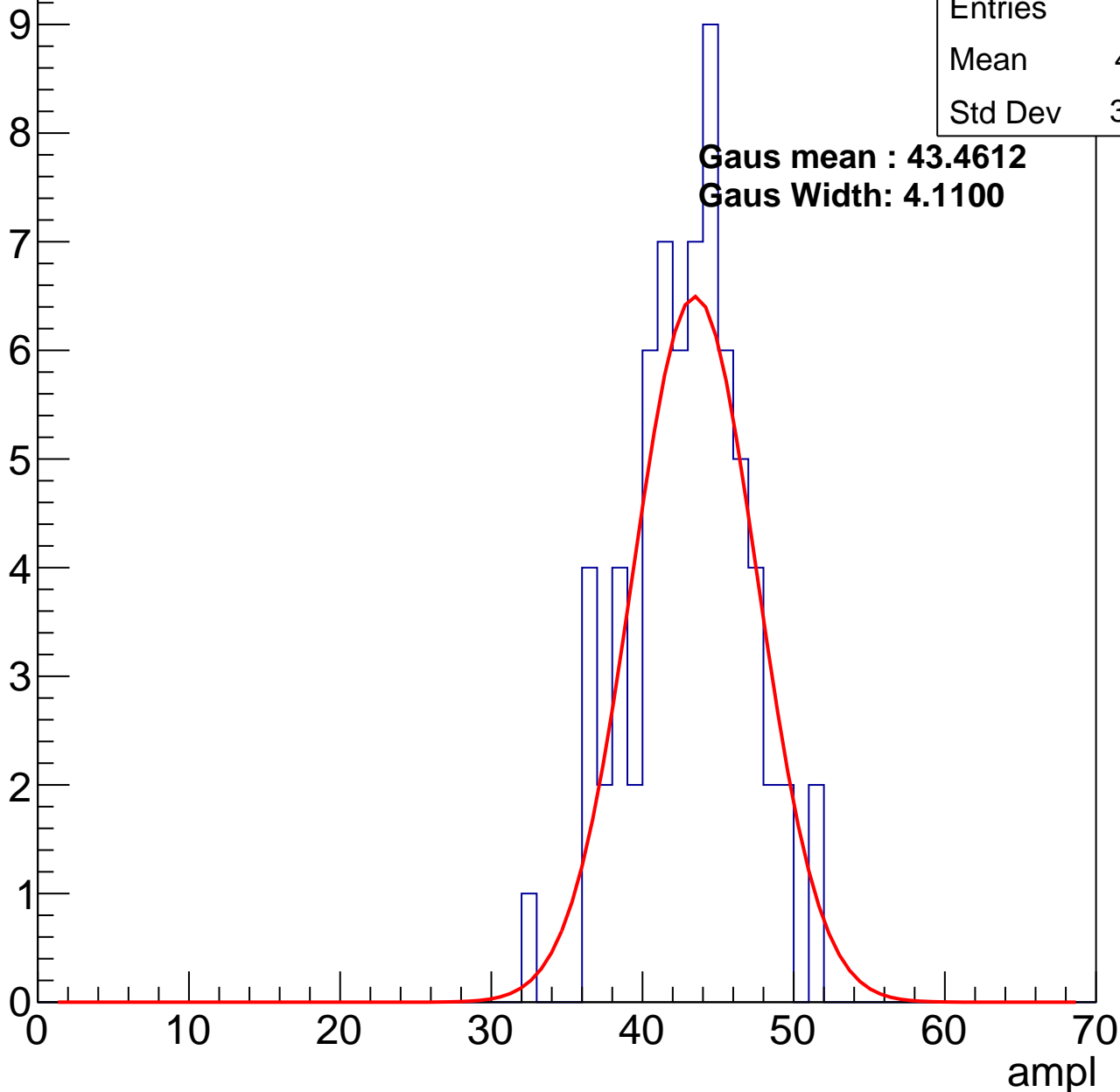
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.61
Std Dev	3.789

Gaus mean : 43.4612

Gaus Width: 4.1100

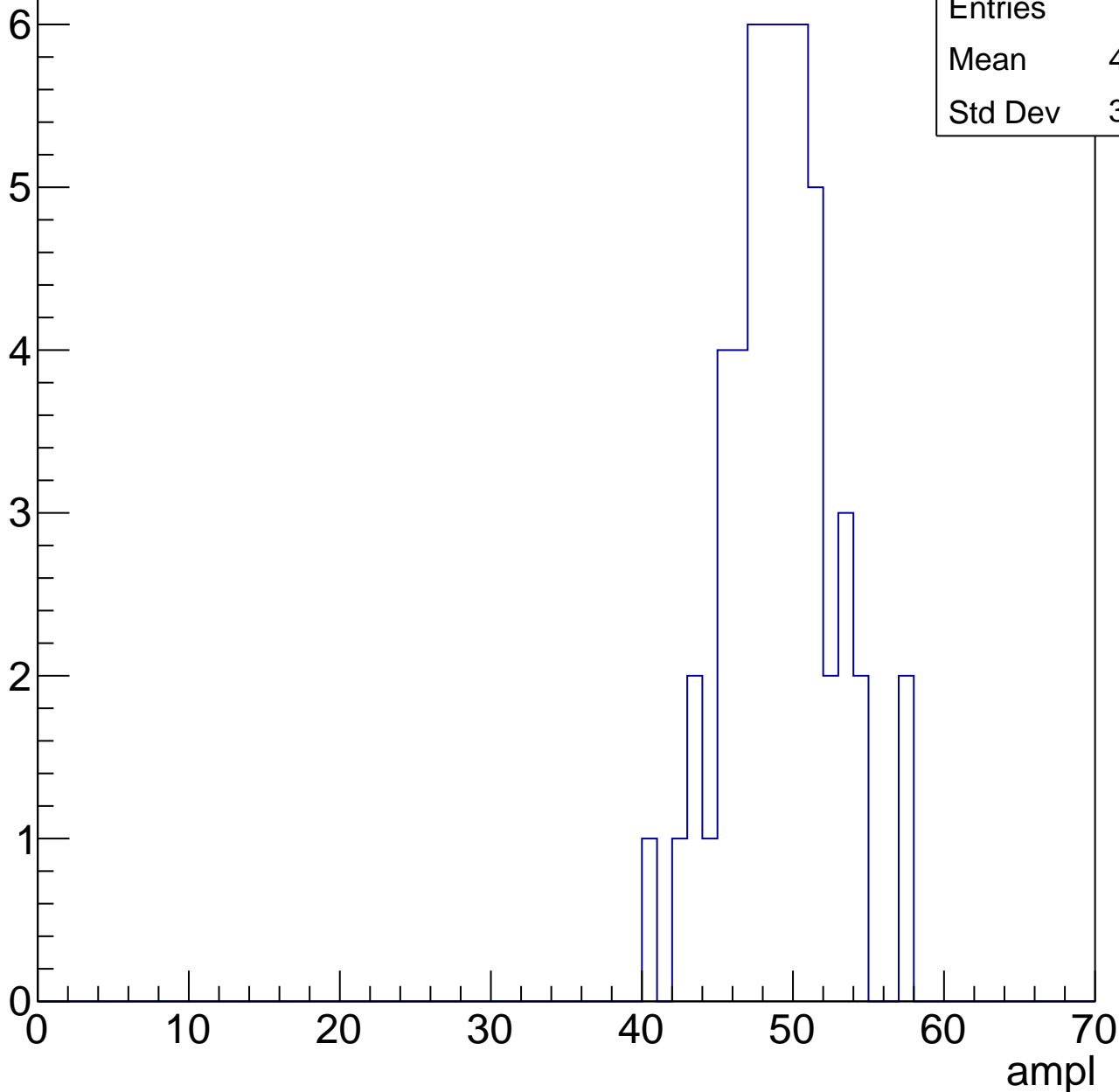


B1L102S, U2-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

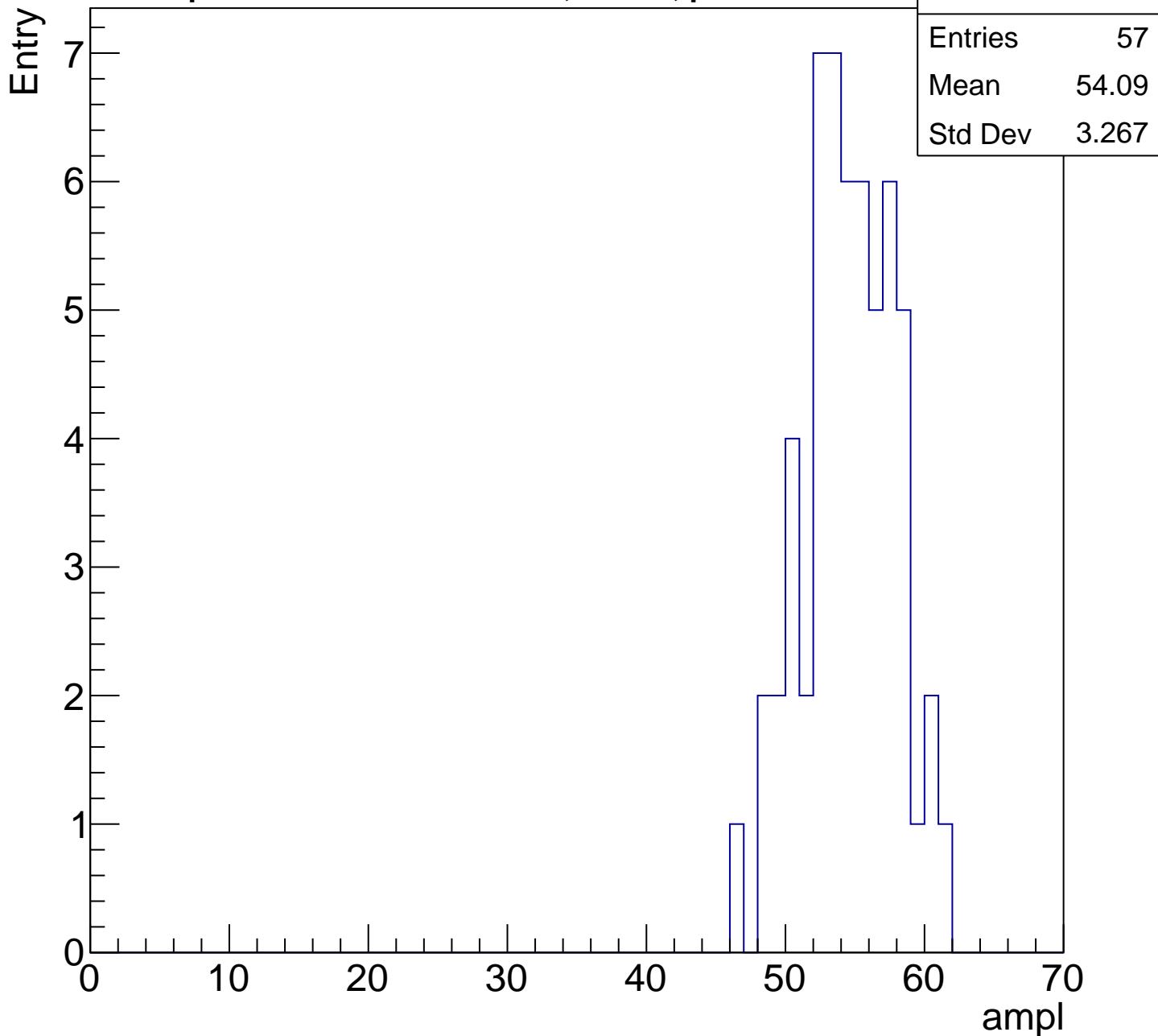
Entry

Entries	51
Mean	48.63
Std Dev	3.486



B1L102S, U2-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

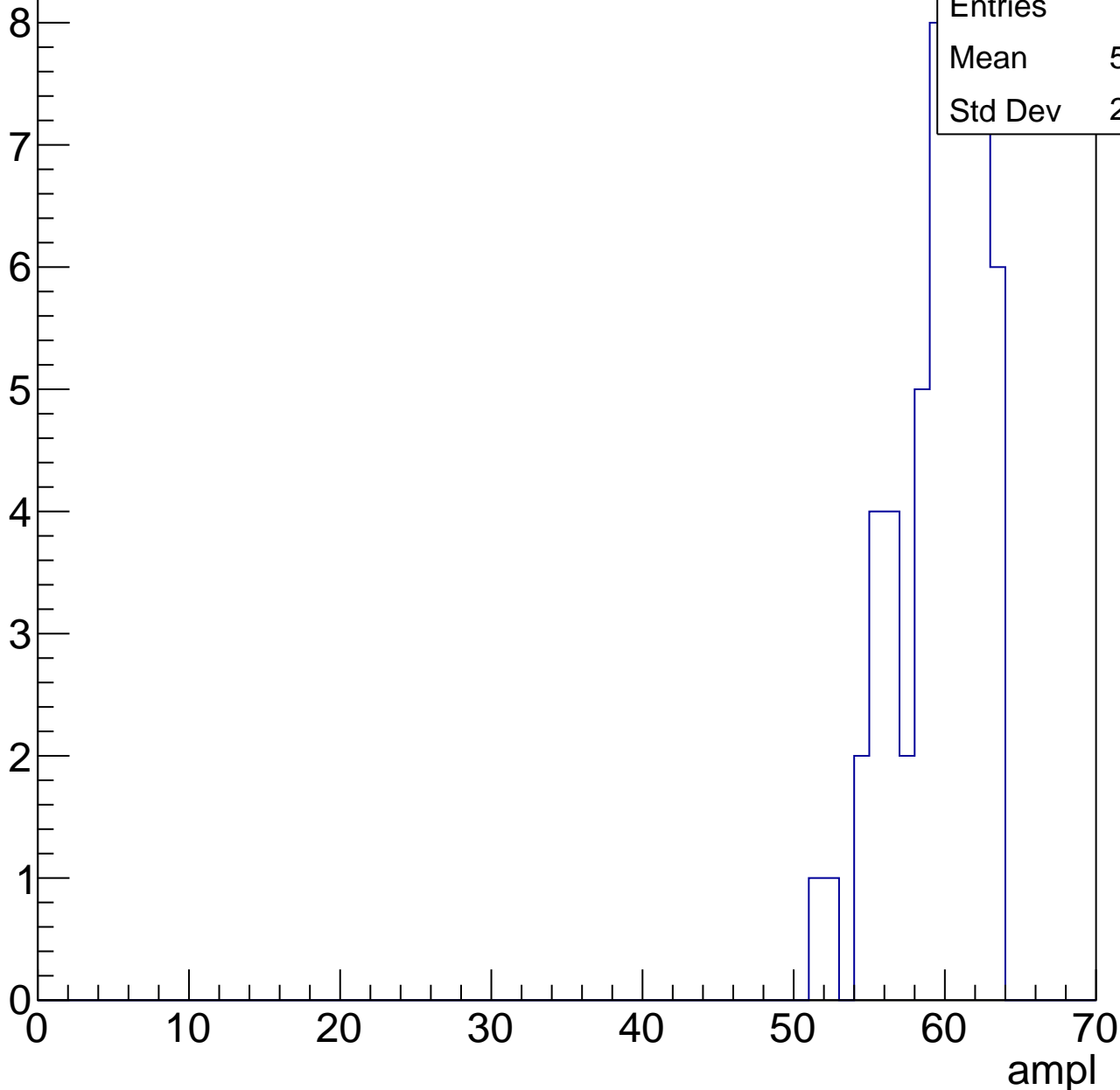


B1L102S, U2-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

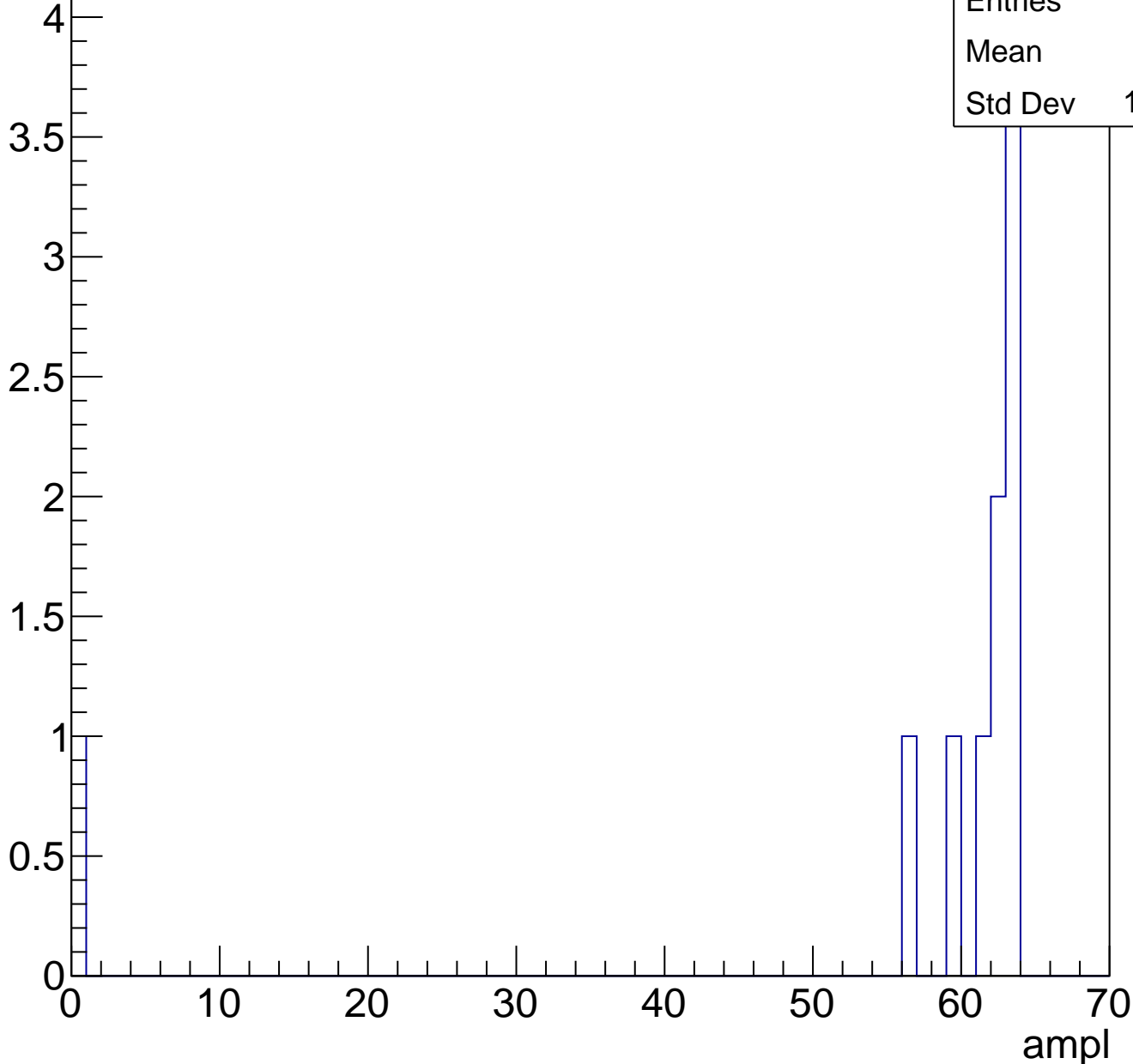
Entries	57
Mean	59.18
Std Dev	2.909



B1L102S, U2-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch44, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	26.95
Std Dev	4.98

Gaus mean : 27.5537

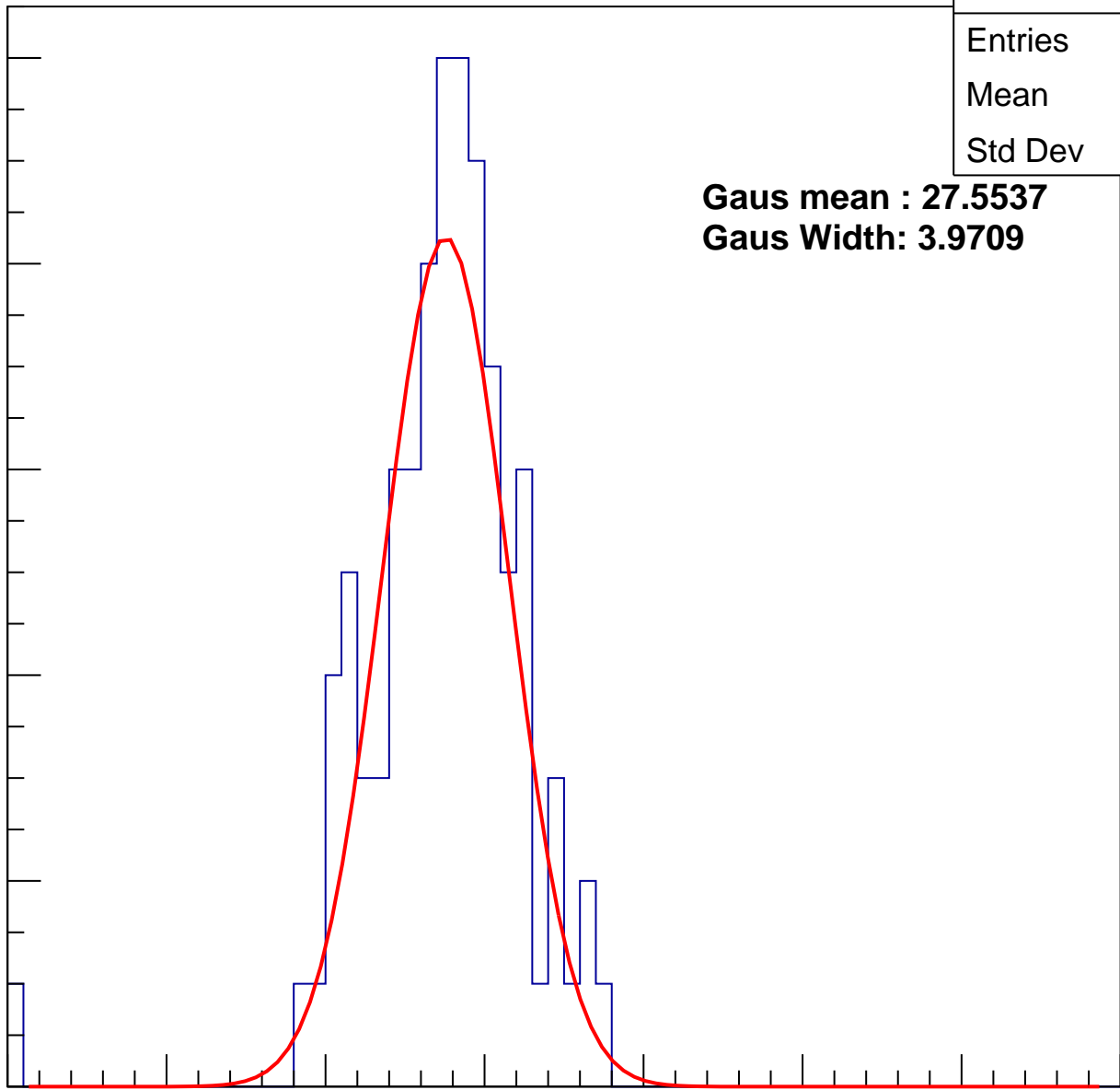
Gaus Width: 3.9709

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch44, adc1

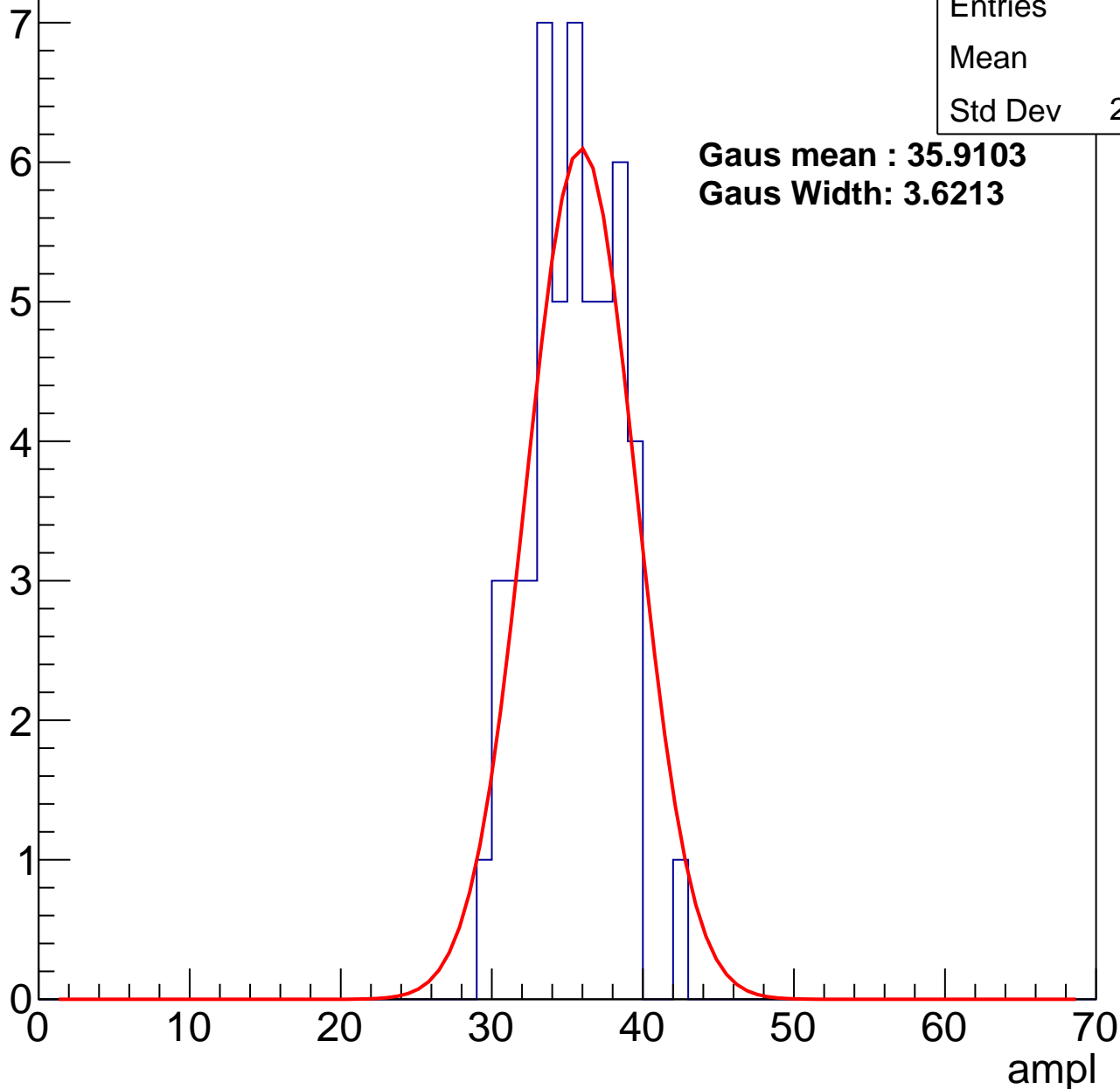
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	34.9
Std Dev	2.865

Gaus mean : 35.9103

Gaus Width: 3.6213



B1L102S, U2-ch44, adc2

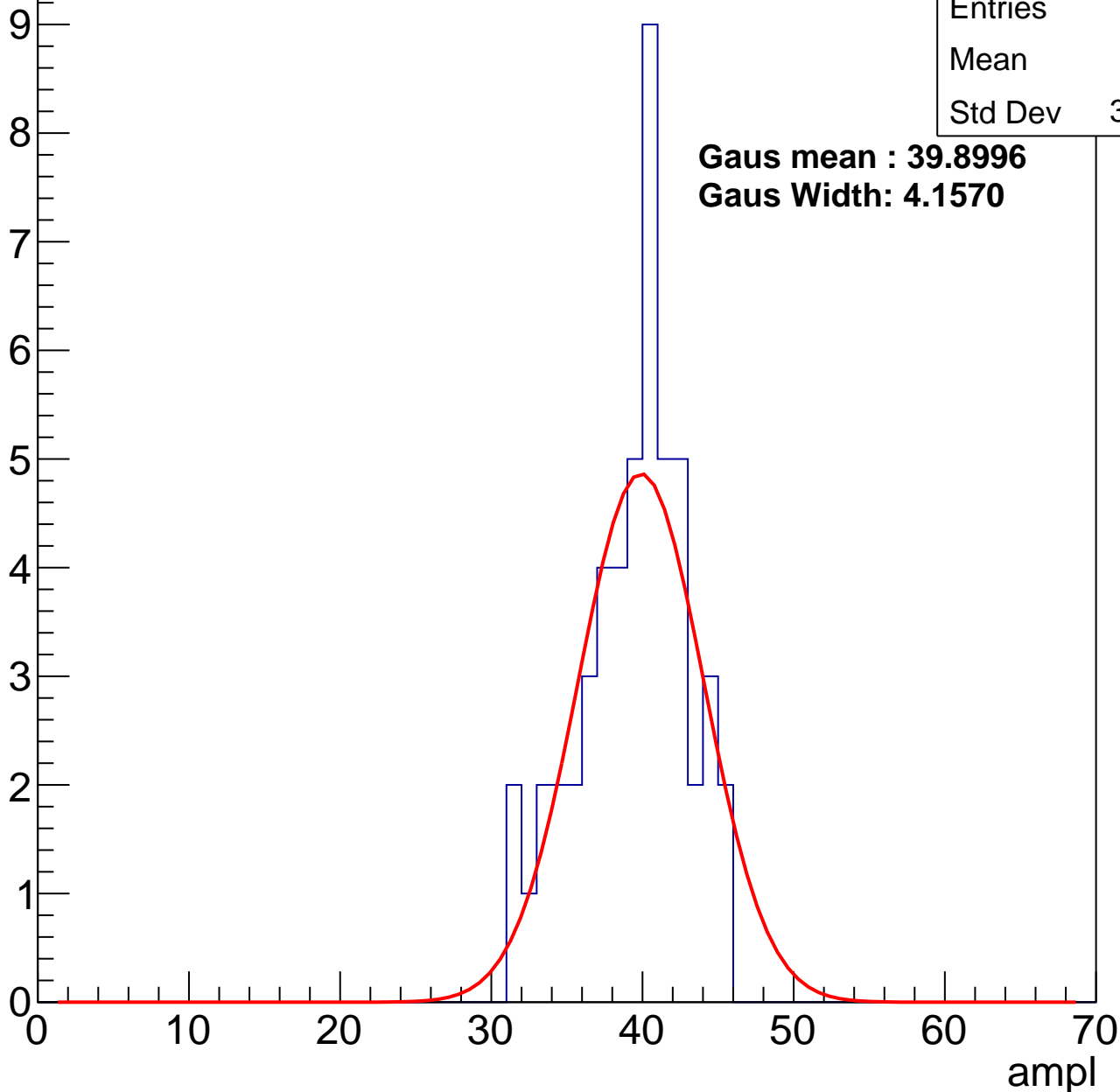
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	38.9
Std Dev	3.516

Gaus mean : 39.8996

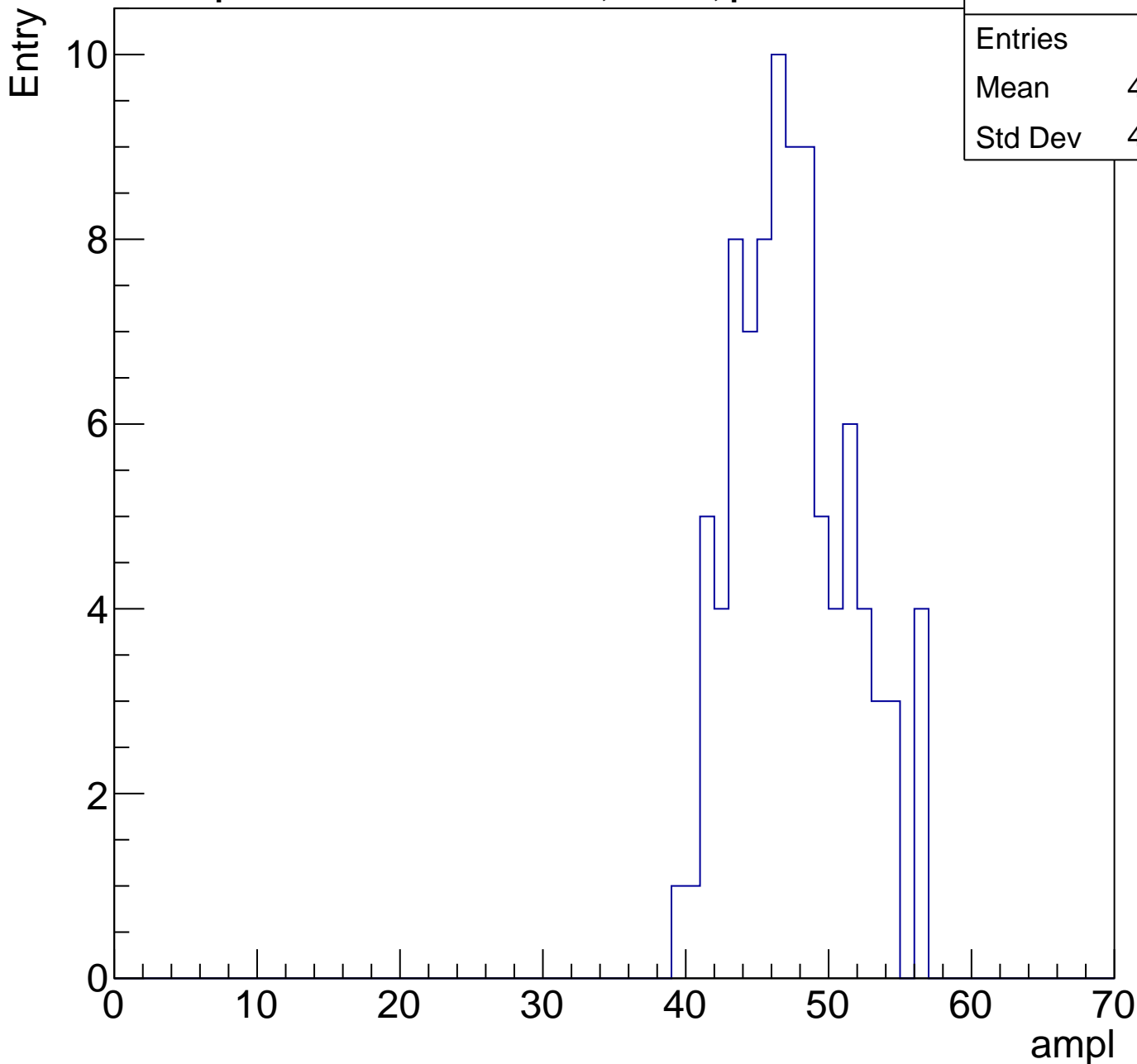
Gaus Width: 4.1570



B1L102S, U2-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	47.07
Std Dev	4.013

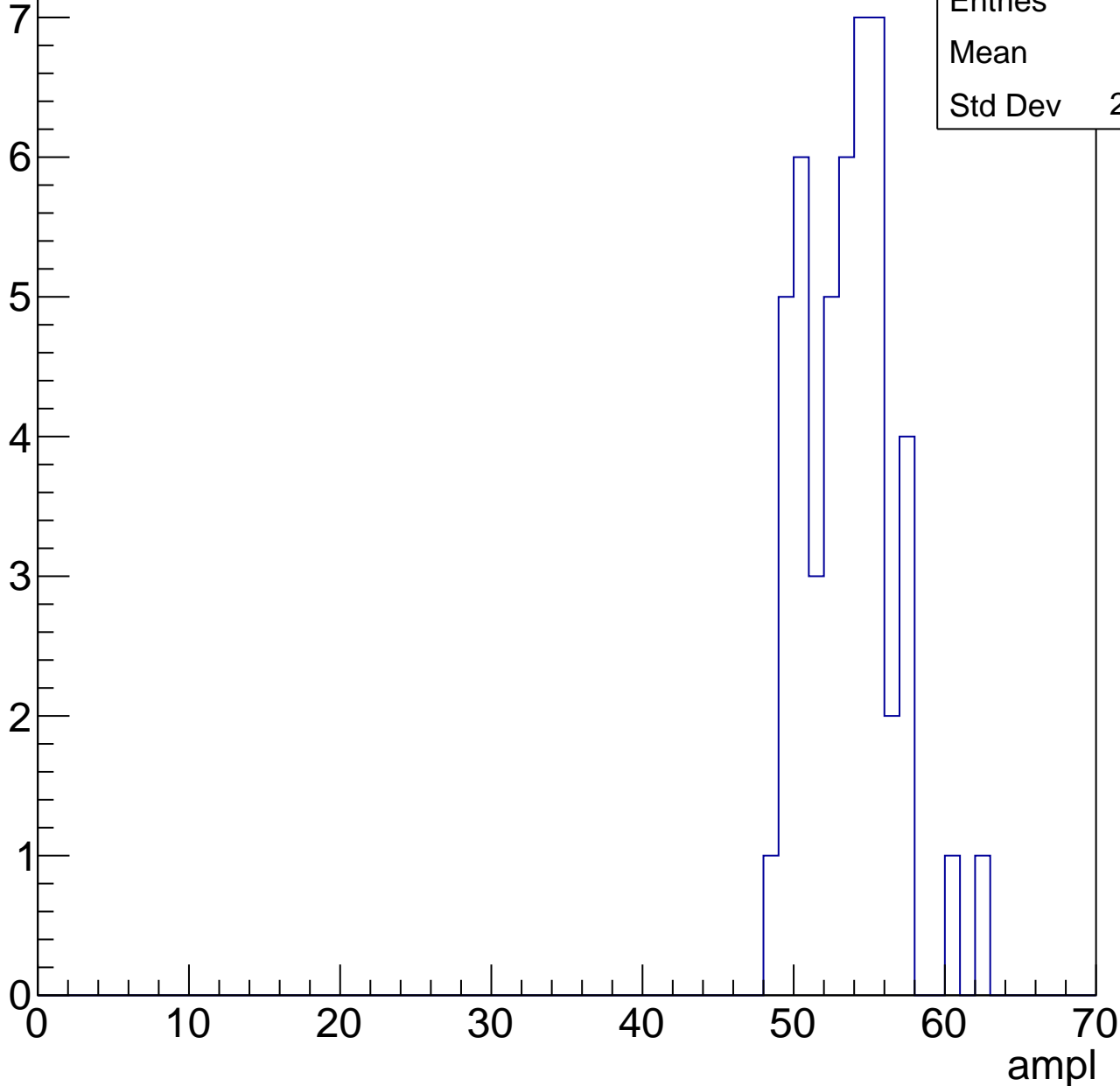


B1L102S, U2-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	53.1
Std Dev	2.967



B1L102S, U2-ch44, adc5

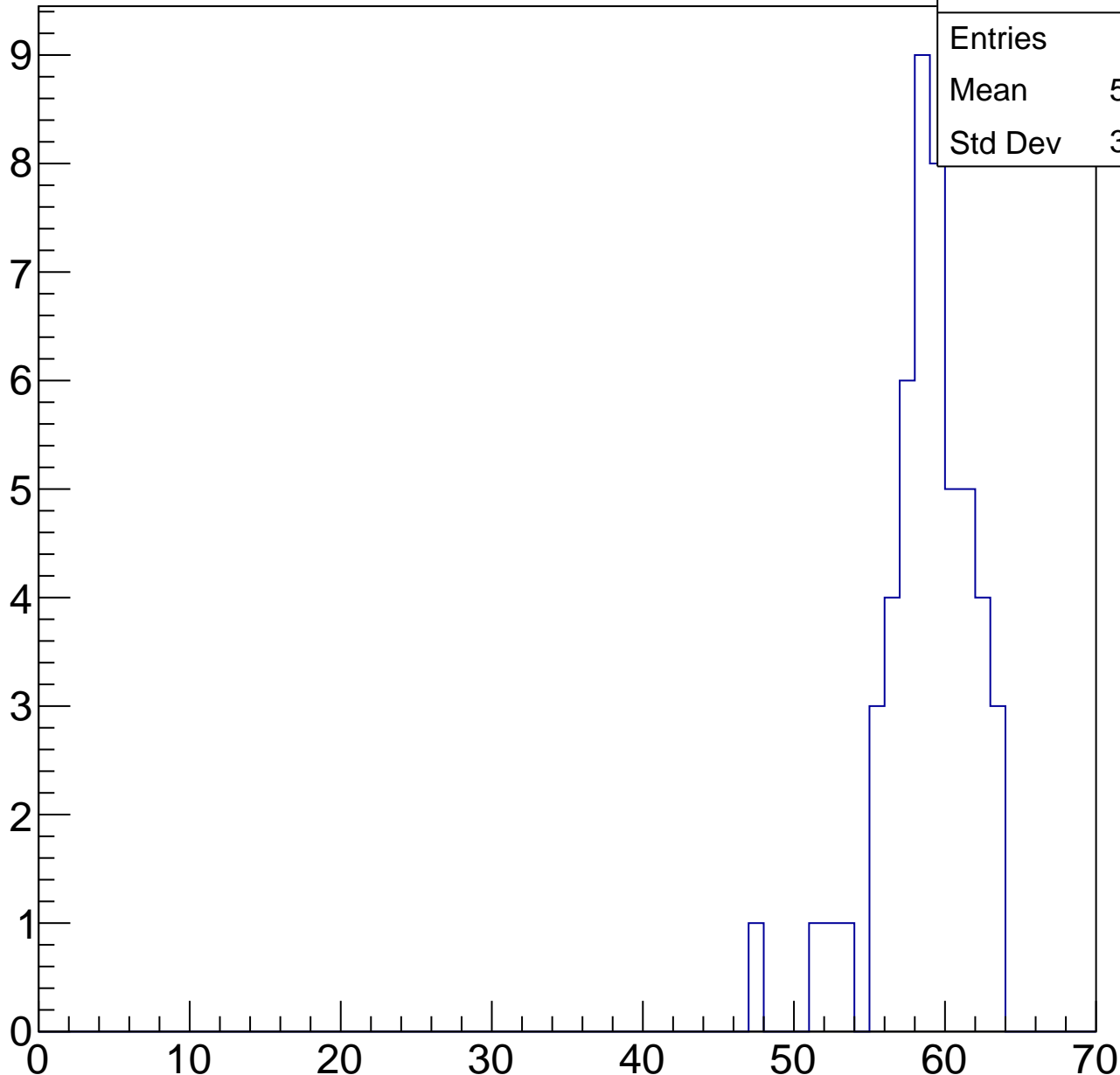
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.24
Std Dev	3.097

ampl

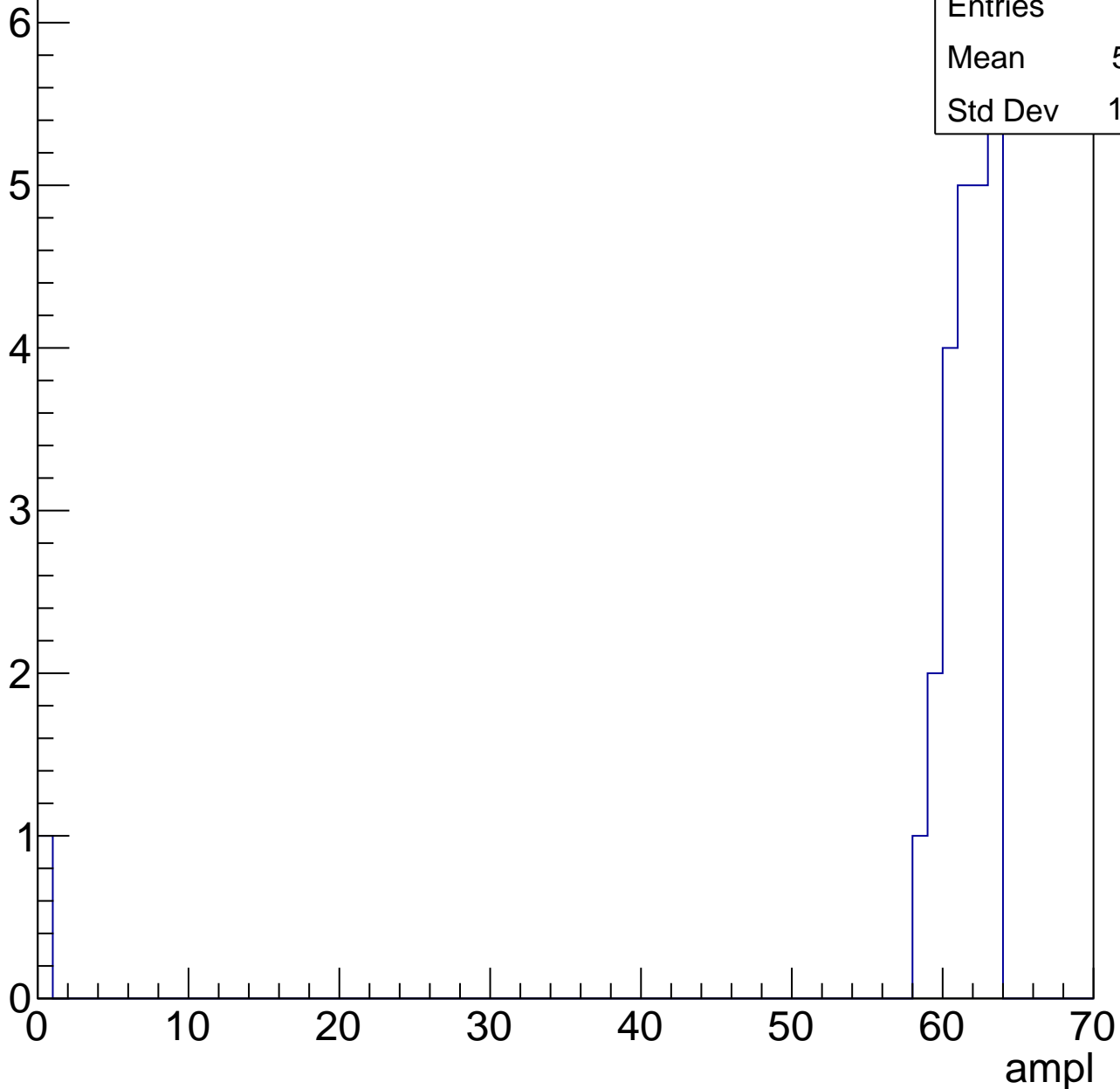


B1L102S, U2-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	58.71
Std Dev	12.32



B1L102S, U2-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch45, adc0

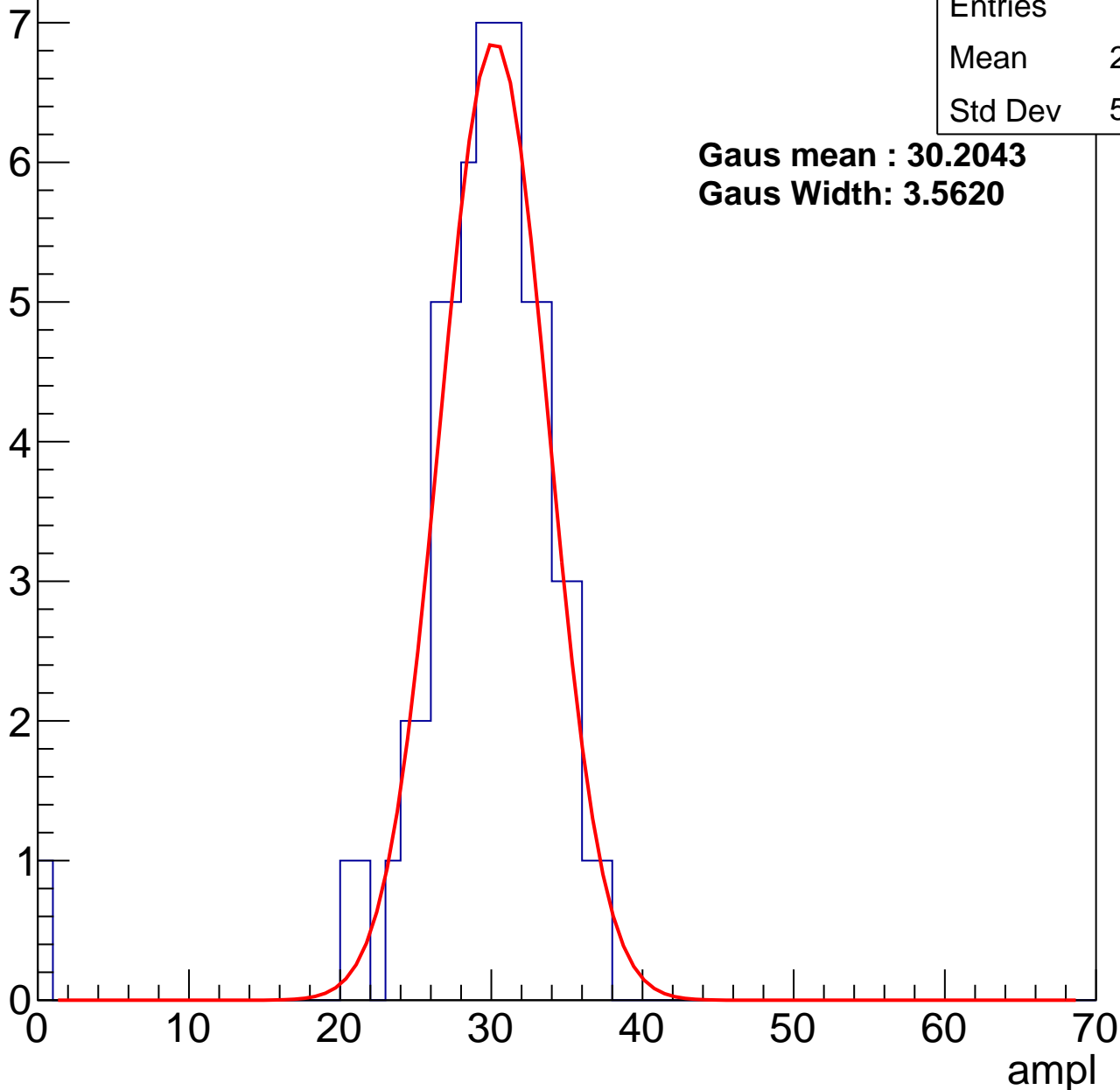
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.05
Std Dev	5.088

Gaus mean : 30.2043

Gaus Width: 3.5620



B1L102S, U2-ch45, adc1

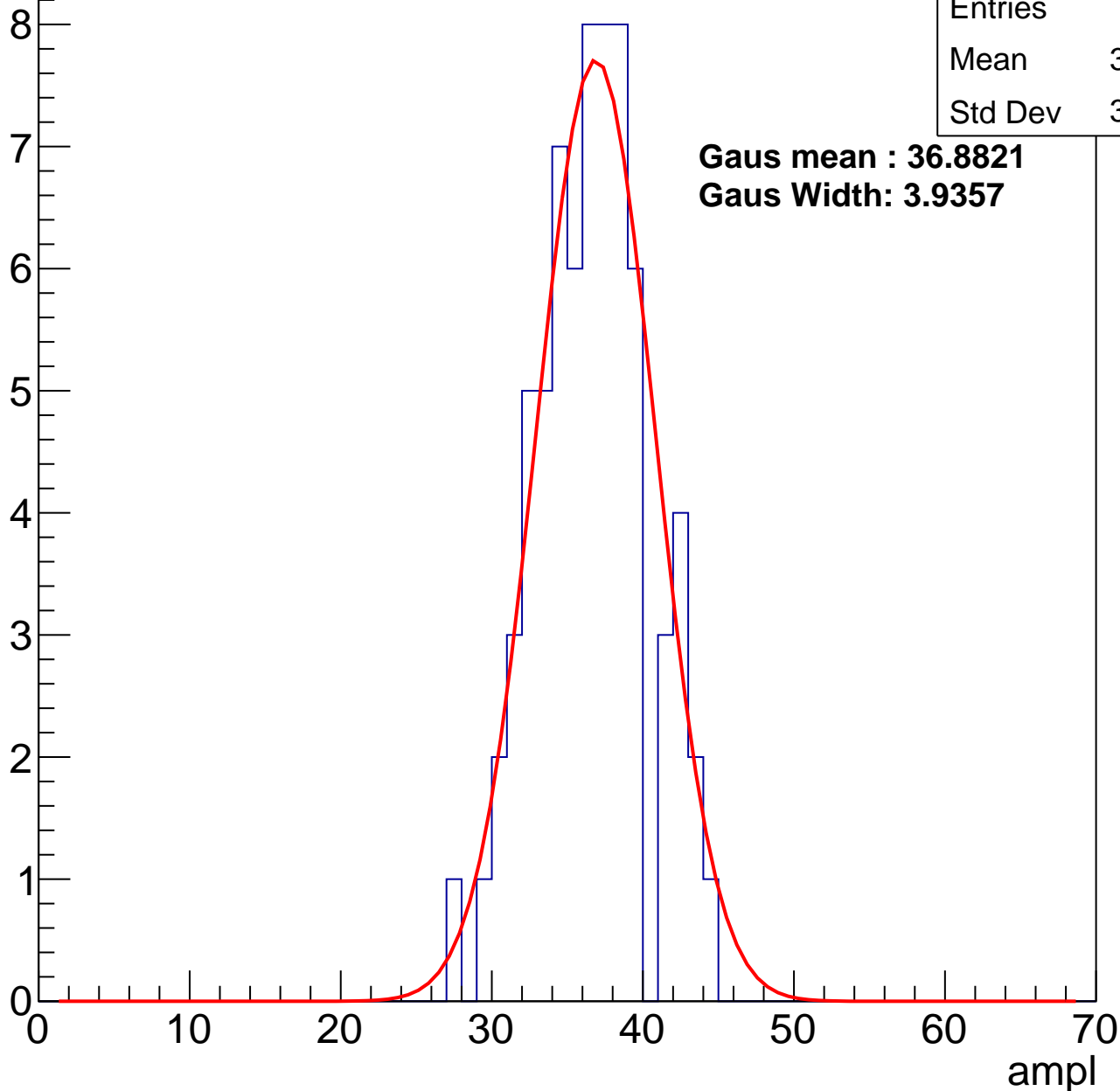
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.07
Std Dev	3.607

Gaus mean : 36.8821

Gaus Width: 3.9357



B1L102S, U2-ch45, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	42.6
Std Dev	3.412

Gaus mean : 42.9784

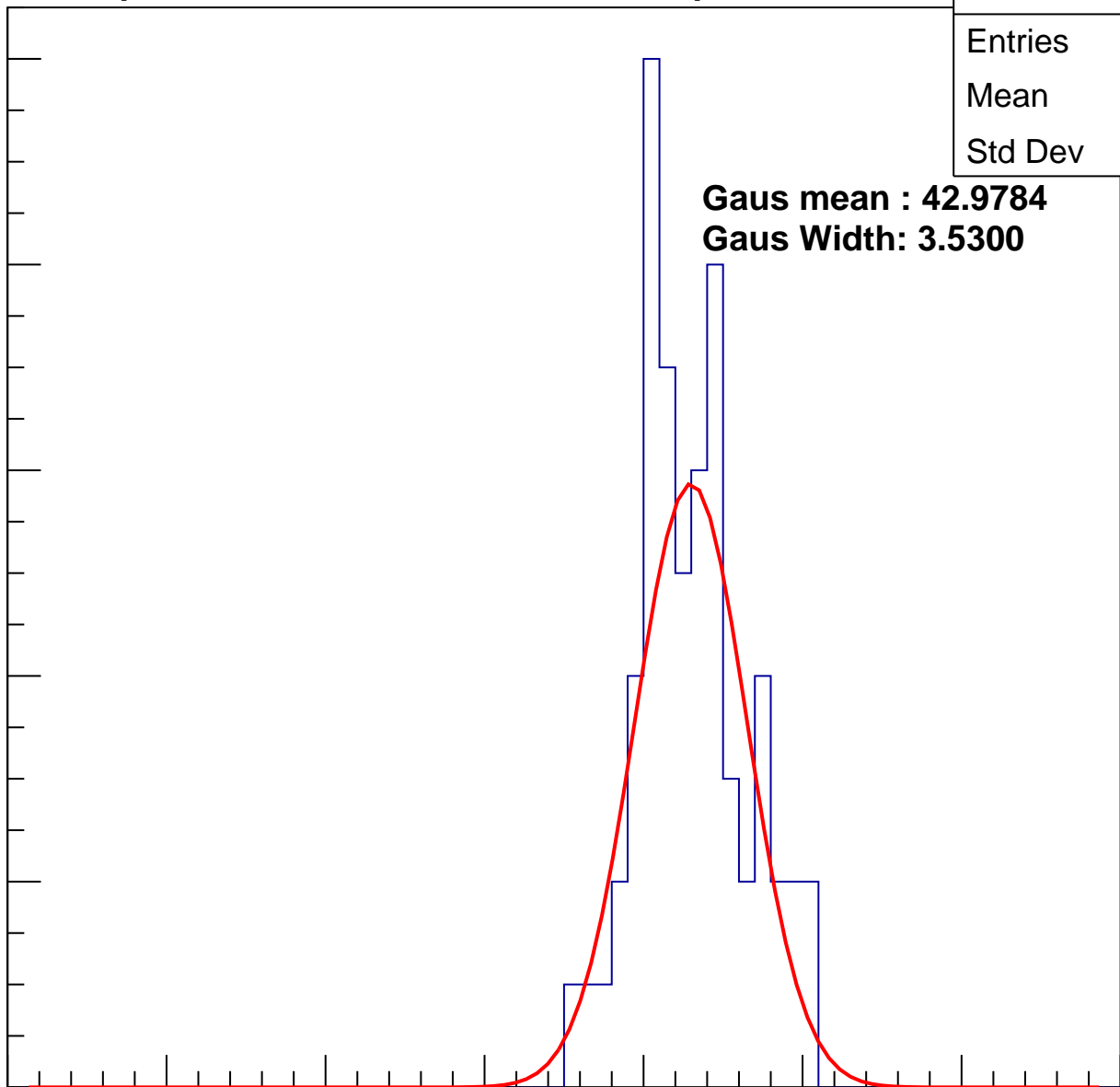
Gaus Width: 3.5300

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

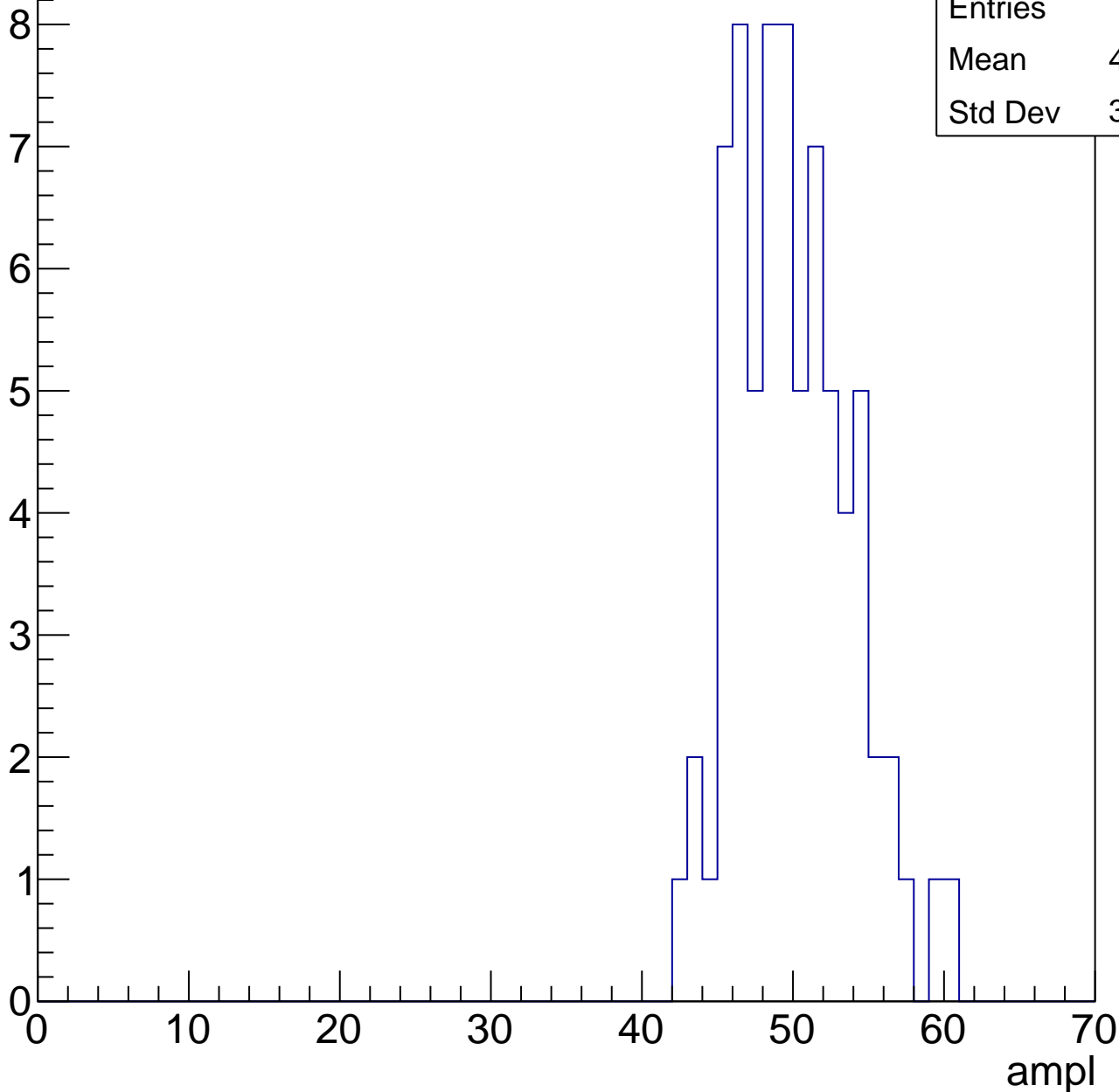


B1L102S, U2-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.49
Std Dev	3.815

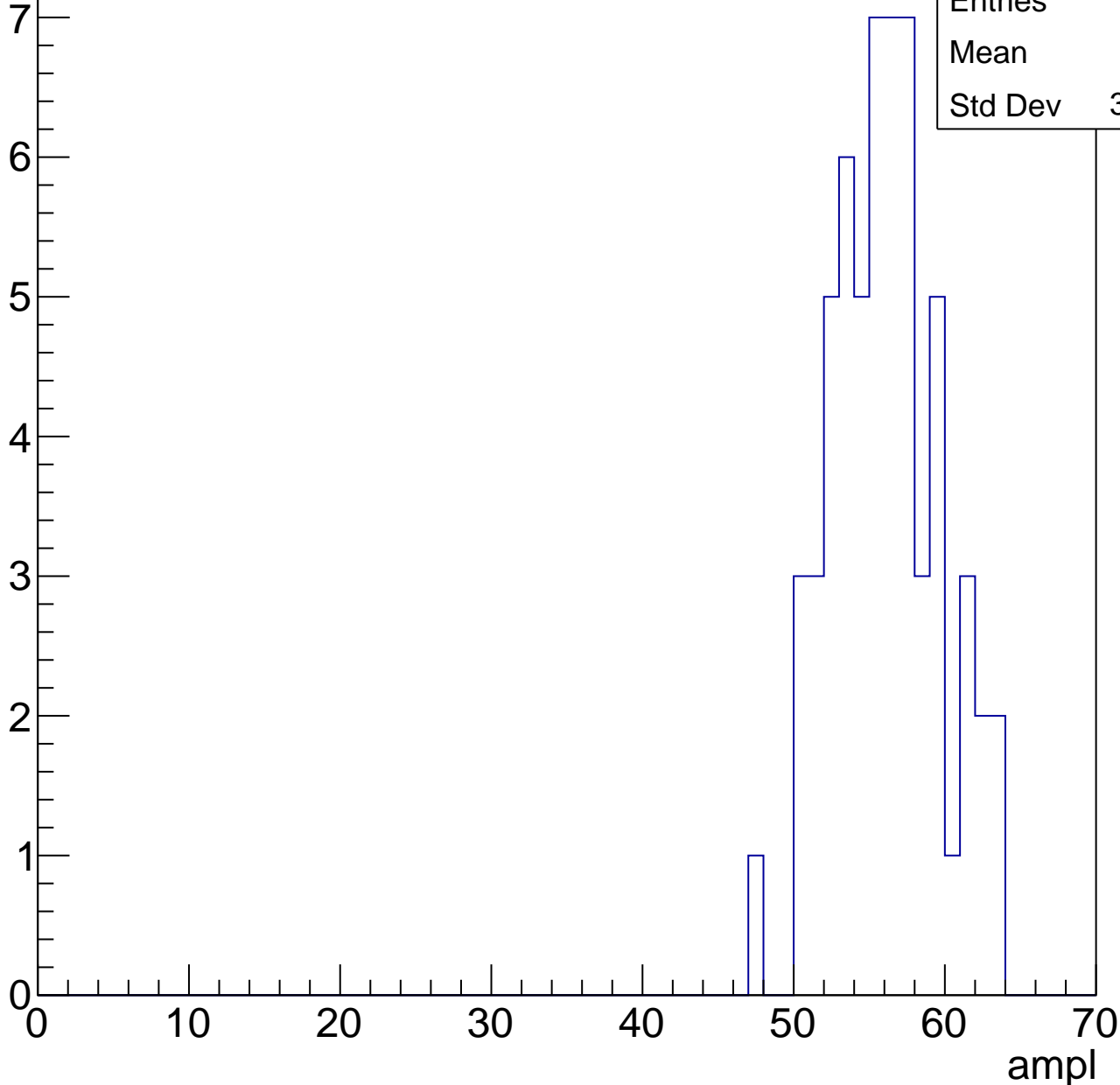


B1L102S, U2-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

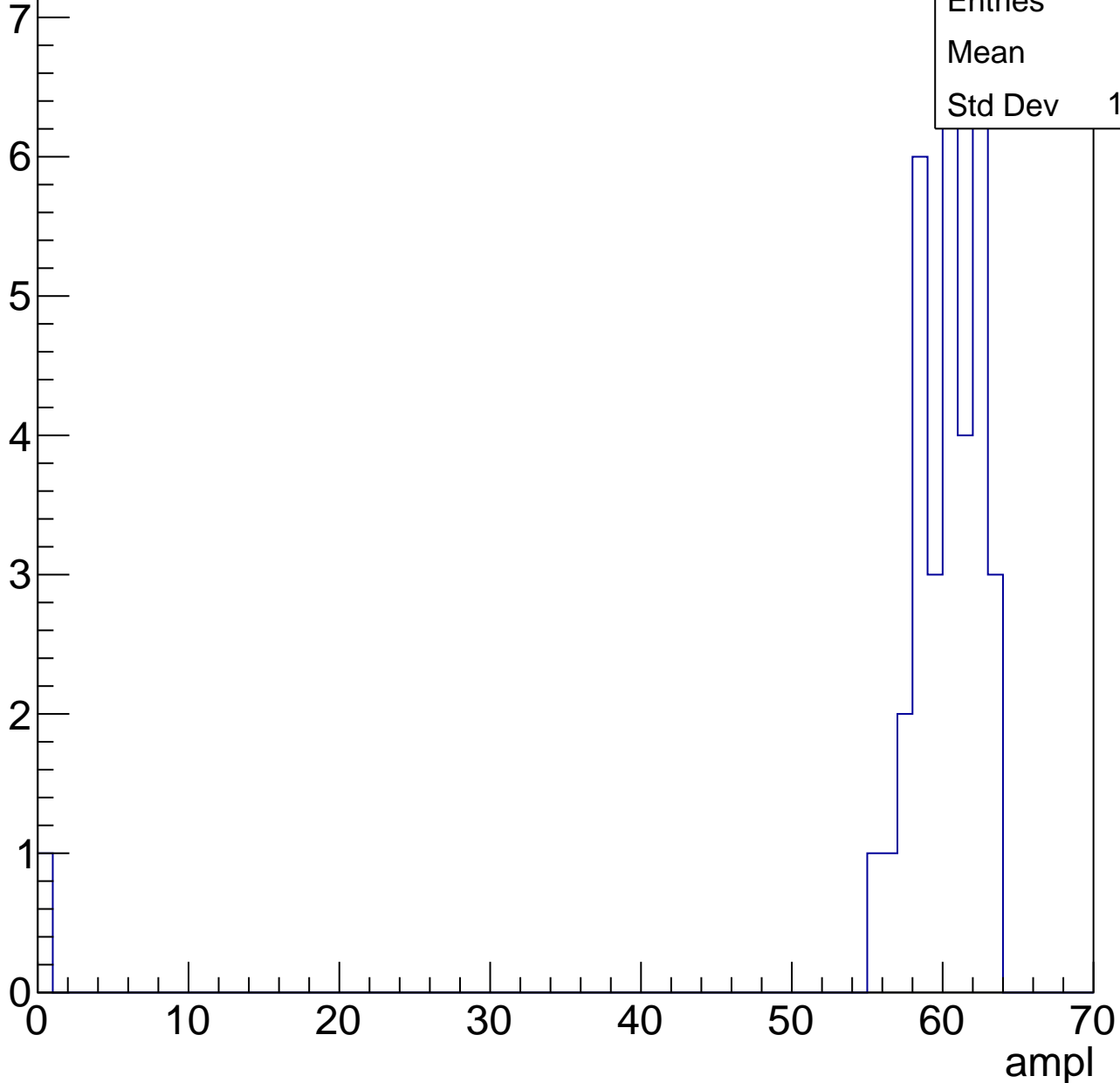
Entries	60
Mean	55.6
Std Dev	3.513



B1L102S, U2-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

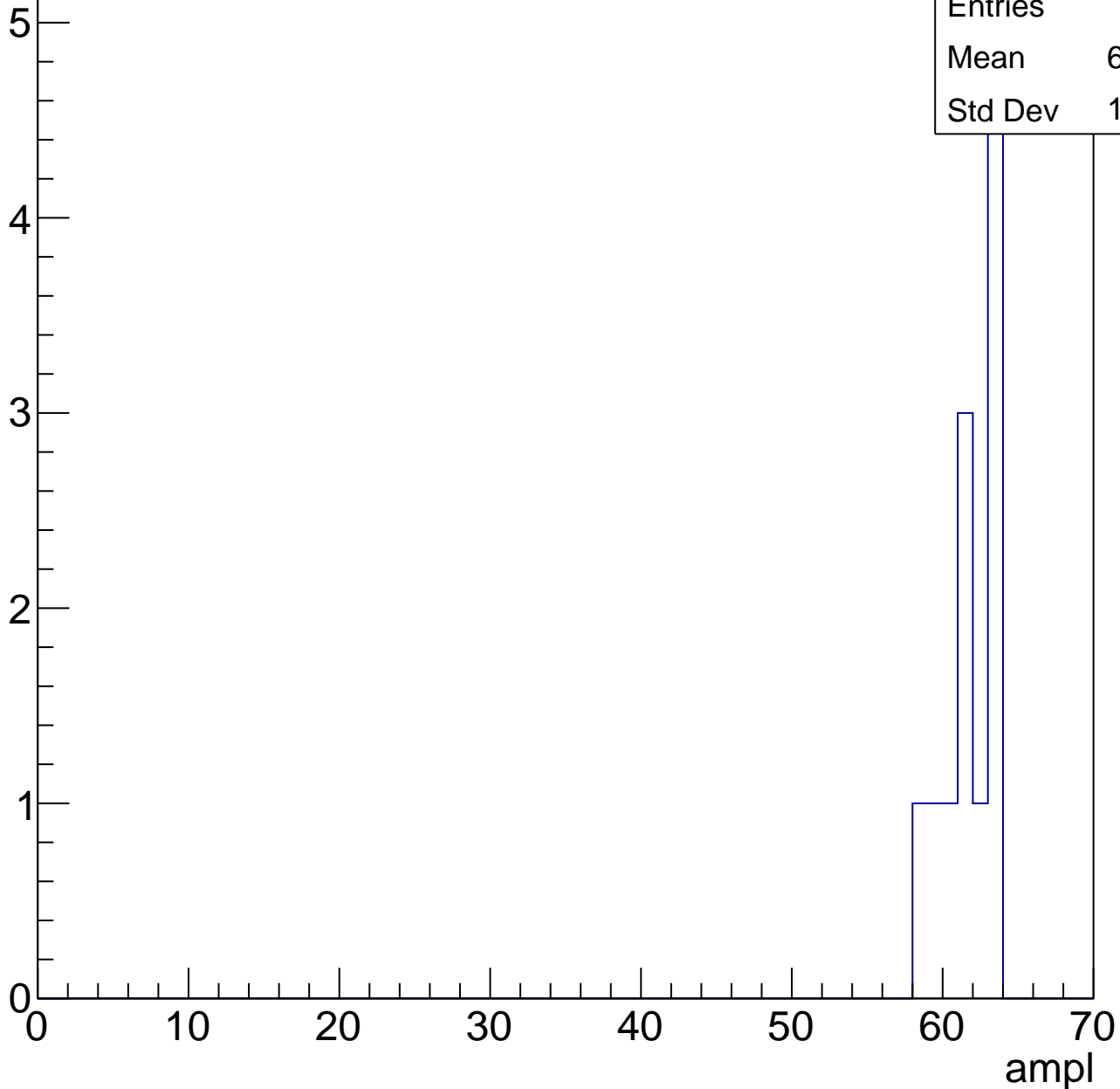


B1L102S, U2-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.42
Std Dev	1.656



B1L102S, U2-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch46, adc0

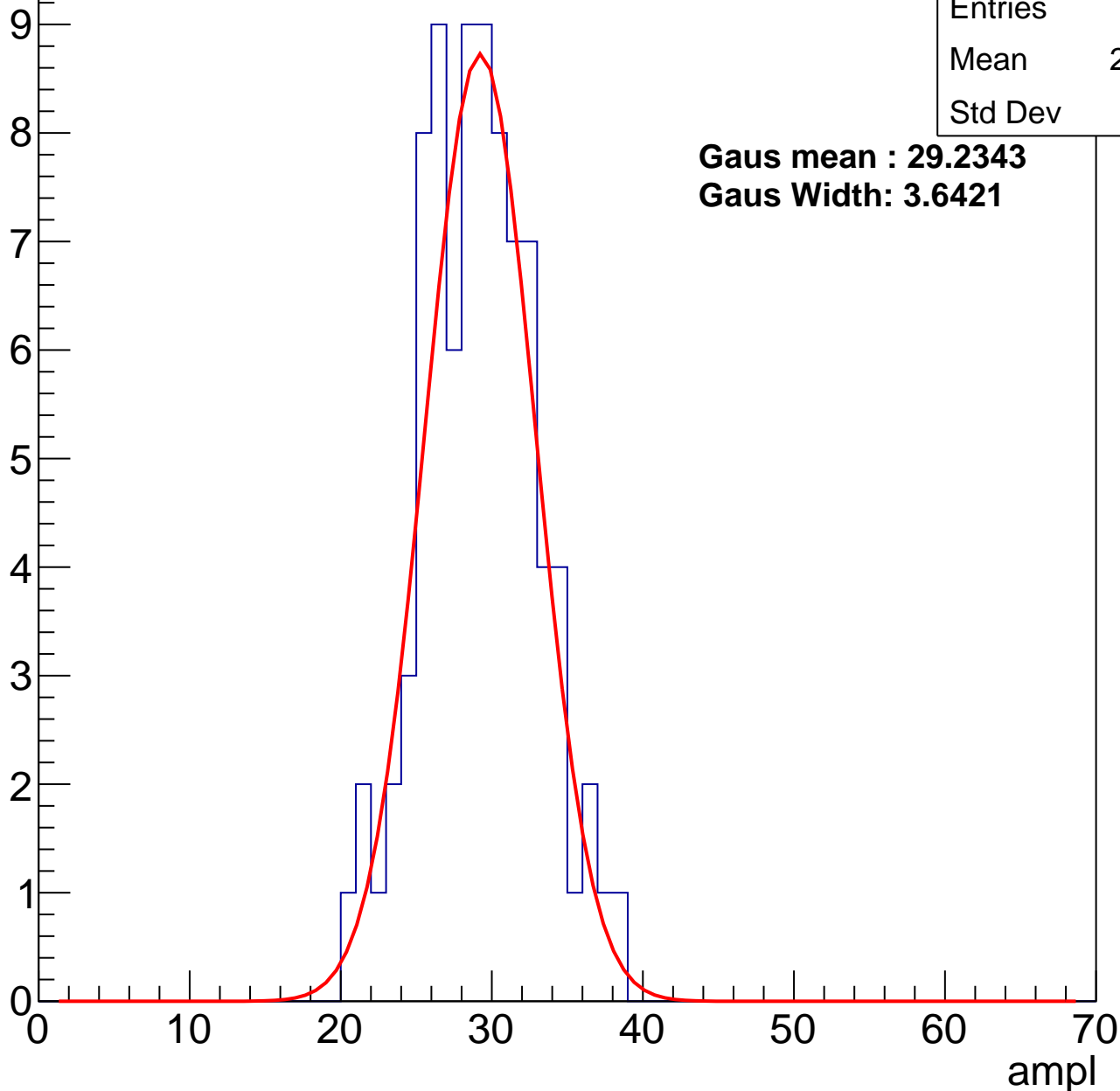
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	28.73
Std Dev	3.73

Gaus mean : 29.2343

Gaus Width: 3.6421



B1L102S, U2-ch46, adc1

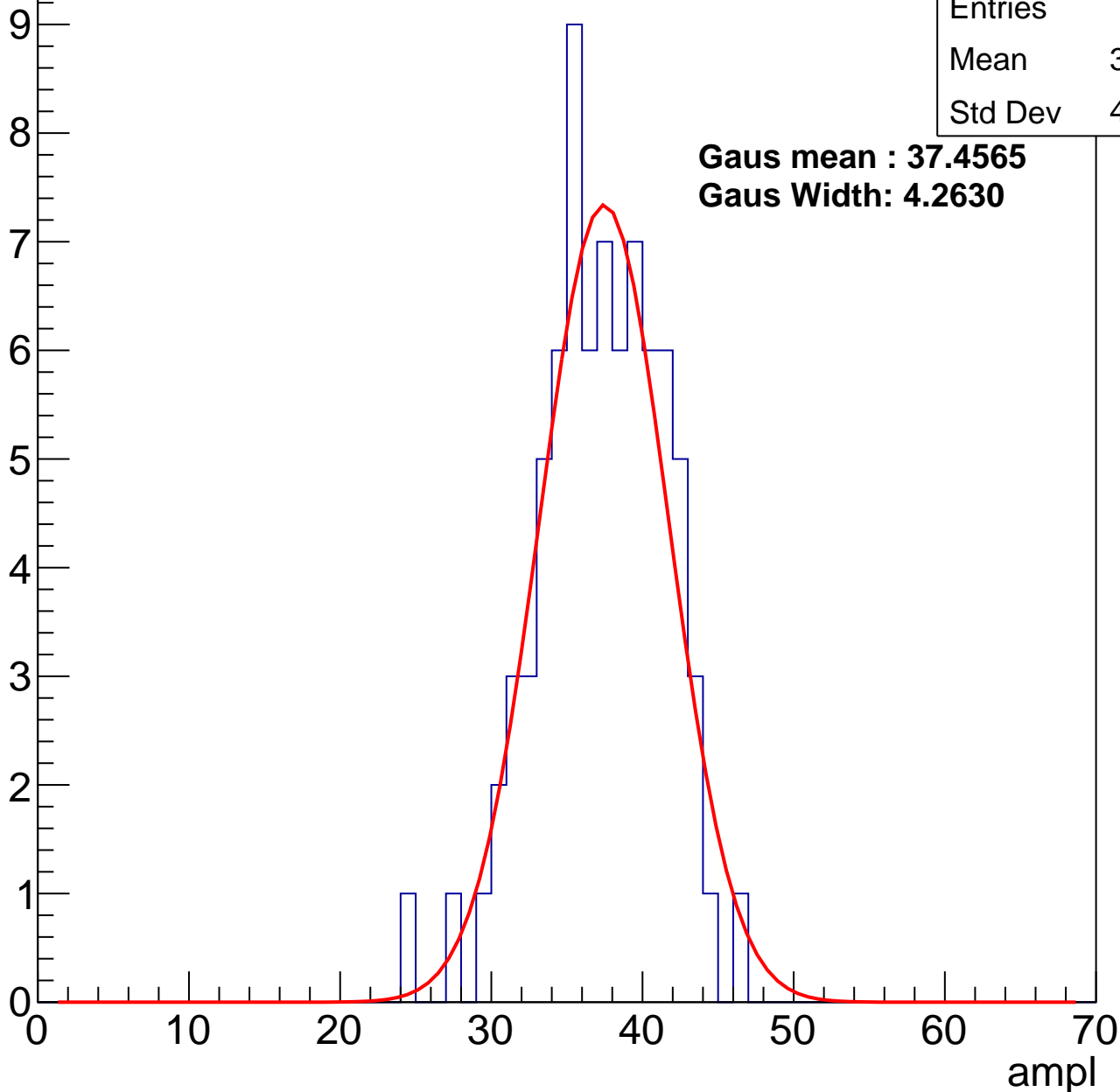
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.76
Std Dev	4.107

Gaus mean : 37.4565

Gaus Width: 4.2630



B1L102S, U2-ch46, adc2

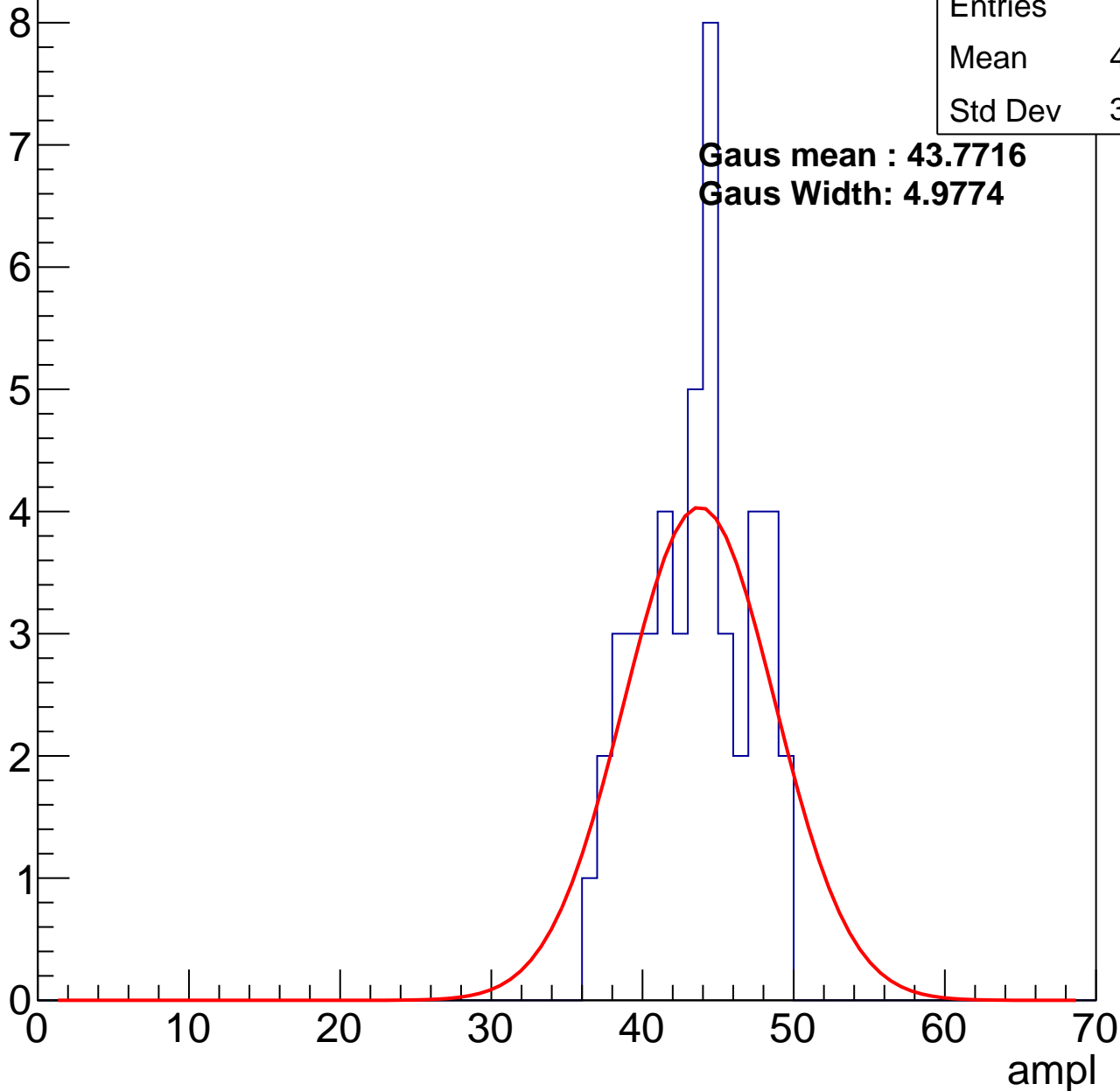
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	43.04
Std Dev	3.464

Gaus mean : 43.7716

Gaus Width: 4.9774

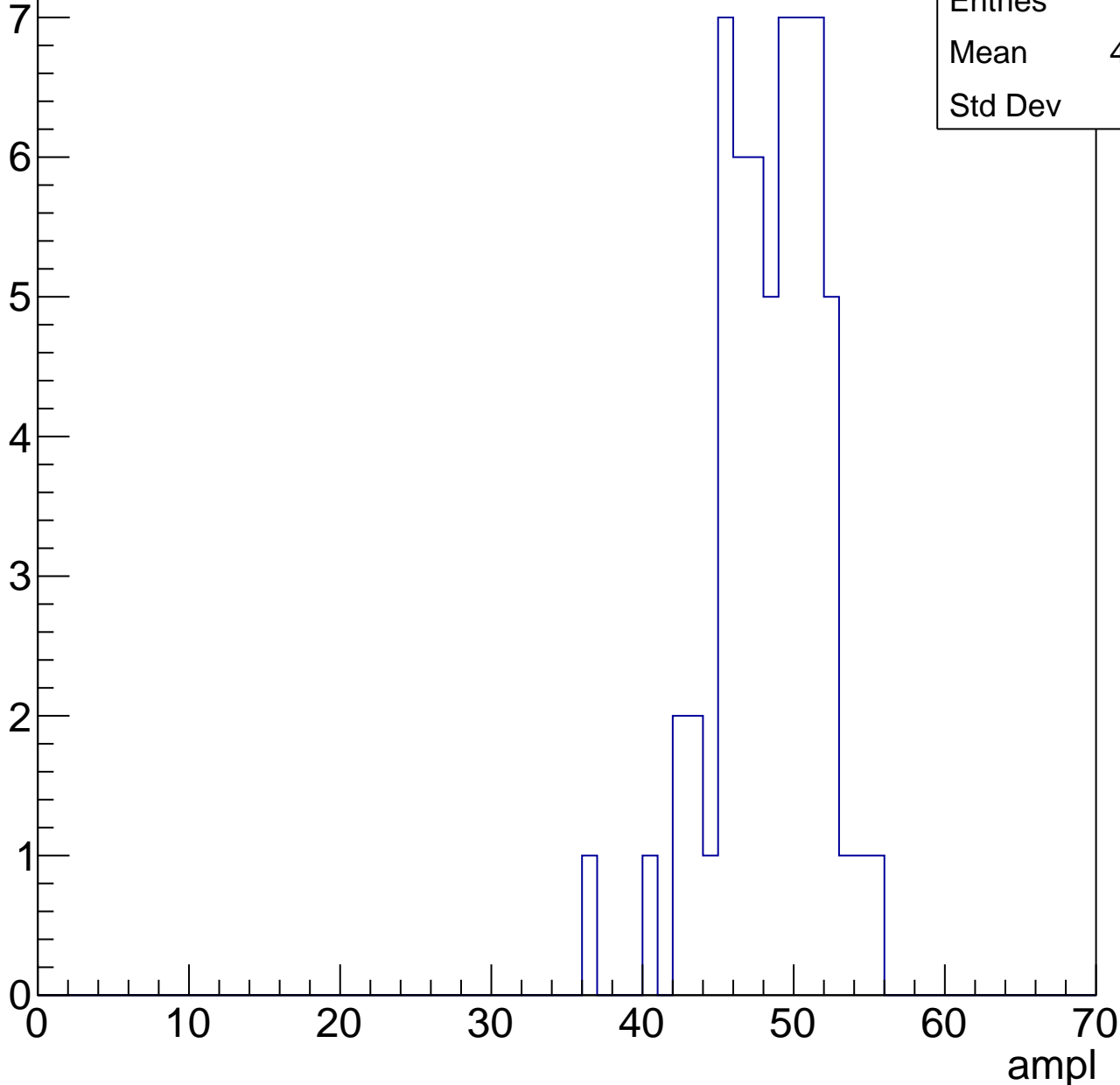


B1L102S, U2-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.92
Std Dev	3.48

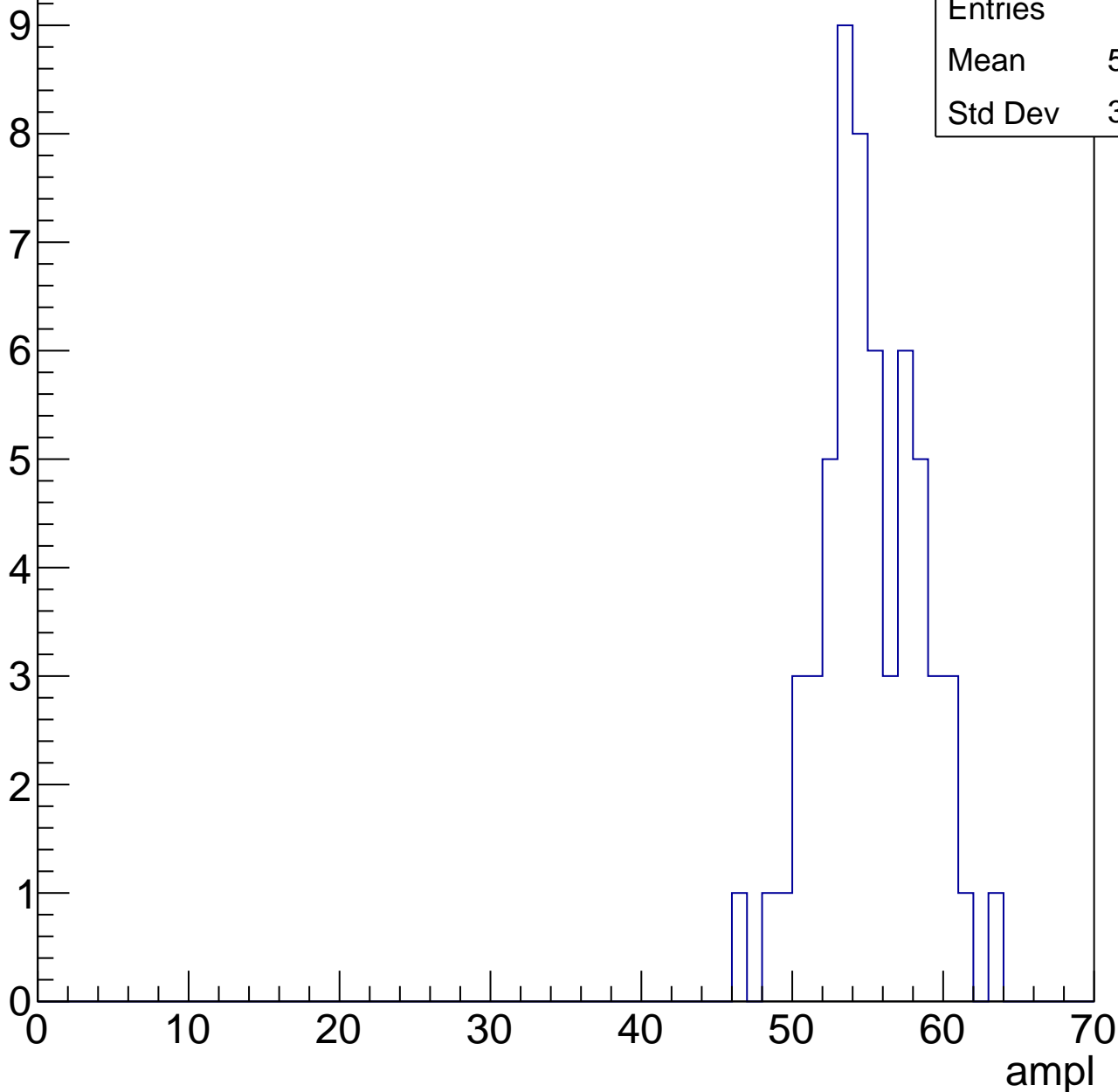


B1L102S, U2-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.68
Std Dev	3.362

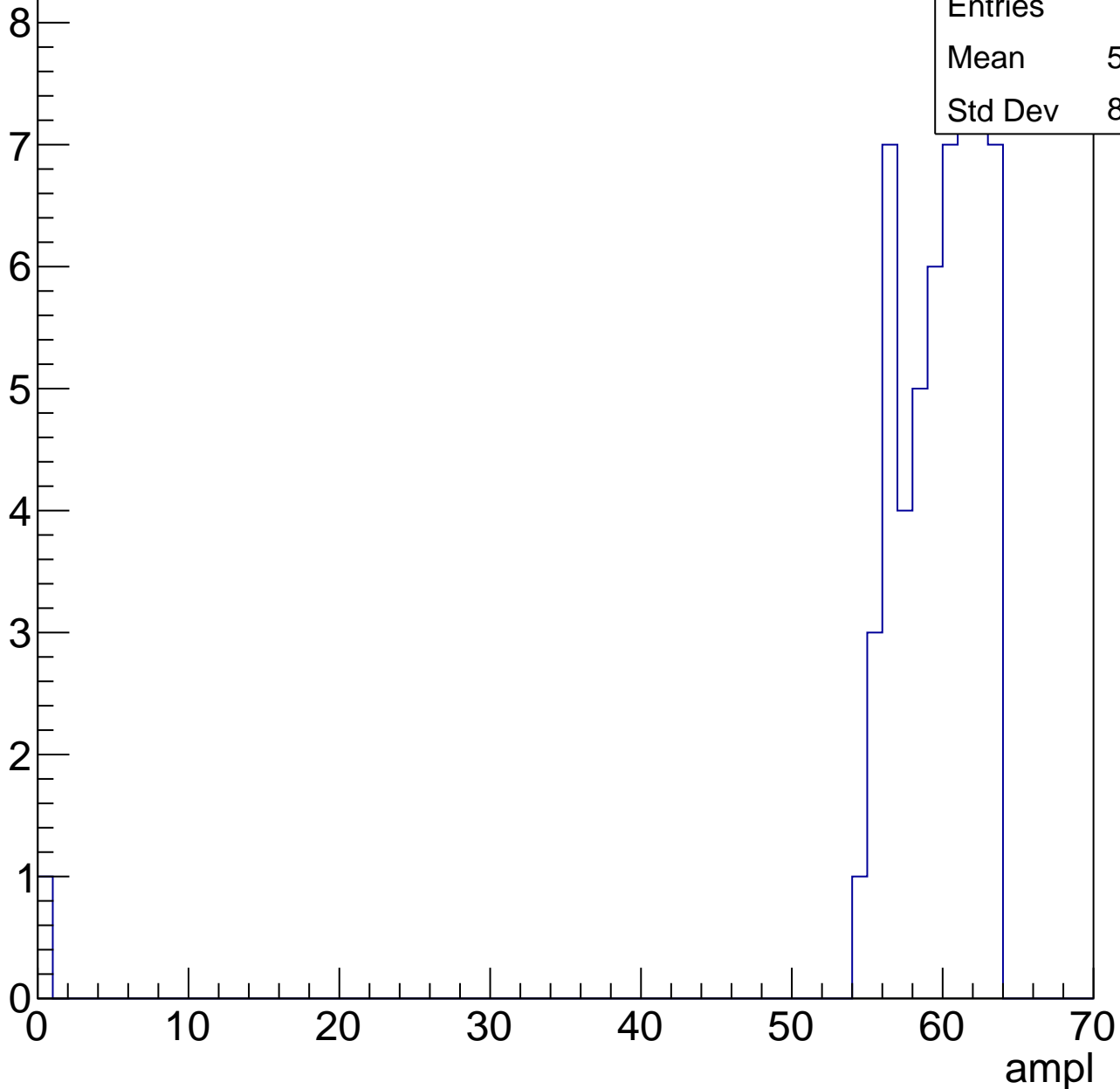


B1L102S, U2-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

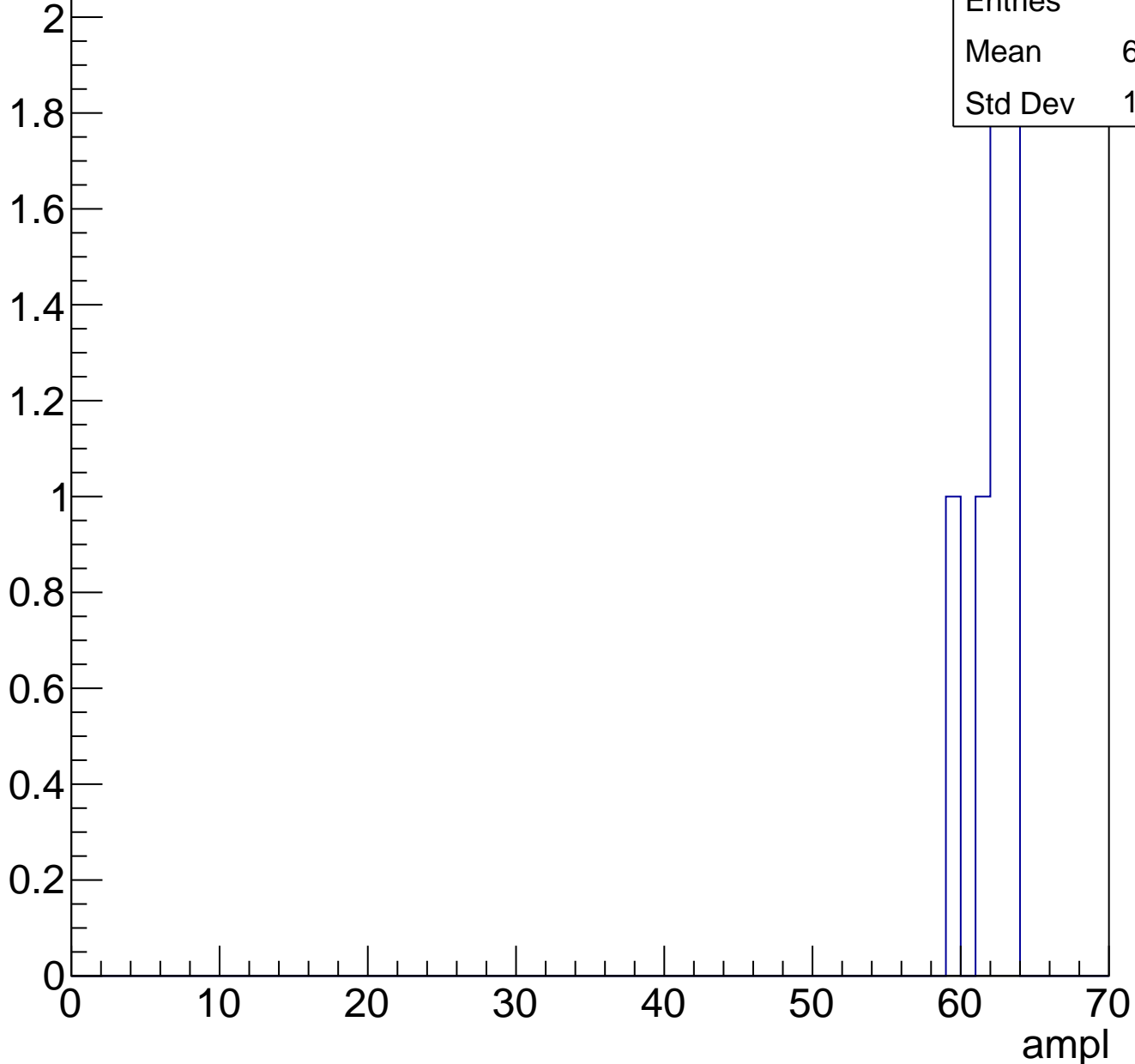
Entries	57
Mean	58.39
Std Dev	8.207



B1L102S, U2-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch47, adc0

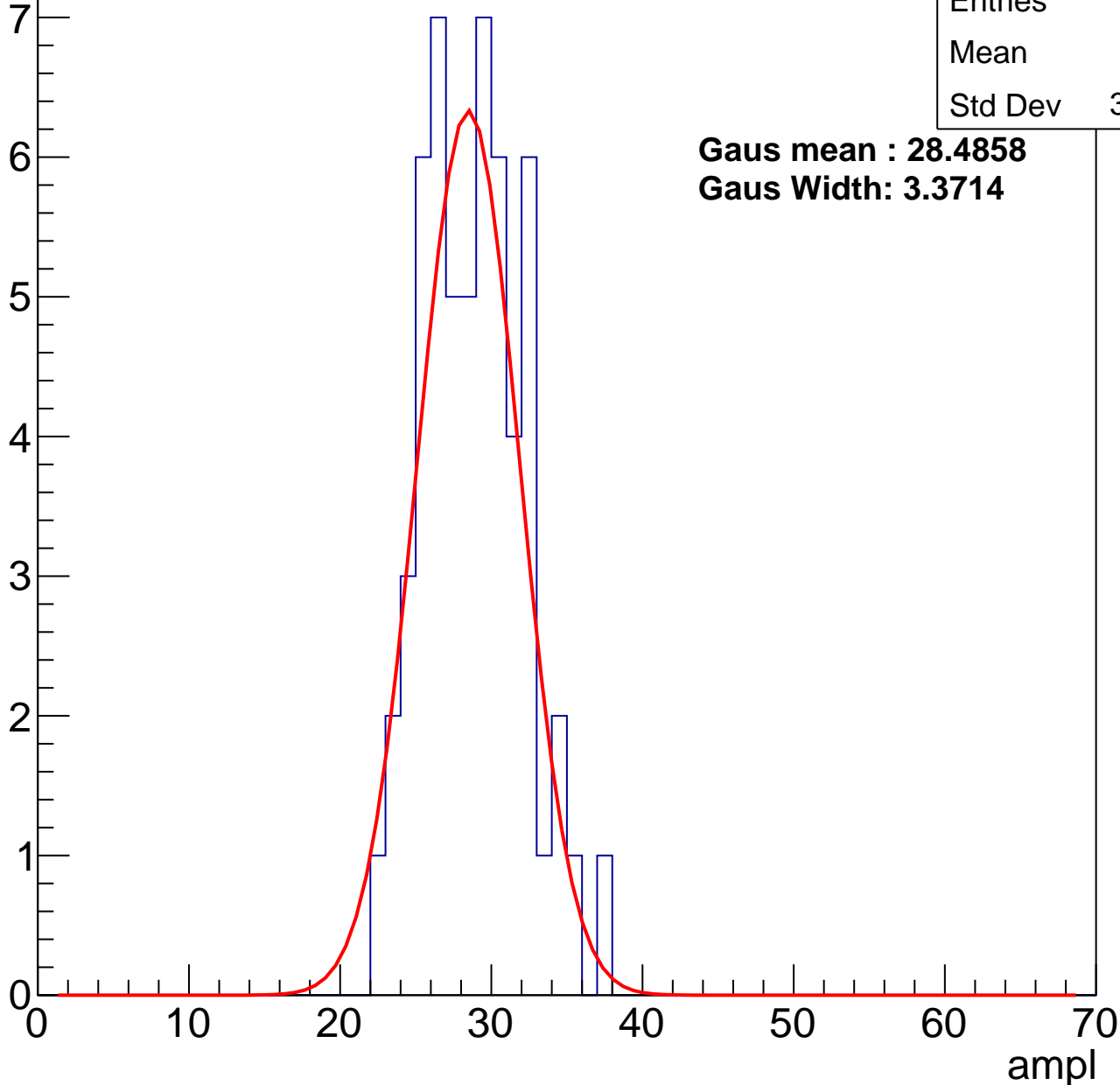
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	28.4
Std Dev	3.249

Gaus mean : 28.4858

Gaus Width: 3.3714



B1L102S, U2-ch47, adc1

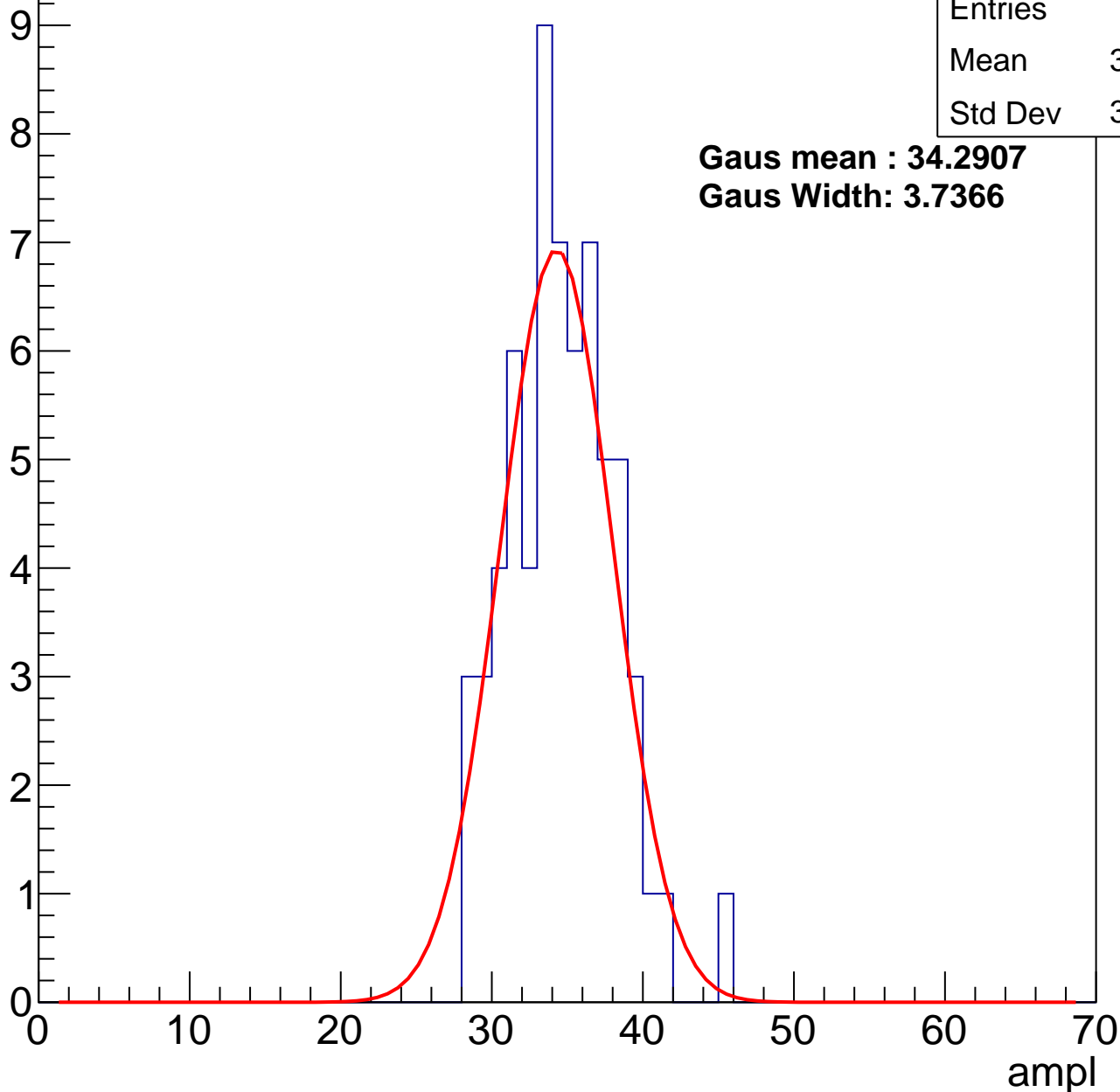
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.15
Std Dev	3.429

Gaus mean : 34.2907

Gaus Width: 3.7366



B1L102S, U2-ch47, adc2

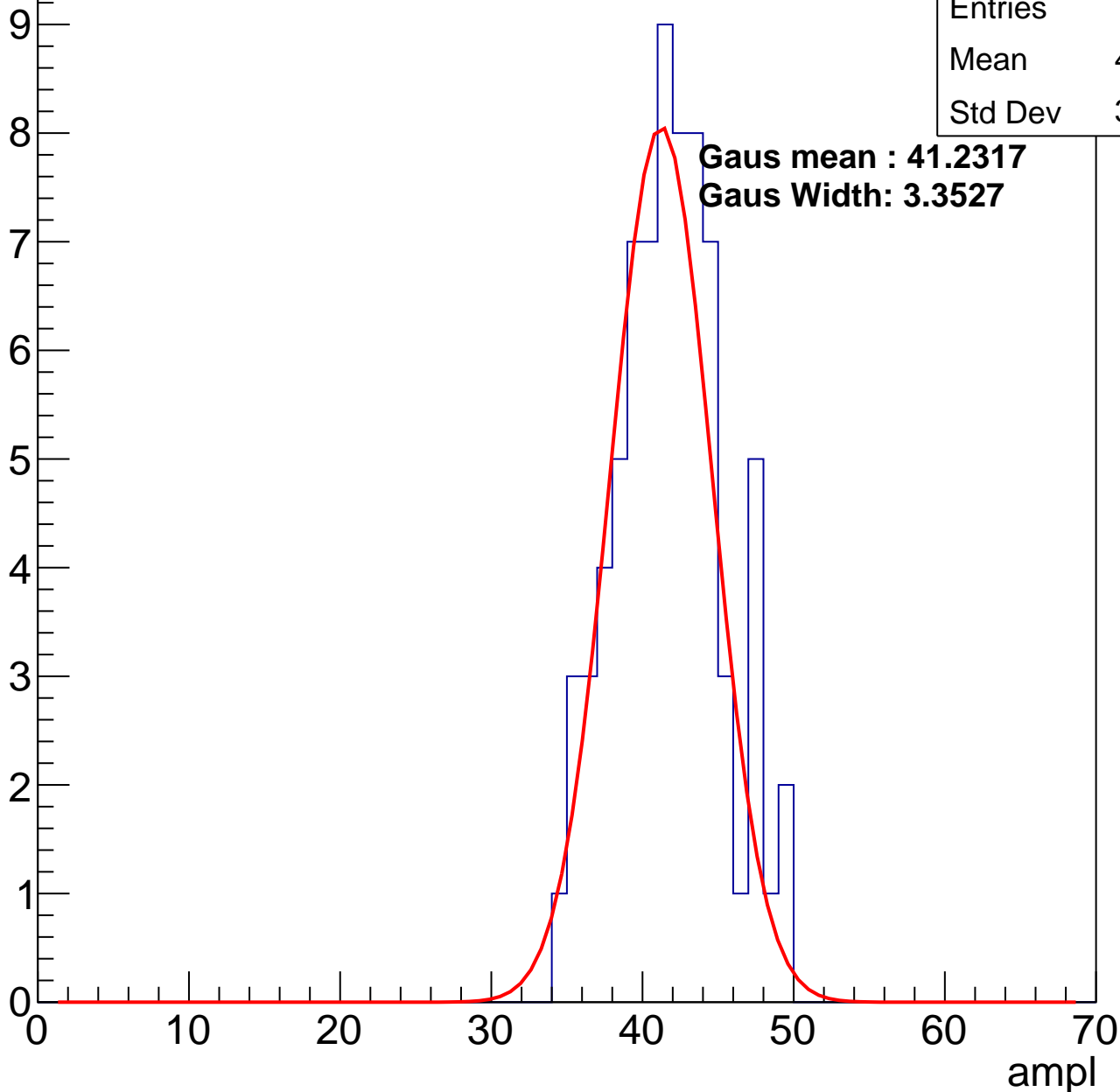
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.31
Std Dev	3.491

Gaus mean : 41.2317

Gaus Width: 3.3527

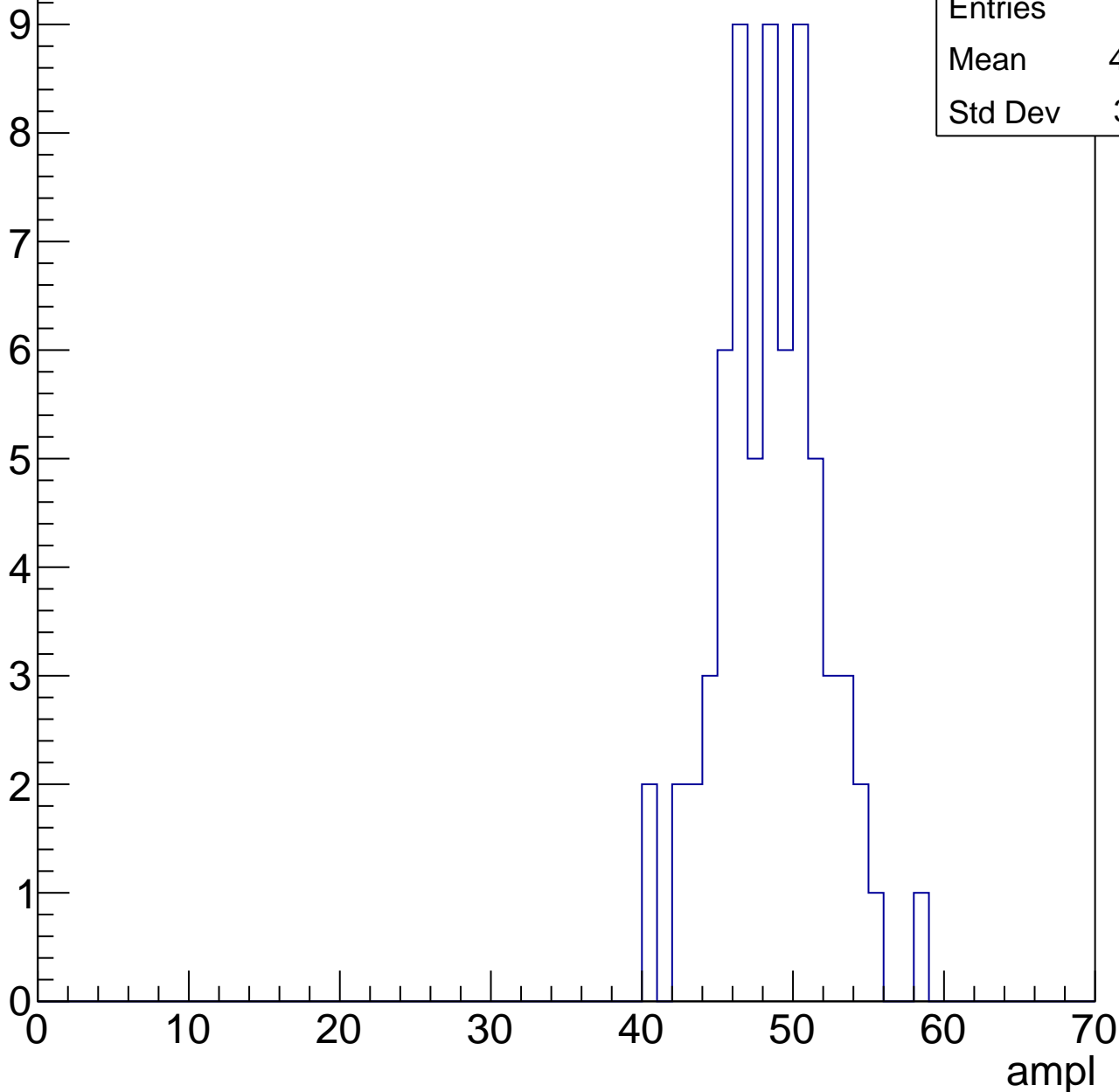


B1L102S, U2-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.06
Std Dev	3.481

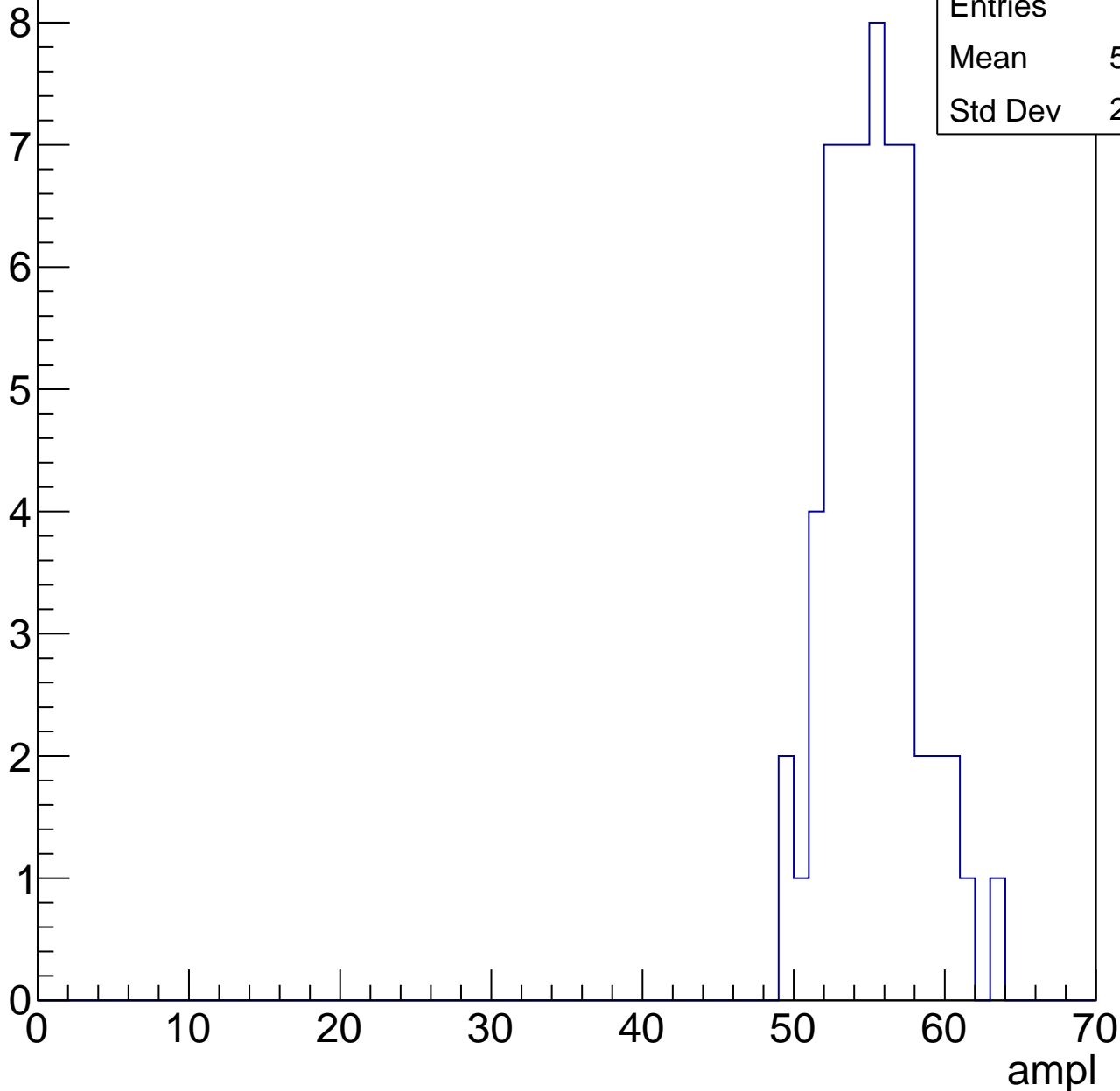


B1L102S, U2-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.72
Std Dev	2.905

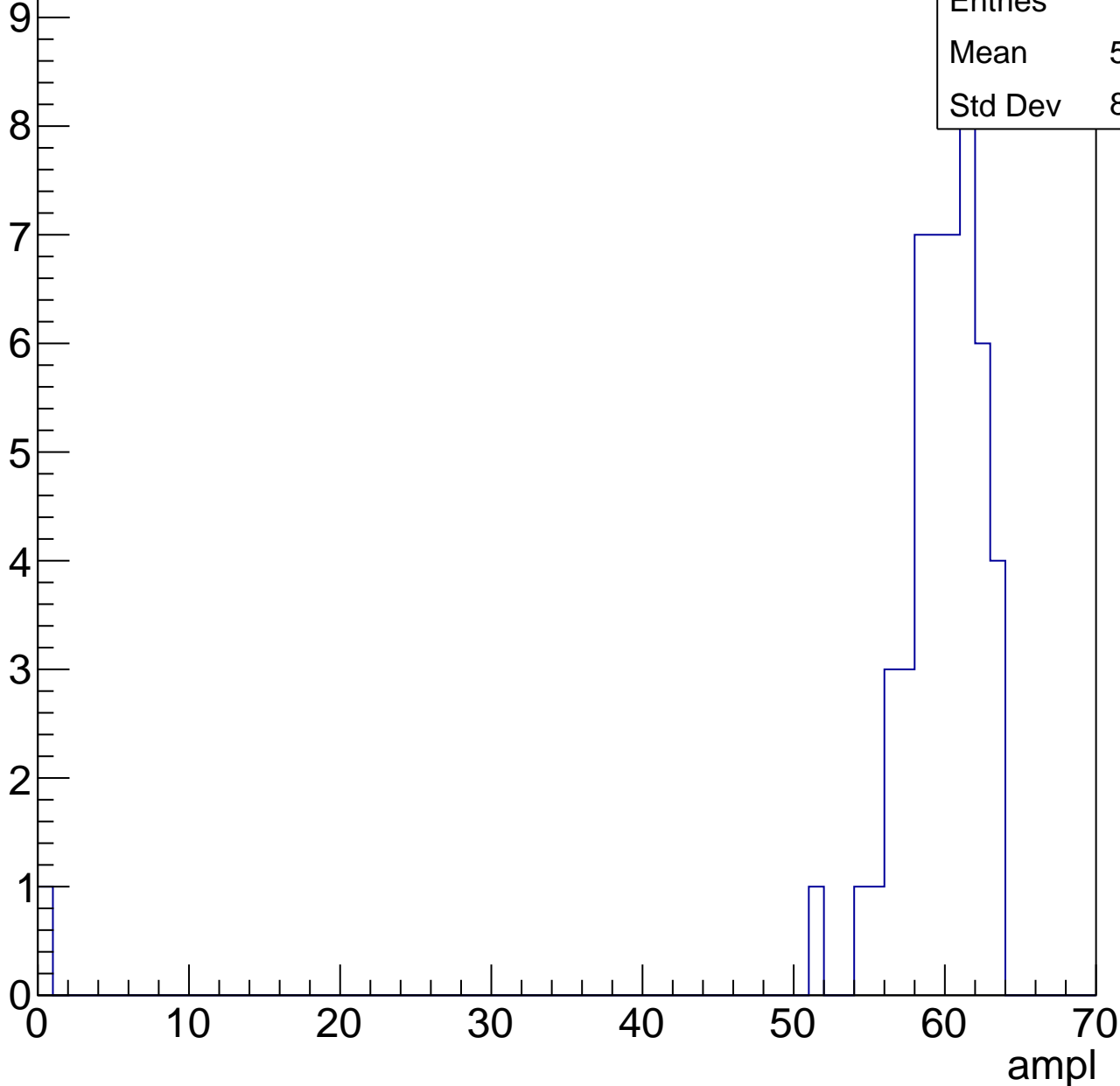


B1L102S, U2-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.22
Std Dev	8.675

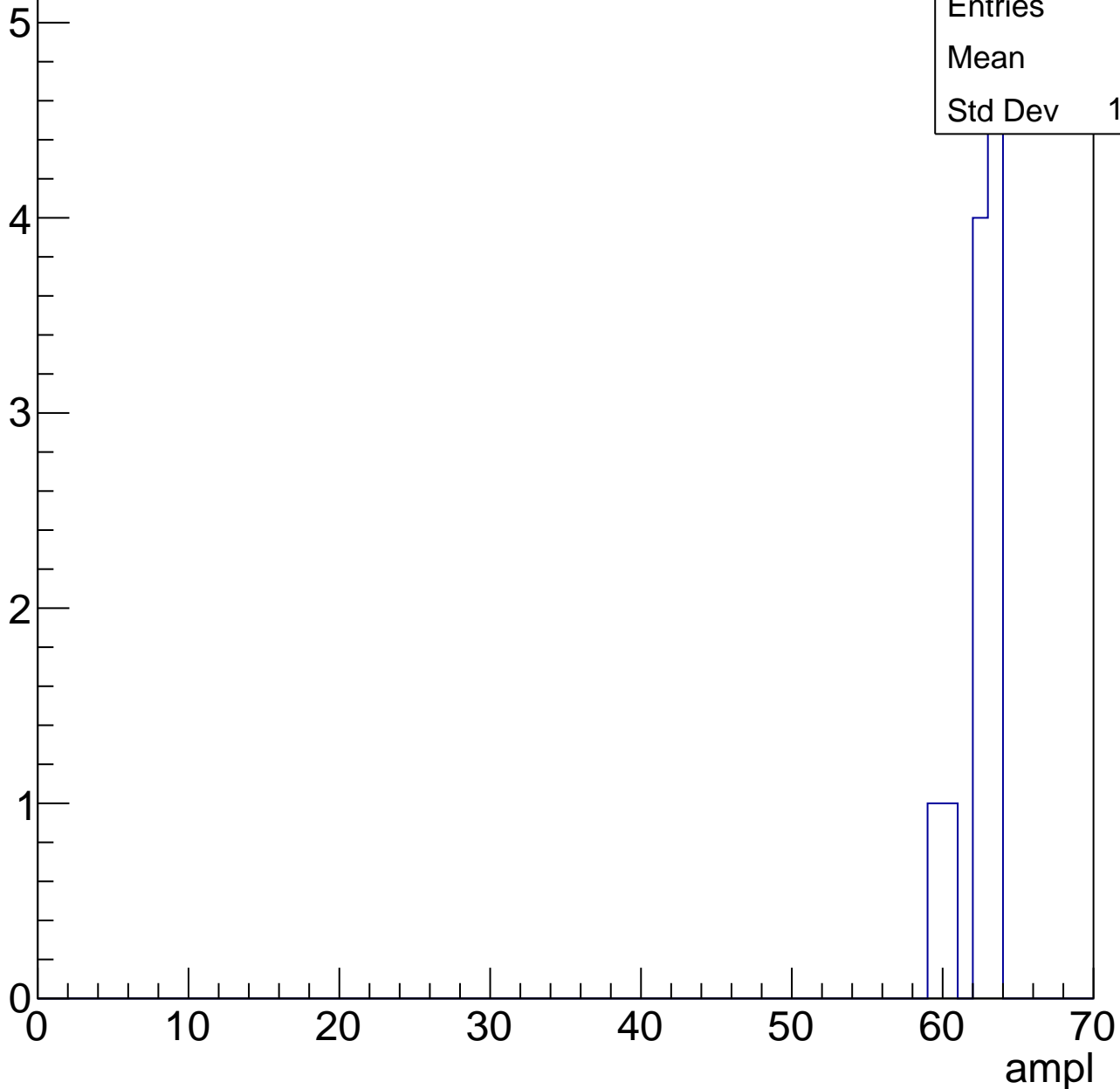


B1L102S, U2-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	62
Std Dev	1.279



B1L102S, U2-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L102S, U2-ch48, adc0

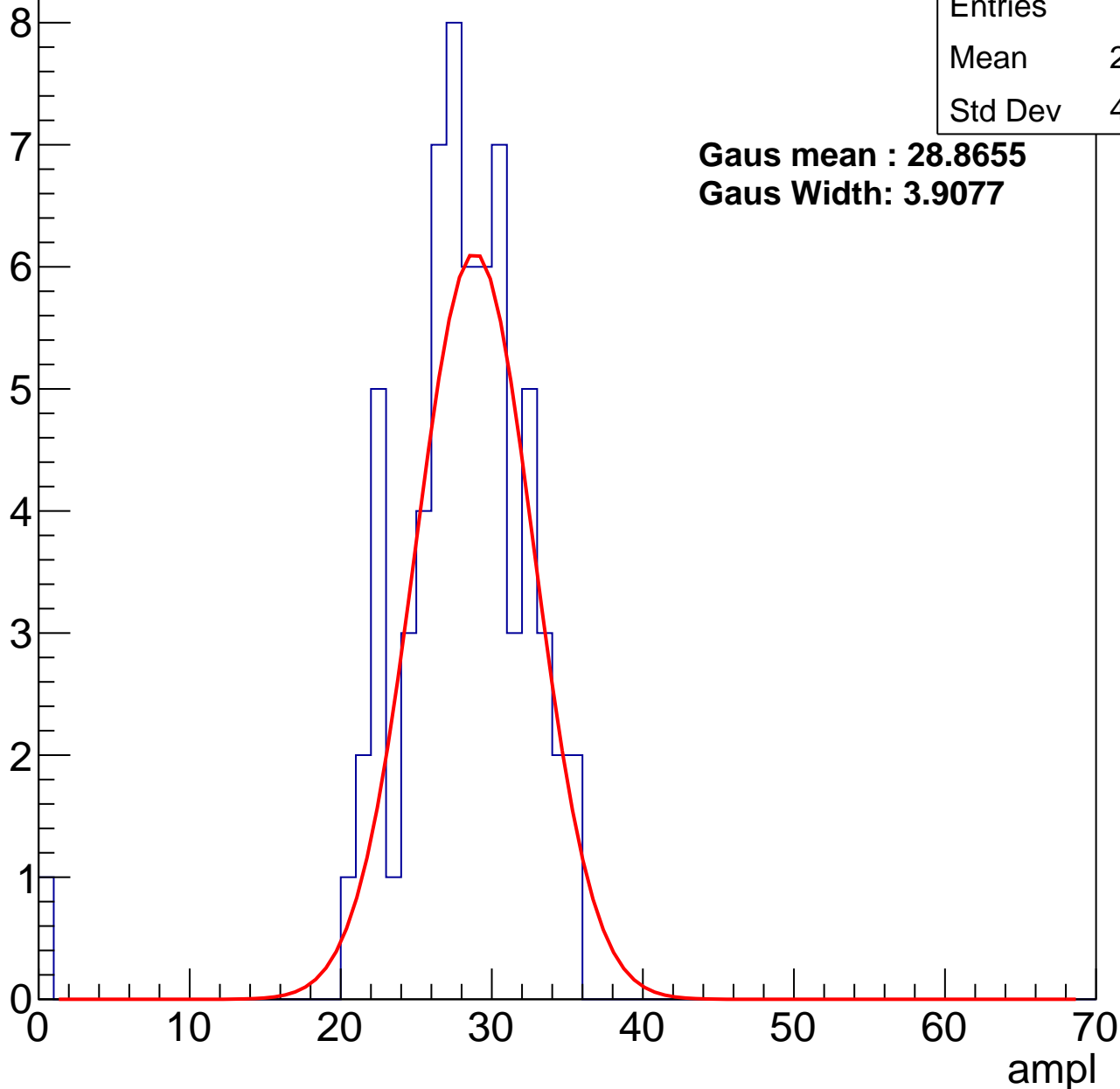
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.38
Std Dev	4.972

Gaus mean : 28.8655

Gaus Width: 3.9077



B1L102S, U2-ch48, adc1

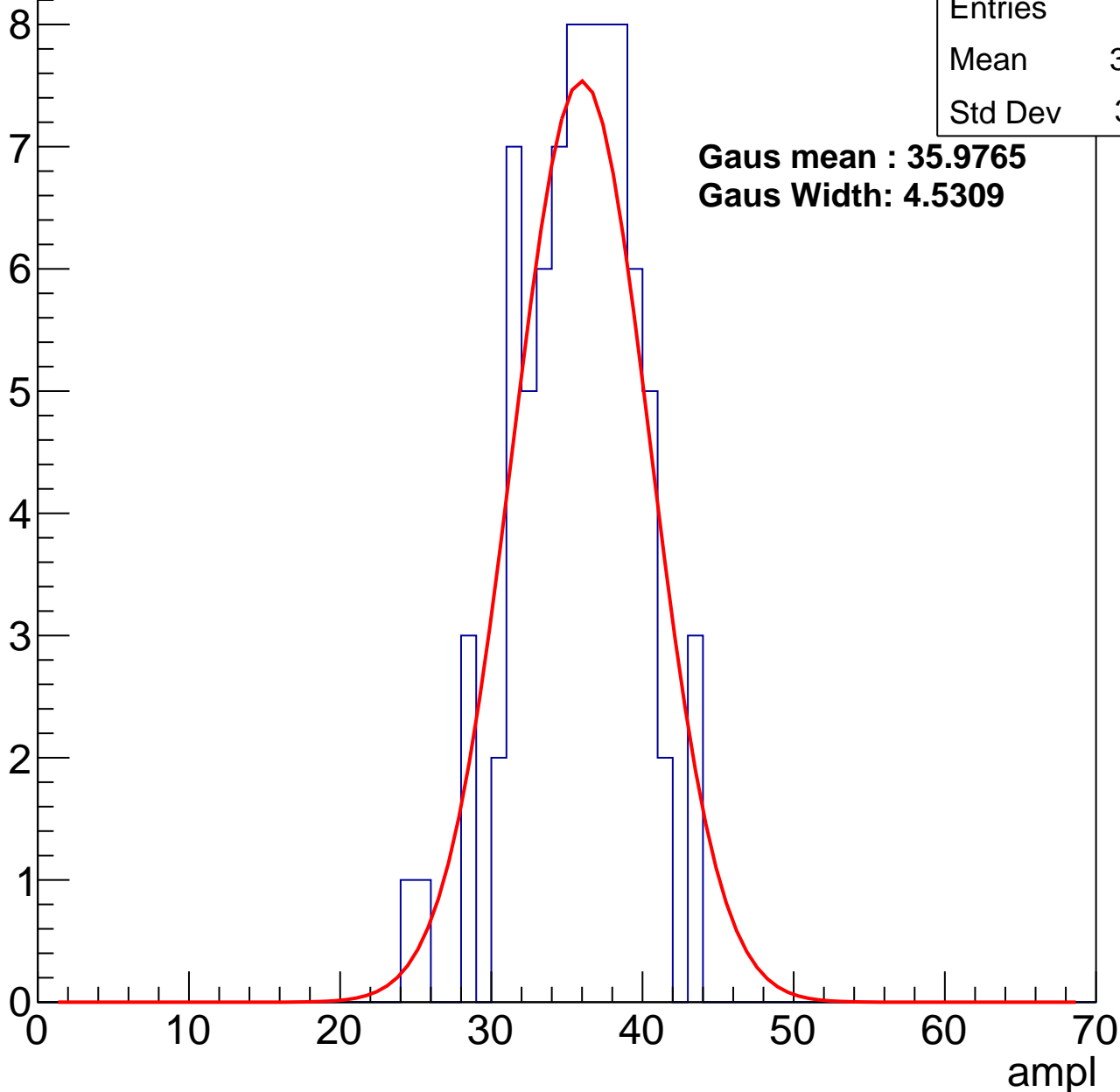
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.24
Std Dev	3.871

Gaus mean : 35.9765

Gaus Width: 4.5309



B1L102S, U2-ch48, adc2

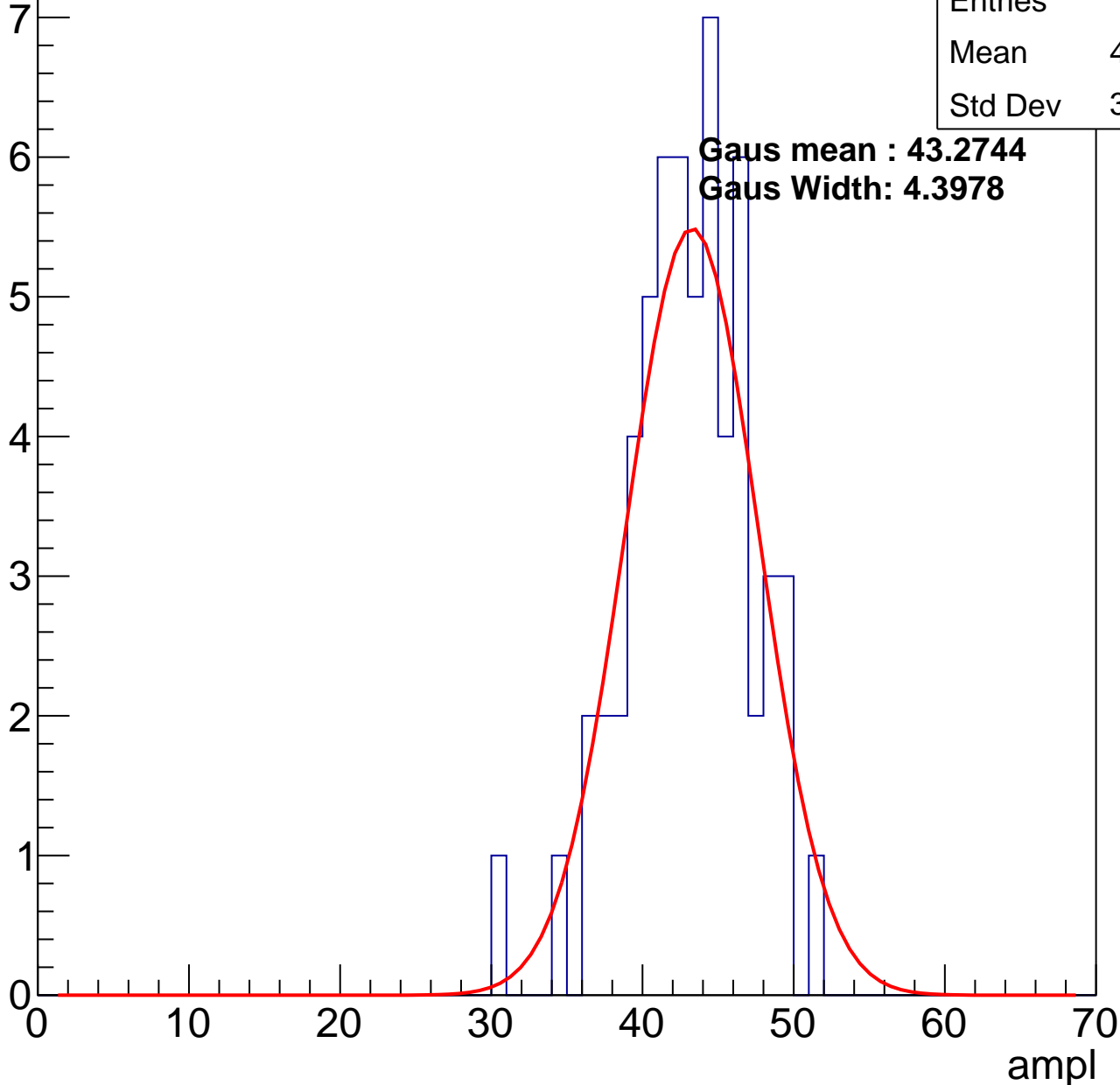
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.58
Std Dev	3.989

Gaus mean : 43.2744

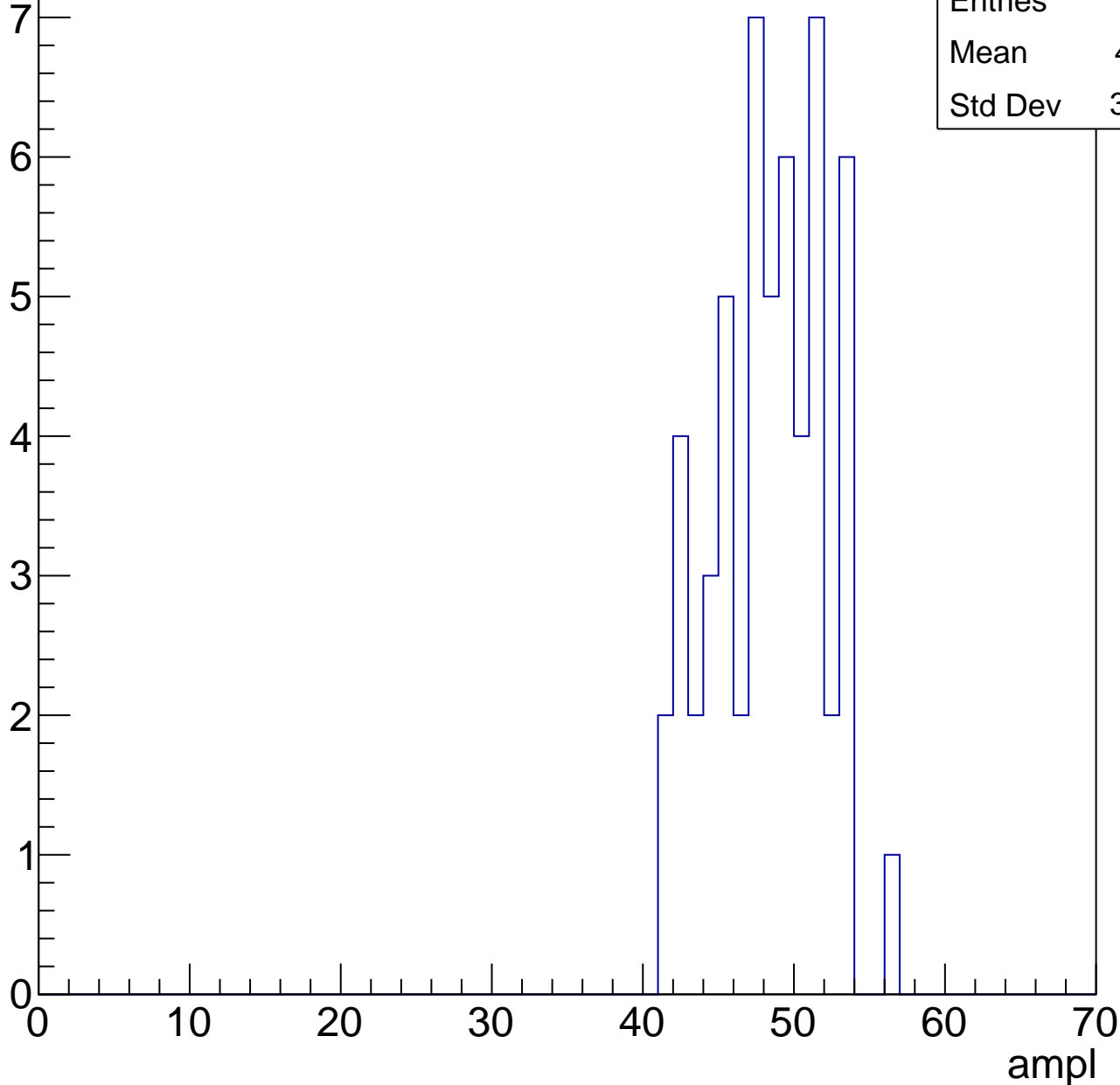
Gaus Width: 4.3978



B1L102S, U2-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



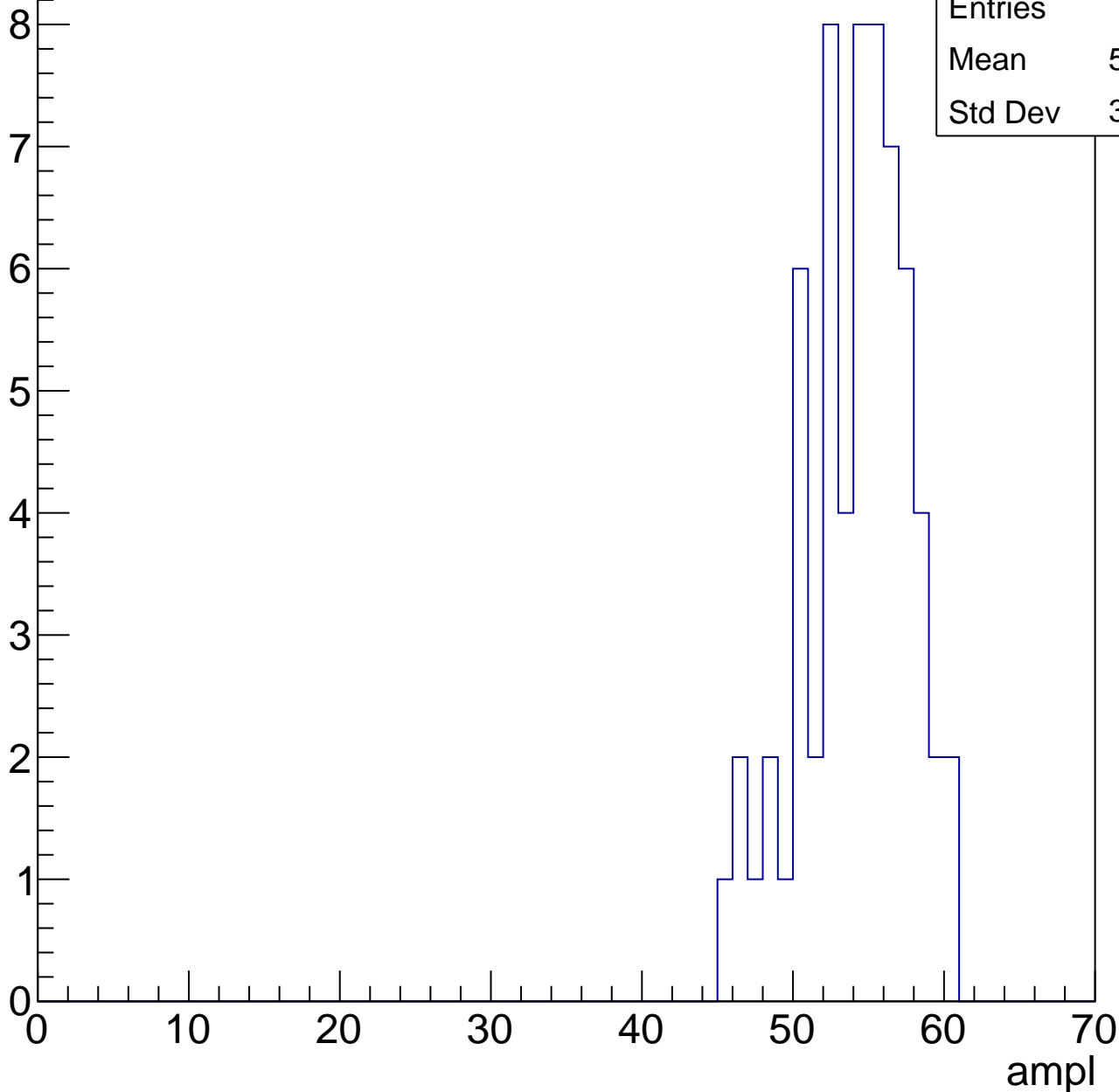
Entries	56
Mean	47.91
Std Dev	3.612

B1L102S, U2-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

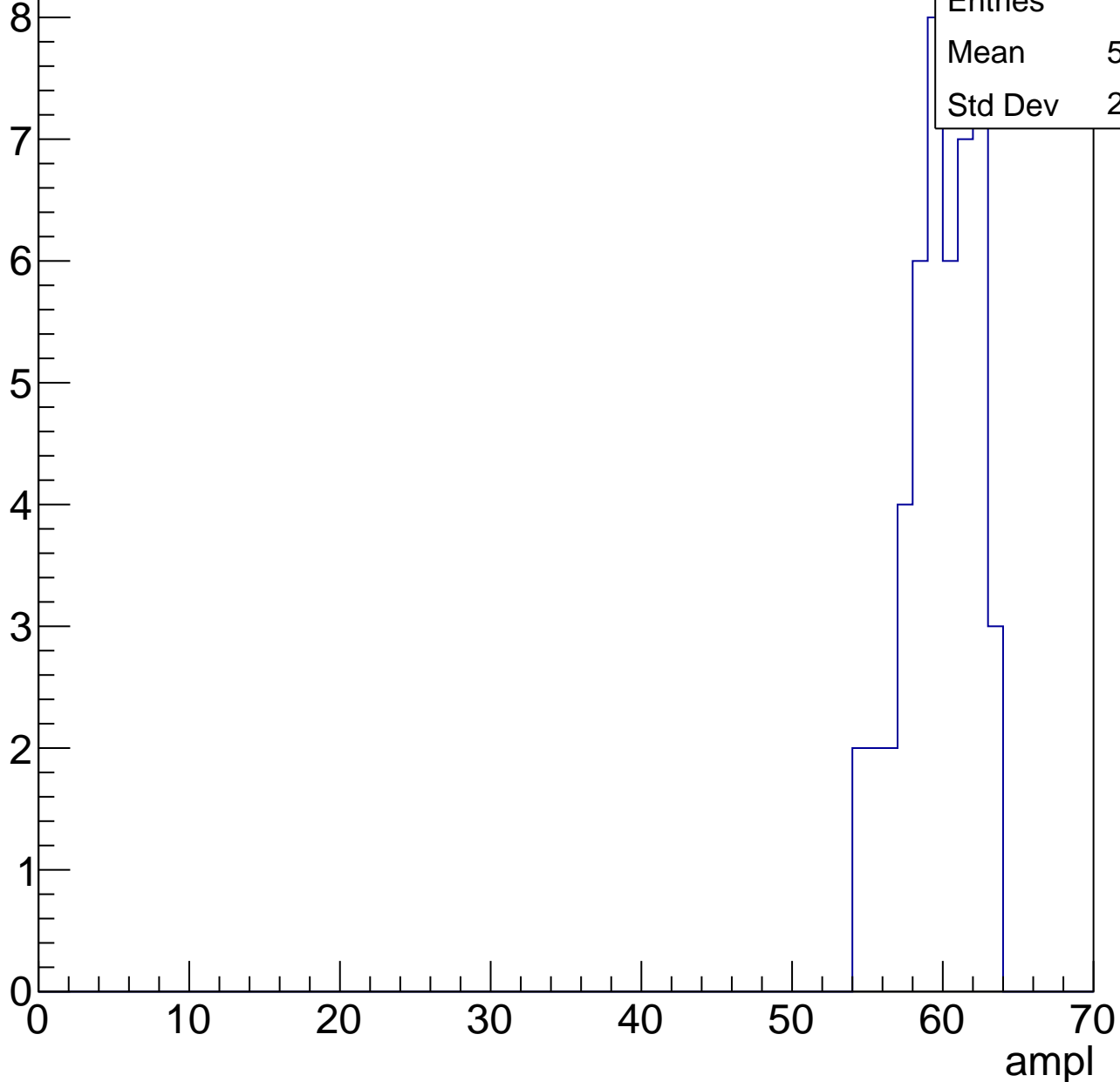
Entries	64
Mean	53.67
Std Dev	3.478



B1L102S, U2-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



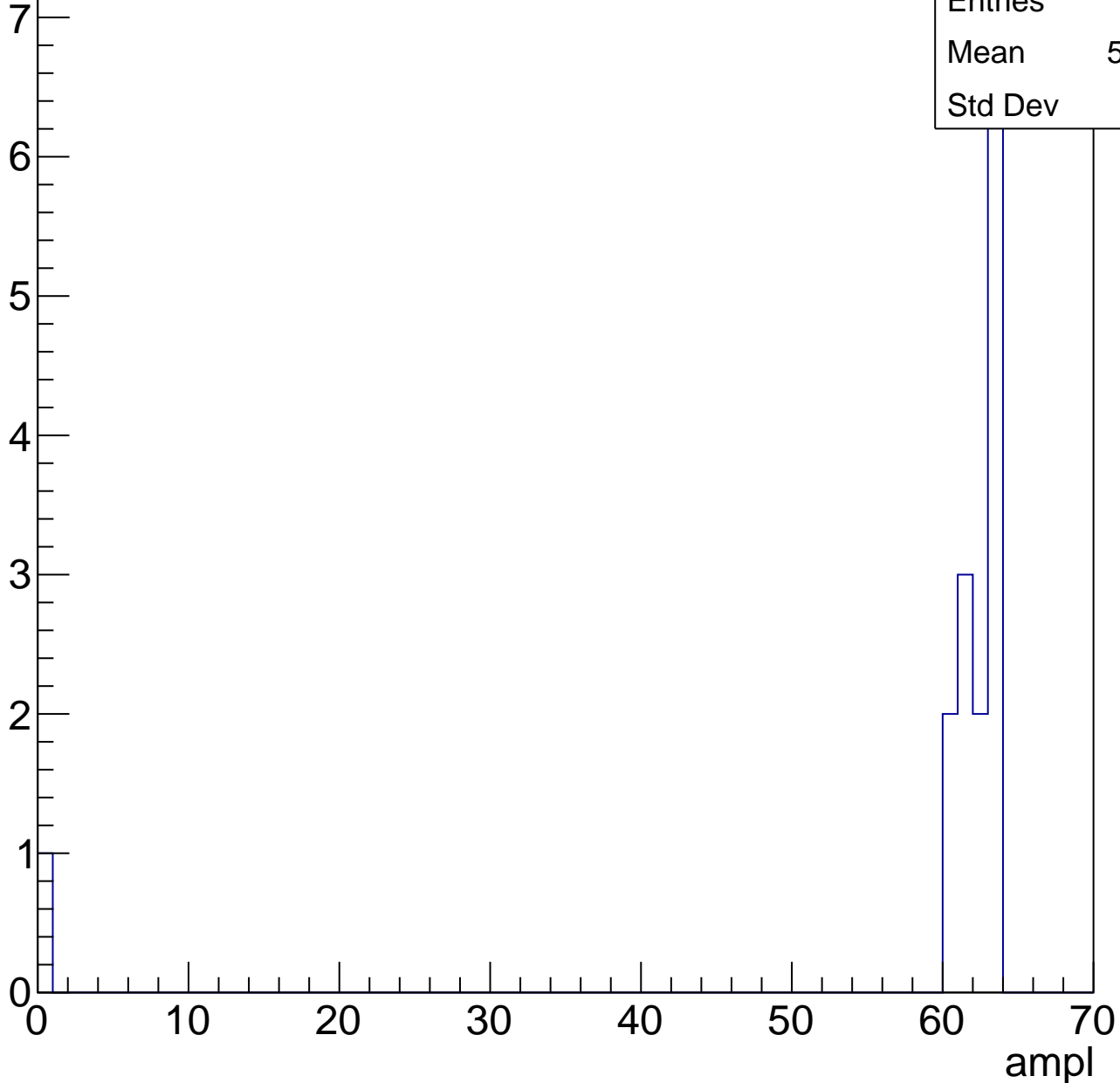
Entries	48
Mean	59.38
Std Dev	2.368

B1L102S, U2-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.87
Std Dev	15.5



B1L102S, U2-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch49, adc0

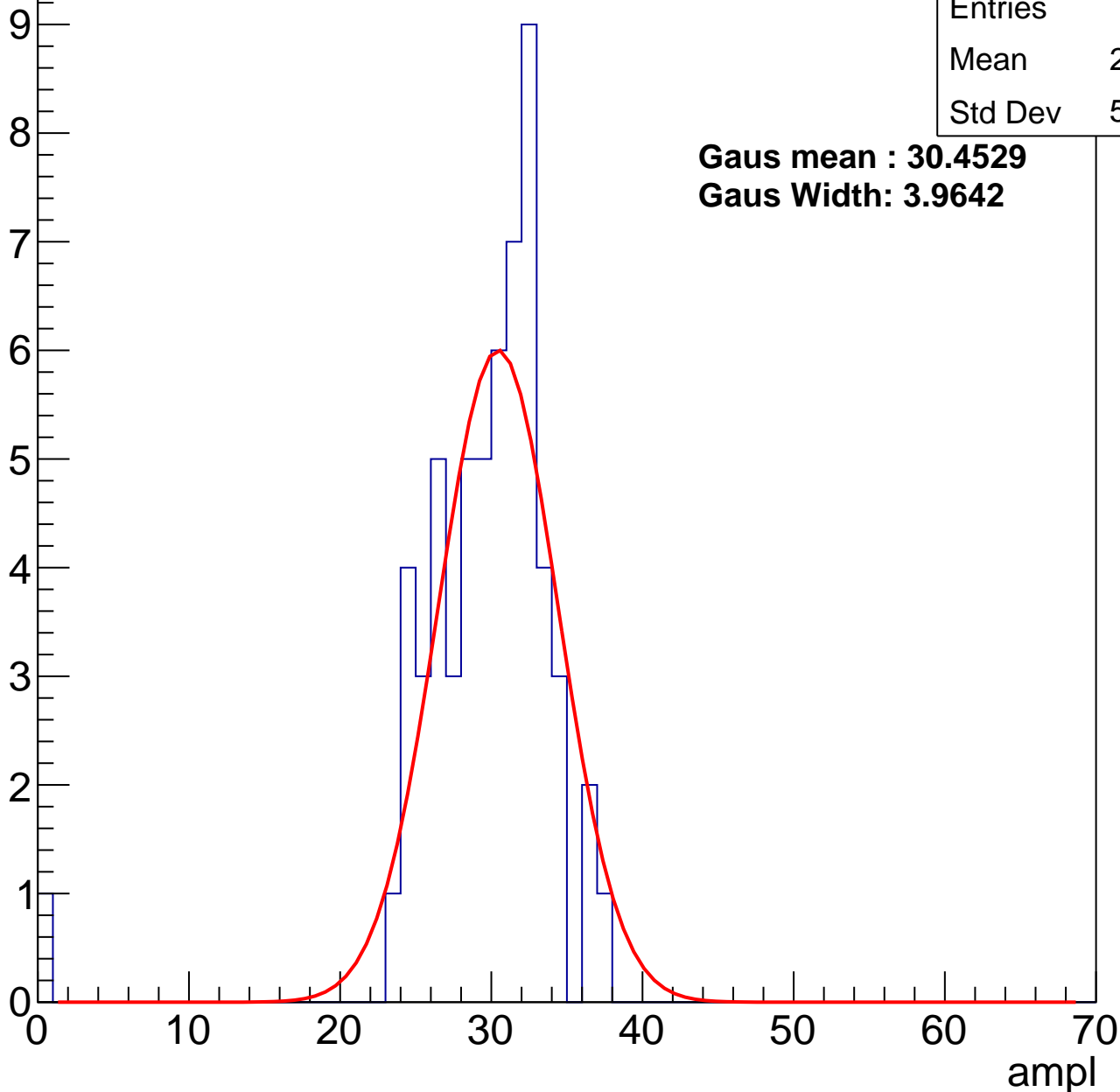
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.12
Std Dev	5.046

Gaus mean : 30.4529

Gaus Width: 3.9642



B1L102S, U2-ch49, adc1

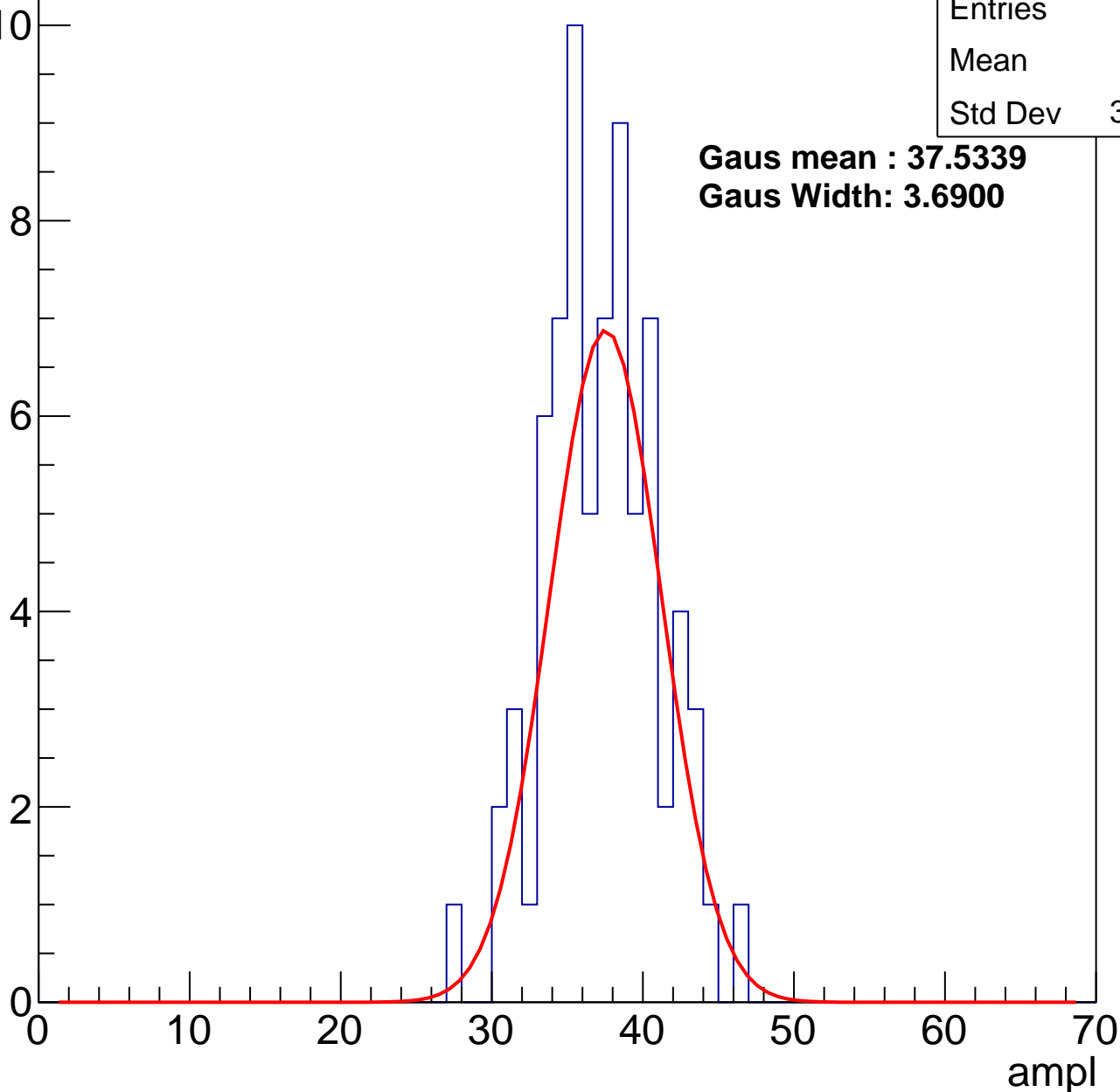
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.8
Std Dev	3.665

Gaus mean : 37.5339

Gaus Width: 3.6900



B1L102S, U2-ch49, adc2

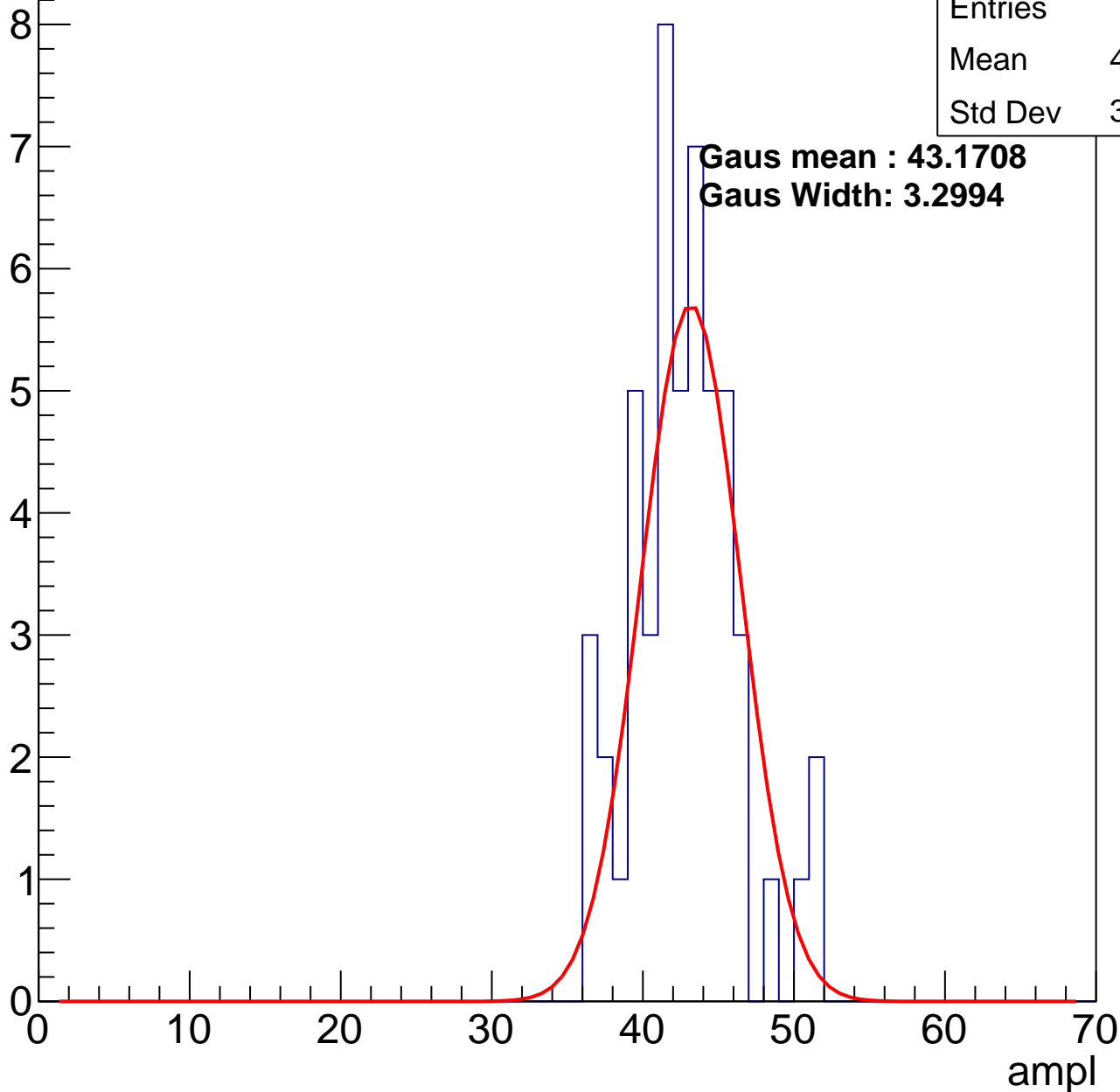
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.29
Std Dev	3.477

Gaus mean : 43.1708

Gaus Width: 3.2994



B1L102S, U2-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	48.77
Std Dev	3.219

Entry

10

8

6

4

2

0

0

10

20

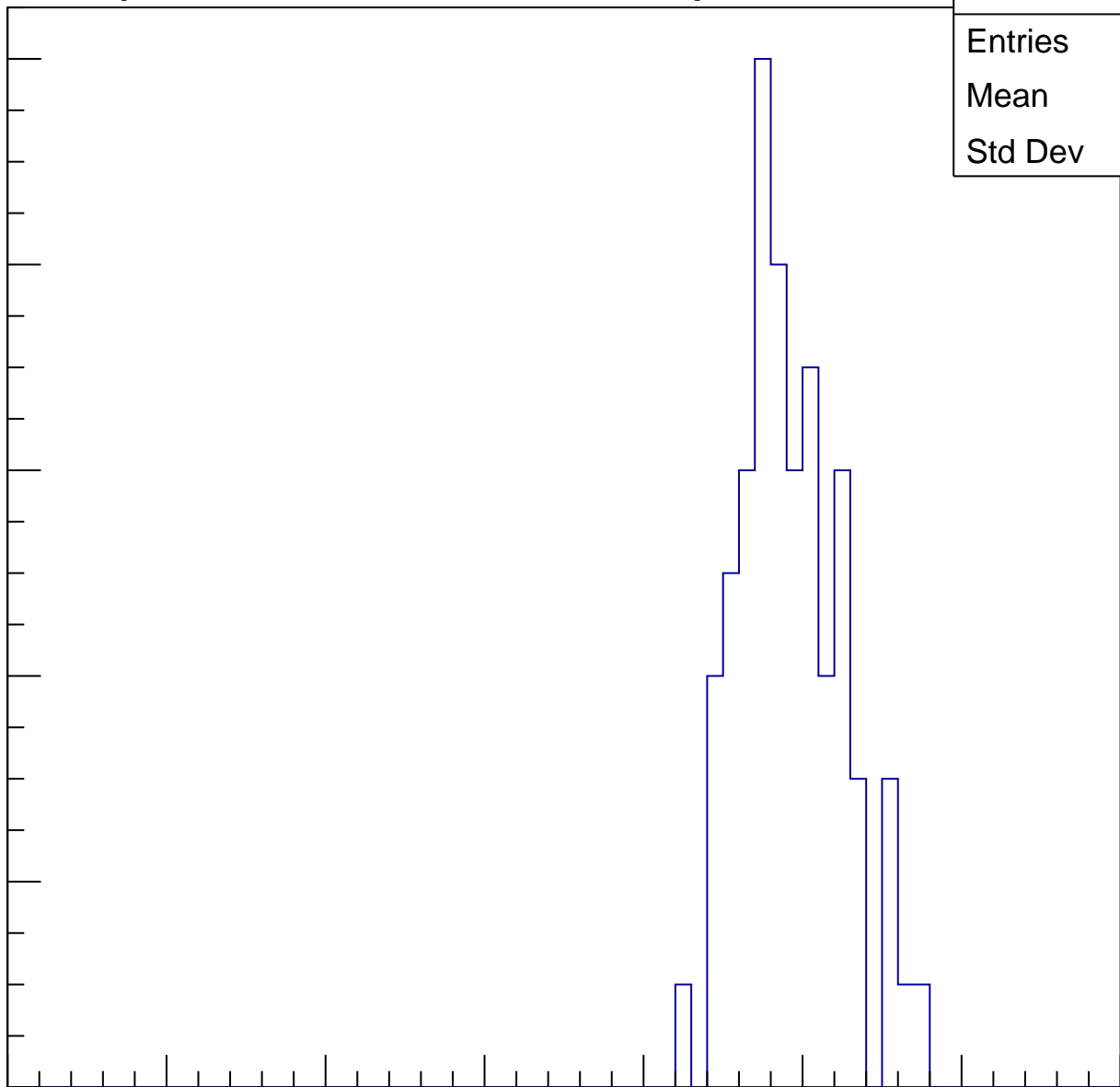
30

40

50

60

ampl

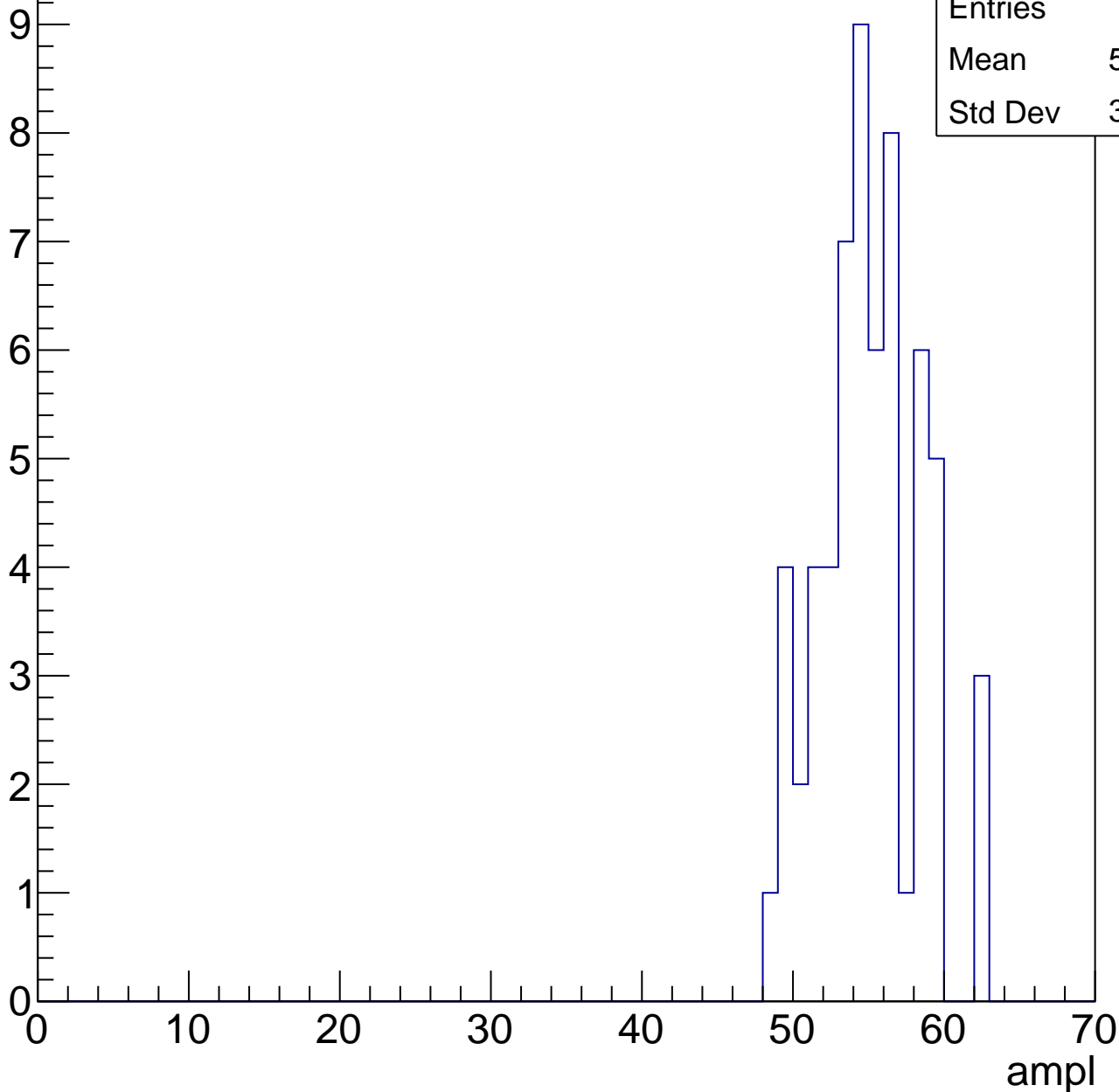


B1L102S, U2-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.62
Std Dev	3.327



B1L102S, U2-ch49, adc5

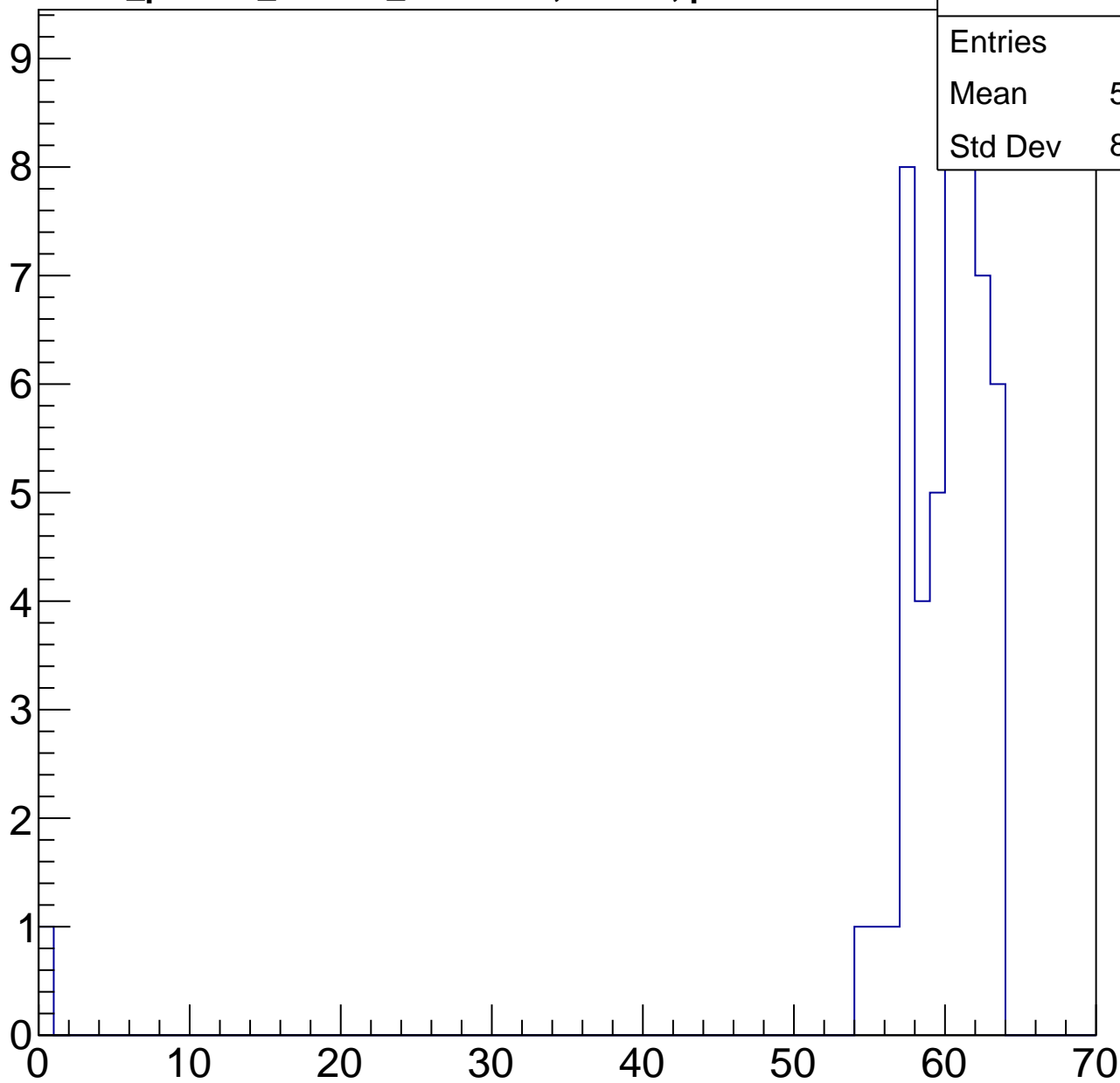
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.63
Std Dev	8.508

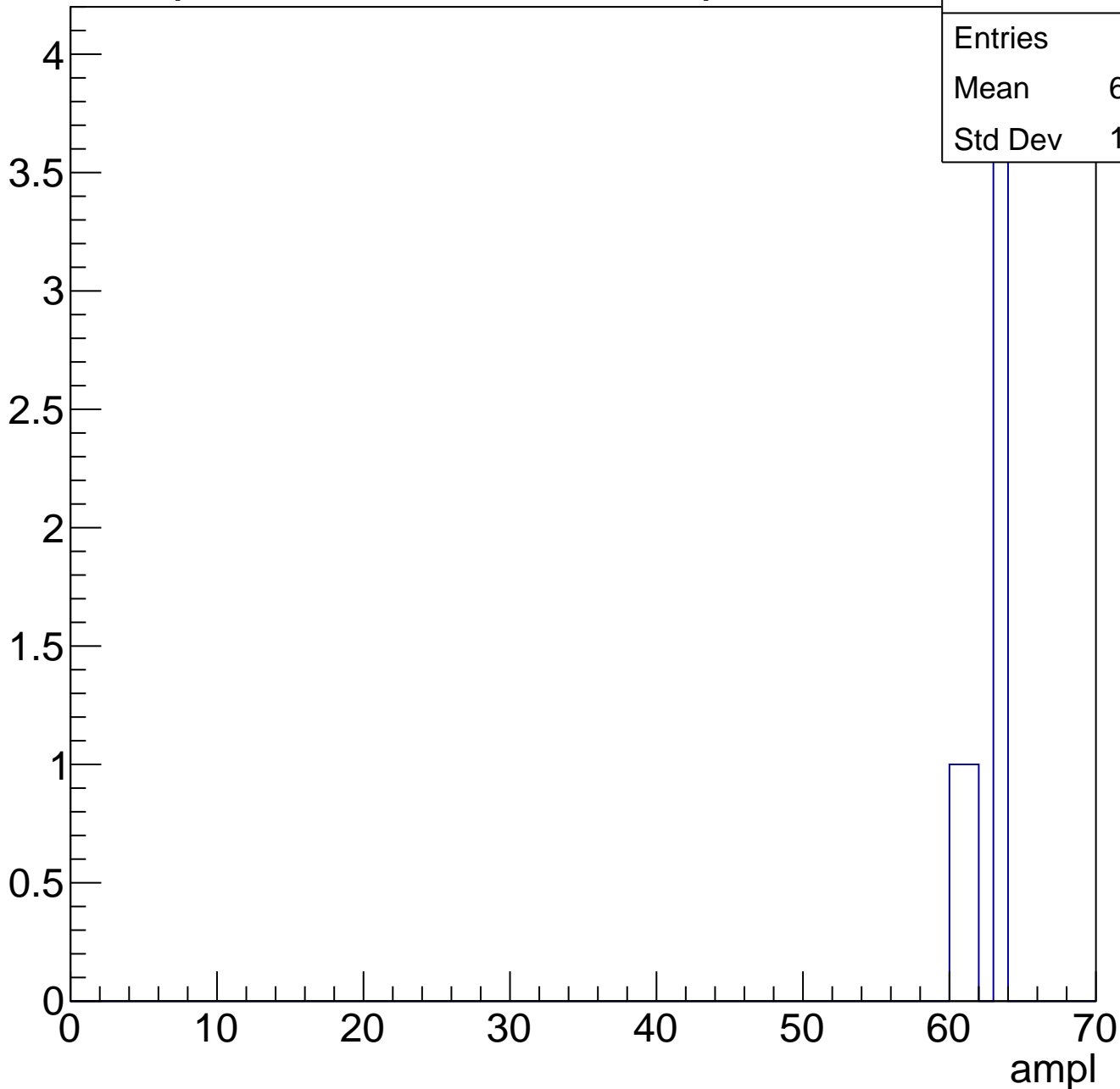
ampl



B1L102S, U2-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch50, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	94
Mean	27.7
Std Dev	6.949

Gaus mean : 29.0674

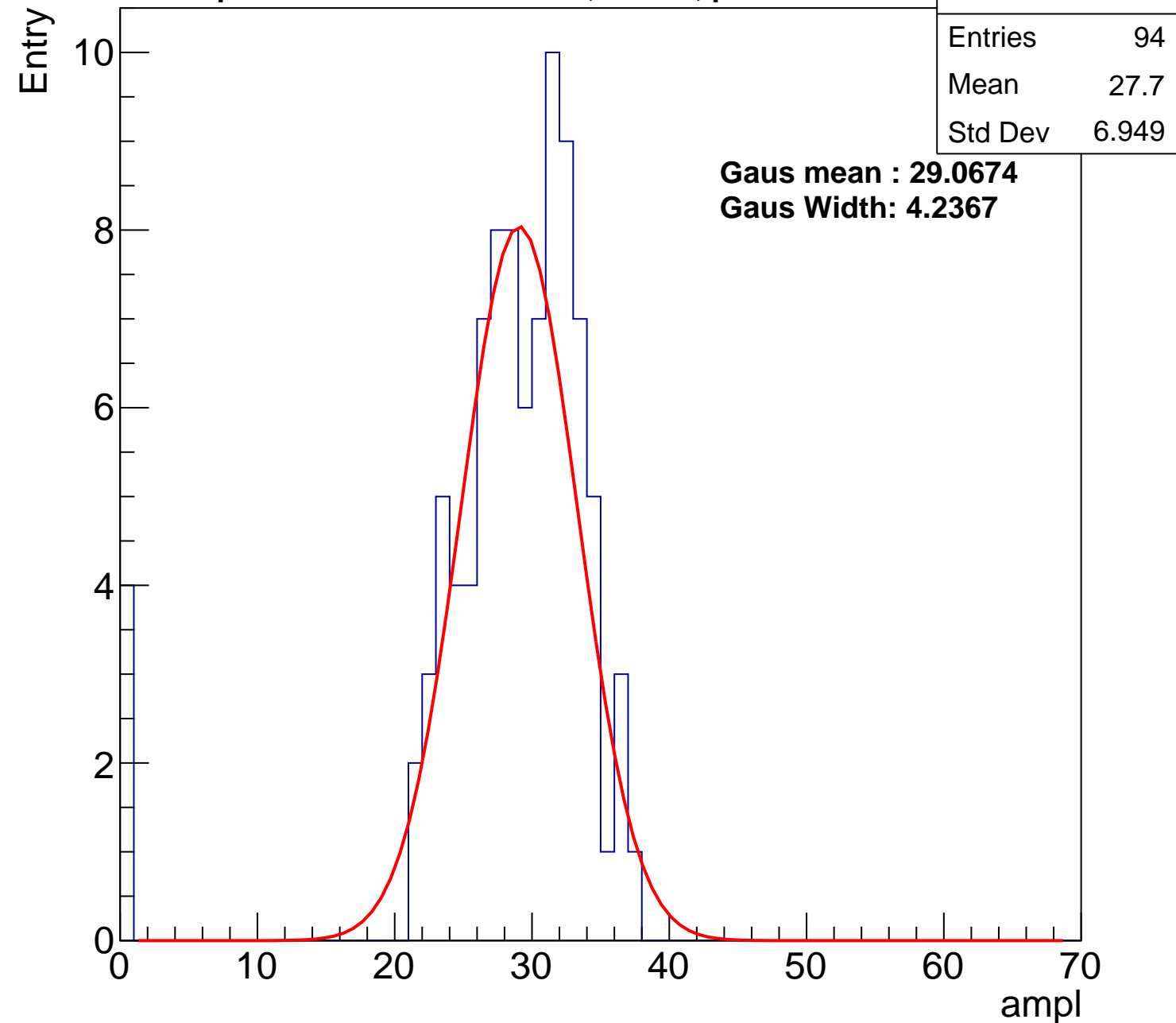
Gaus Width: 4.2367

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch50, adc1

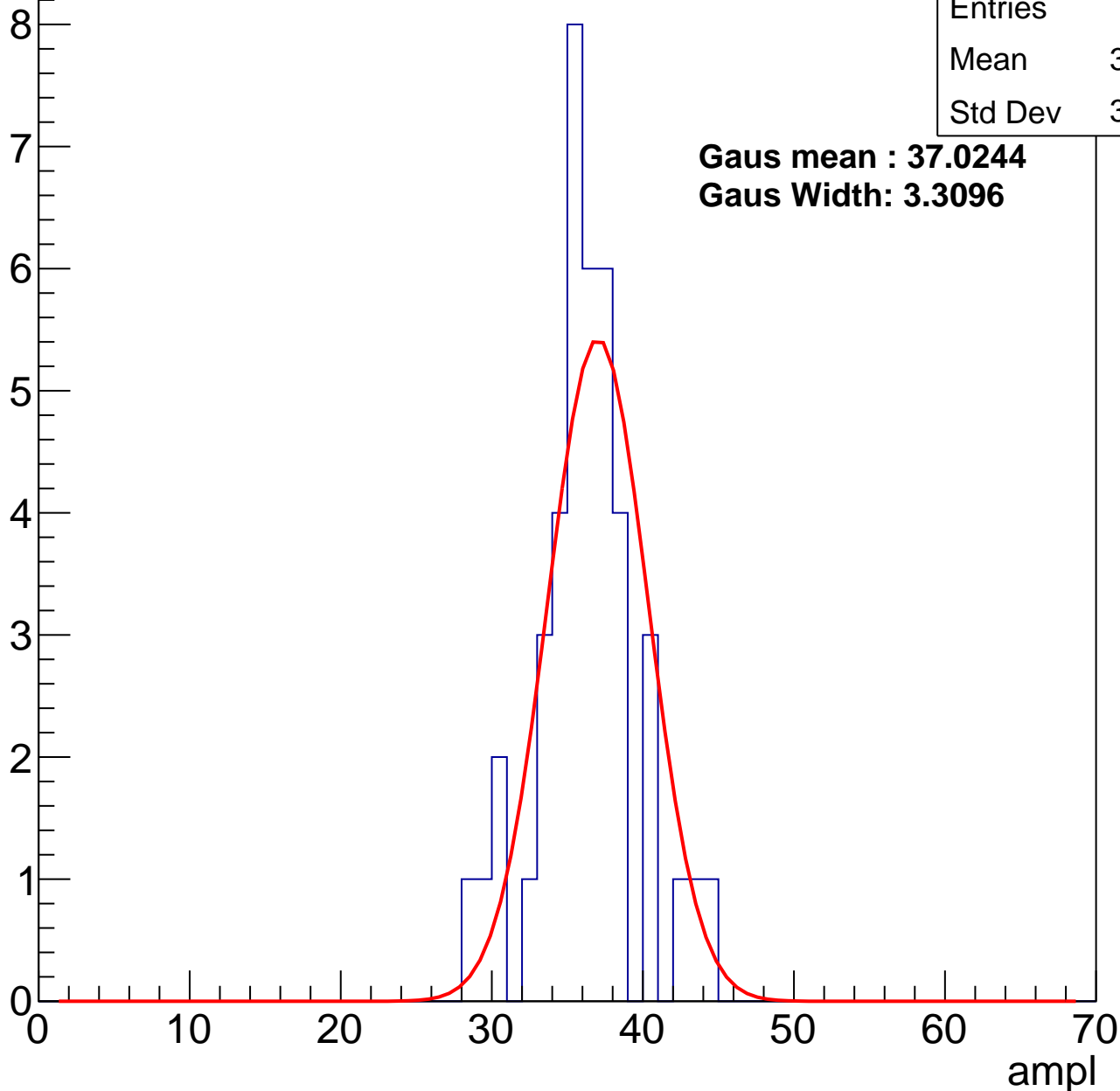
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	35.79
Std Dev	3.342

Gaus mean : 37.0244

Gaus Width: 3.3096



B1L102S, U2-ch50, adc2

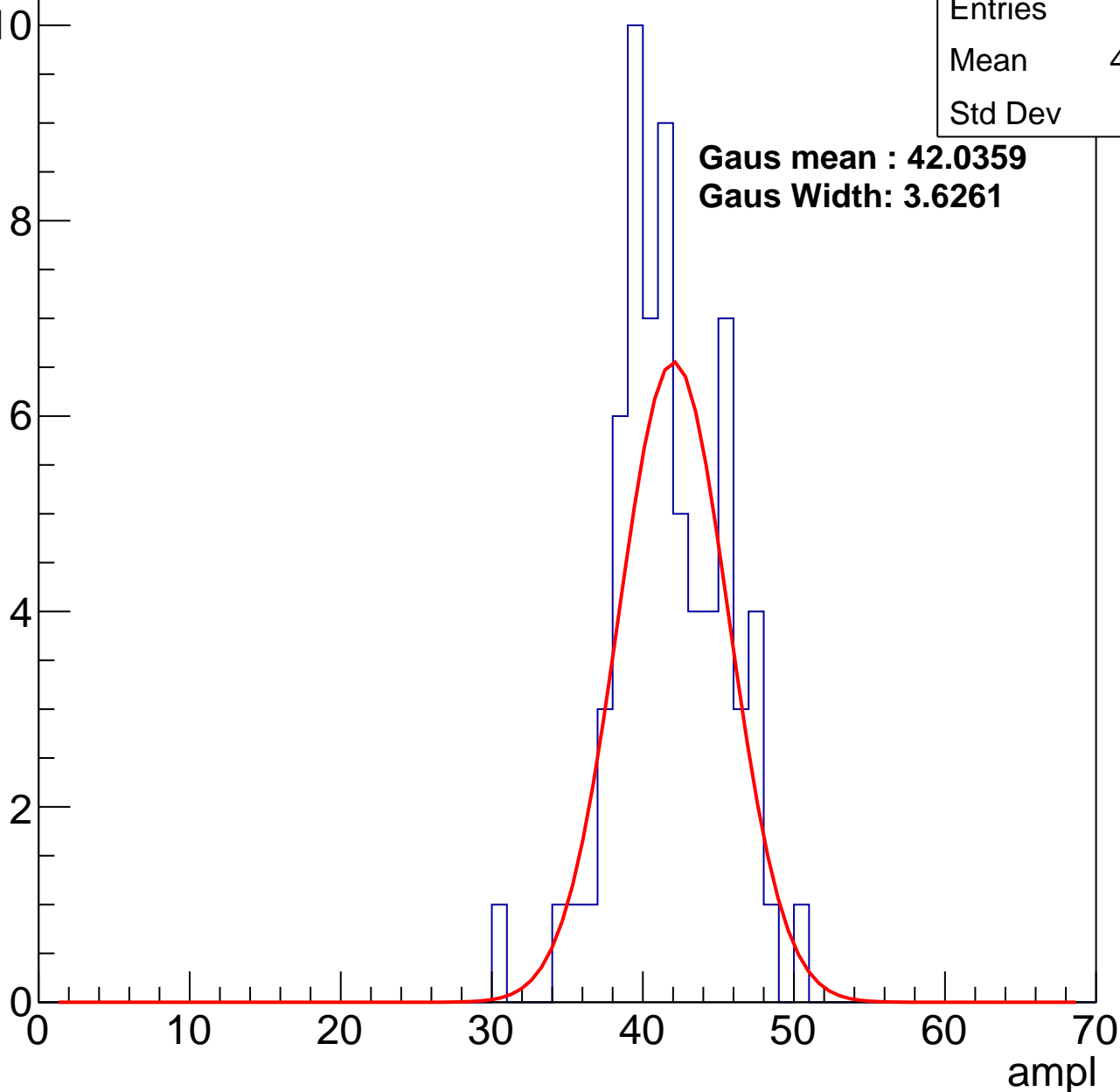
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.32
Std Dev	3.62

Gaus mean : 42.0359

Gaus Width: 3.6261

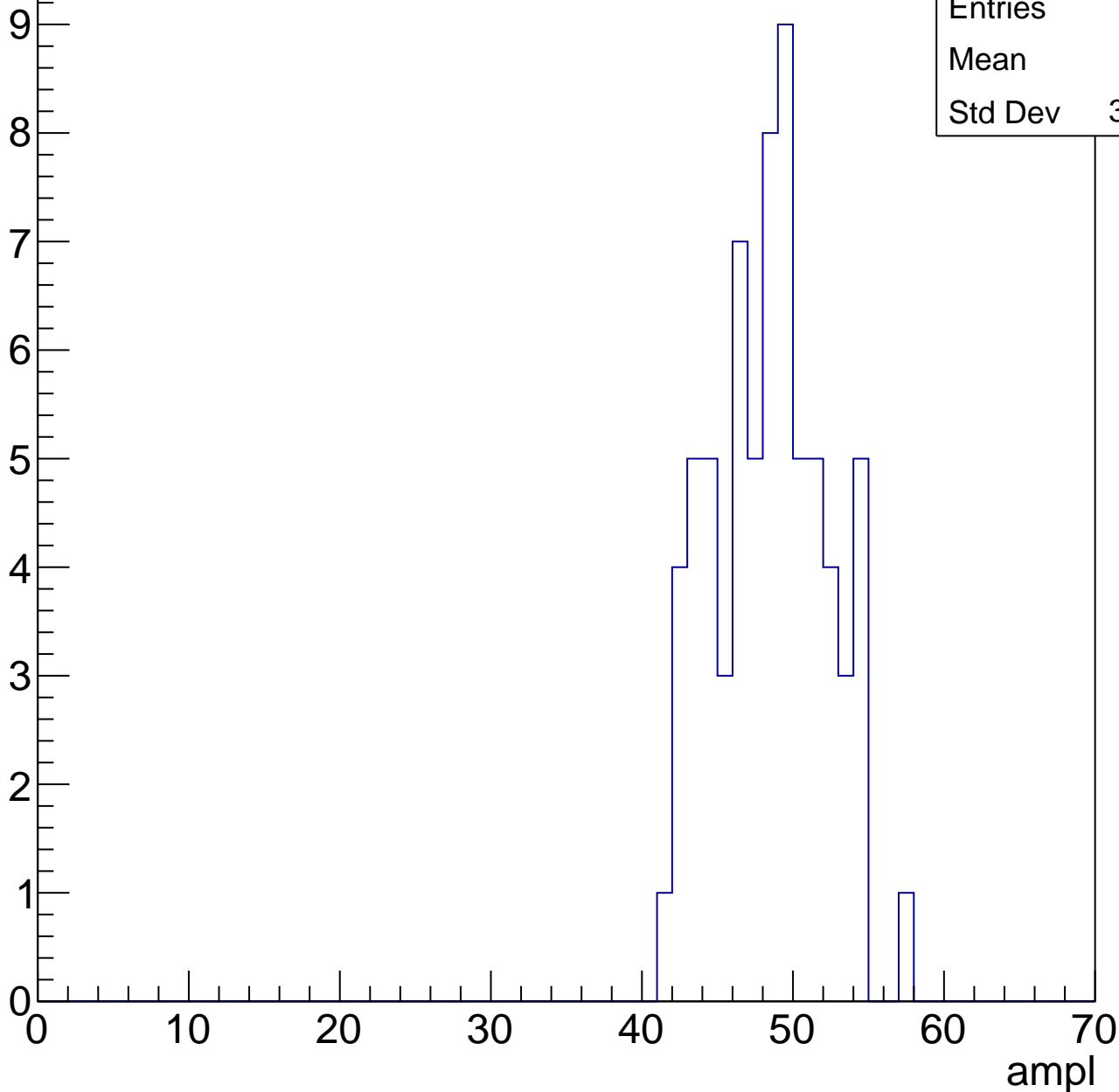


B1L102S, U2-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48
Std Dev	3.649

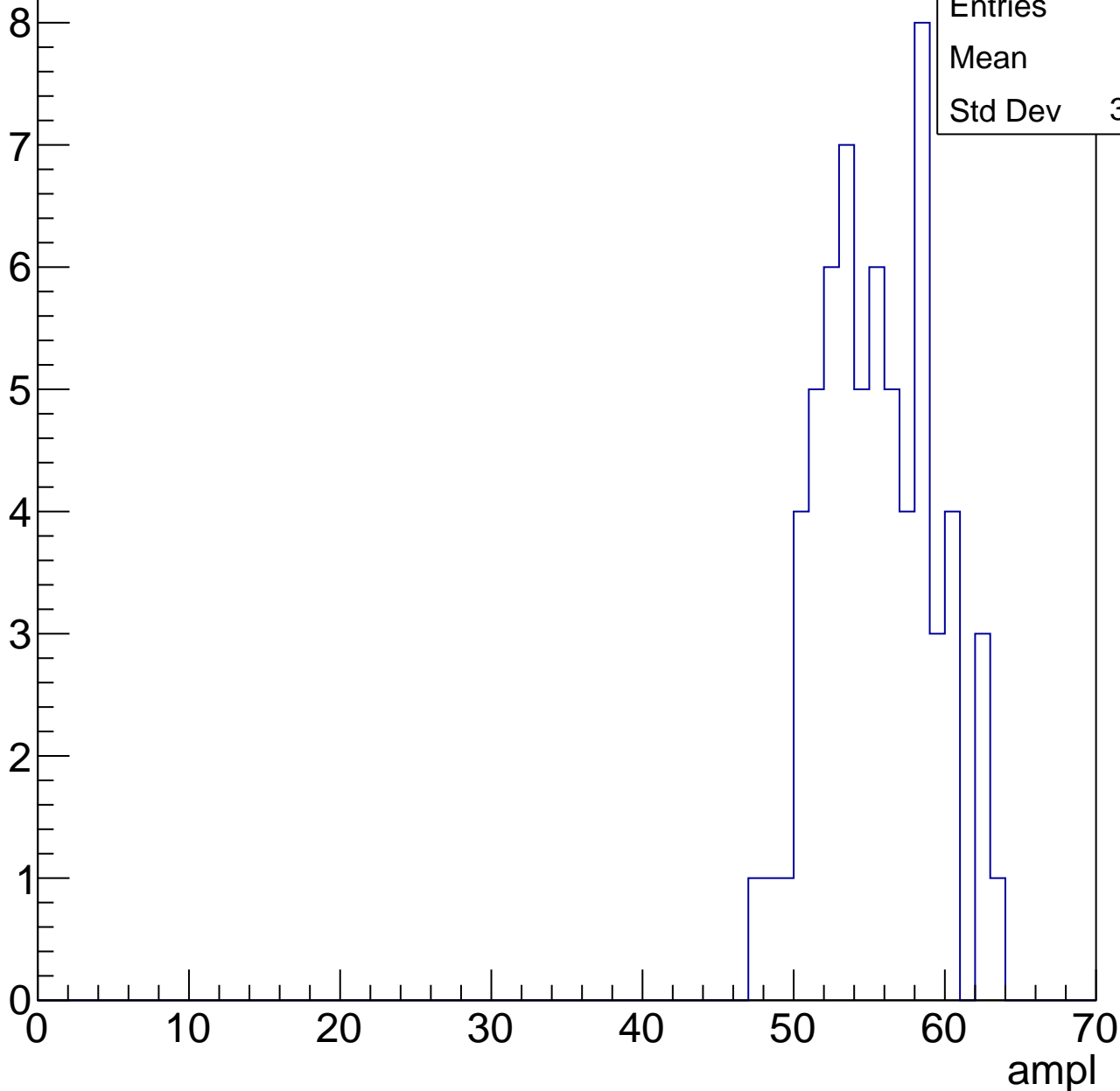


B1L102S, U2-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55
Std Dev	3.683

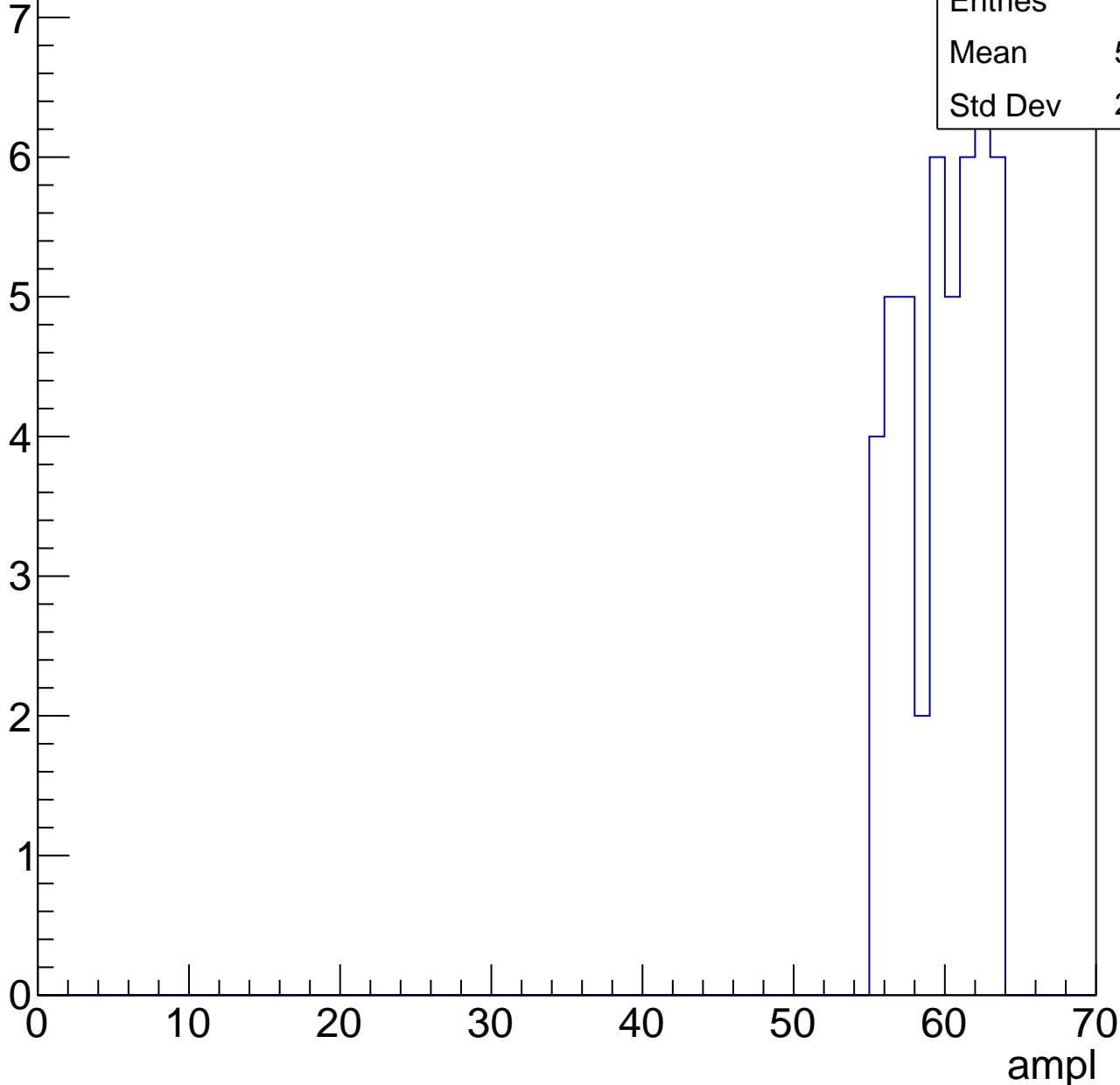


B1L102S, U2-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.41
Std Dev	2.601



B1L102S, U2-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

9

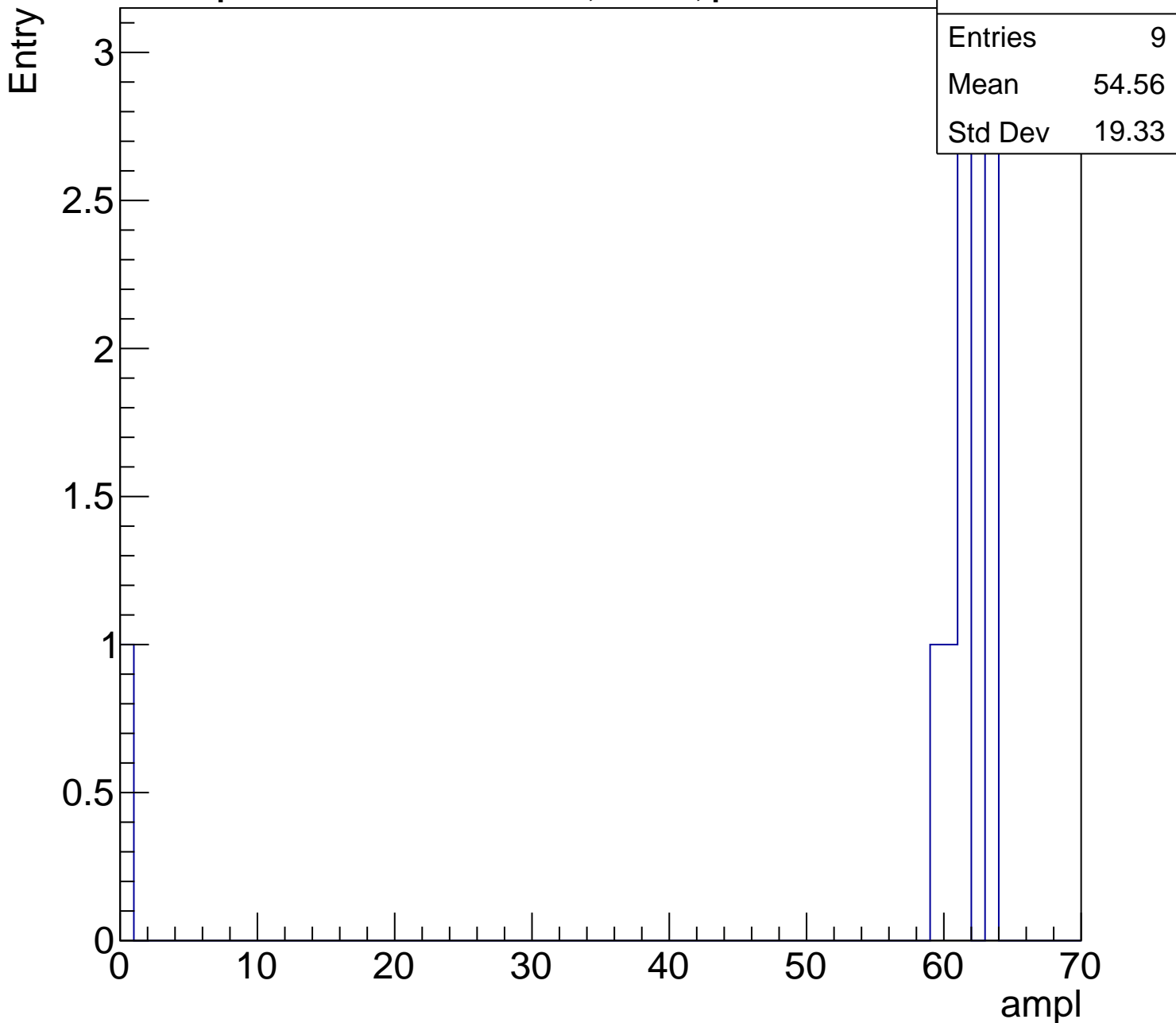
Mean

54.56

Std Dev

19.33

ampl



B1L102S, U2-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch51, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	26.87
Std Dev	5.564

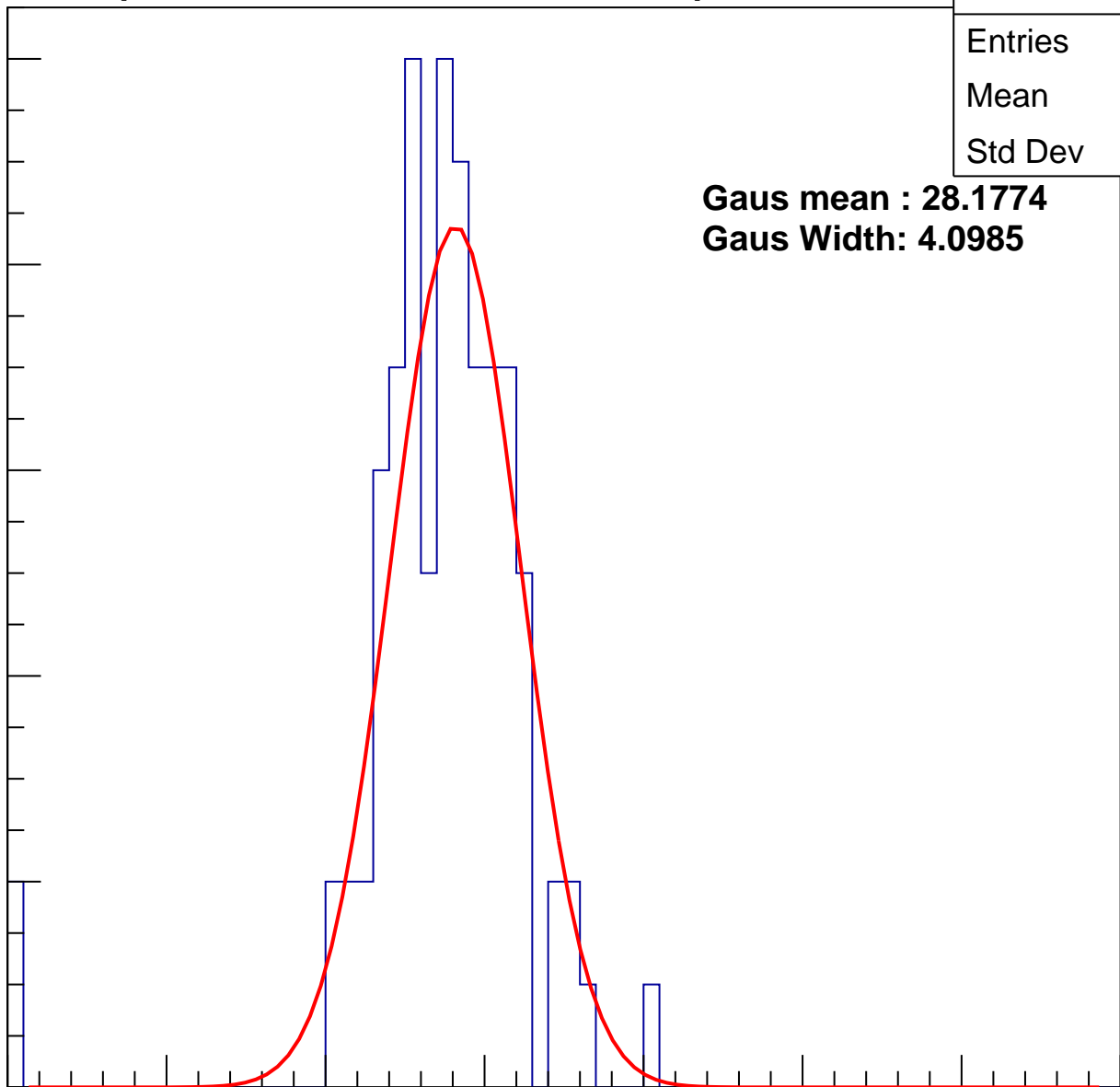
Gaus mean : 28.1774

Gaus Width: 4.0985

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch51, adc1

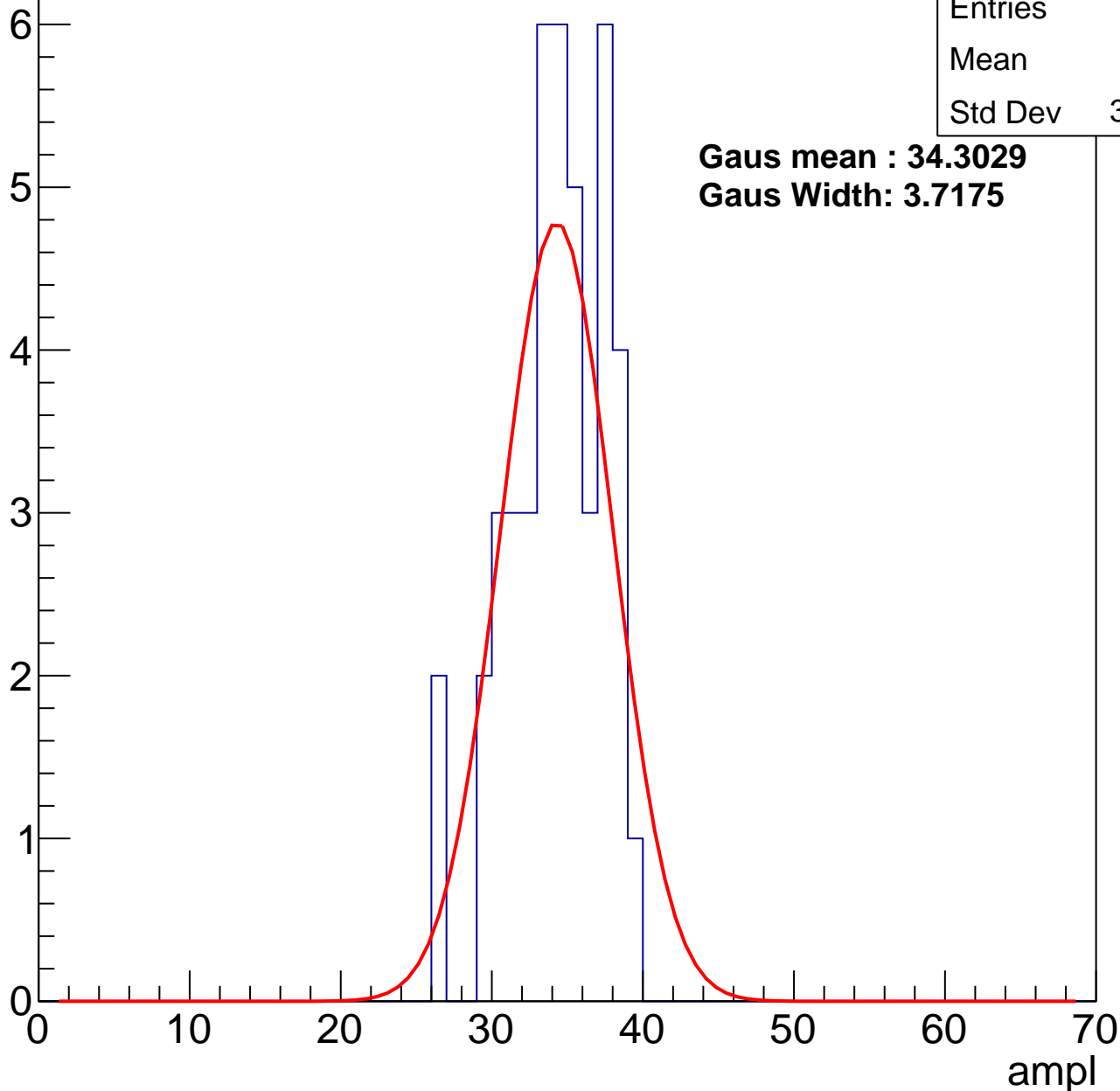
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	33.8
Std Dev	3.123

Gaus mean : 34.3029

Gaus Width: 3.7175



B1L102S, U2-ch51, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	39.88
Std Dev	3.763

Gaus mean : 40.3741

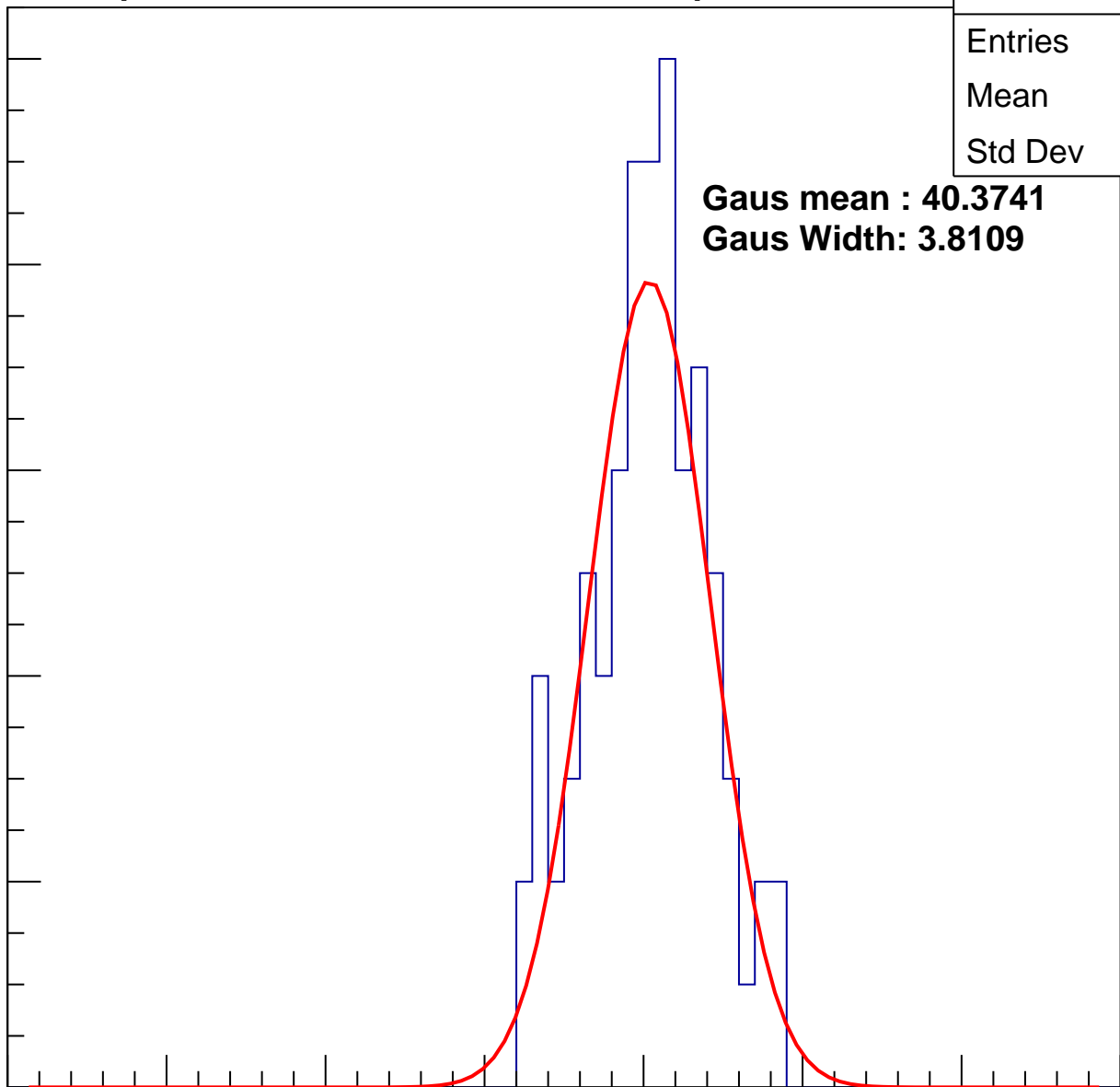
Gaus Width: 3.8109

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

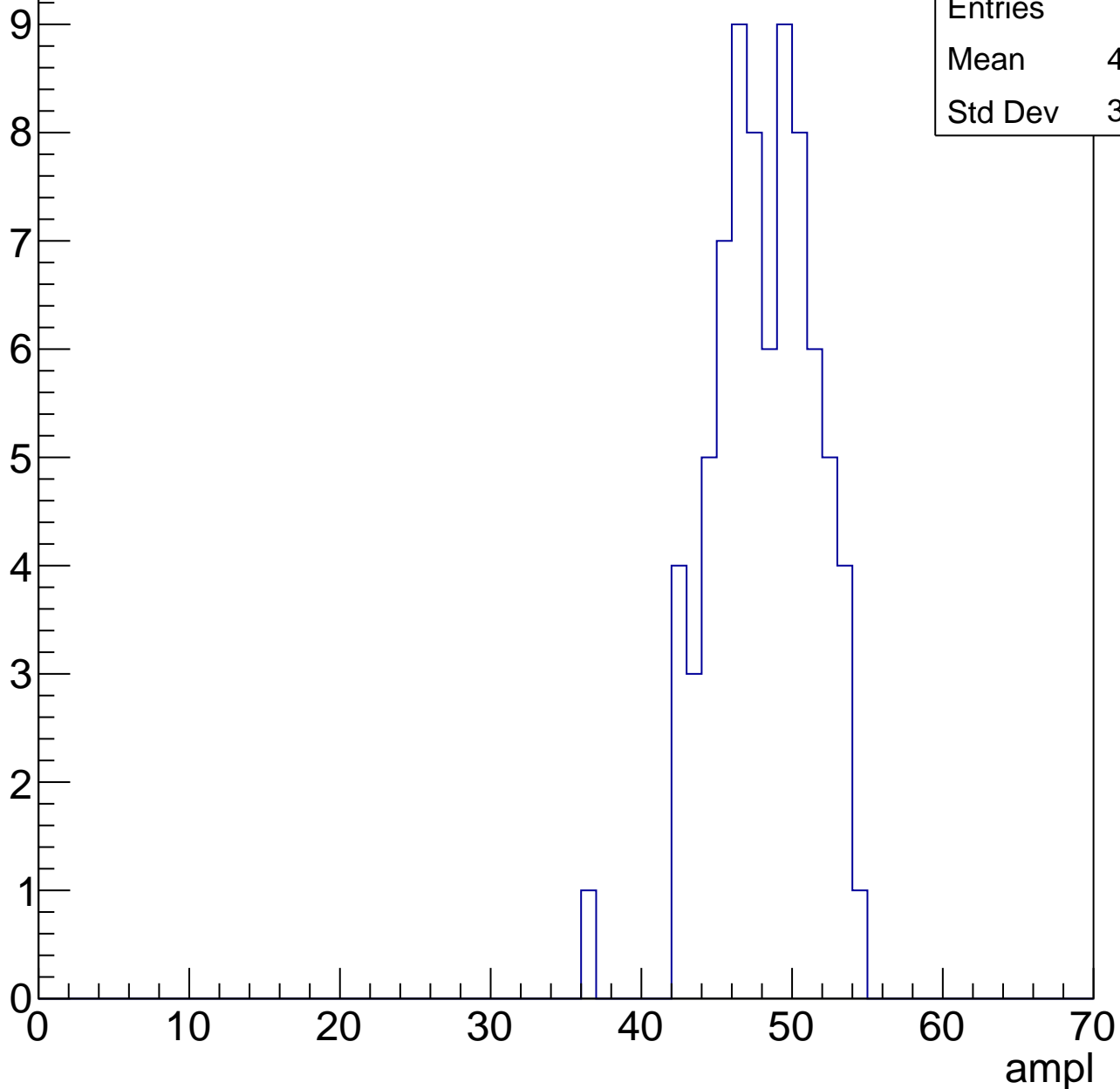
ampl



B1L102S, U2-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

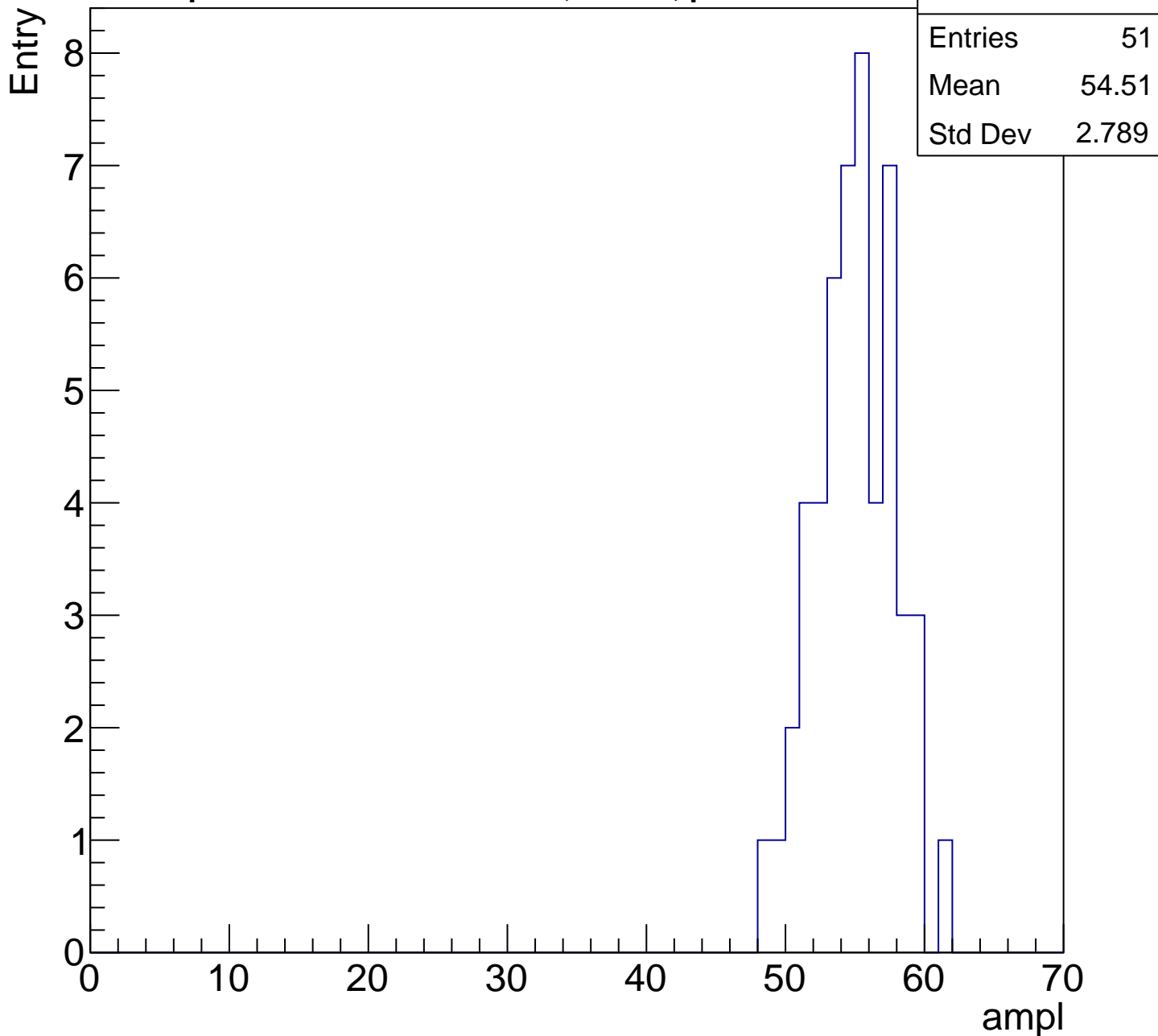
Entry



Entries	76
Mean	47.62
Std Dev	3.344

B1L102S, U2-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

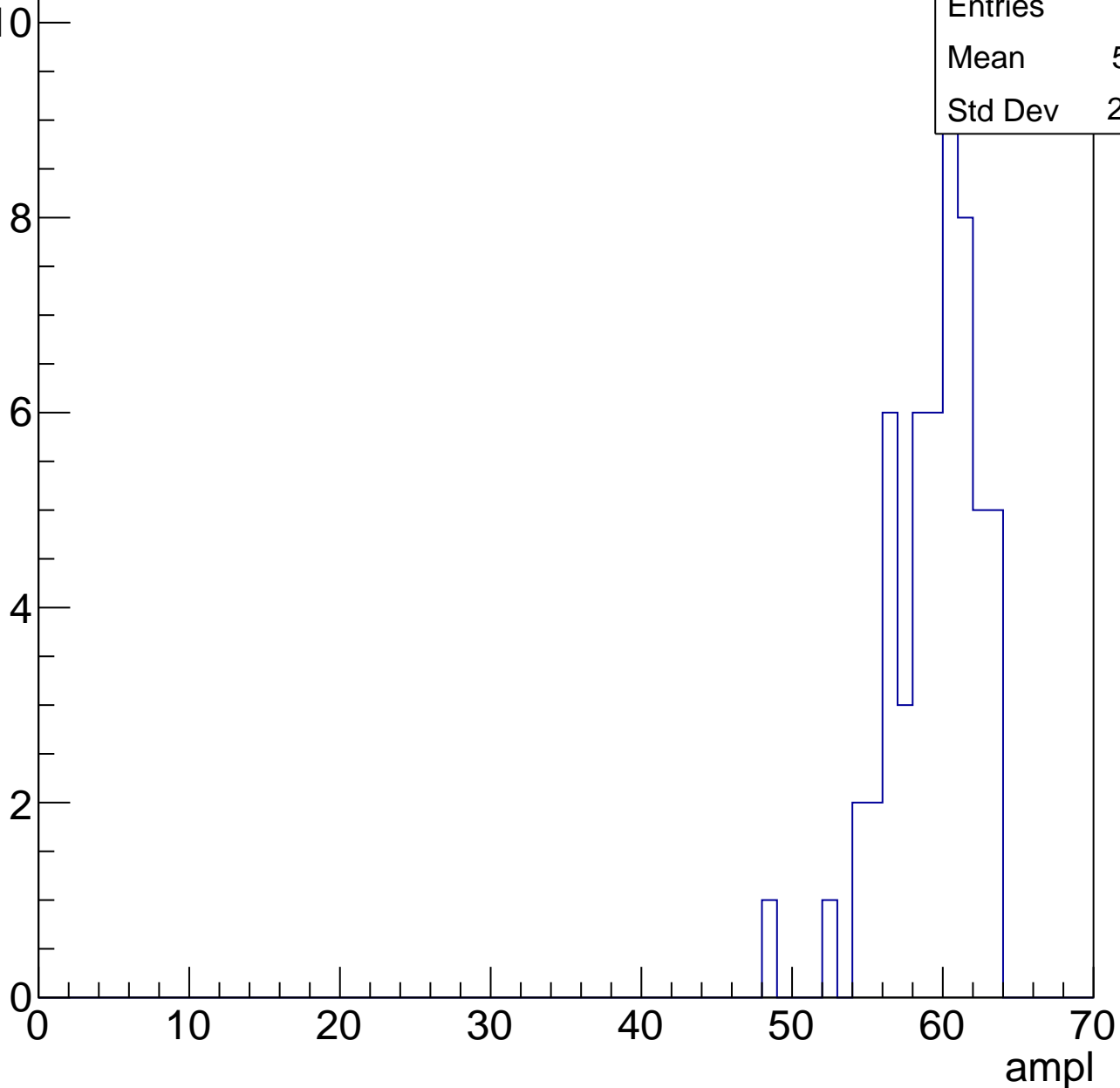


B1L102S, U2-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.91
Std Dev	2.986

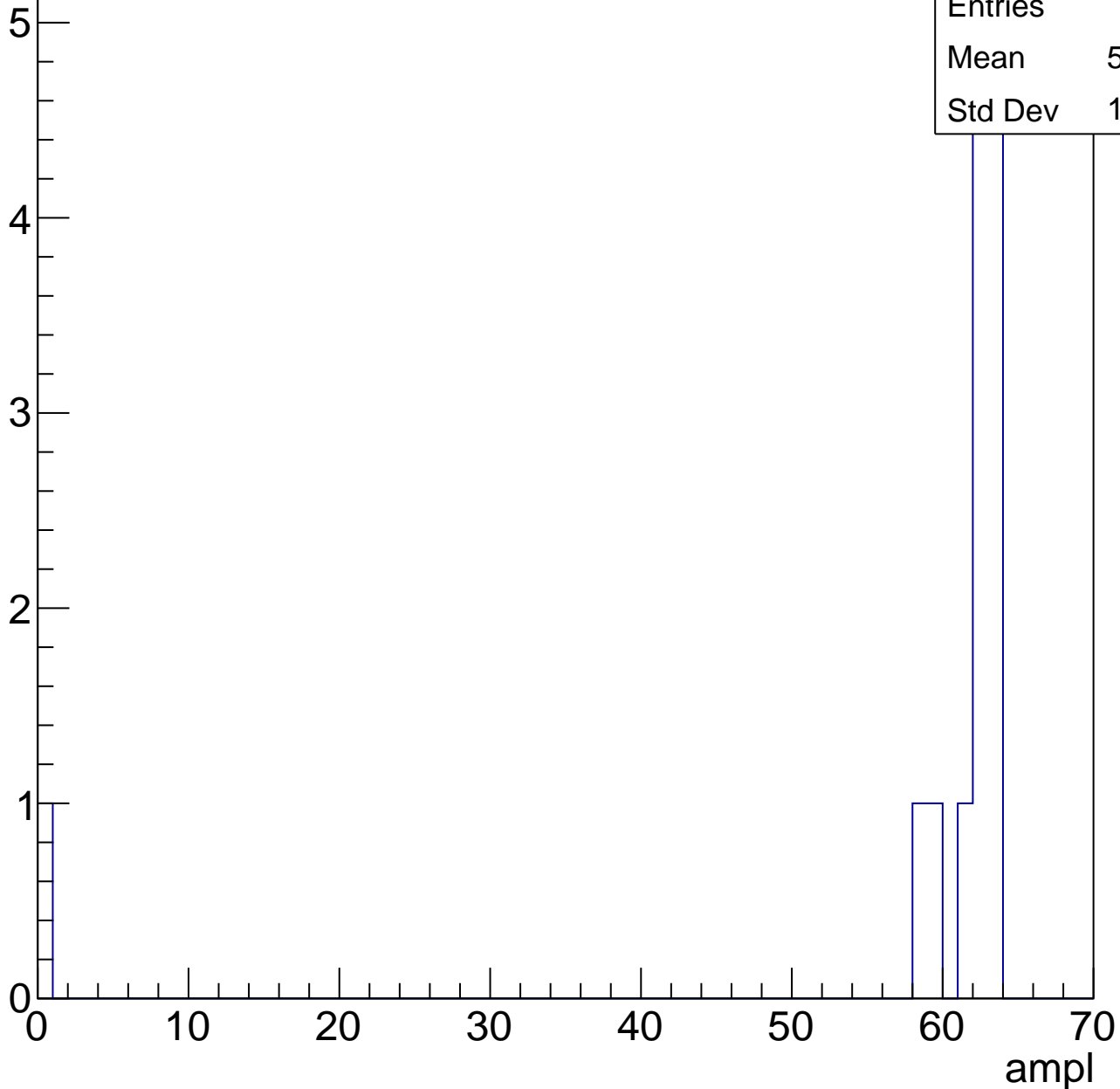


B1L102S, U2-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.36
Std Dev	15.98



B1L102S, U2-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch52, adc0

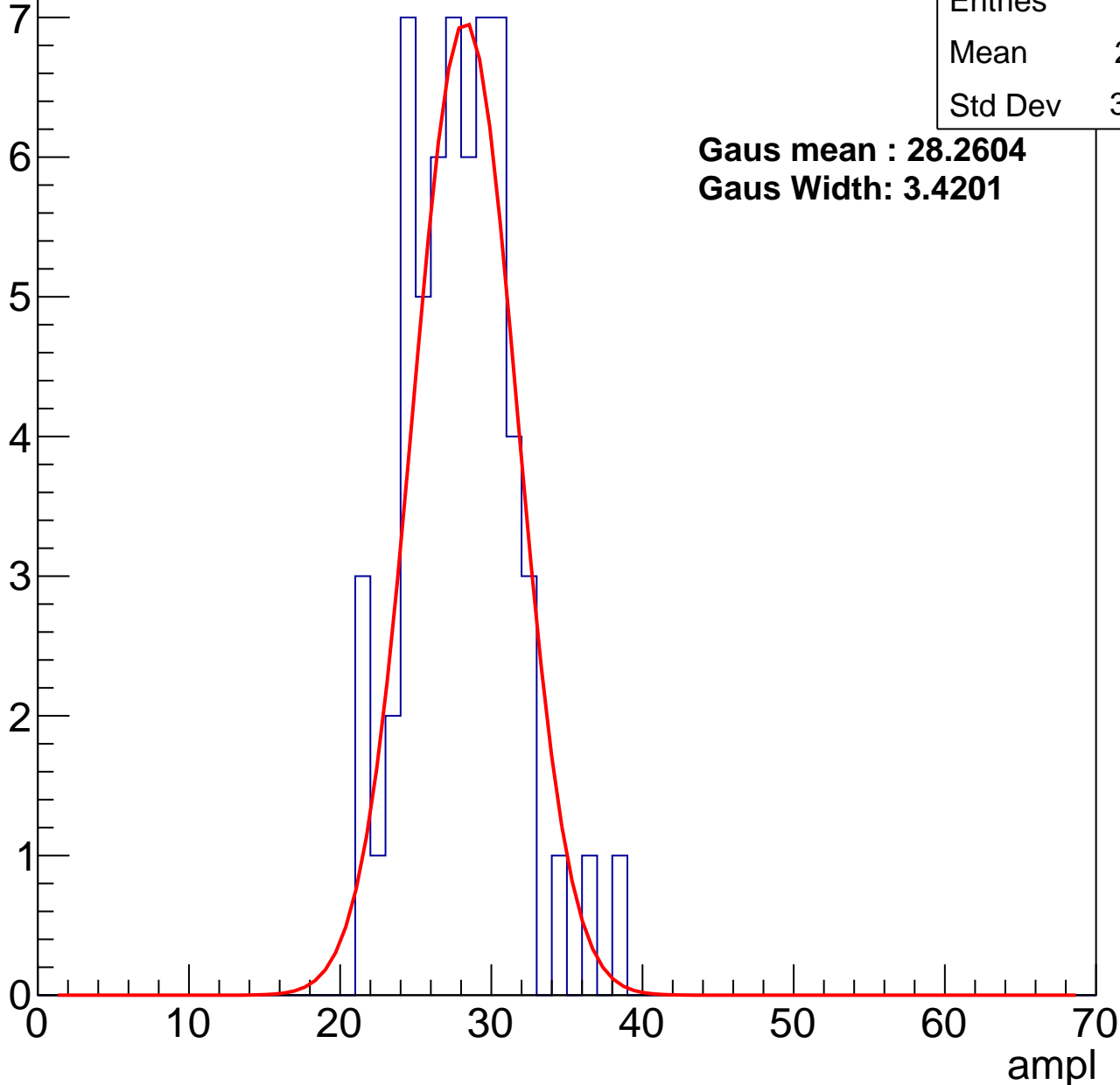
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.51
Std Dev	3.448

Gaus mean : 28.2604

Gaus Width: 3.4201



B1L102S, U2-ch52, adc1

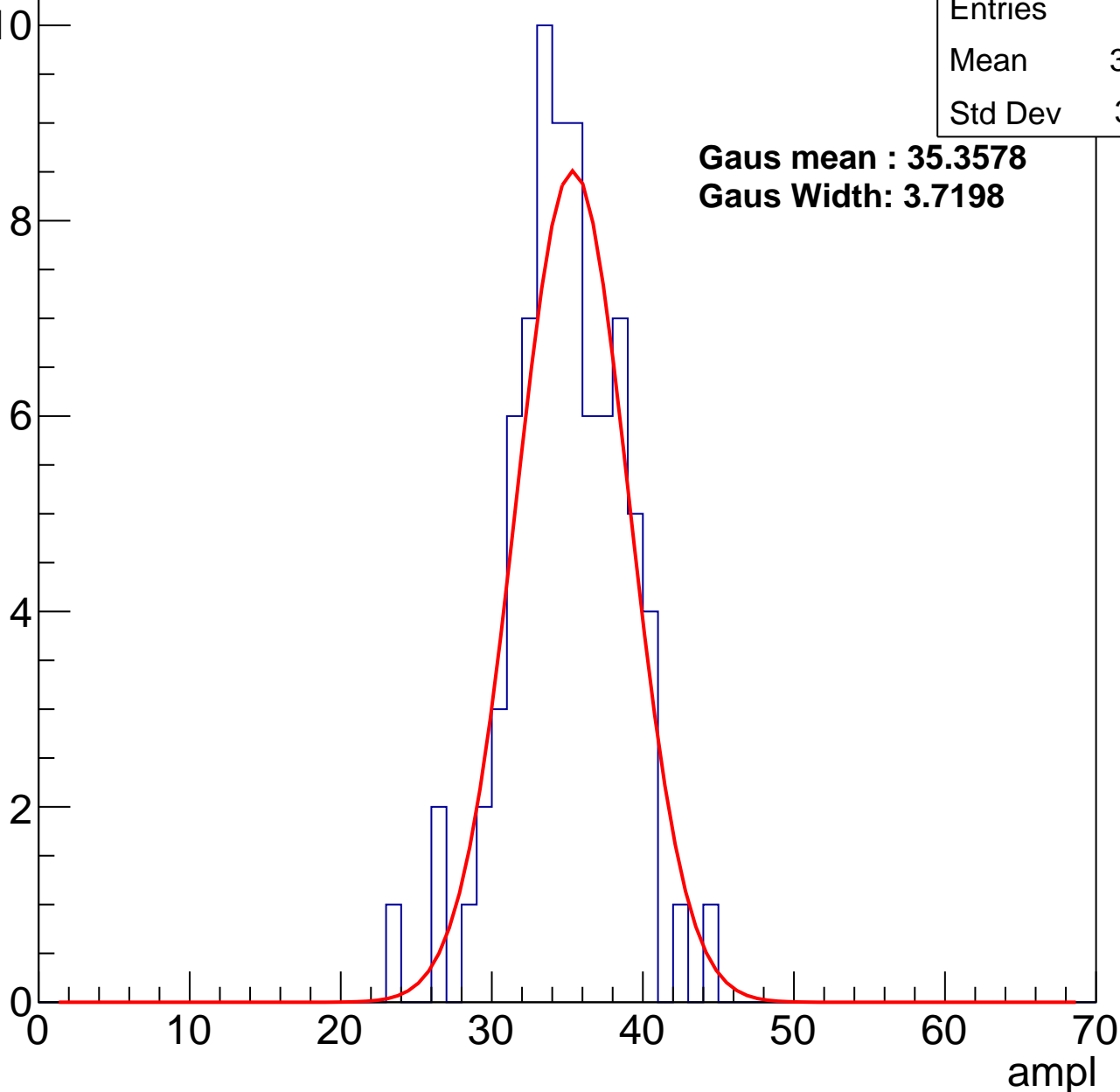
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.46
Std Dev	3.691

Gaus mean : 35.3578

Gaus Width: 3.7198



B1L102S, U2-ch52, adc2

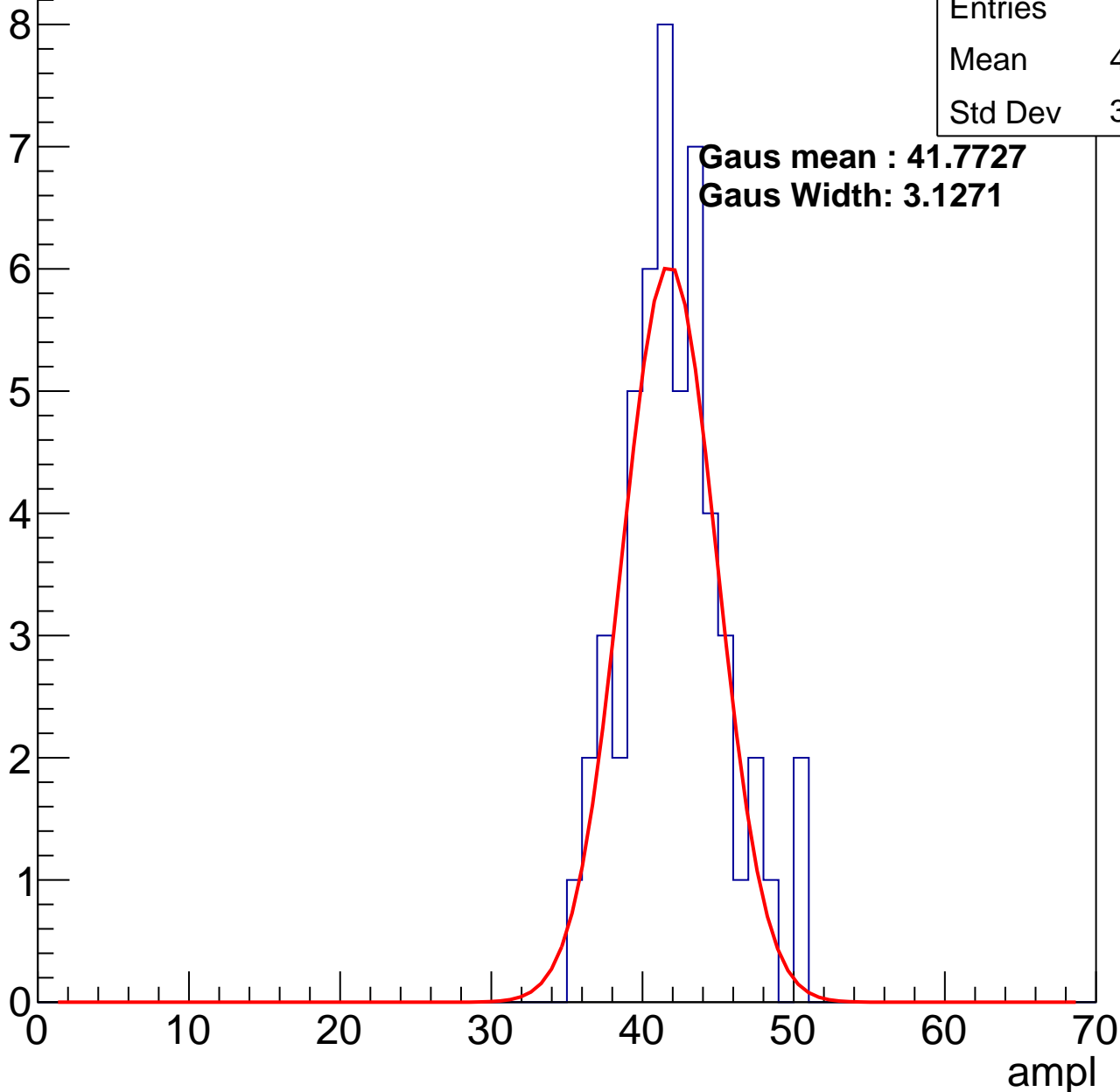
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	41.67
Std Dev	3.338

Gaus mean : 41.7727

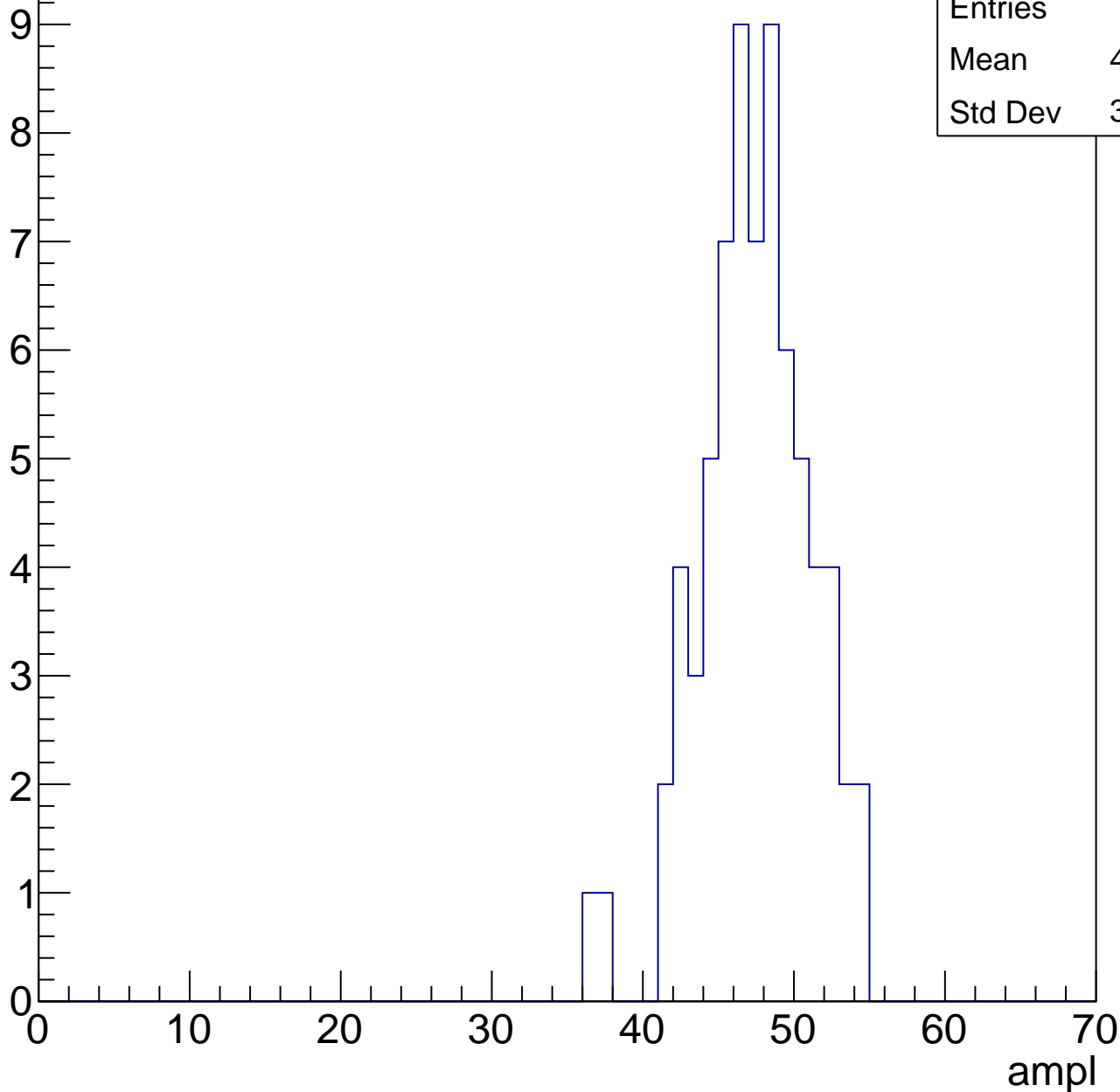
Gaus Width: 3.1271



B1L102S, U2-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

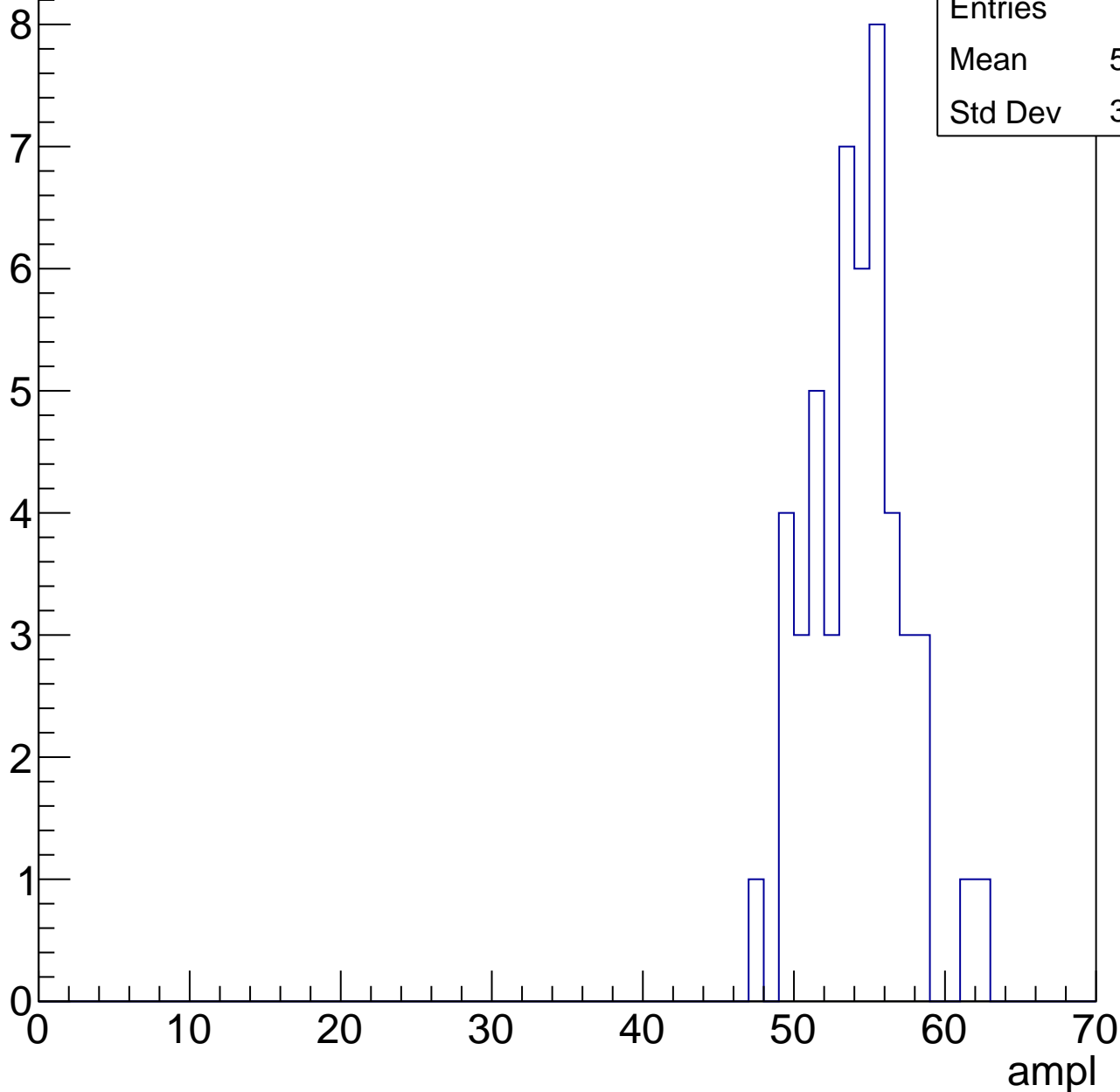


B1L102S, U2-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.69
Std Dev	3.092



B1L102S, U2-ch52, adc5

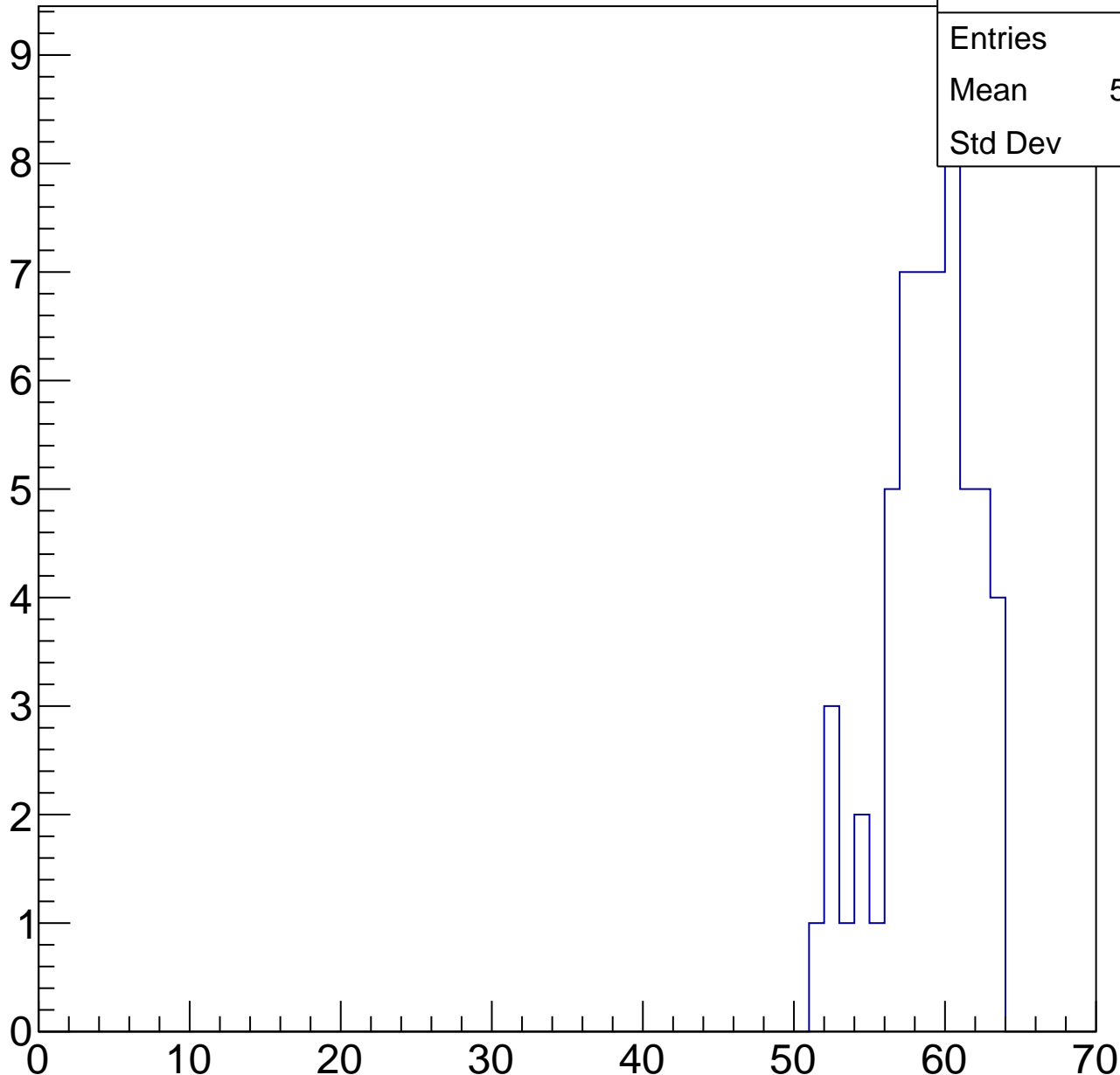
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	57
Mean	58.39
Std Dev	2.99

ampl

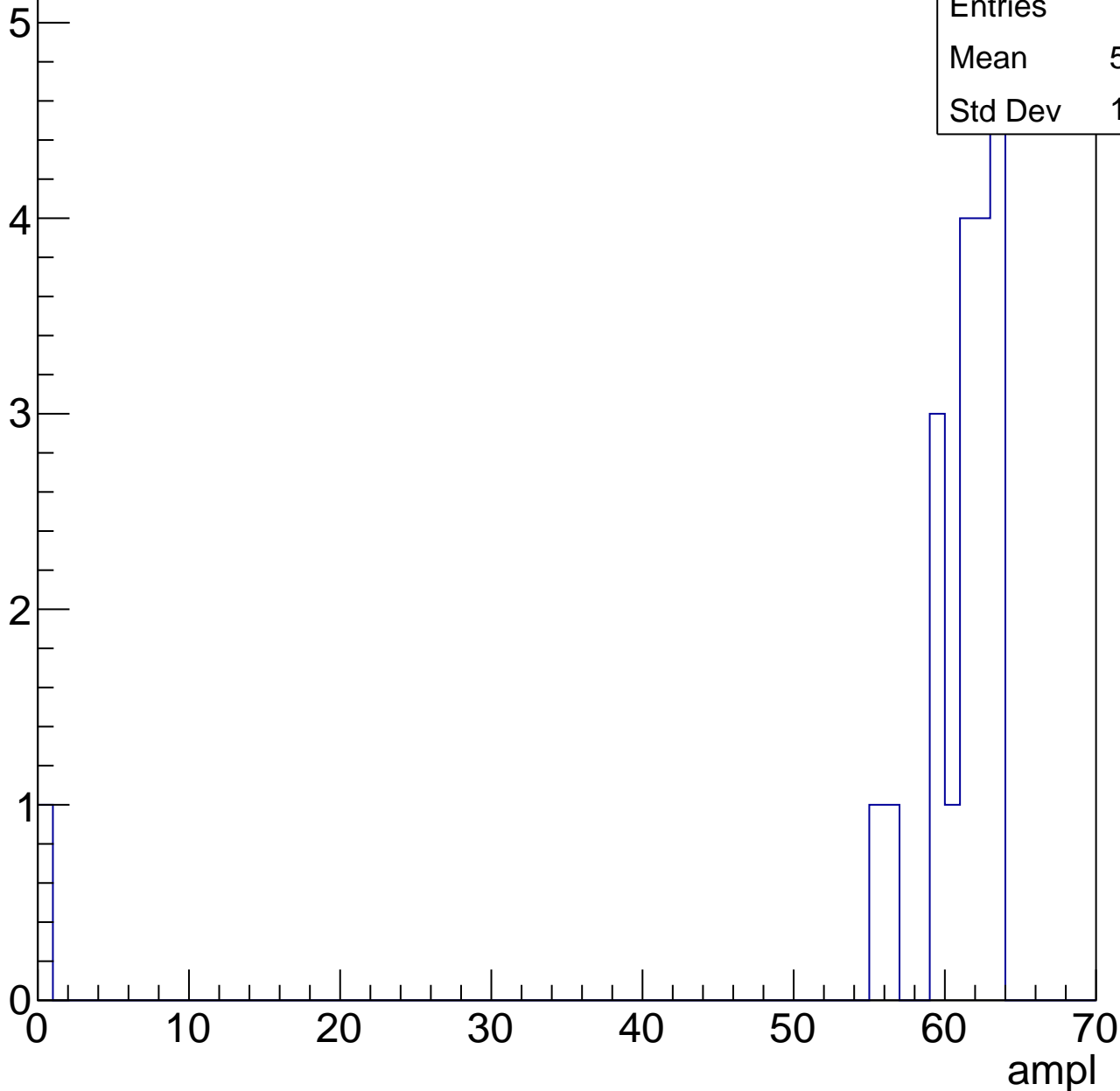


B1L102S, U2-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	57.75
Std Dev	13.43



B1L102S, U2-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U2-ch53, adc0

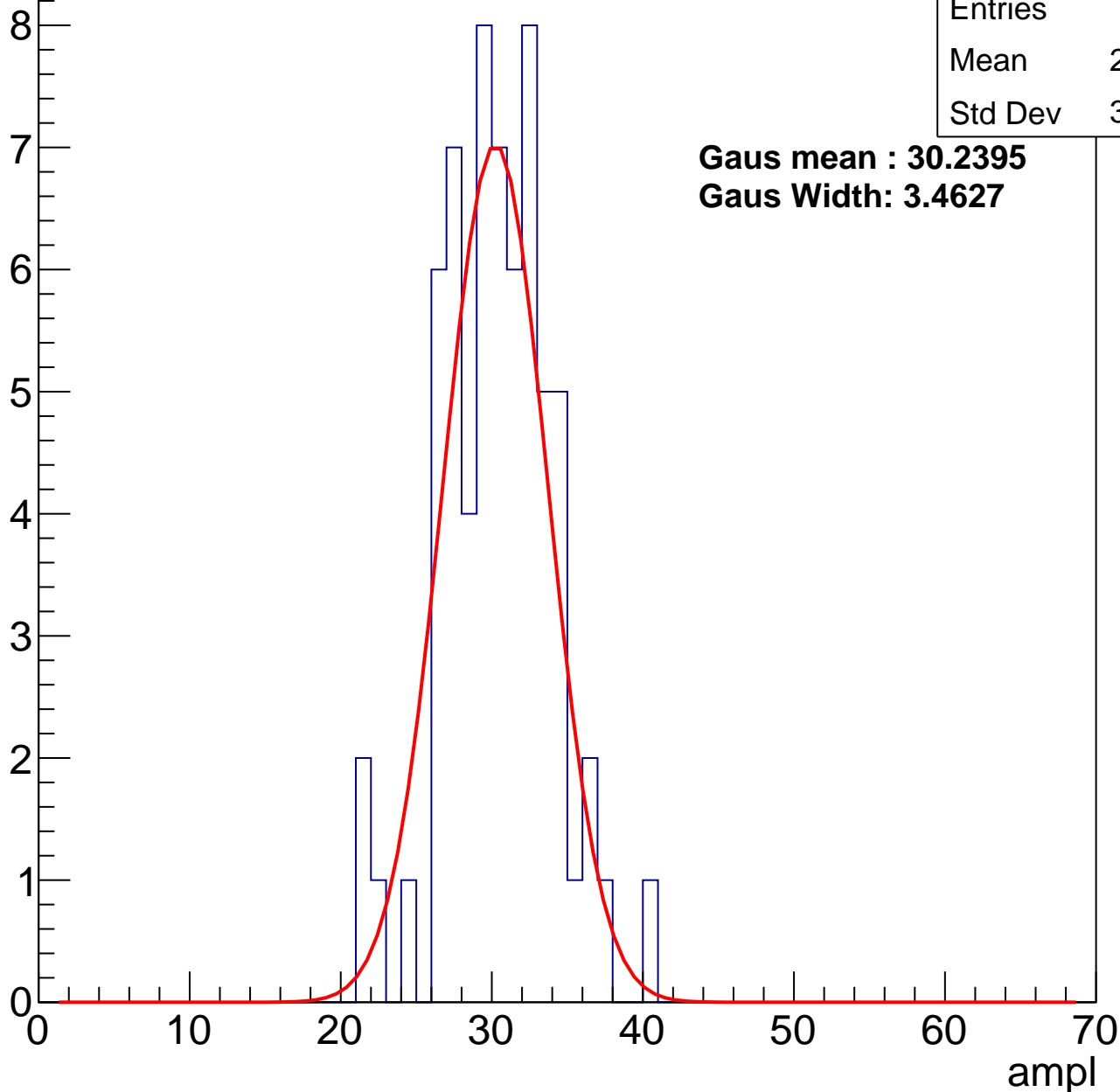
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.97
Std Dev	3.625

Gaus mean : 30.2395

Gaus Width: 3.4627



B1L102S, U2-ch53, adc1

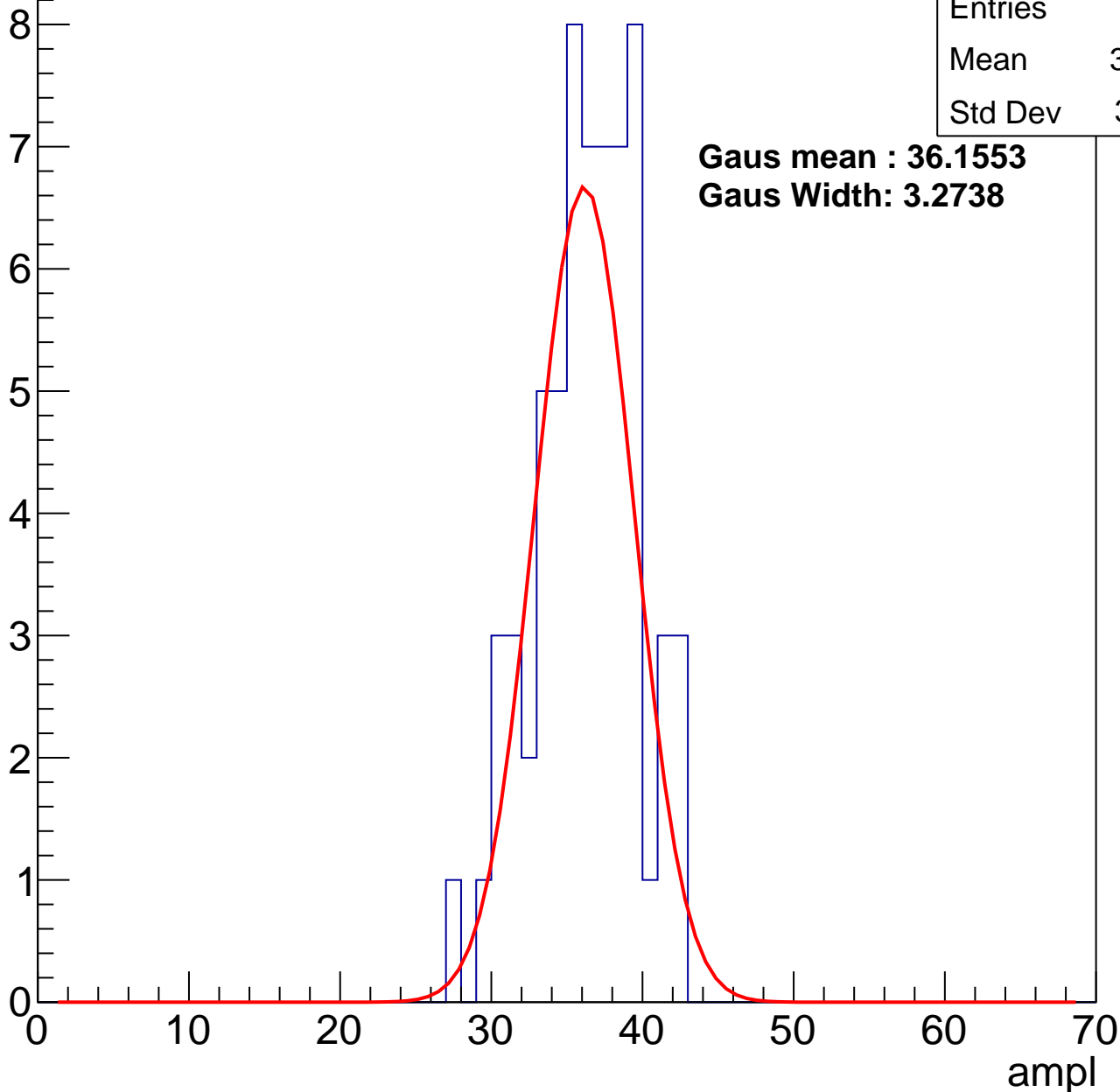
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.88
Std Dev	3.361

Gaus mean : 36.1553

Gaus Width: 3.2738



B1L102S, U2-ch53, adc2

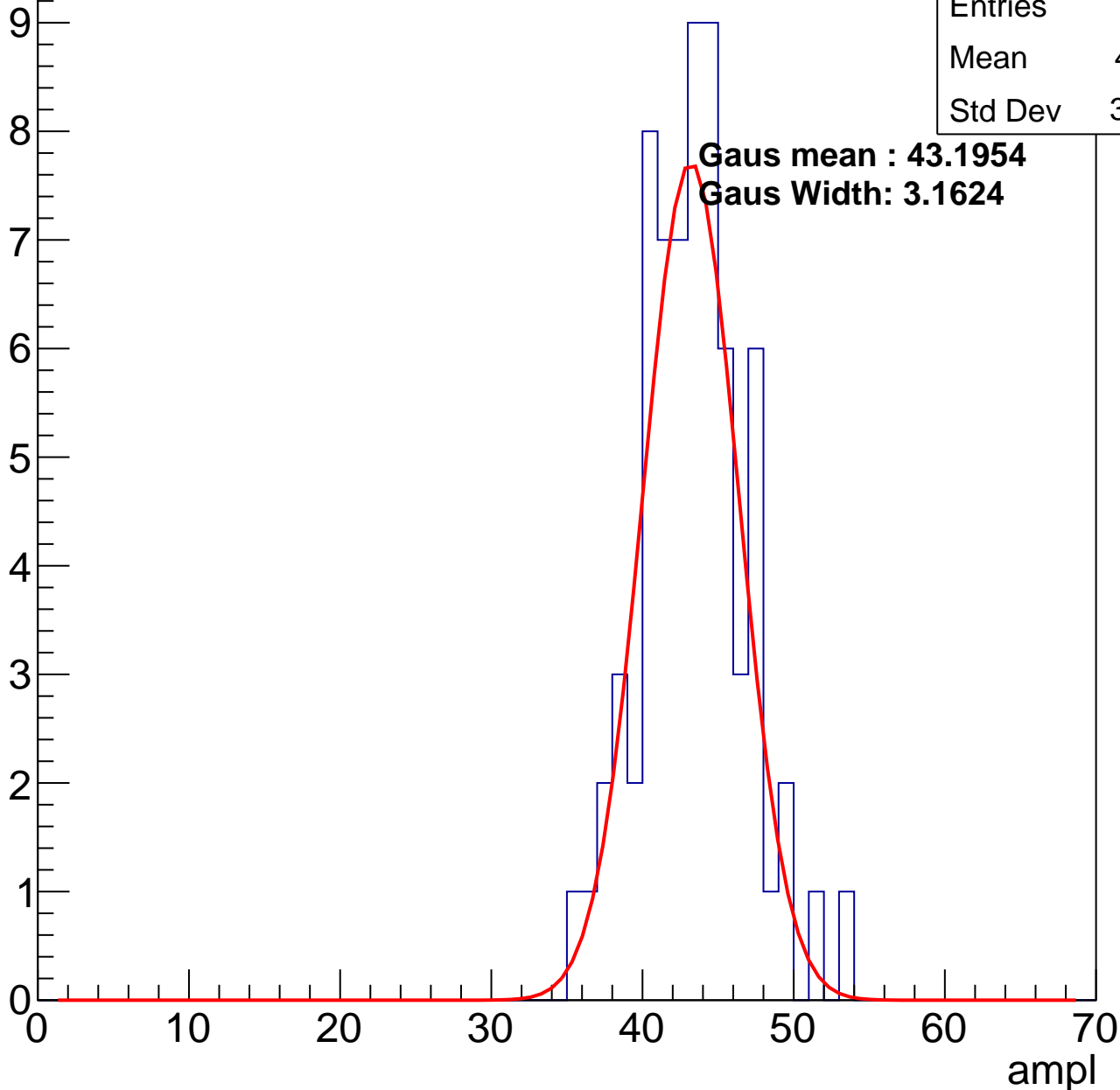
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.91
Std Dev	3.438

Gaus mean : 43.1954

Gaus Width: 3.1624

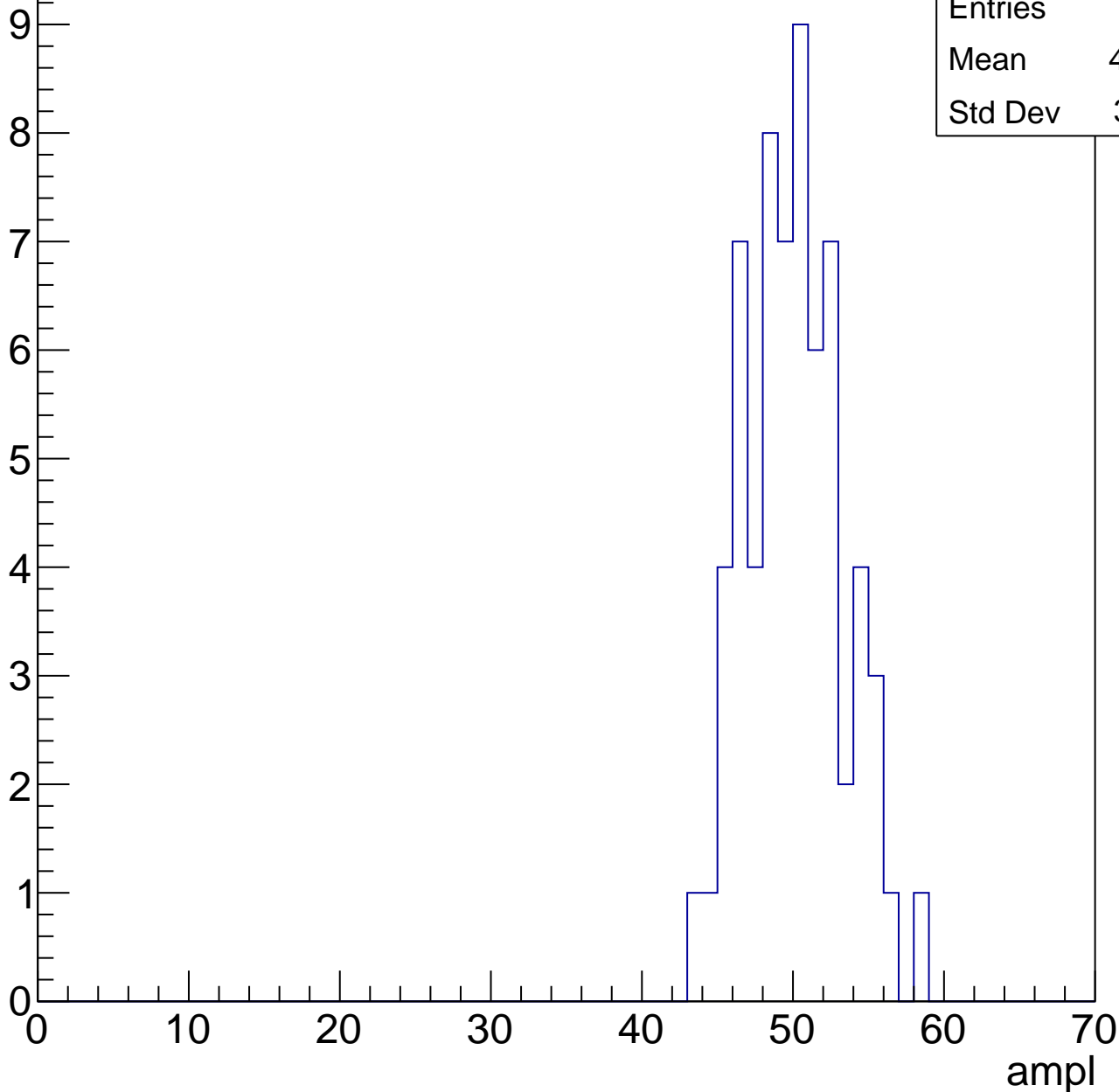


B1L102S, U2-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

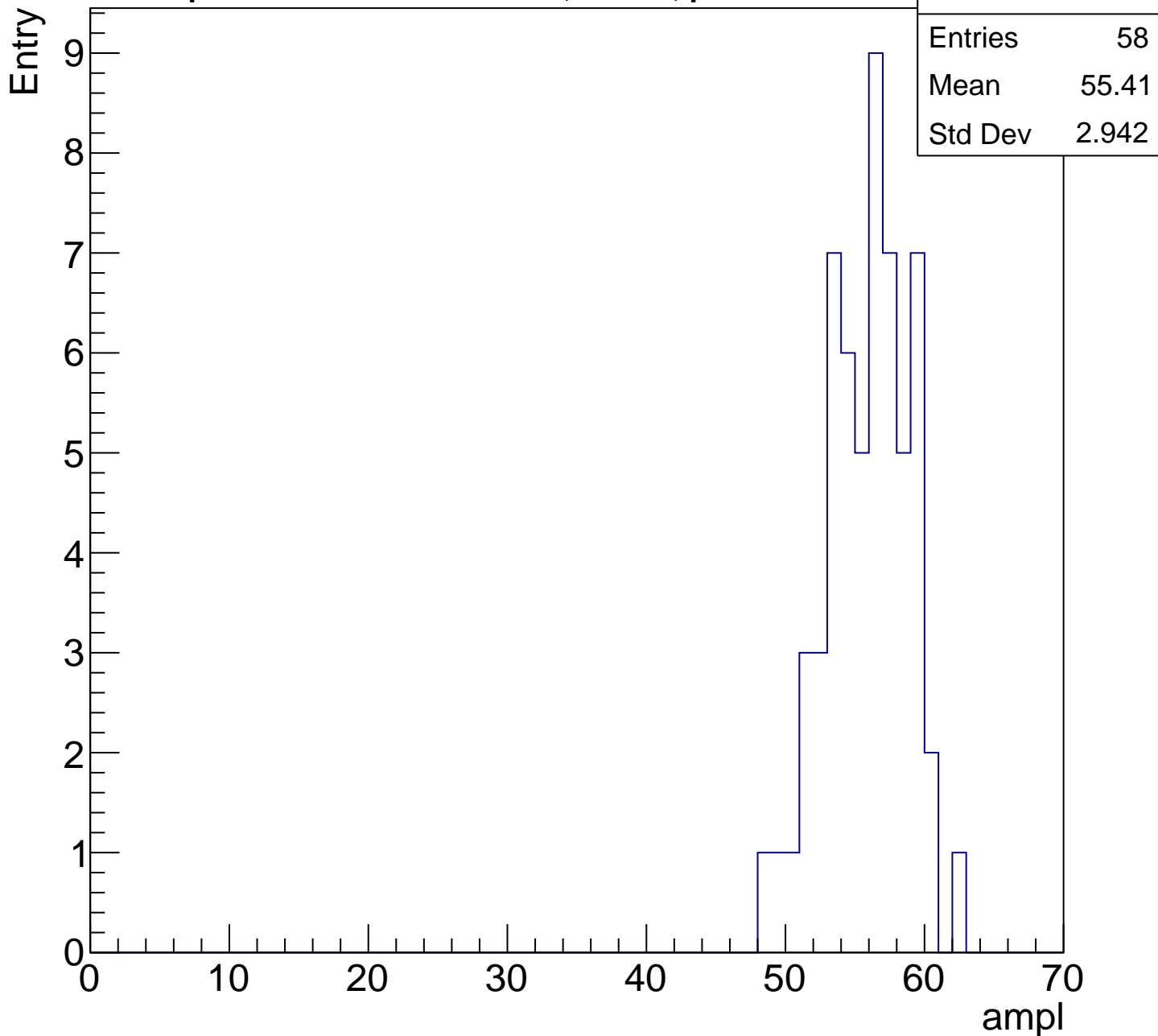
Entry

Entries	65
Mean	49.62
Std Dev	3.171



B1L102S, U2-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

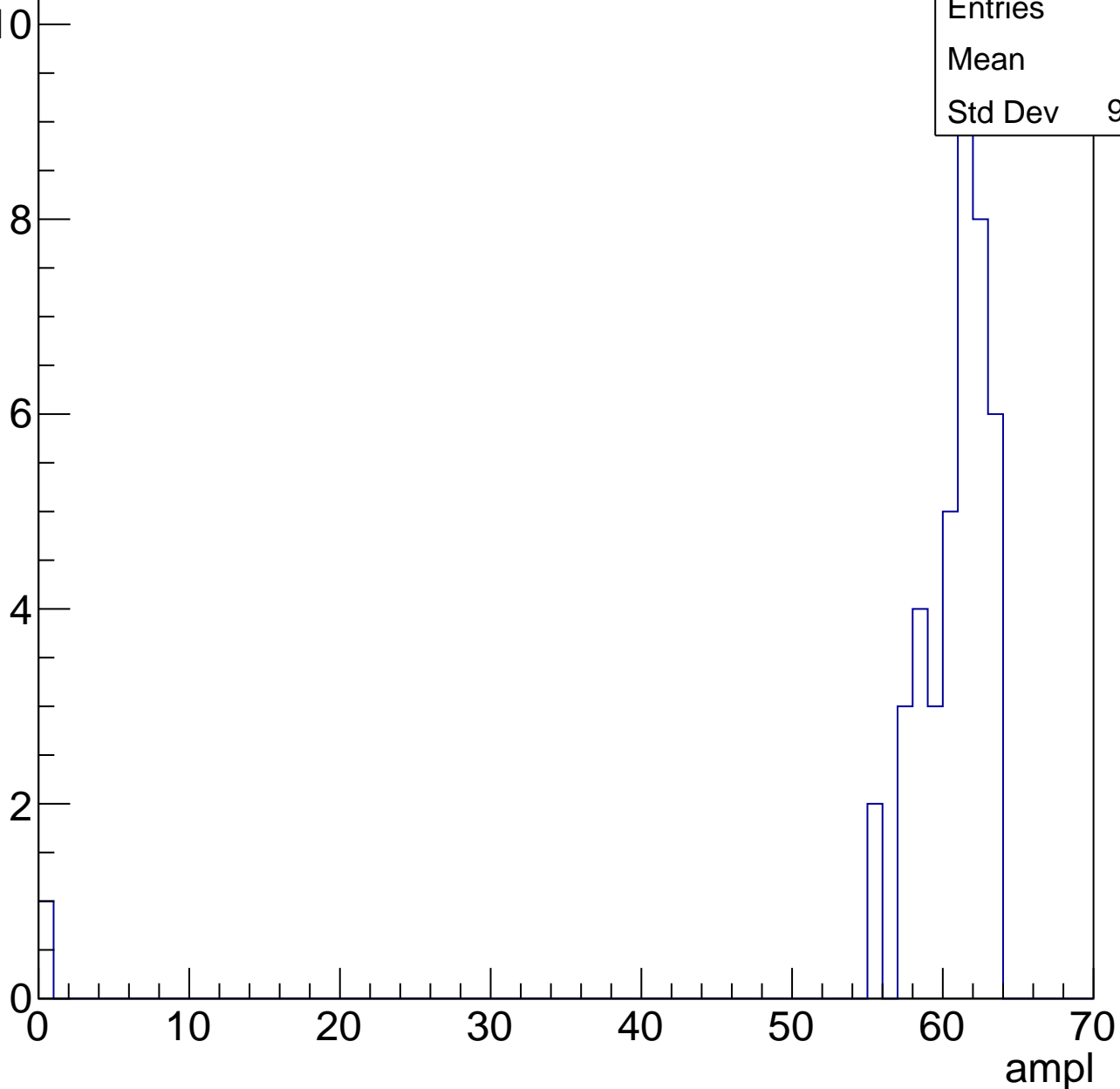


B1L102S, U2-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

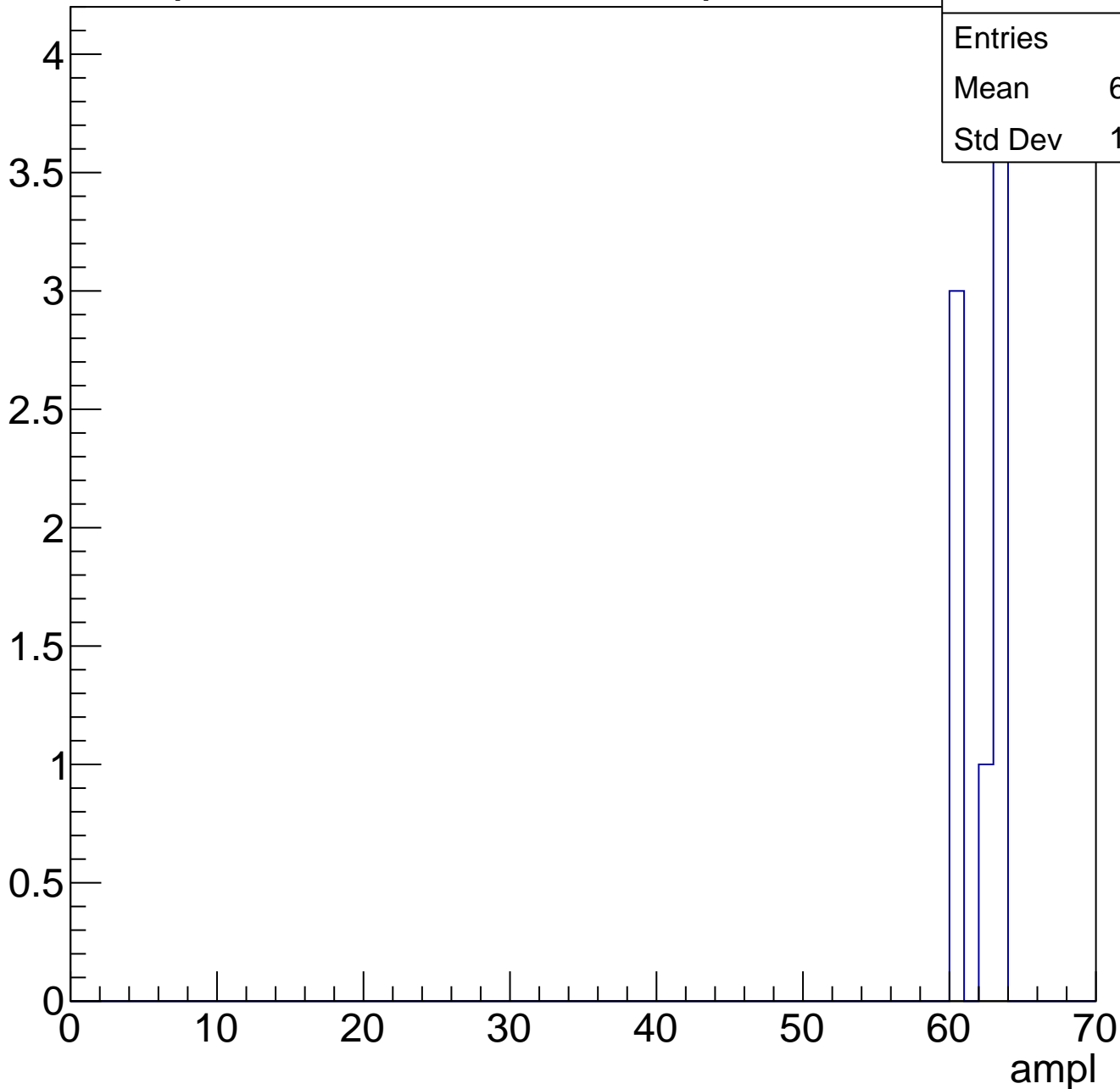
Entries	42
Mean	58.9
Std Dev	9.439



B1L102S, U2-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch54, adc0

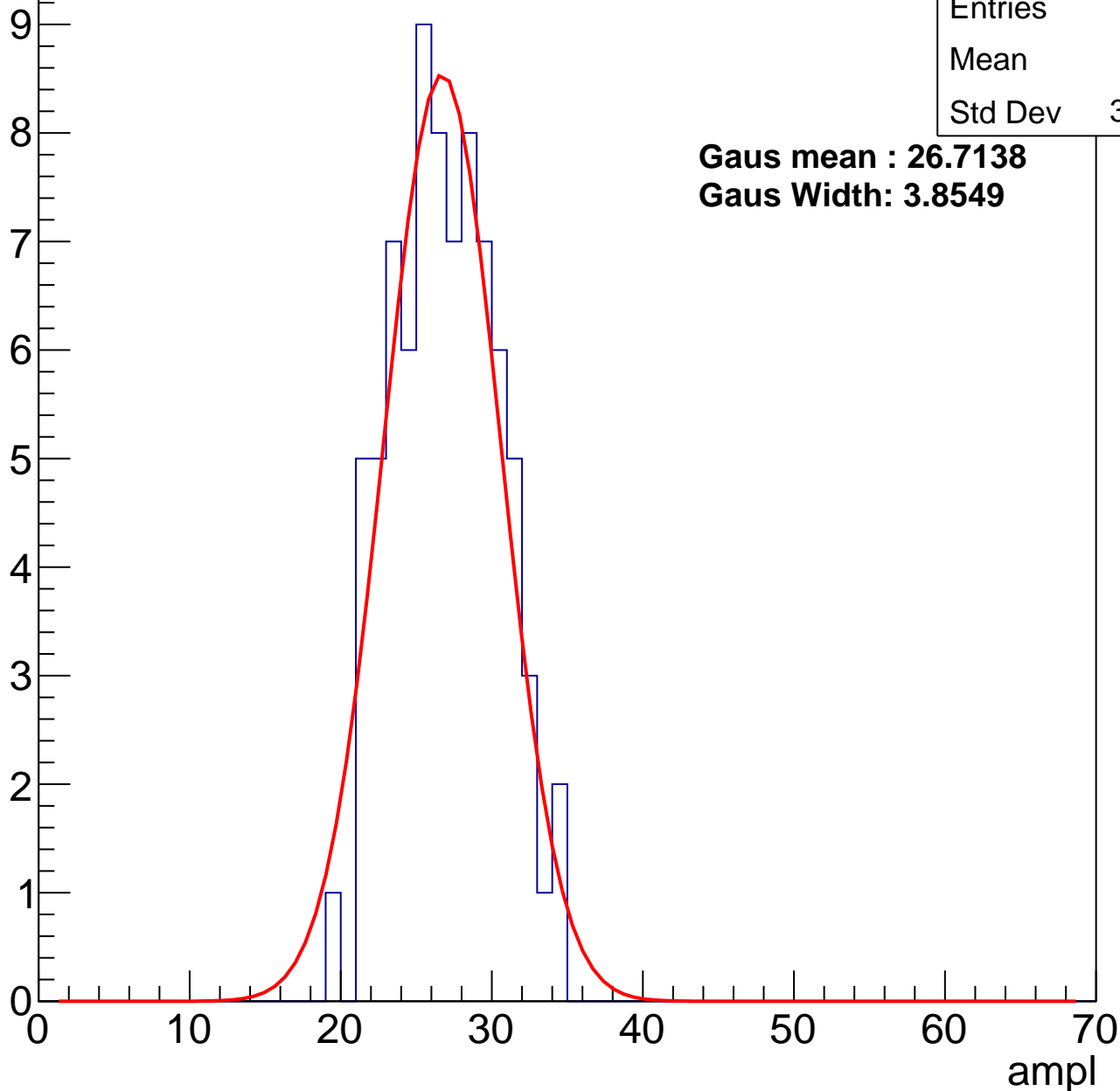
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	26.5
Std Dev	3.424

Gaus mean : 26.7138

Gaus Width: 3.8549



B1L102S, U2-ch54, adc1

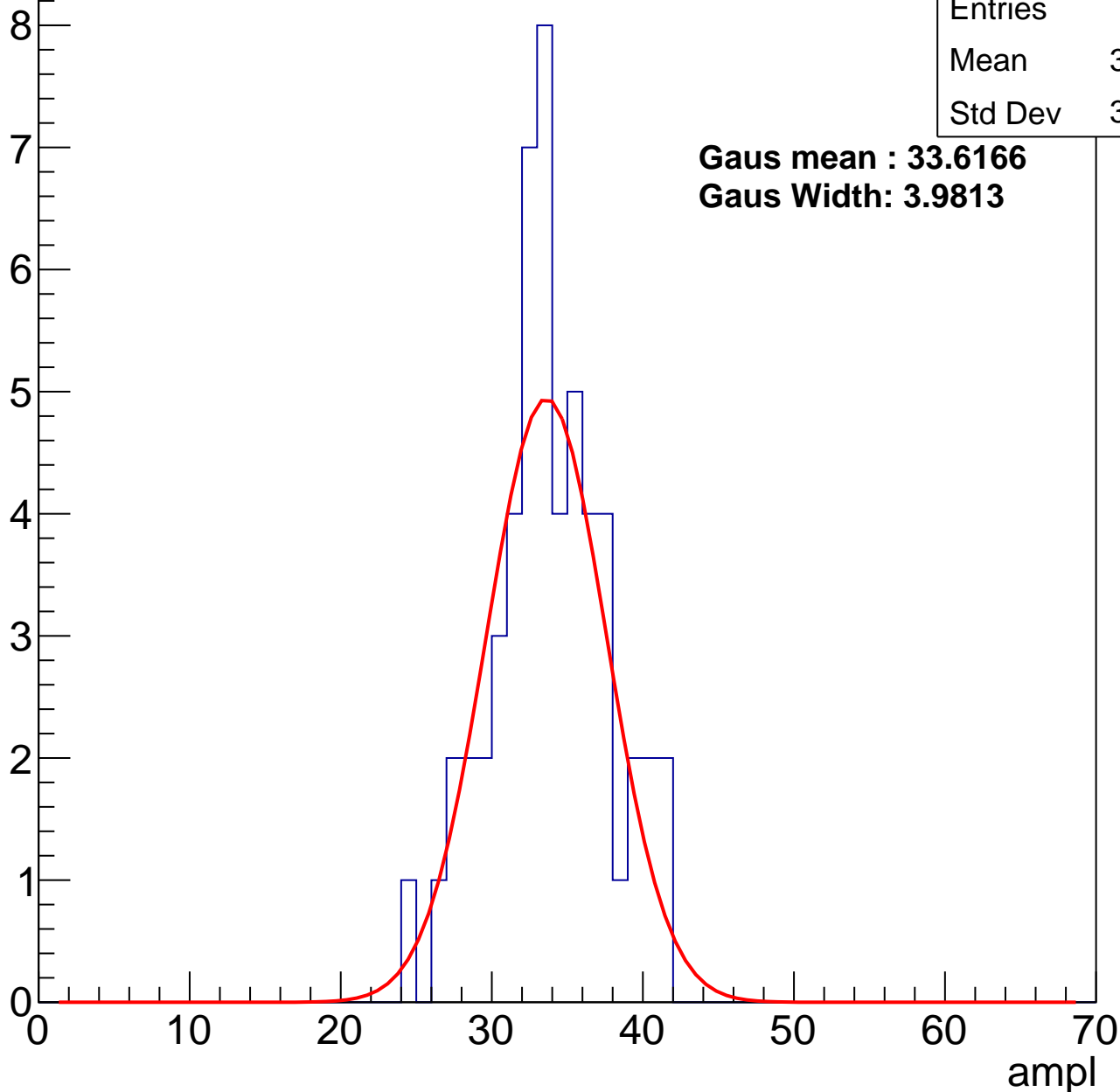
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	33.35
Std Dev	3.787

Gaus mean : 33.6166

Gaus Width: 3.9813



B1L102S, U2-ch54, adc2

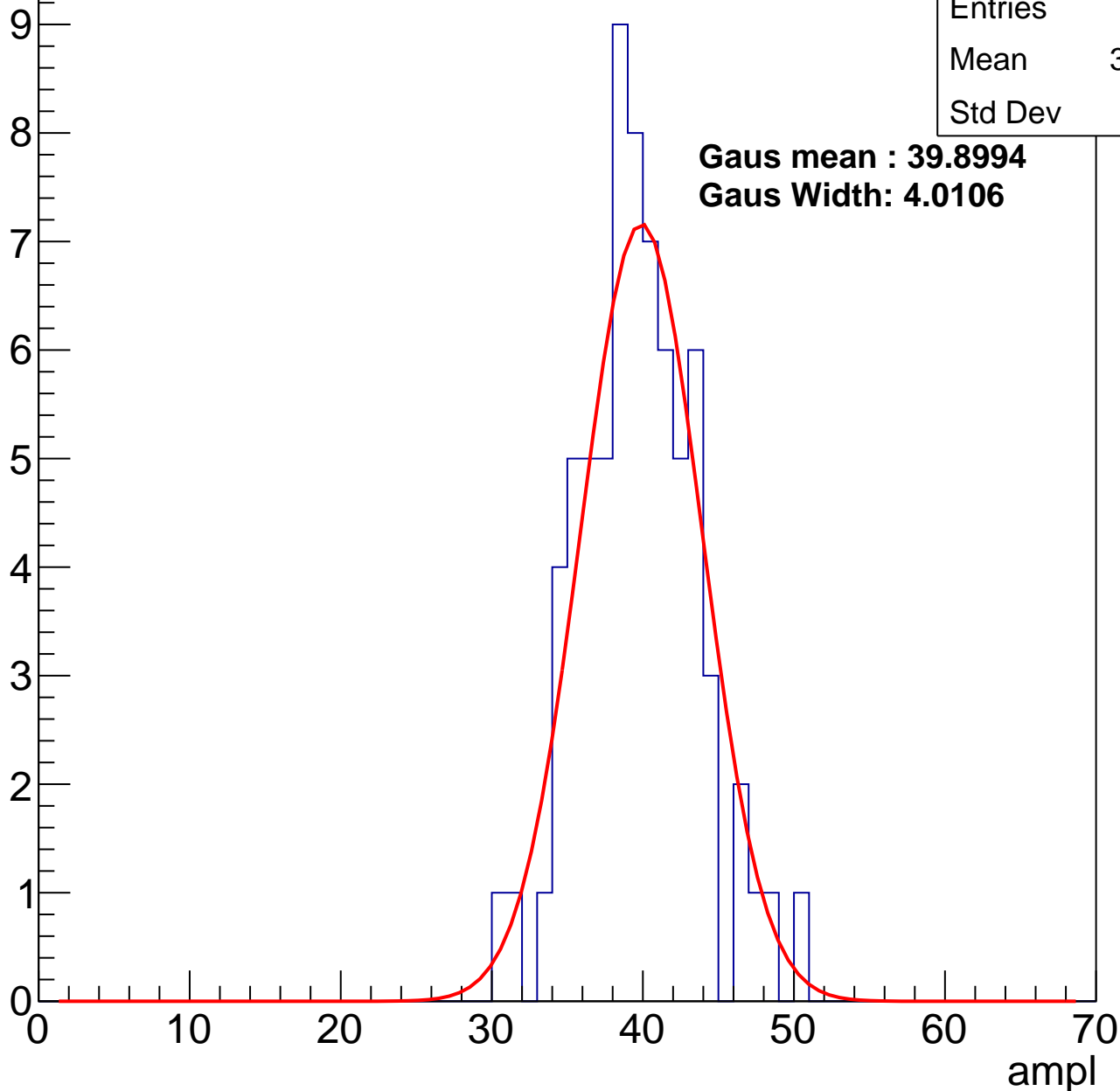
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	39.25
Std Dev	3.83

Gaus mean : 39.8994

Gaus Width: 4.0106

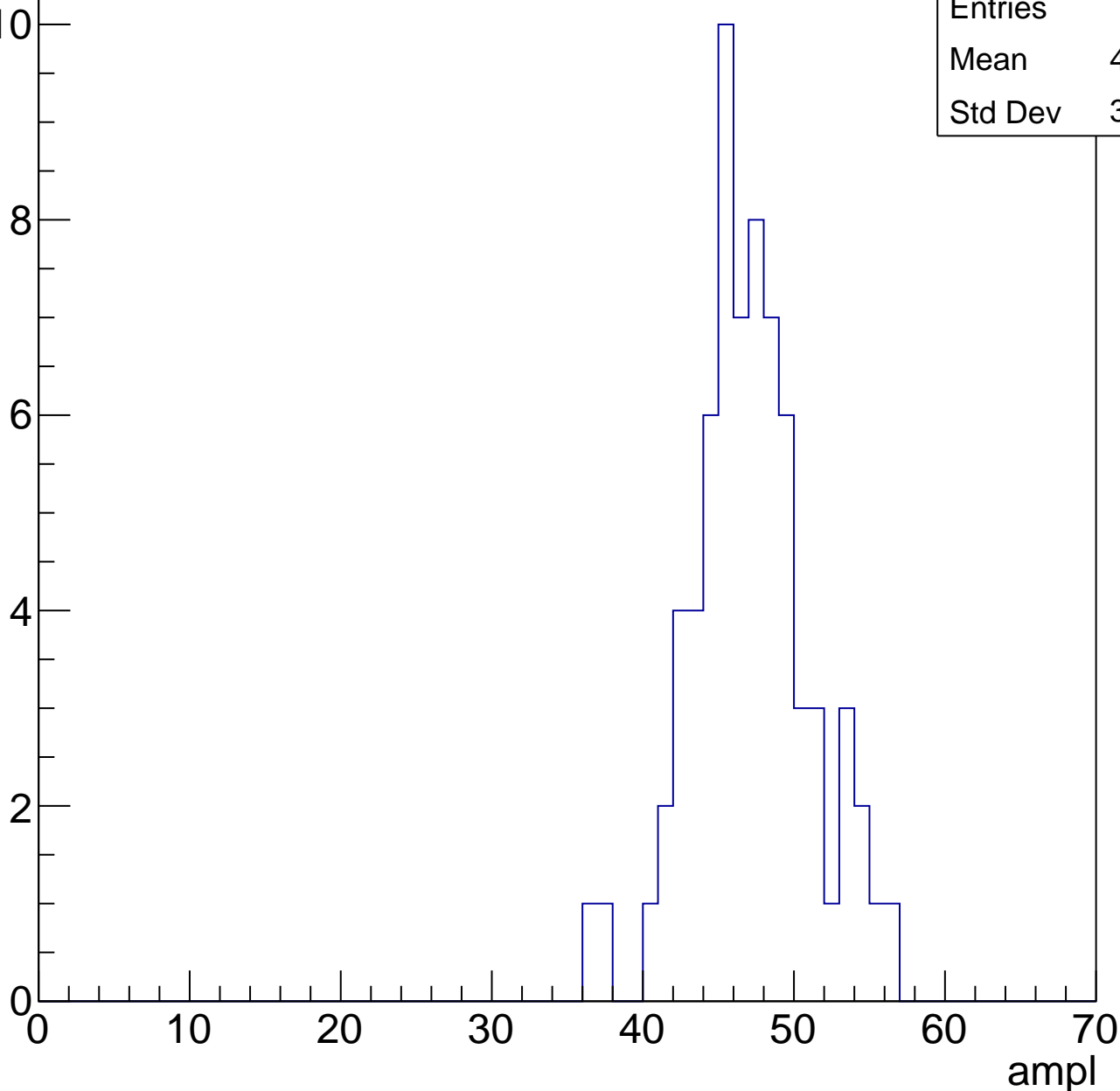


B1L102S, U2-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

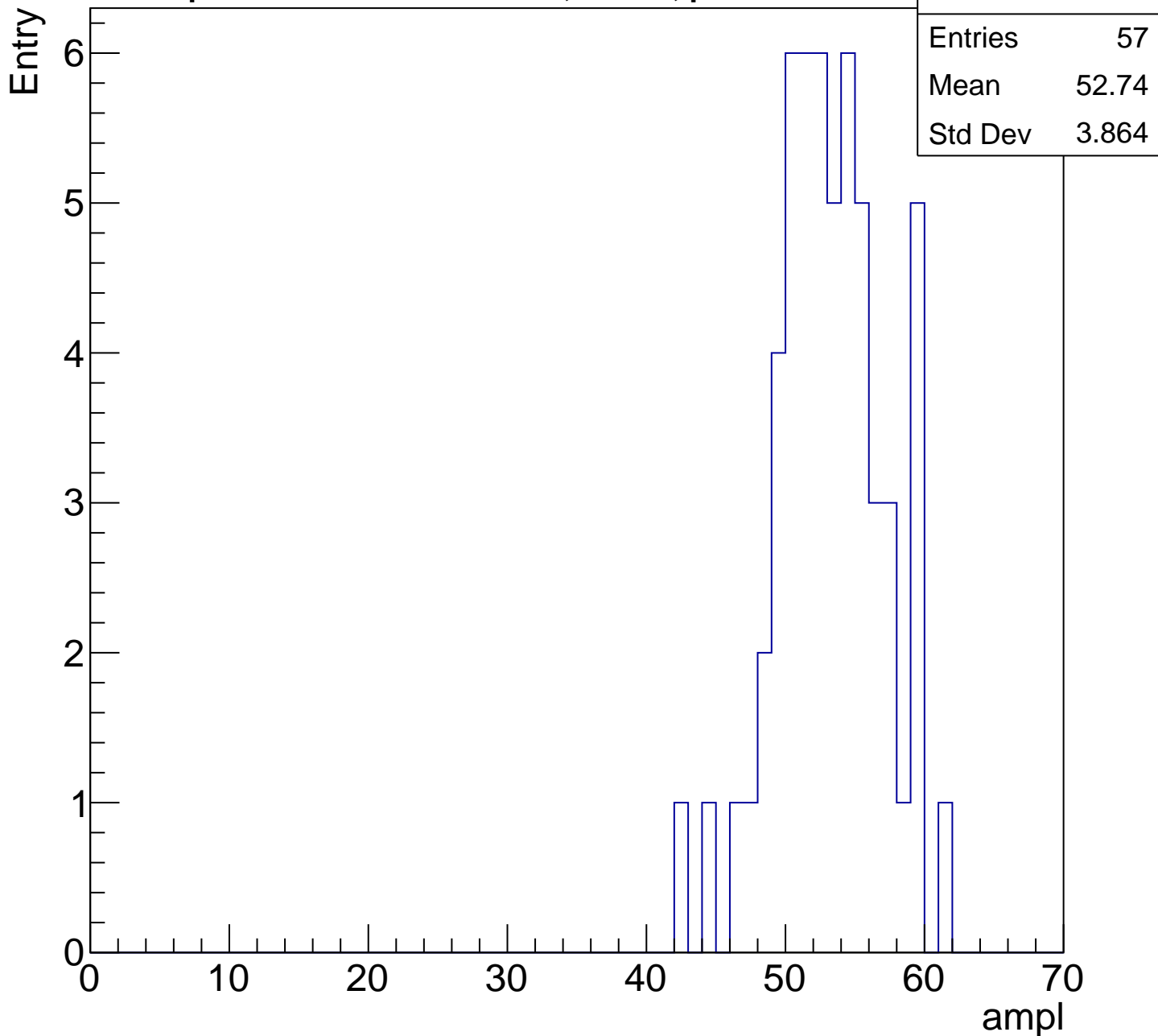
Entry

Entries	71
Mean	46.62
Std Dev	3.898



B1L102S, U2-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

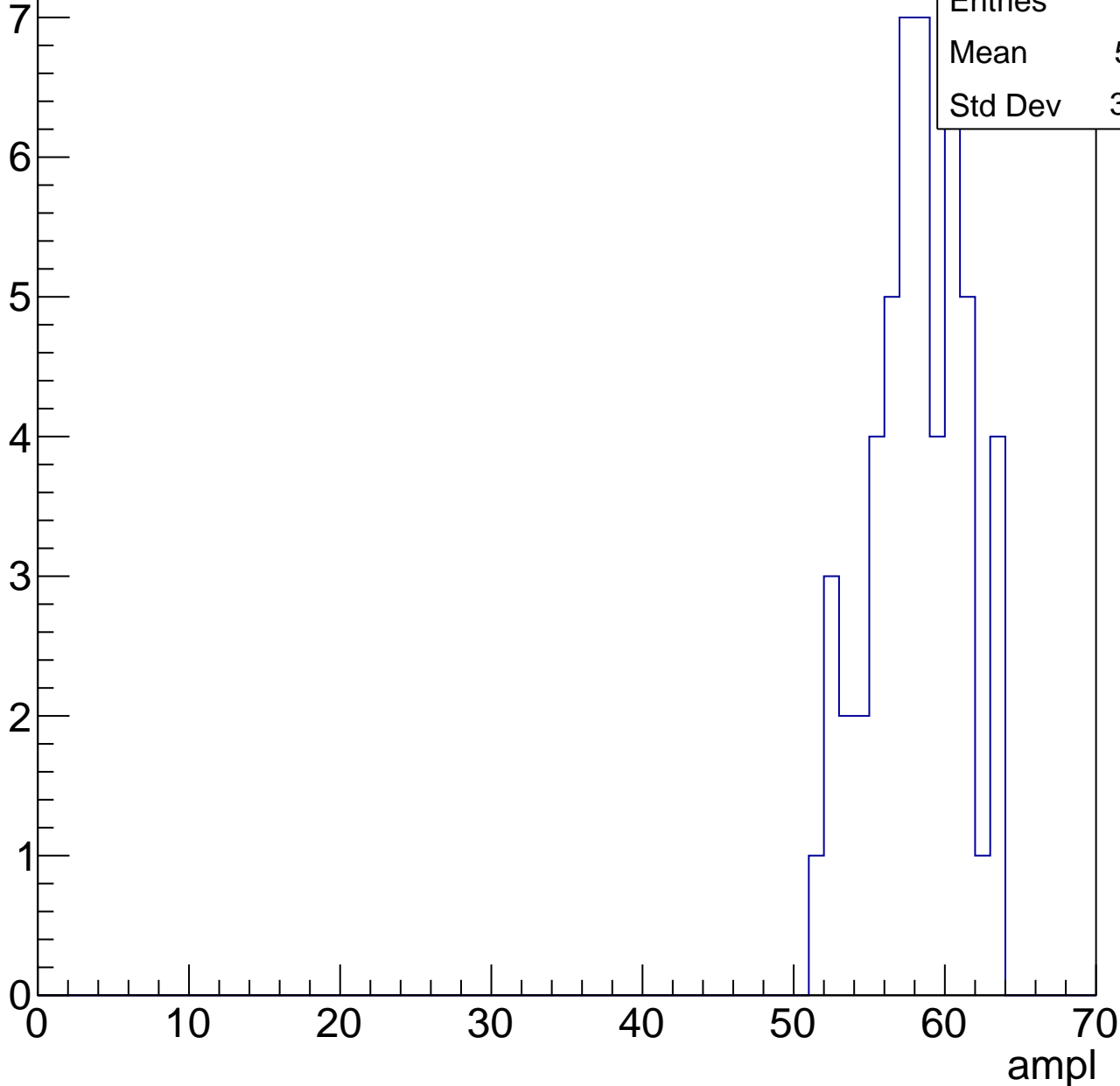


B1L102S, U2-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.71
Std Dev	3.072

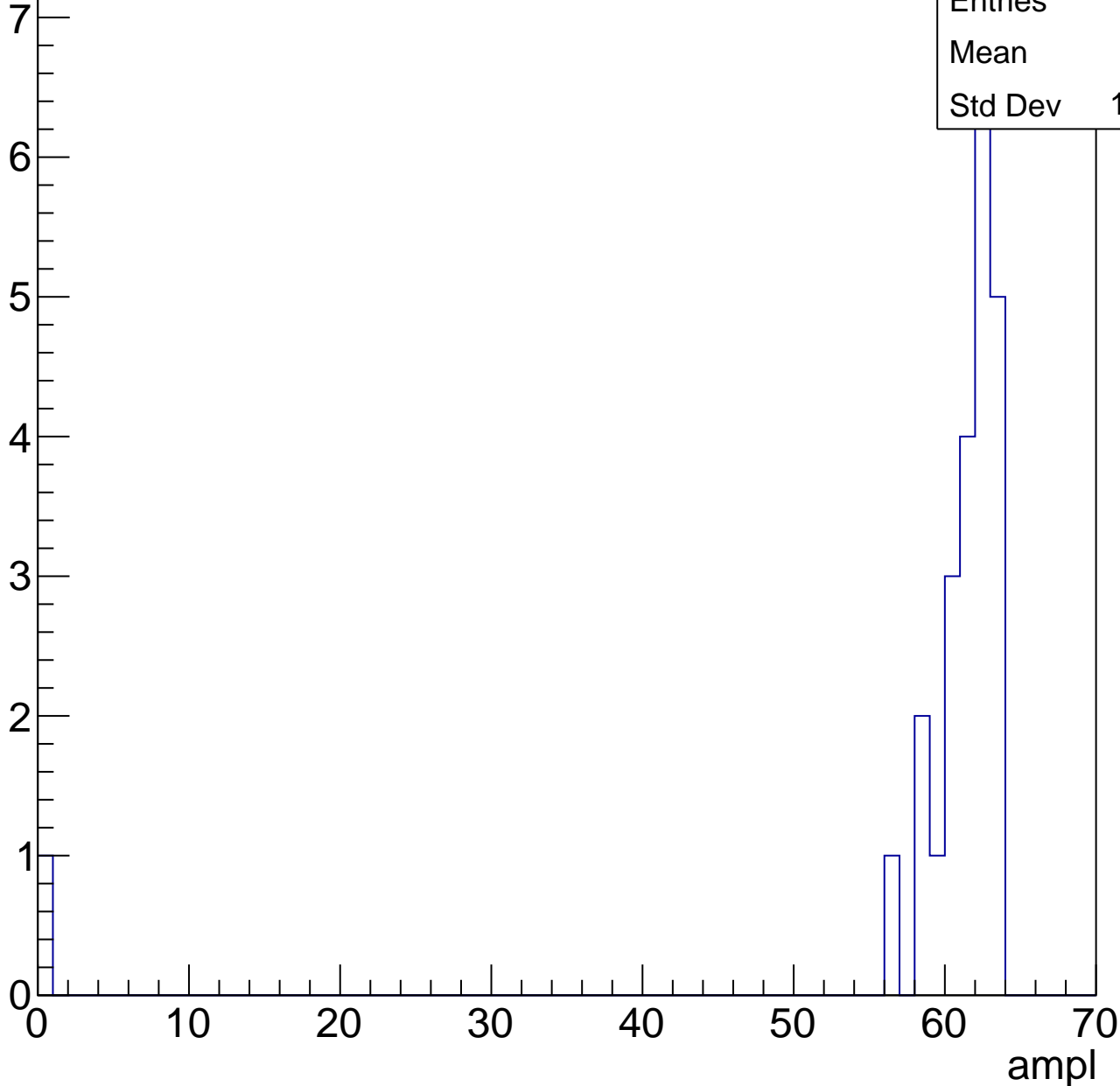


B1L102S, U2-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

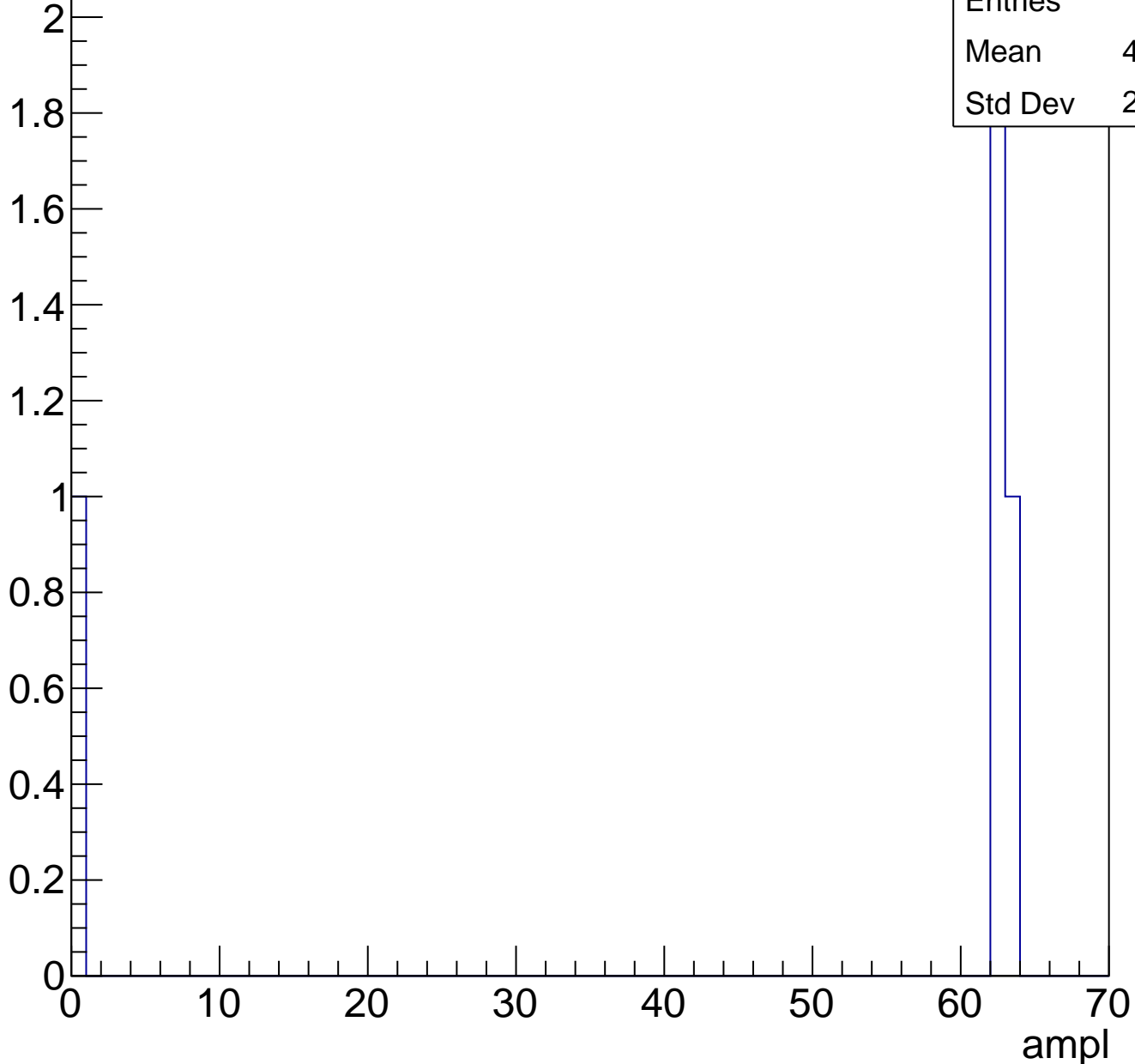
Entries	24
Mean	58.5
Std Dev	12.33



B1L102S, U2-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L102S, U2-ch55, adc0

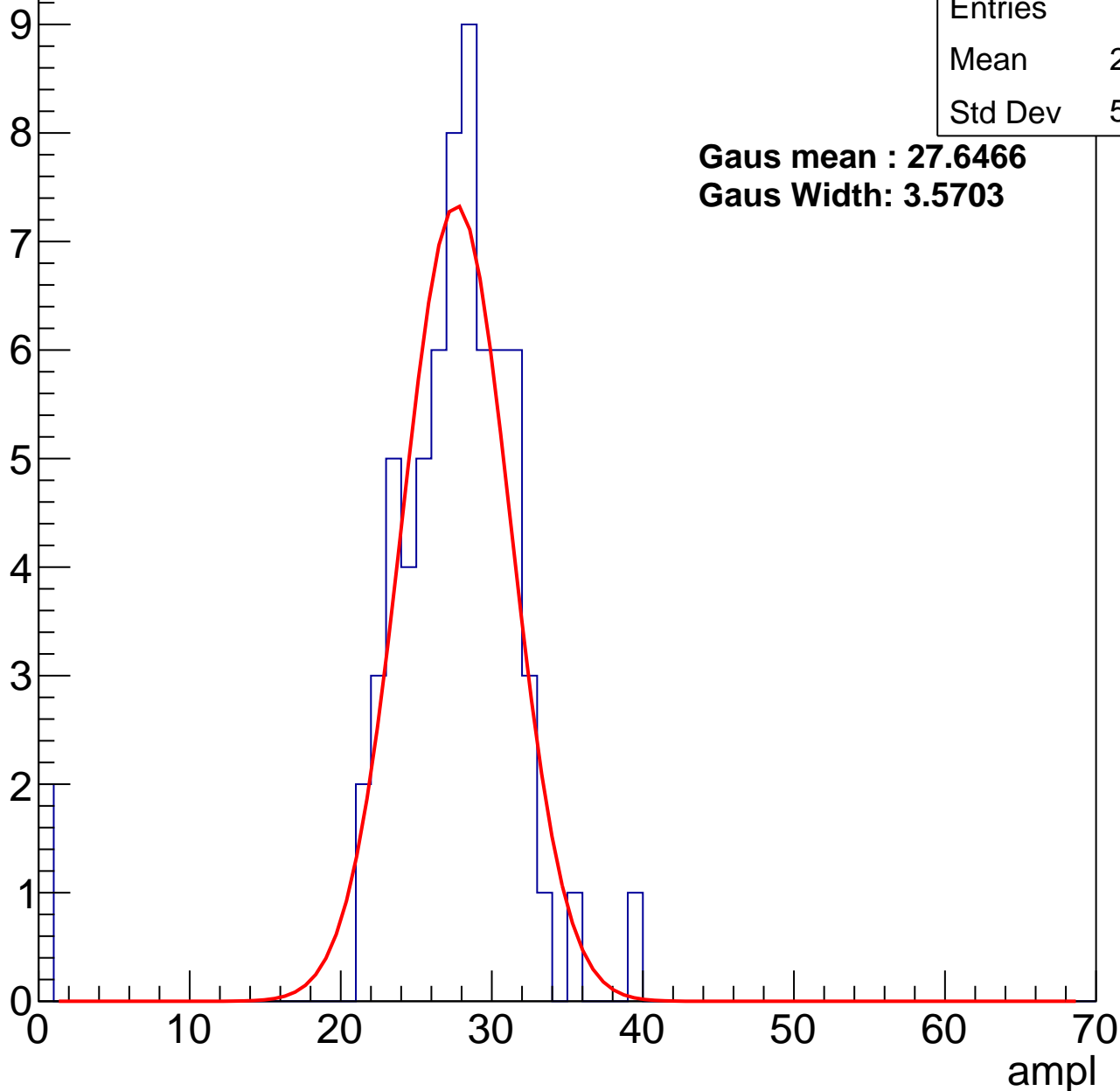
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	26.63
Std Dev	5.739

Gaus mean : 27.6466

Gaus Width: 3.5703



B1L102S, U2-ch55, adc1

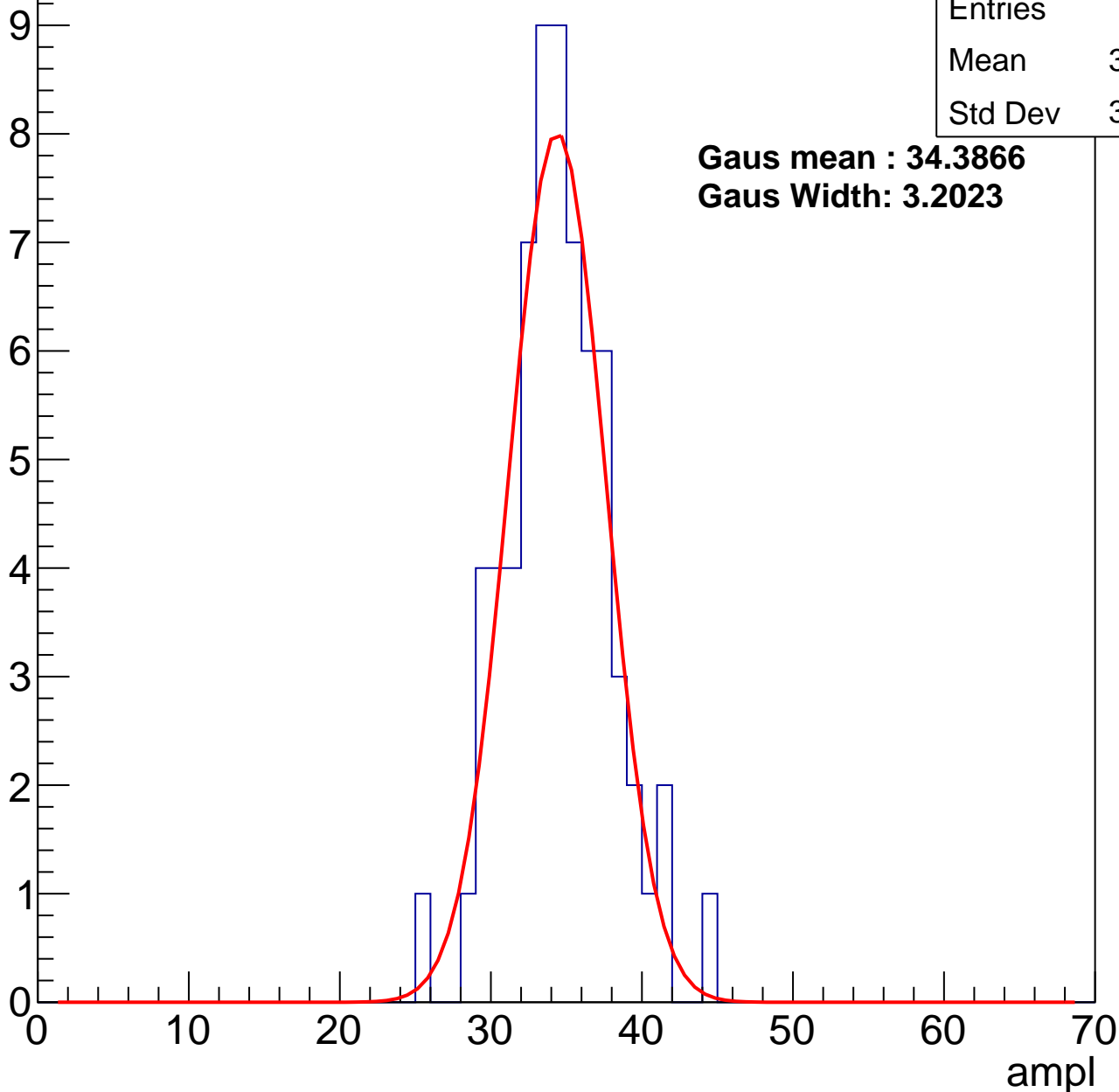
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.04
Std Dev	3.392

Gaus mean : 34.3866

Gaus Width: 3.2023



B1L102S, U2-ch55, adc2

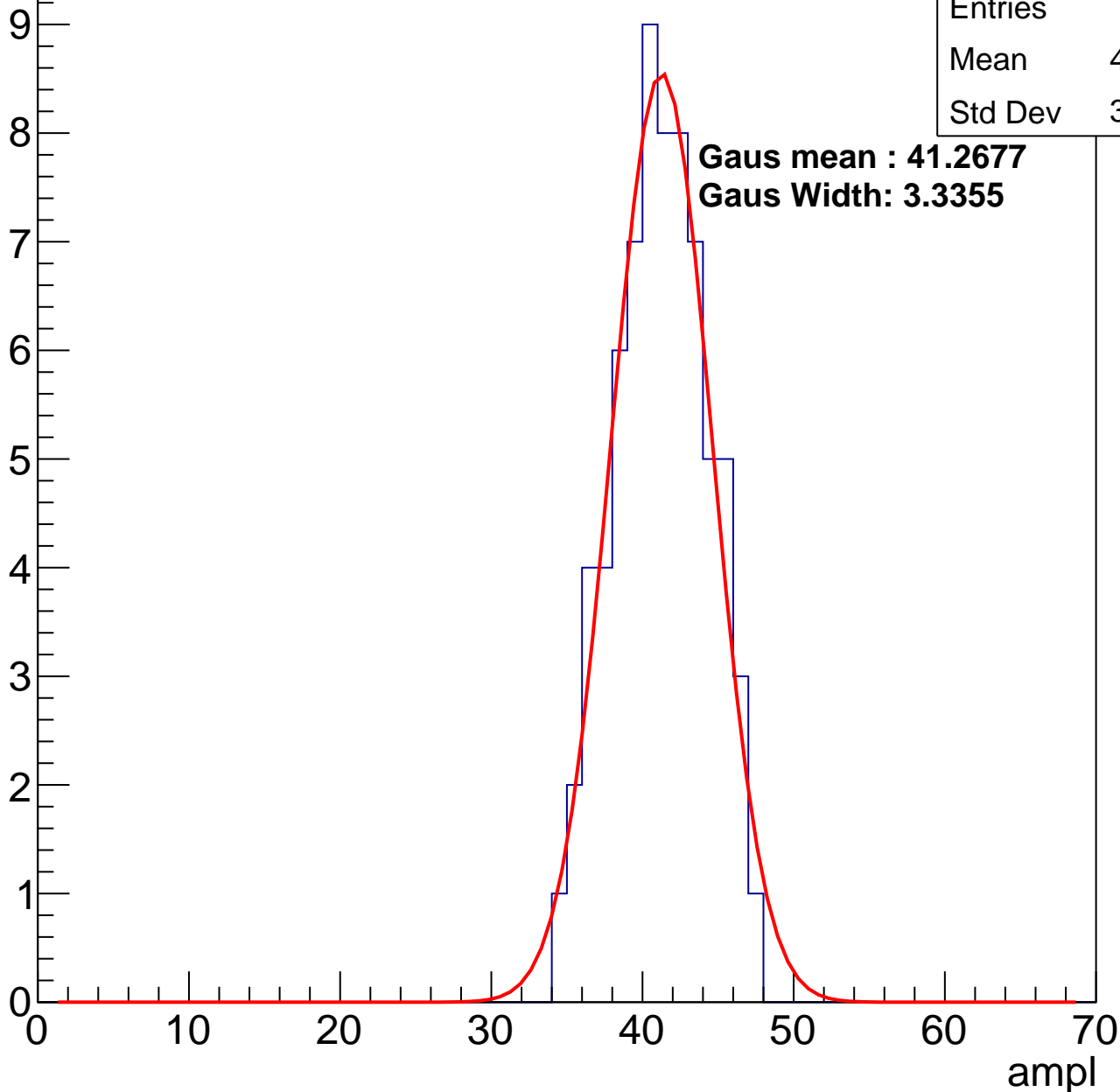
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.74
Std Dev	3.032

Gaus mean : 41.2677

Gaus Width: 3.3355

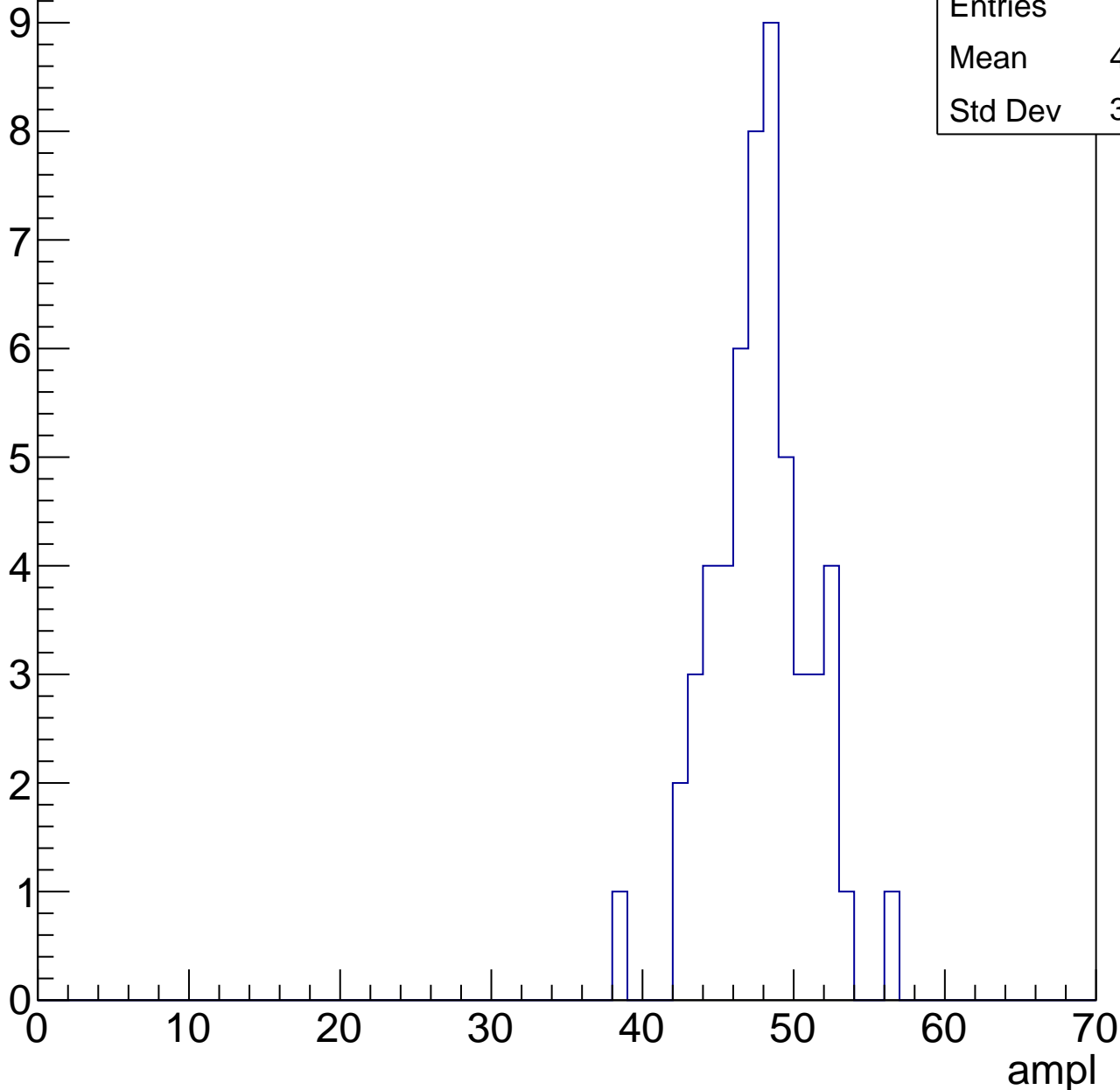


B1L102S, U2-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	47.33
Std Dev	3.197

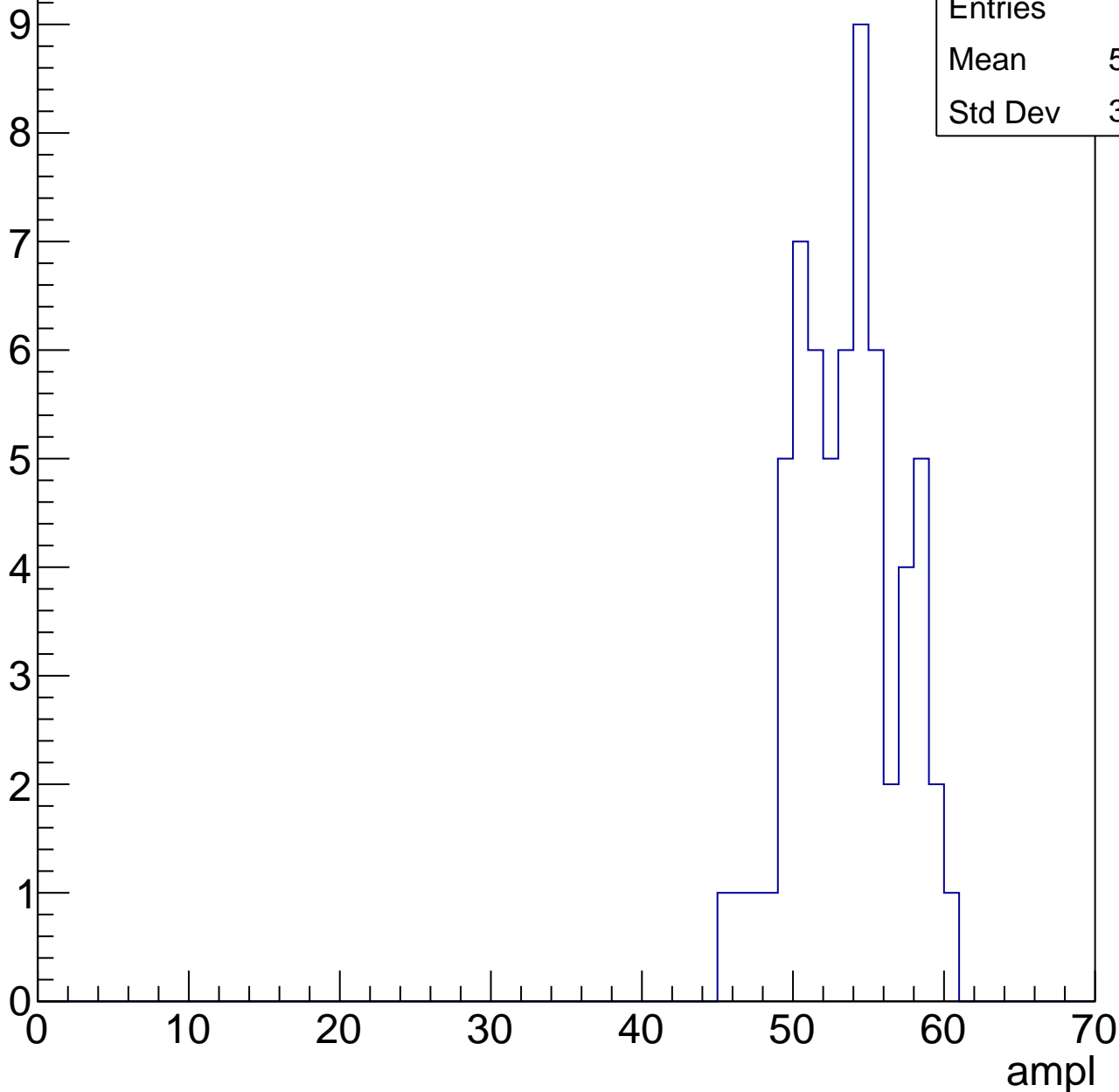


B1L102S, U2-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.05
Std Dev	3.377

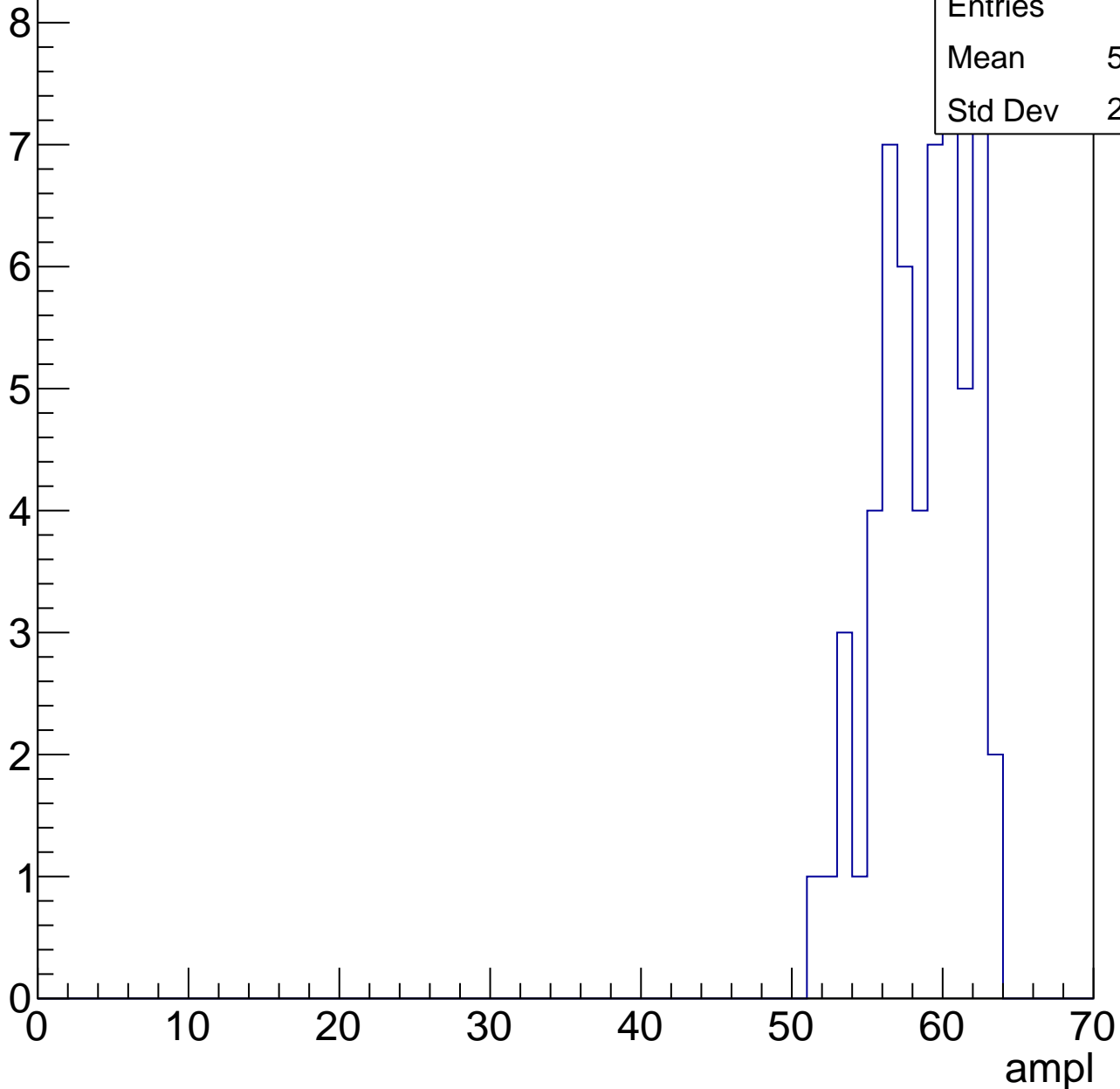


B1L102S, U2-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.28
Std Dev	2.972

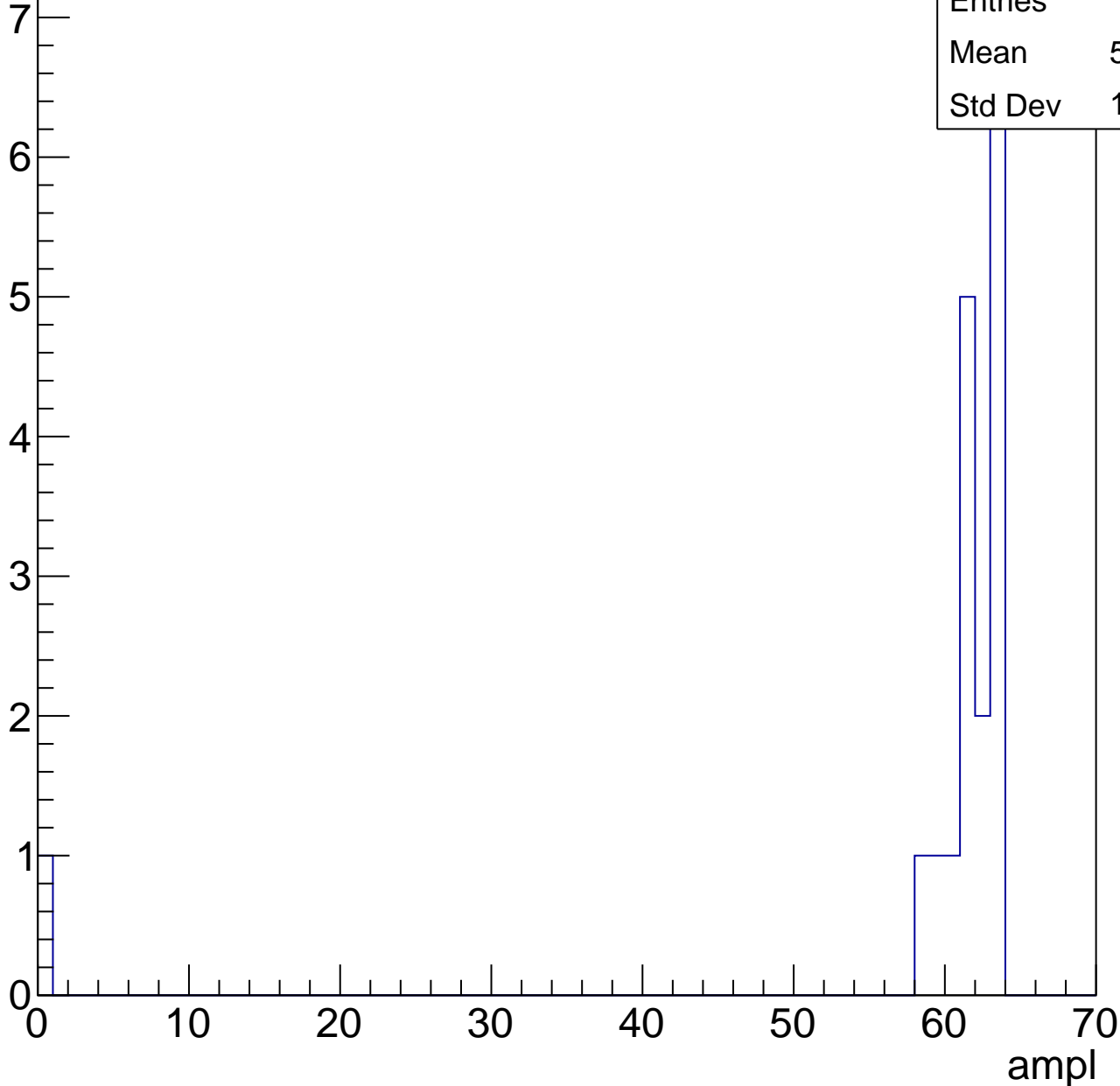


B1L102S, U2-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58.17
Std Dev	14.18



B1L102S, U2-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	63
Std Dev	0

B1L102S, U2-ch56, adc0

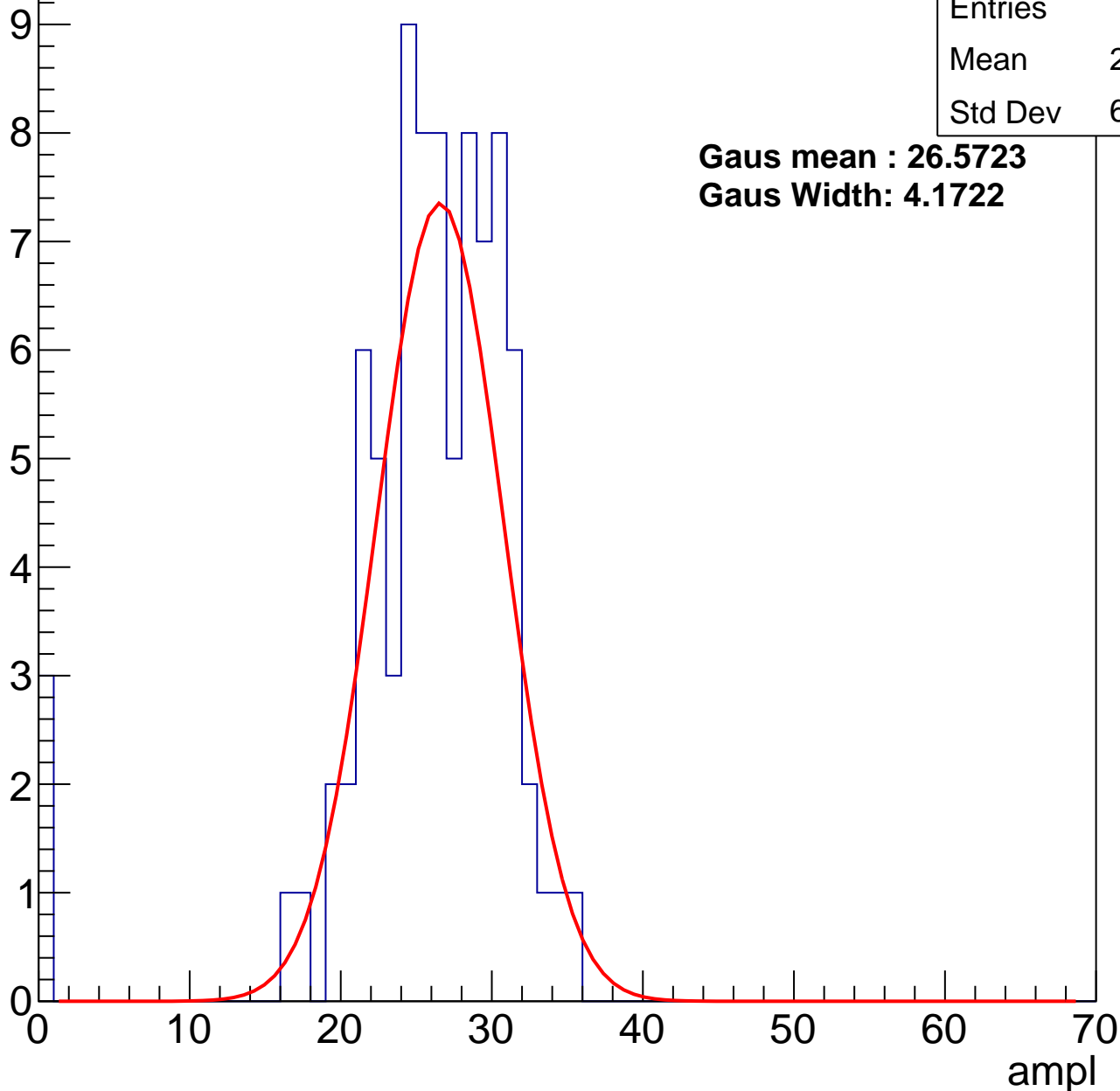
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	25.22
Std Dev	6.132

Gaus mean : 26.5723

Gaus Width: 4.1722



B1L102S, U2-ch56, adc1

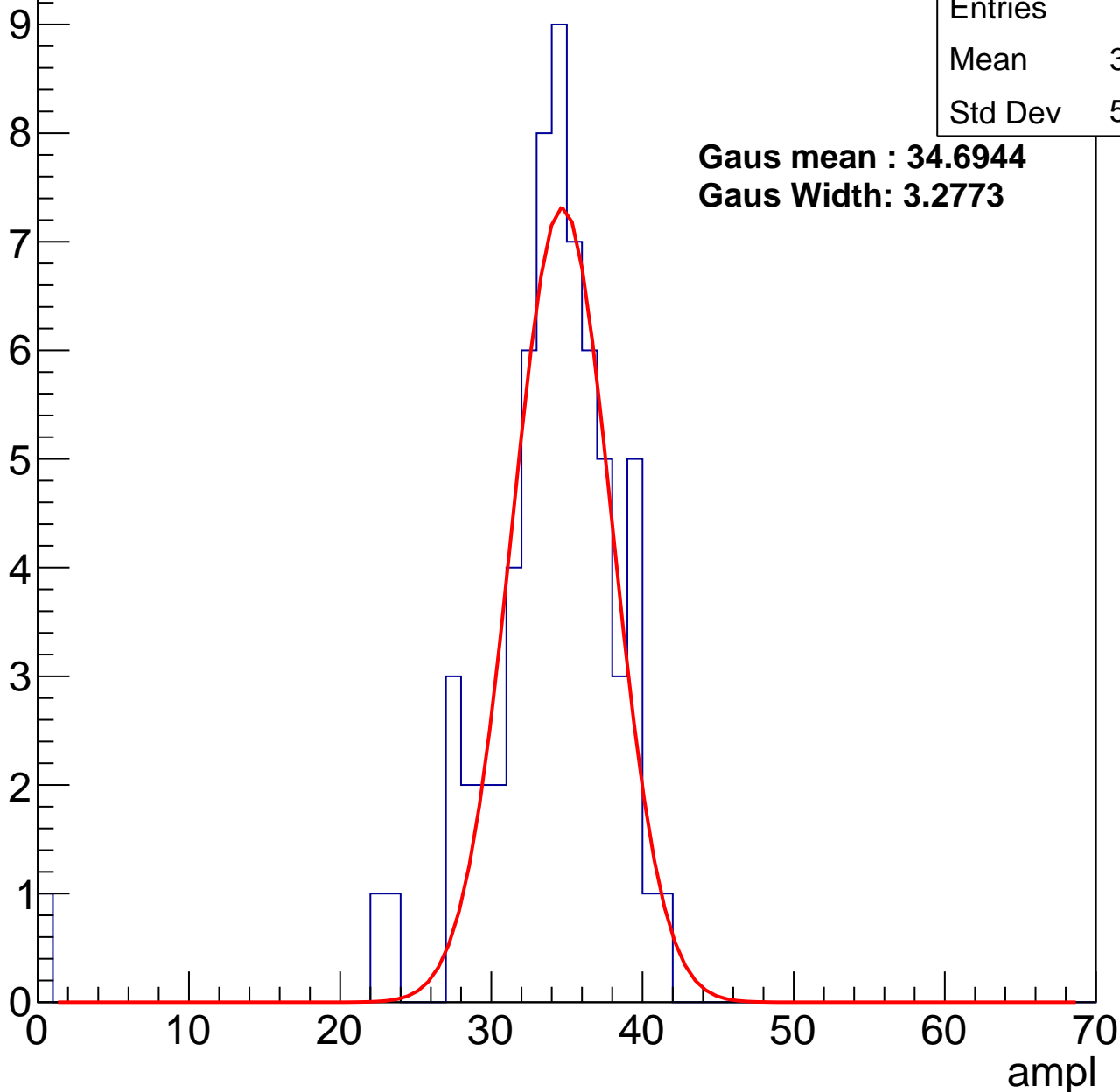
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.16
Std Dev	5.568

Gaus mean : 34.6944

Gaus Width: 3.2773



B1L102S, U2-ch56, adc2

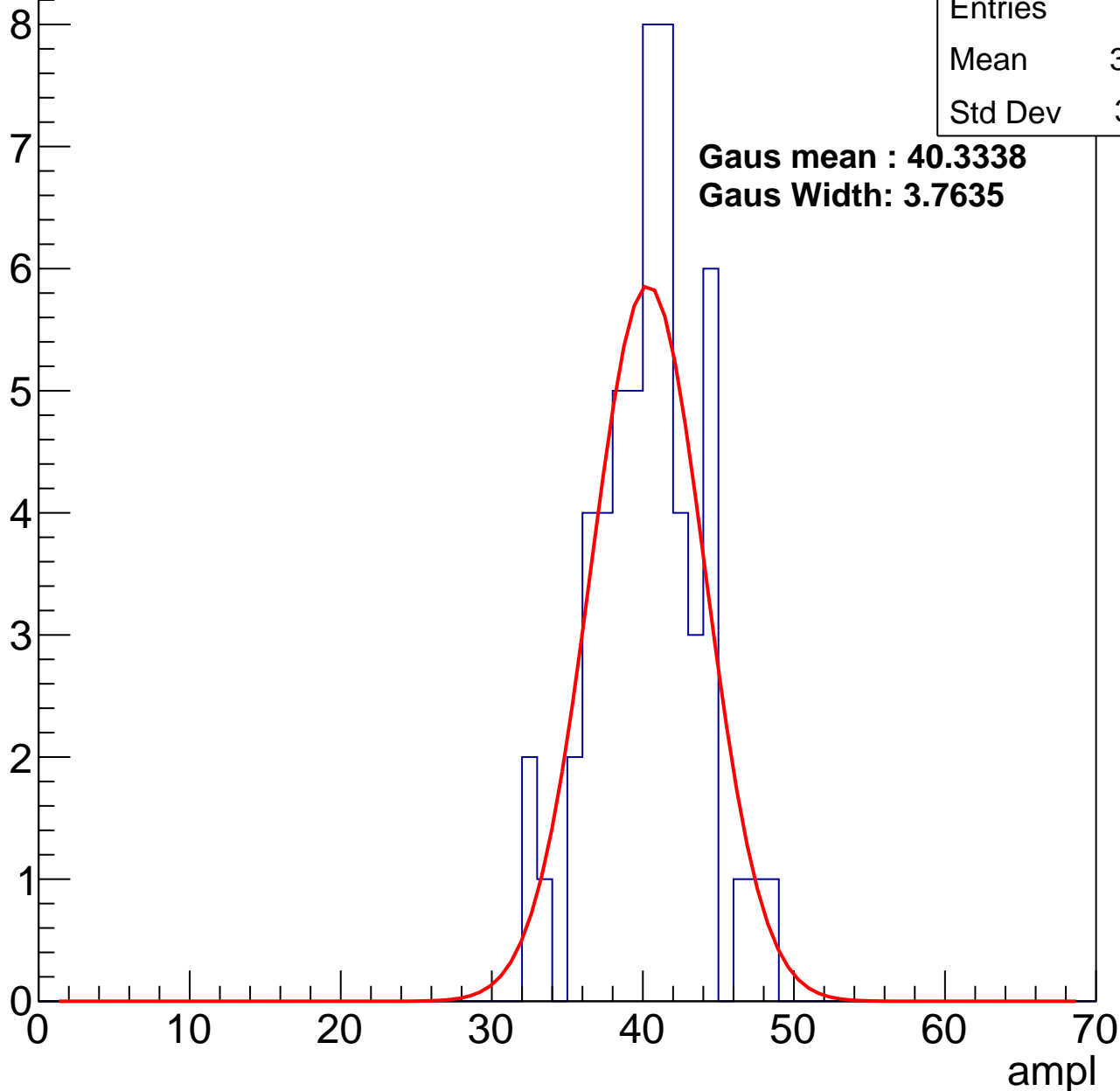
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	39.89
Std Dev	3.431

Gaus mean : 40.3338

Gaus Width: 3.7635



B1L102S, U2-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

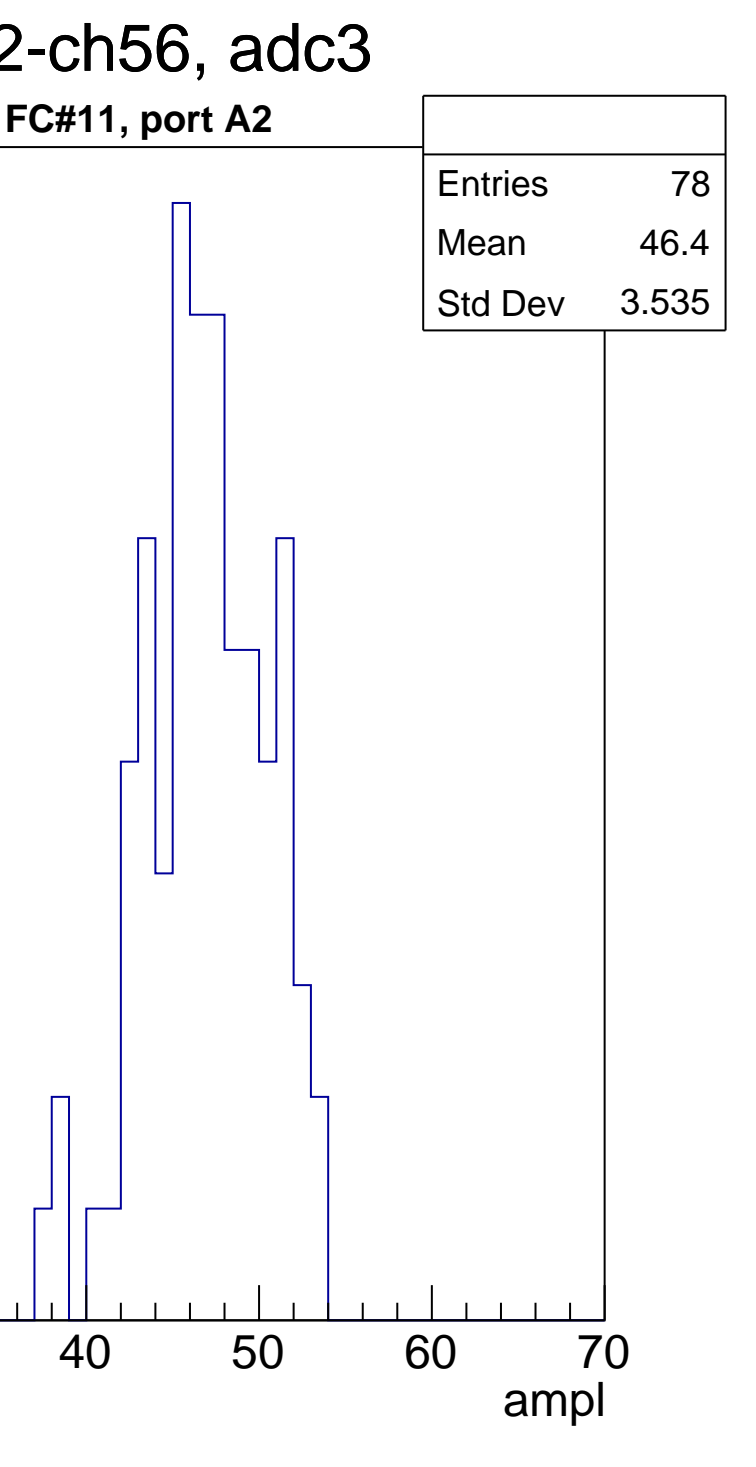
Entries	78
Mean	46.4
Std Dev	3.535

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

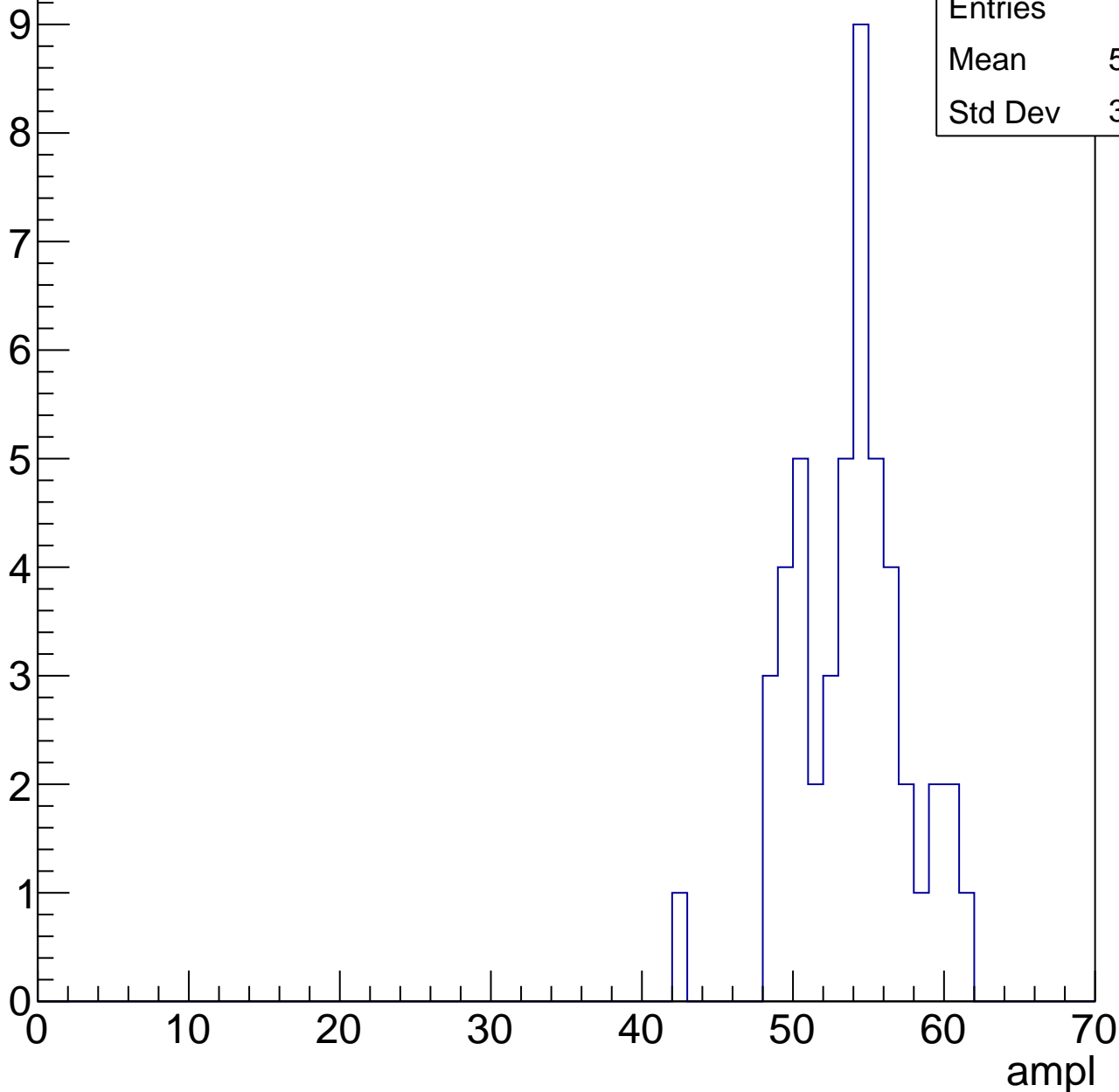


B1L102S, U2-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.29
Std Dev	3.692

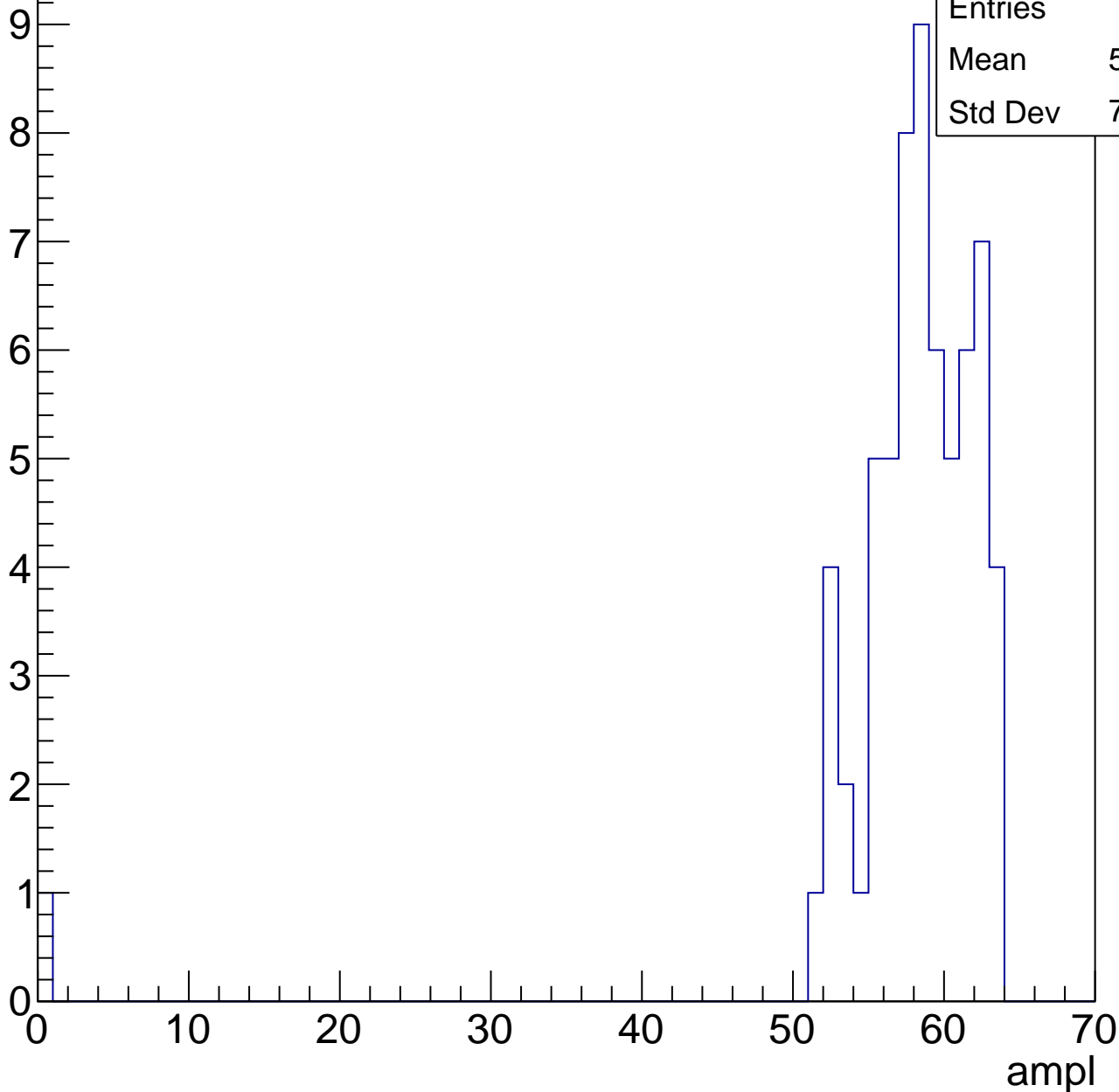


B1L102S, U2-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	57.16
Std Dev	7.849

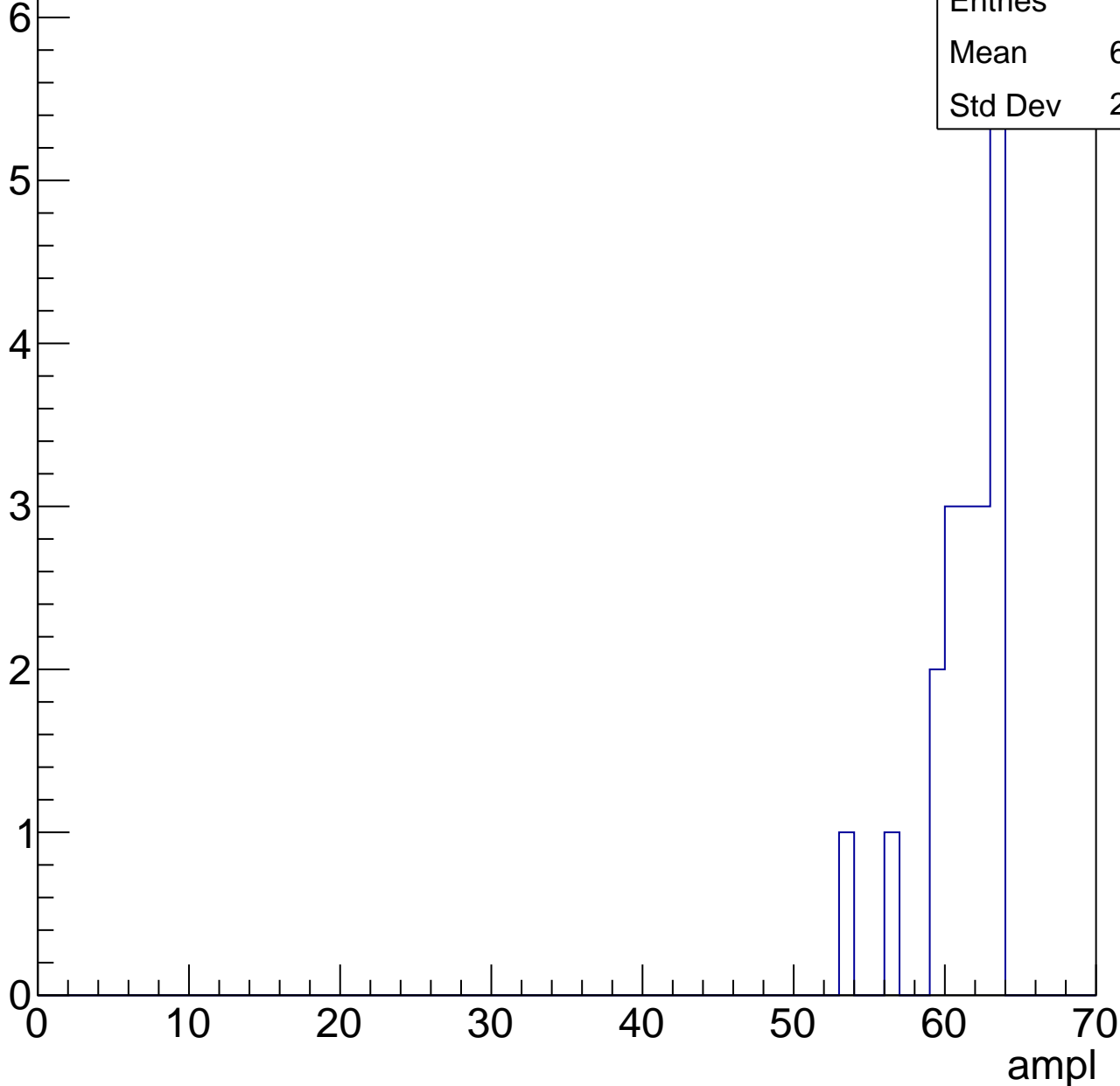


B1L102S, U2-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	60.74
Std Dev	2.572



B1L102S, U2-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch57, adc0

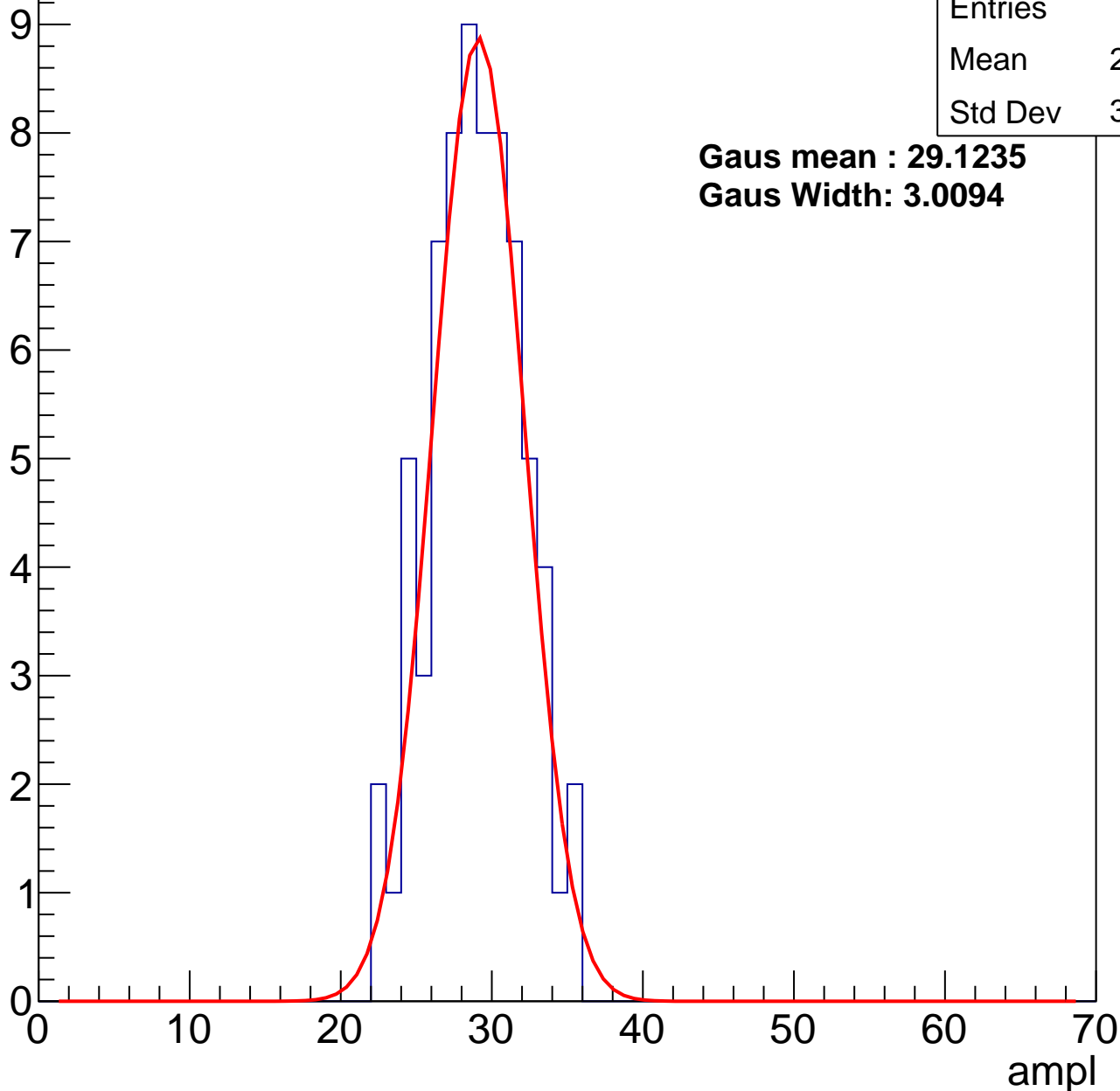
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.53
Std Dev	3.018

Gaus mean : 29.1235

Gaus Width: 3.0094



B1L102S, U2-ch57, adc1

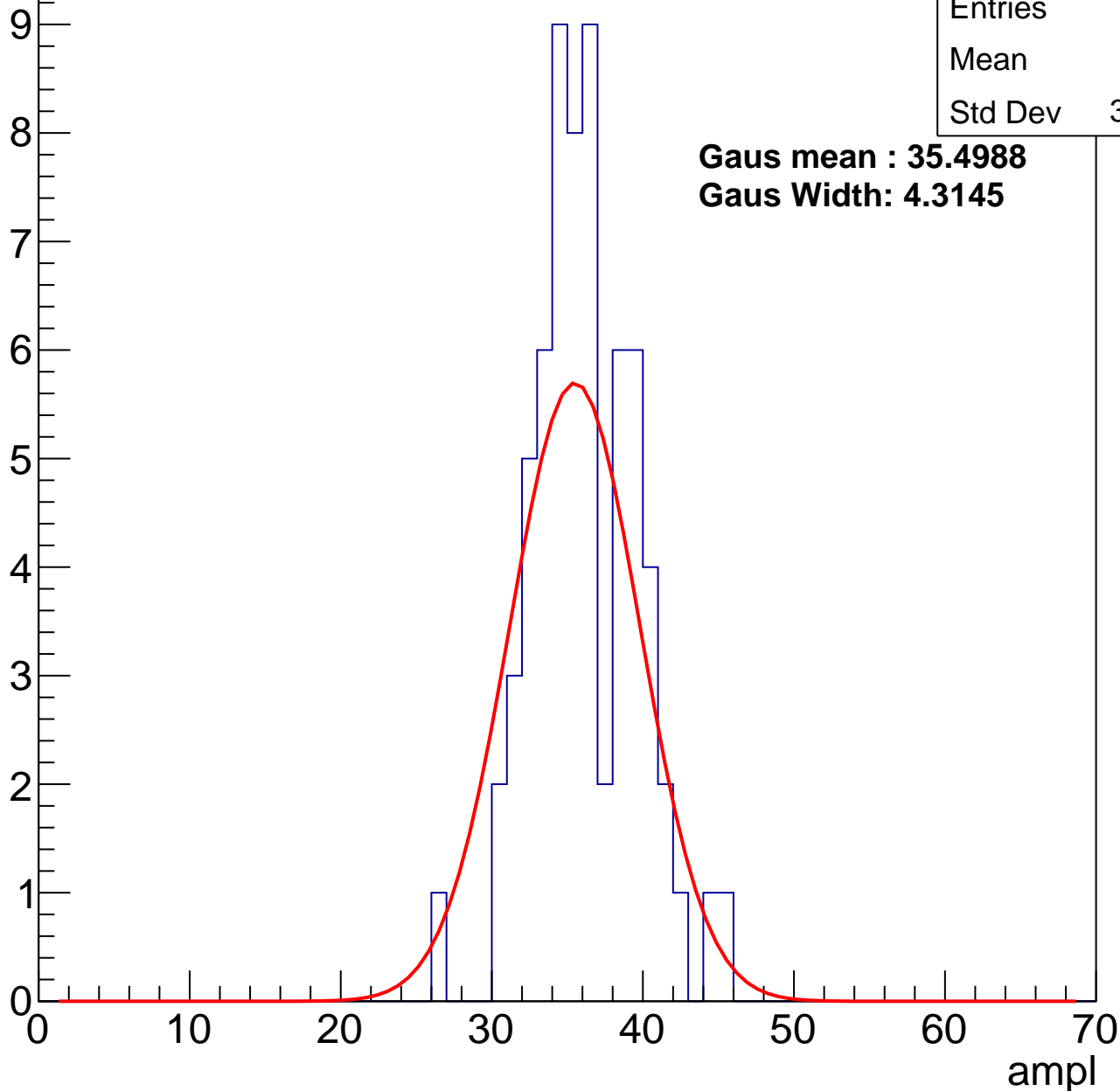
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.7
Std Dev	3.468

Gaus mean : 35.4988

Gaus Width: 4.3145



B1L102S, U2-ch57, adc2

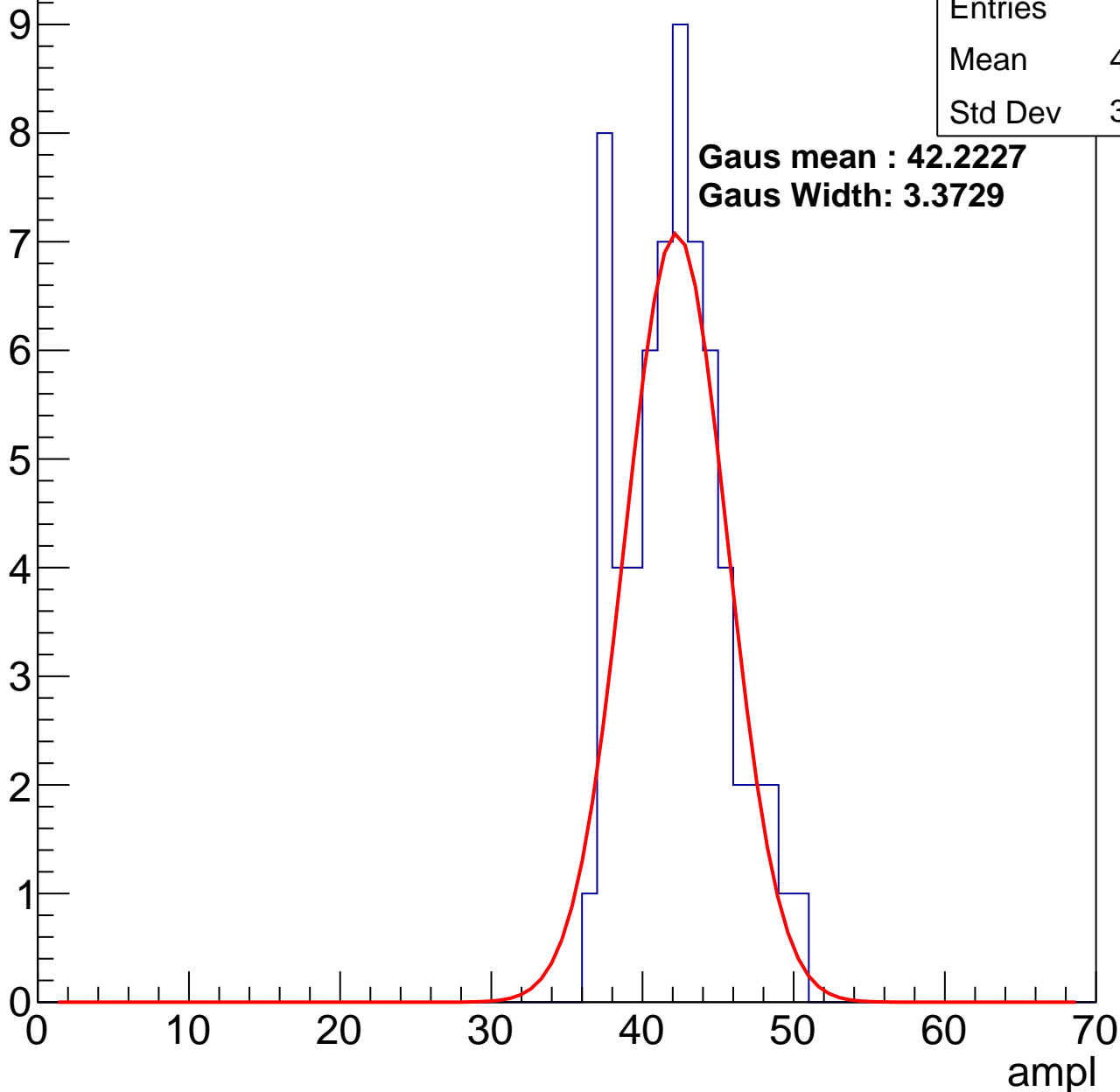
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.73
Std Dev	3.299

Gaus mean : 42.2227

Gaus Width: 3.3729

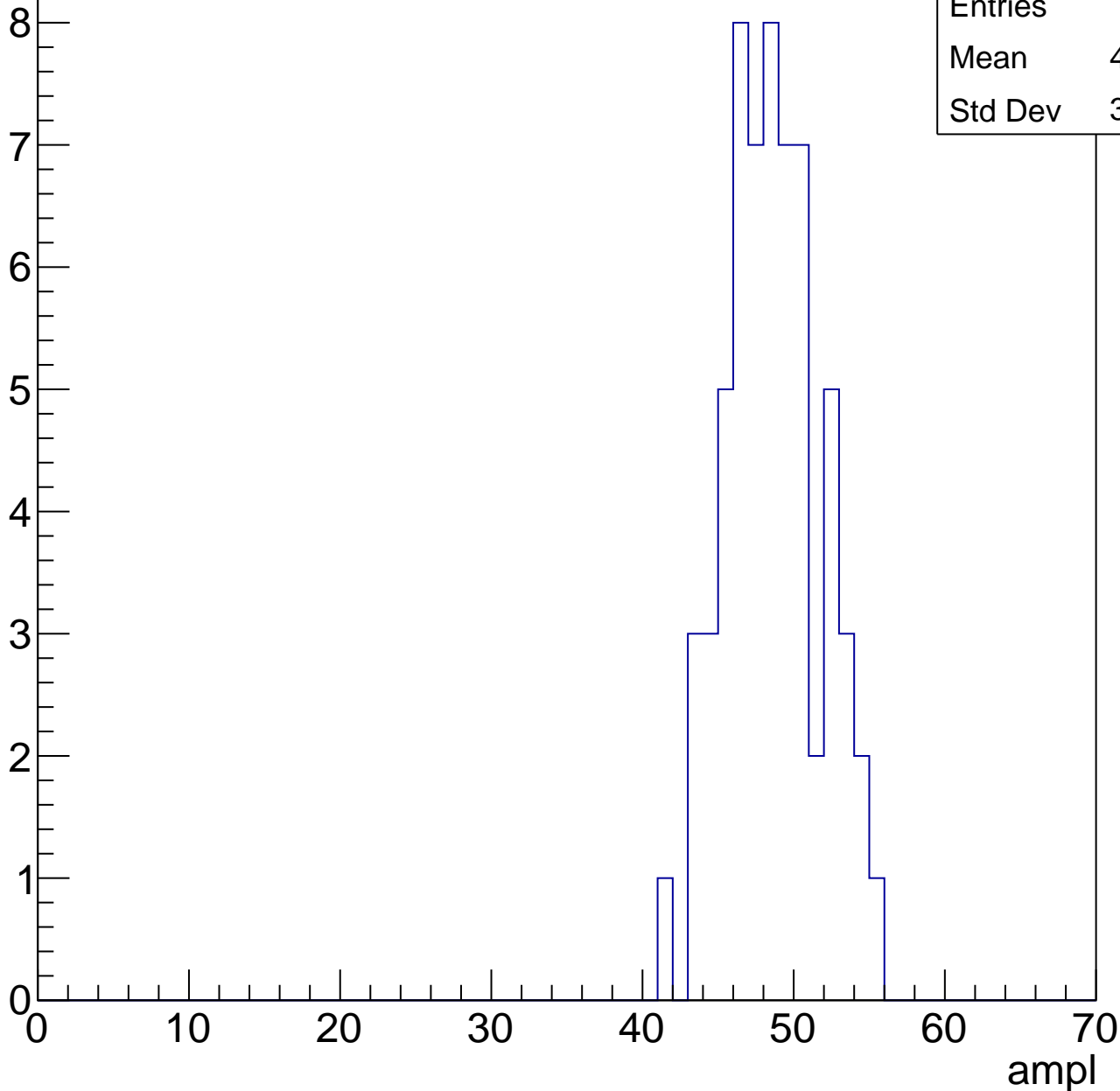


B1L102S, U2-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

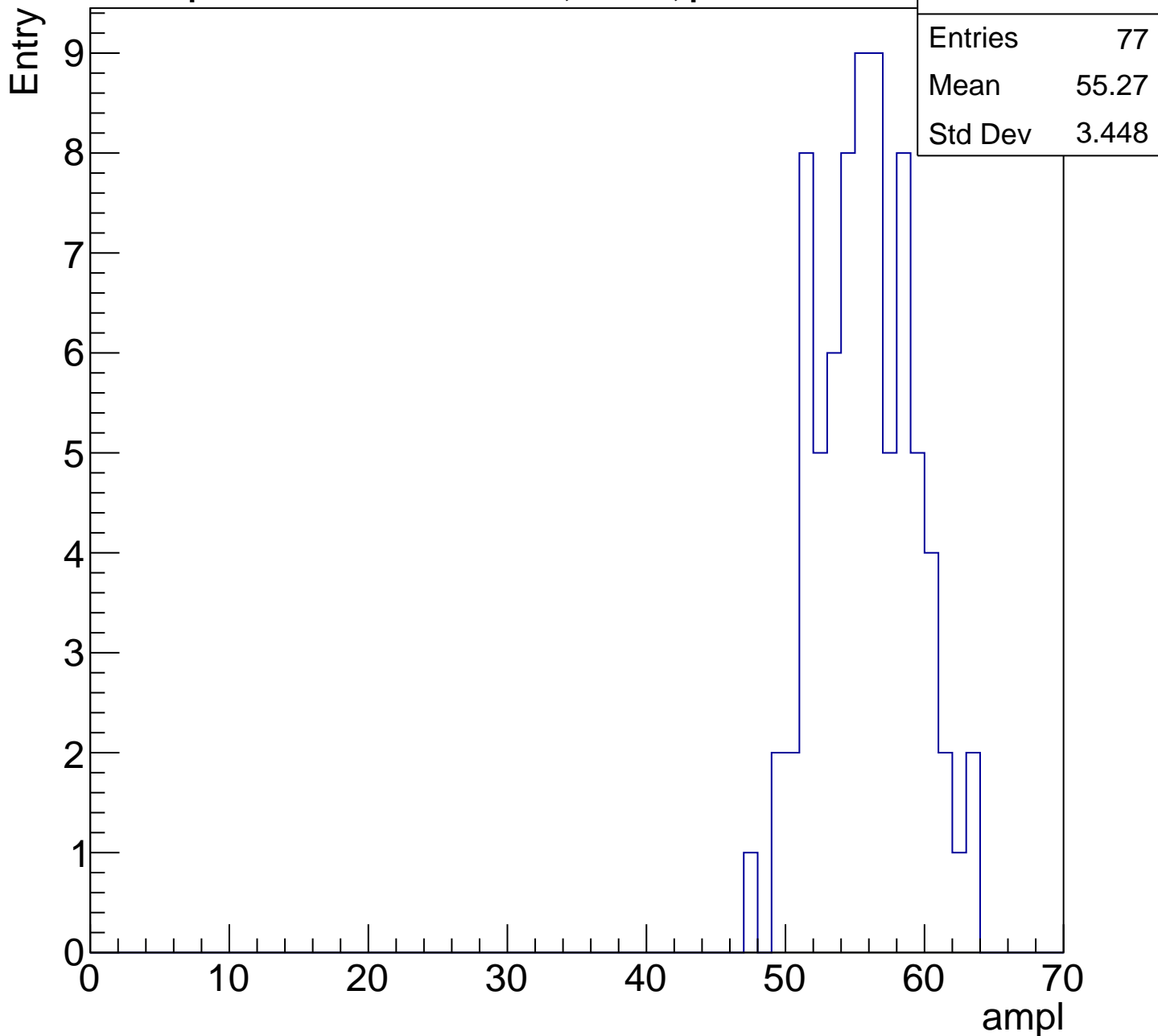
Entry

Entries	62
Mean	48.15
Std Dev	3.068



B1L102S, U2-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

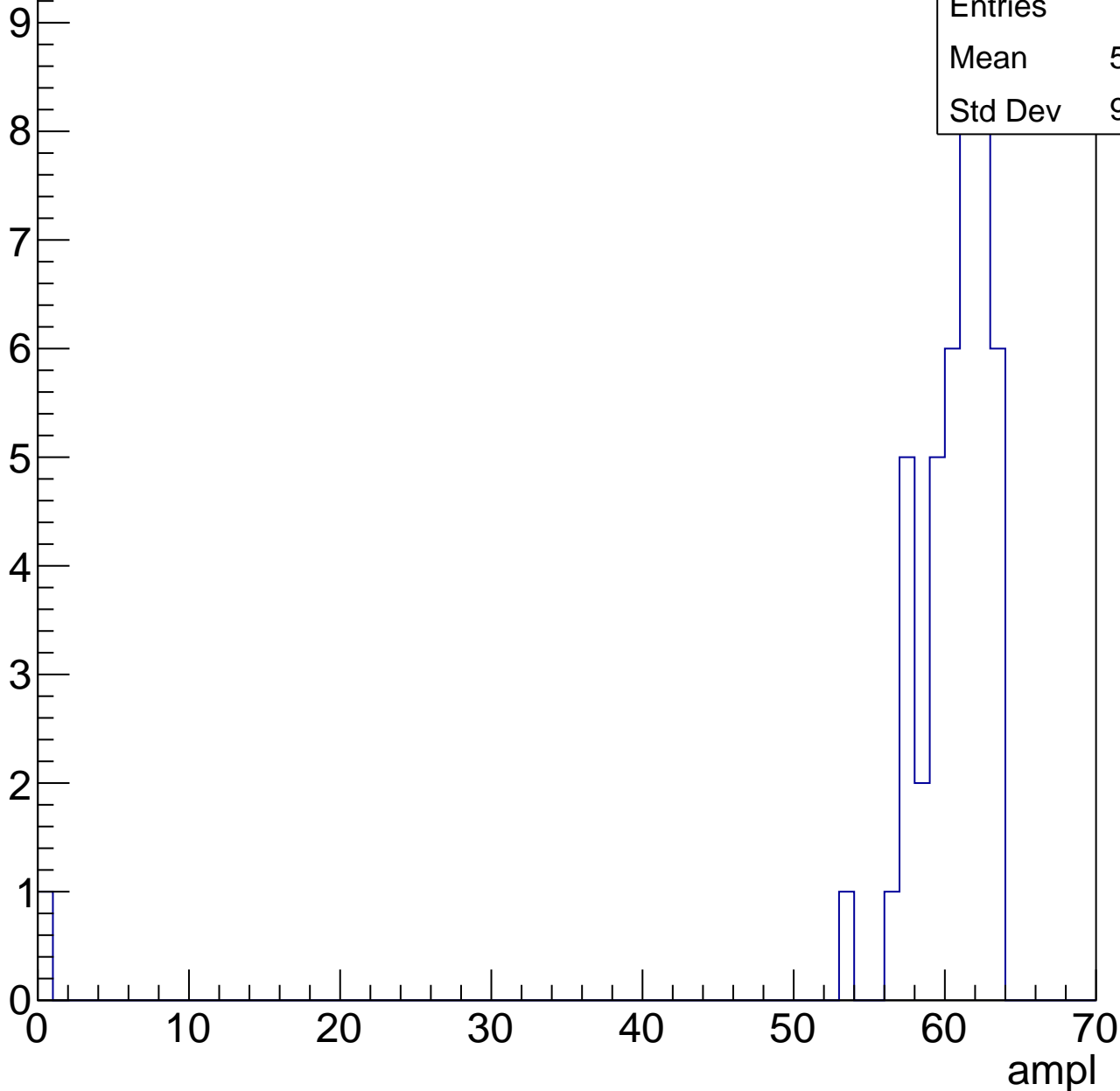


B1L102S, U2-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.89
Std Dev	9.149



B1L102S, U2-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L102S, U2-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch58, adc0

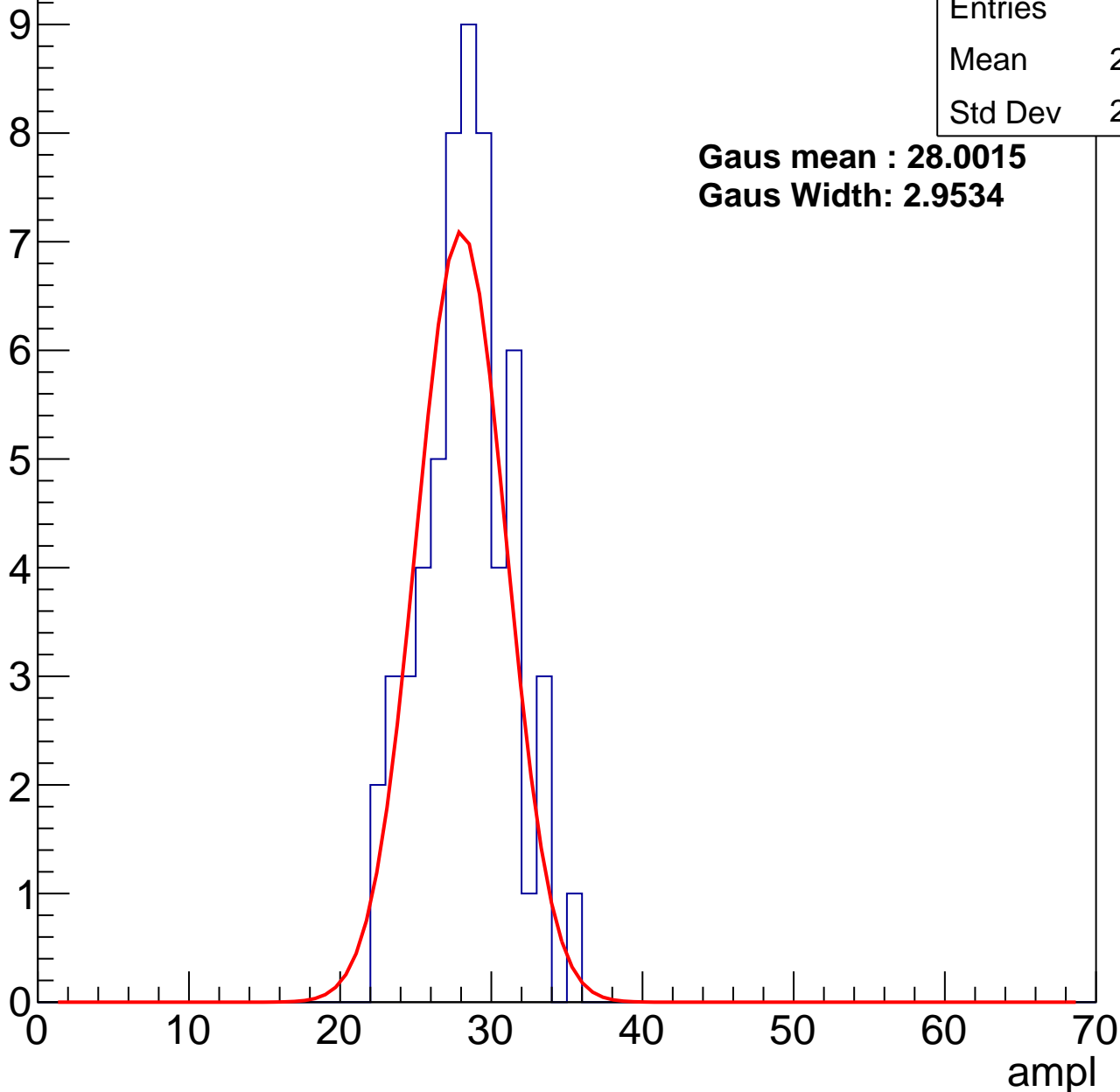
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.84
Std Dev	2.889

Gaus mean : 28.0015

Gaus Width: 2.9534



B1L102S, U2-ch58, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	34.48
Std Dev	5.565

Gaus mean : 36.2102

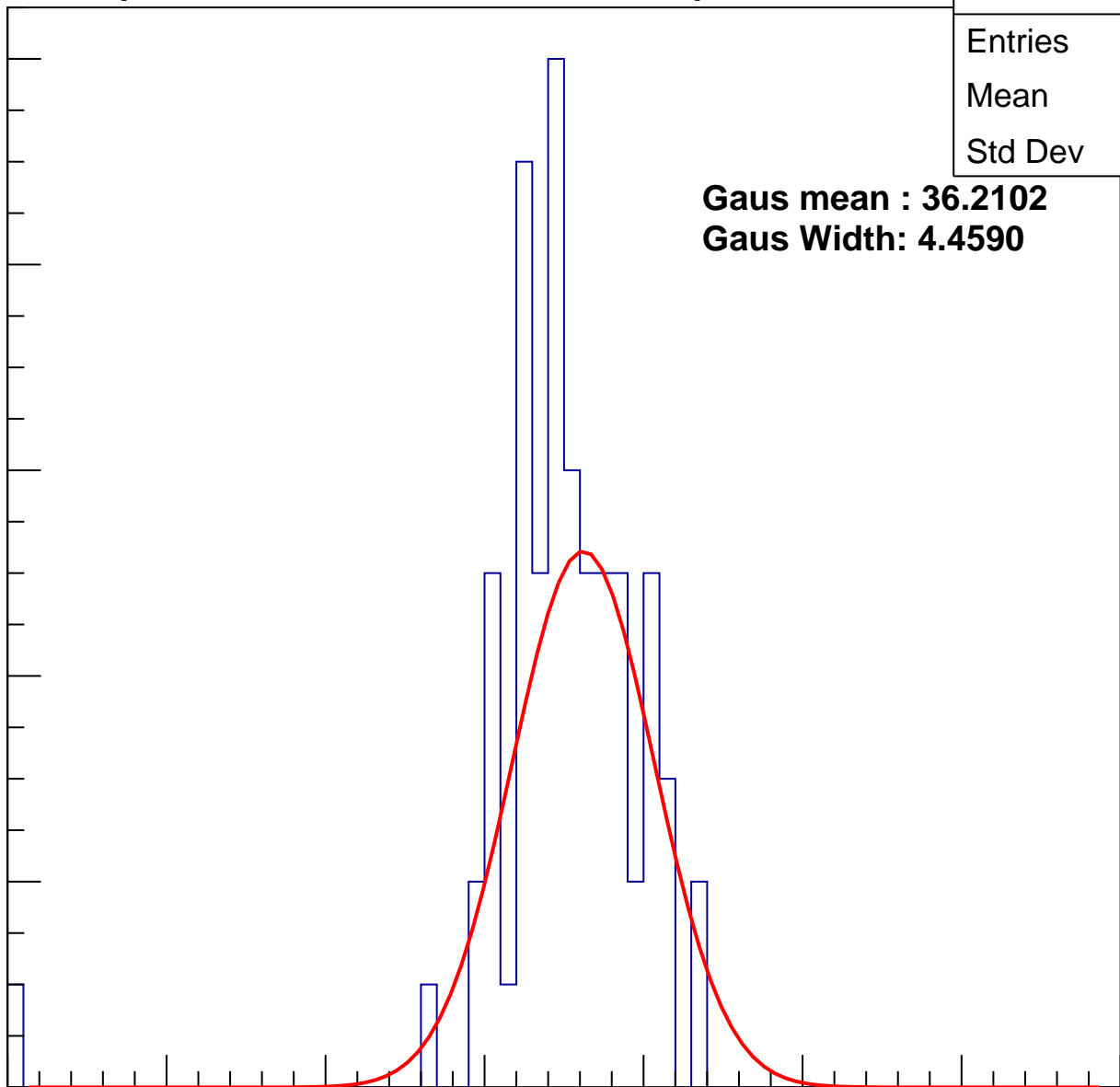
Gaus Width: 4.4590

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch58, adc2

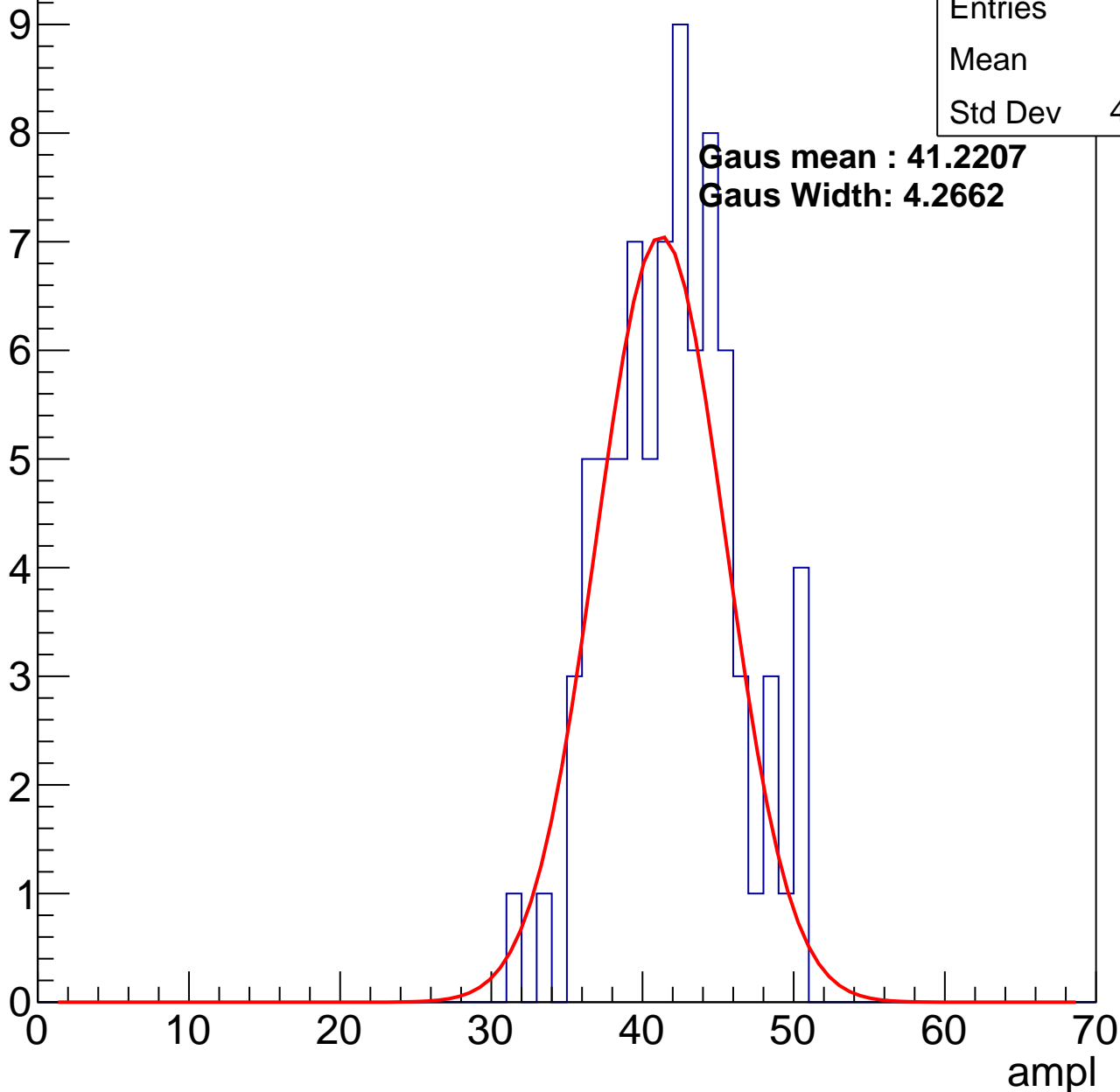
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.5
Std Dev	4.156

Gaus mean : 41.2207

Gaus Width: 4.2662

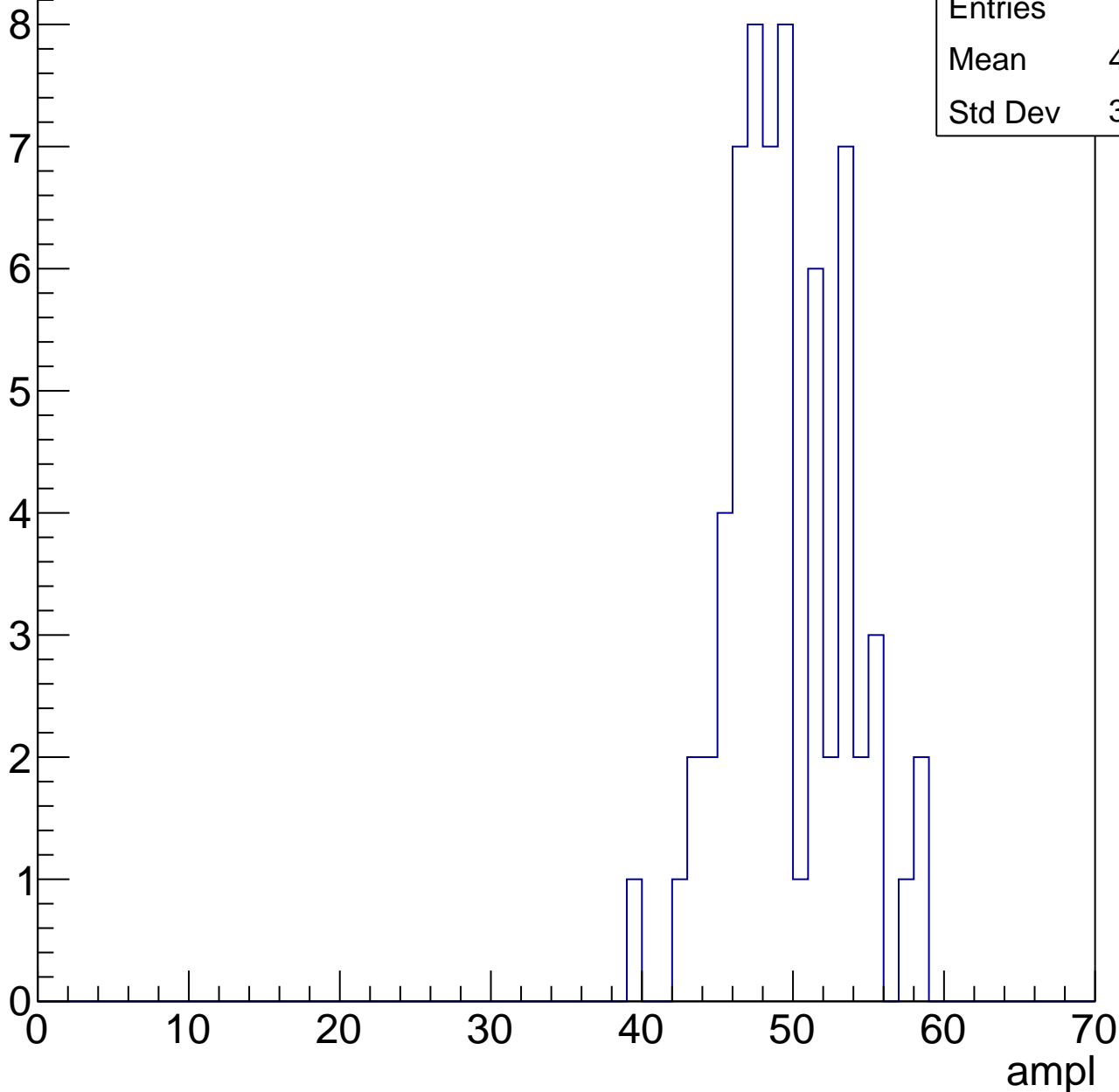


B1L102S, U2-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.03
Std Dev	3.905

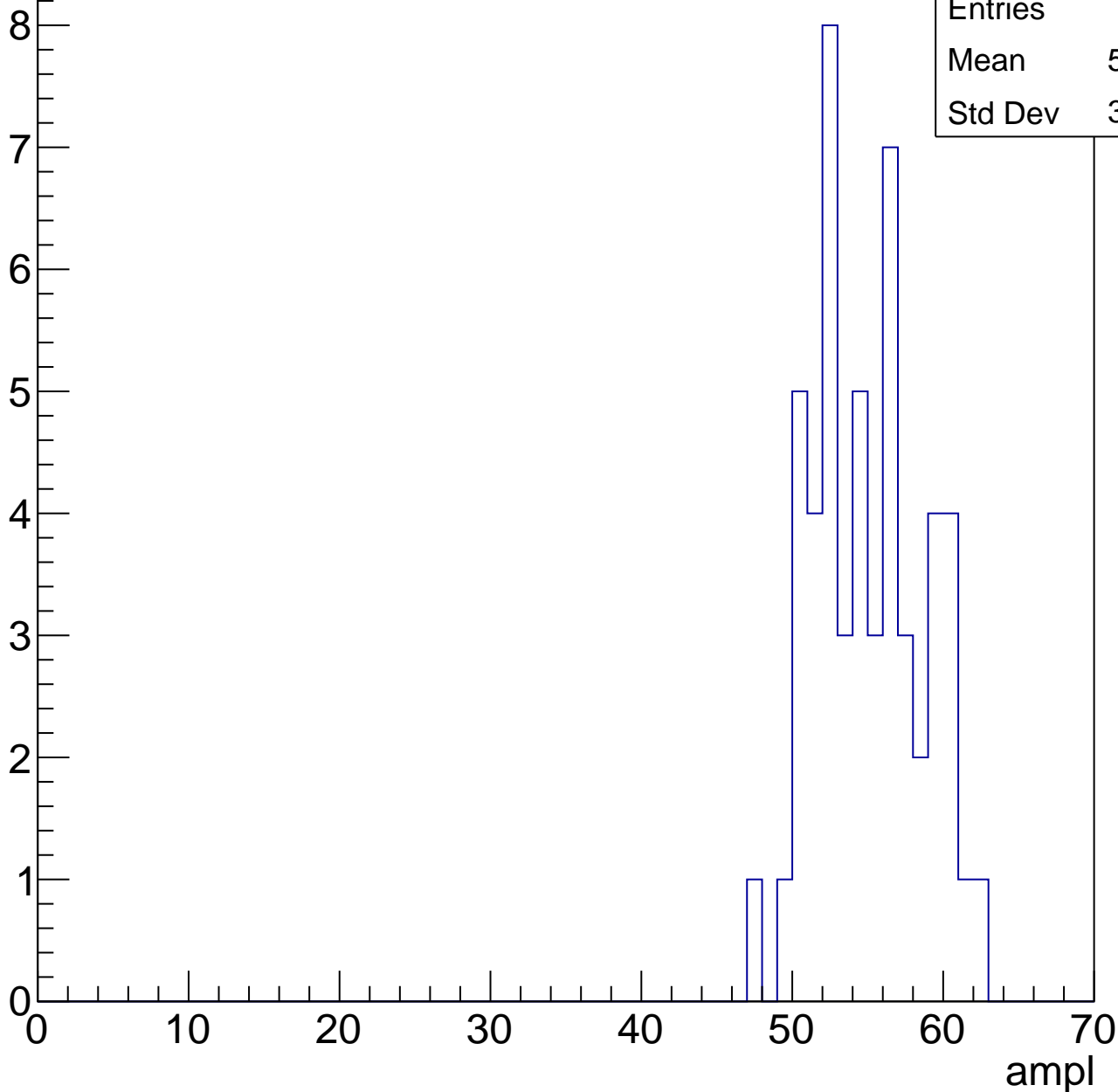


B1L102S, U2-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.58
Std Dev	3.548

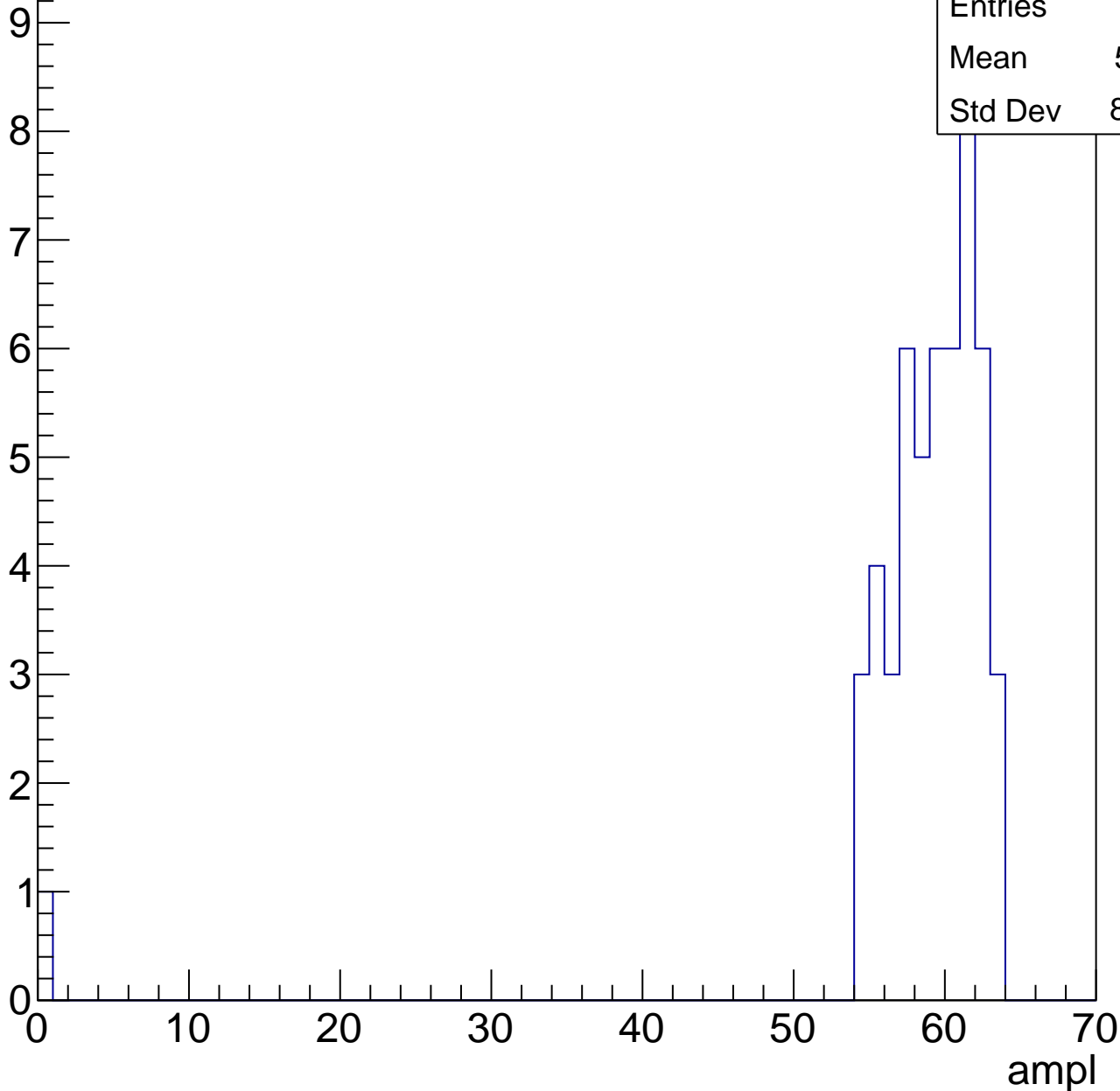


B1L102S, U2-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.81
Std Dev	8.488

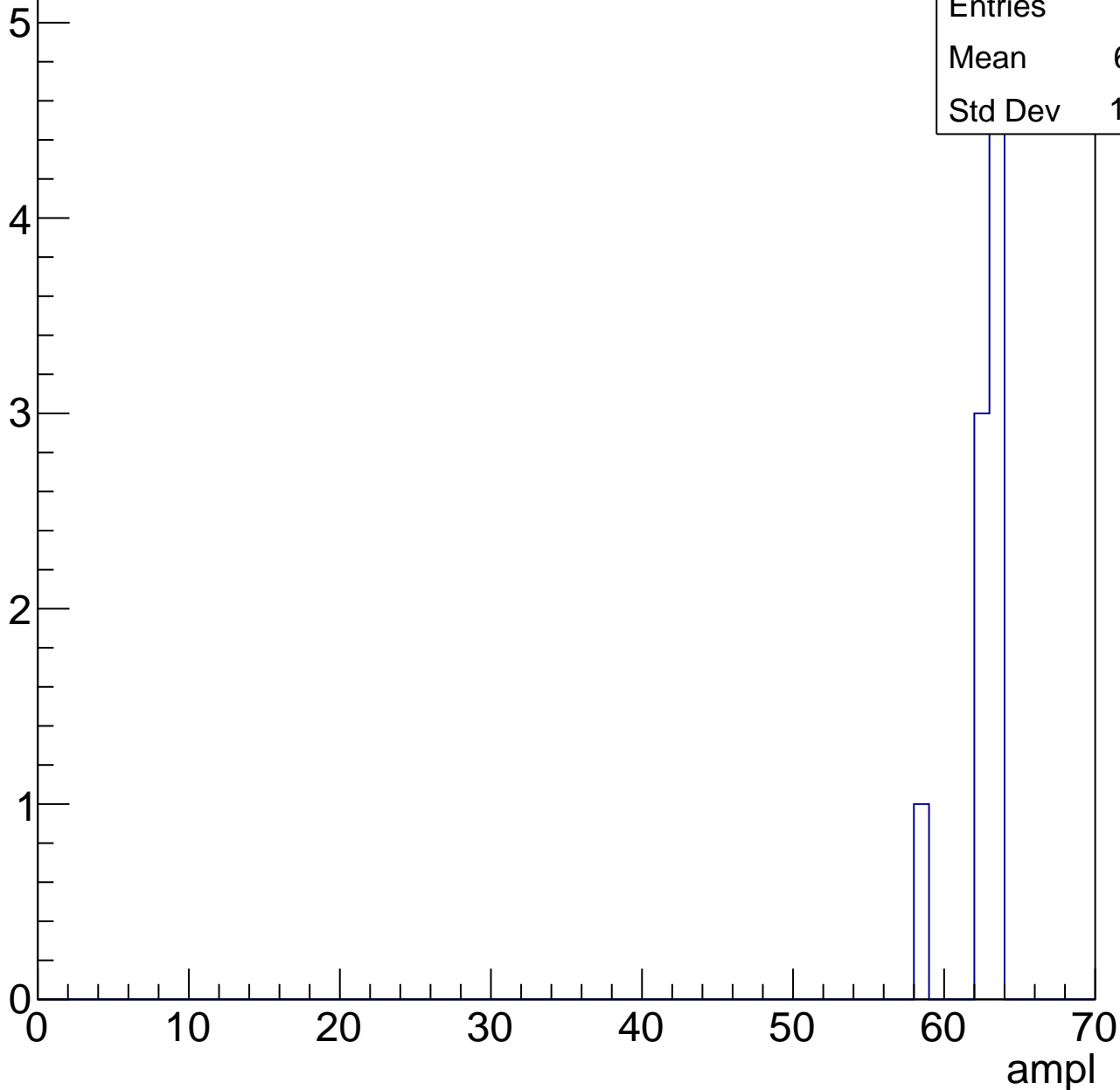


B1L102S, U2-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

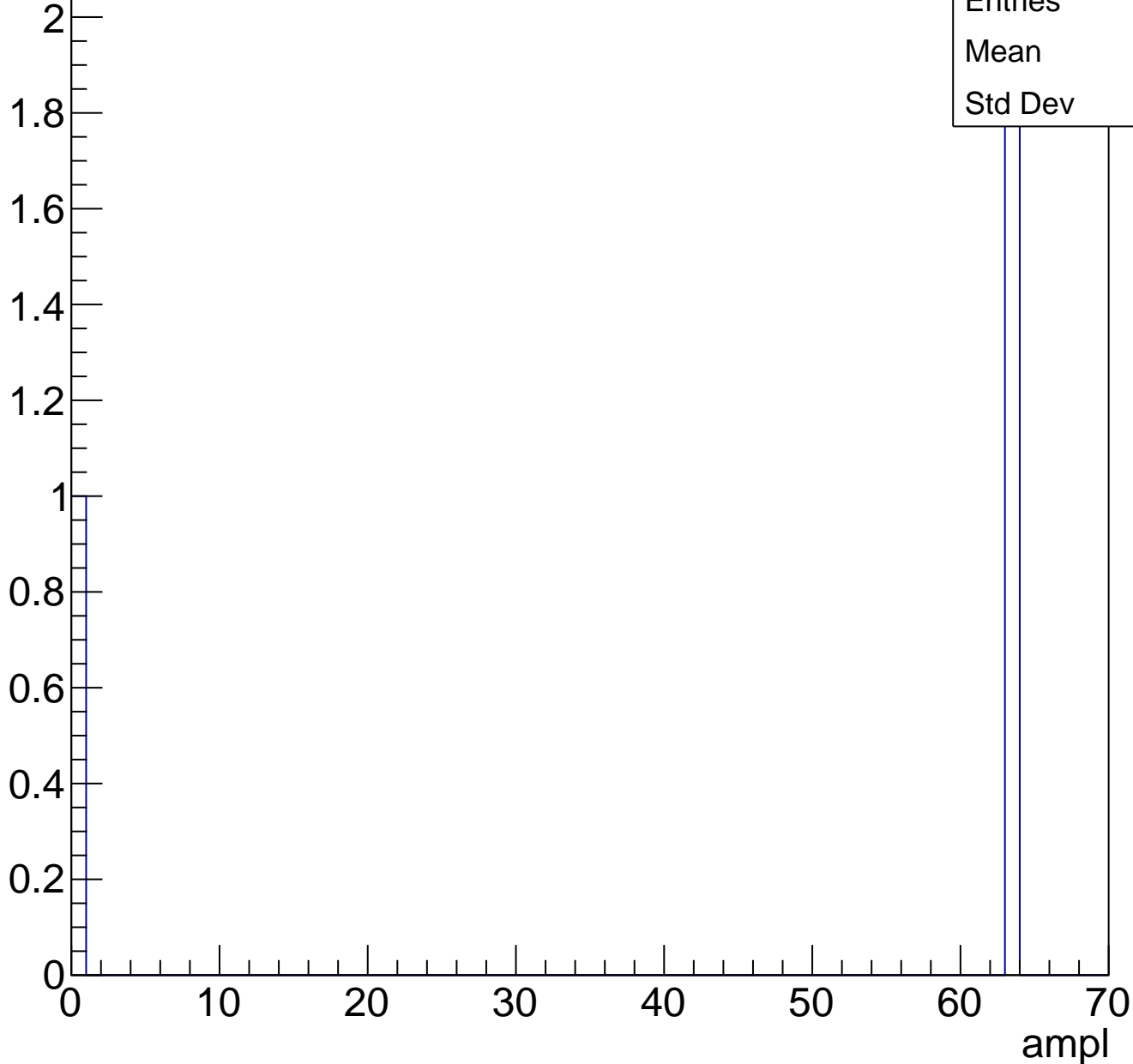
Entries	9
Mean	62.11
Std Dev	1.523



B1L102S, U2-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch59, adc0

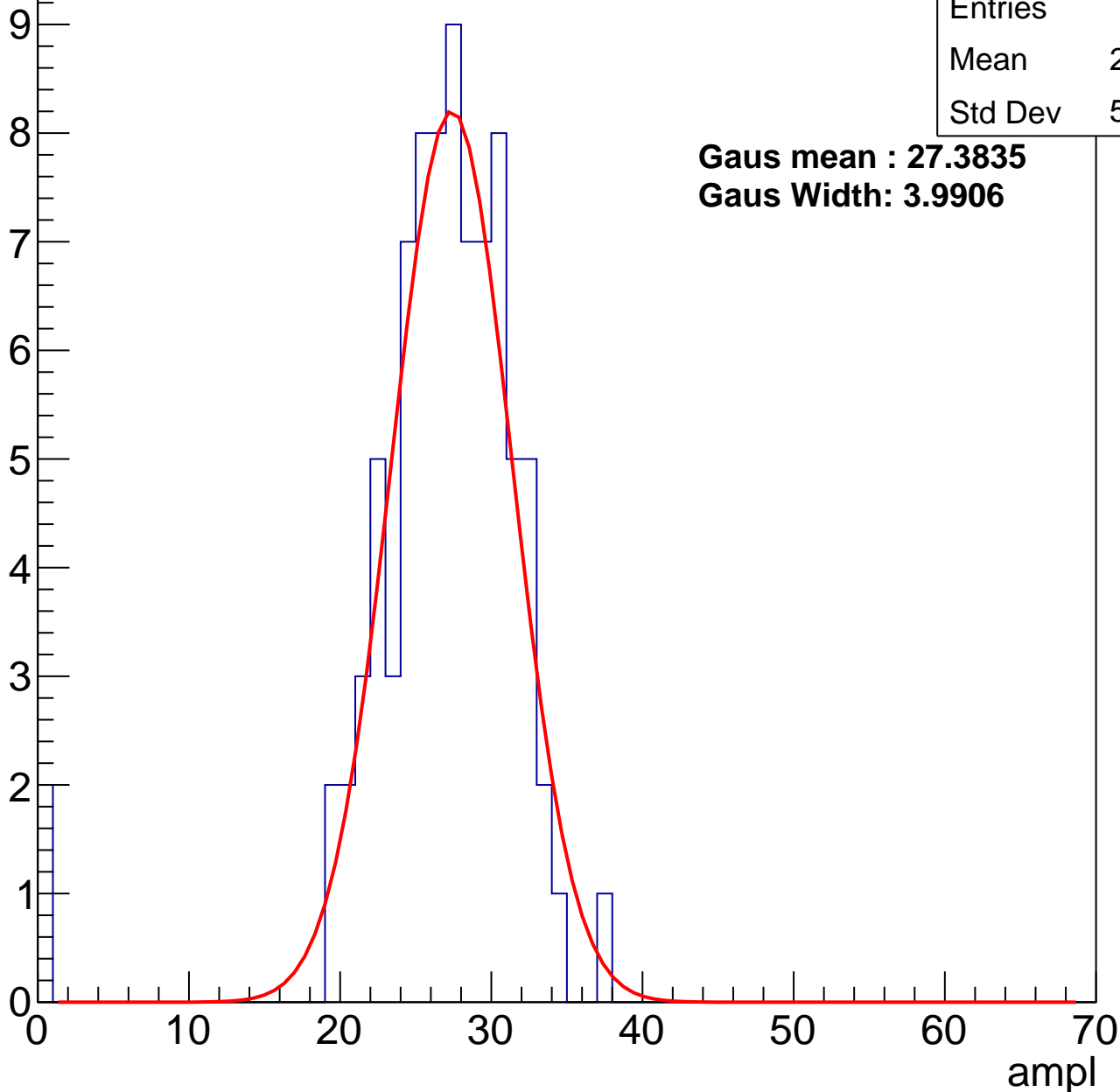
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	26.24
Std Dev	5.479

Gaus mean : 27.3835

Gaus Width: 3.9906



B1L102S, U2-ch59, adc1

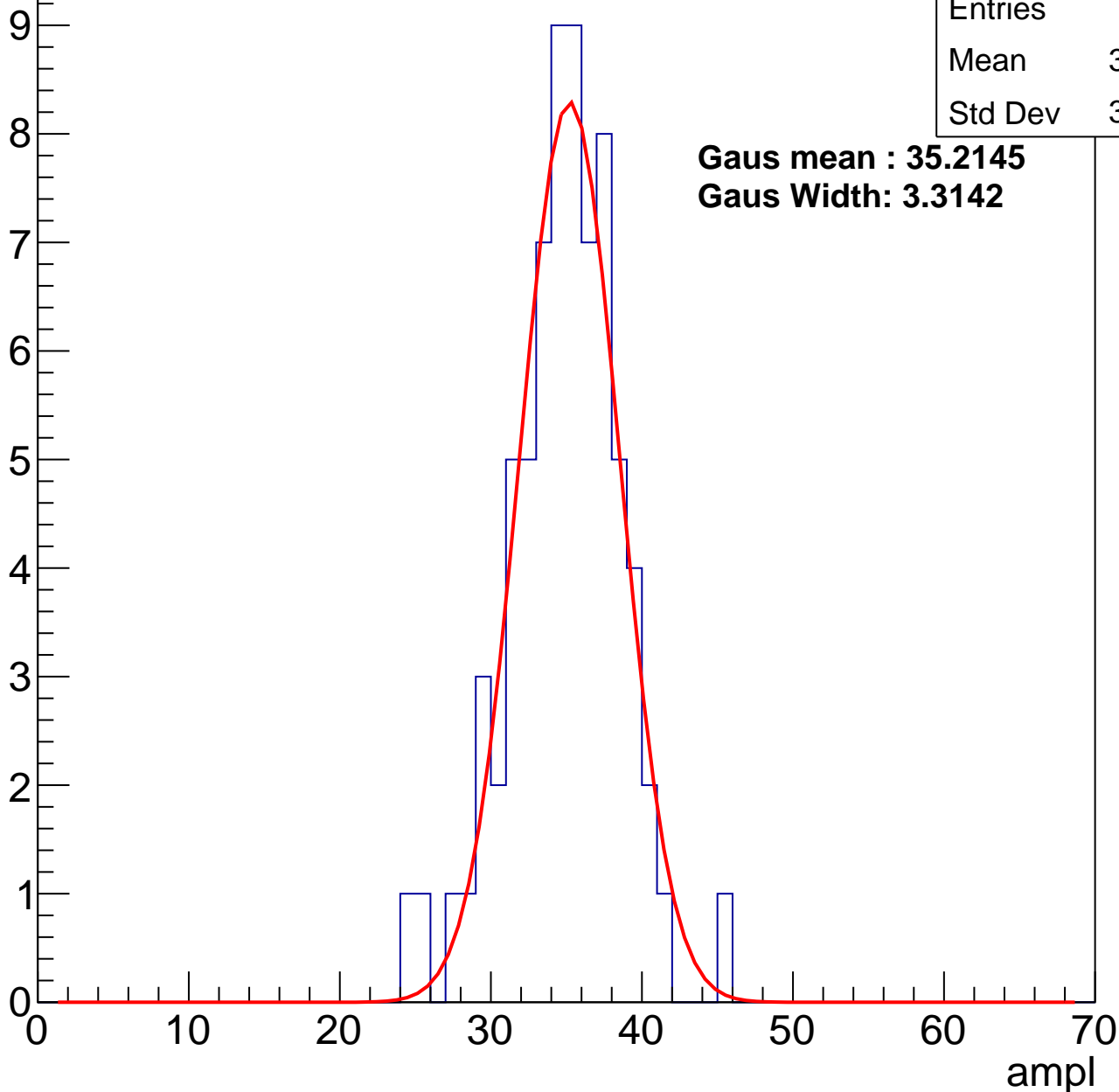
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.42
Std Dev	3.658

Gaus mean : 35.2145

Gaus Width: 3.3142



B1L102S, U2-ch59, adc2

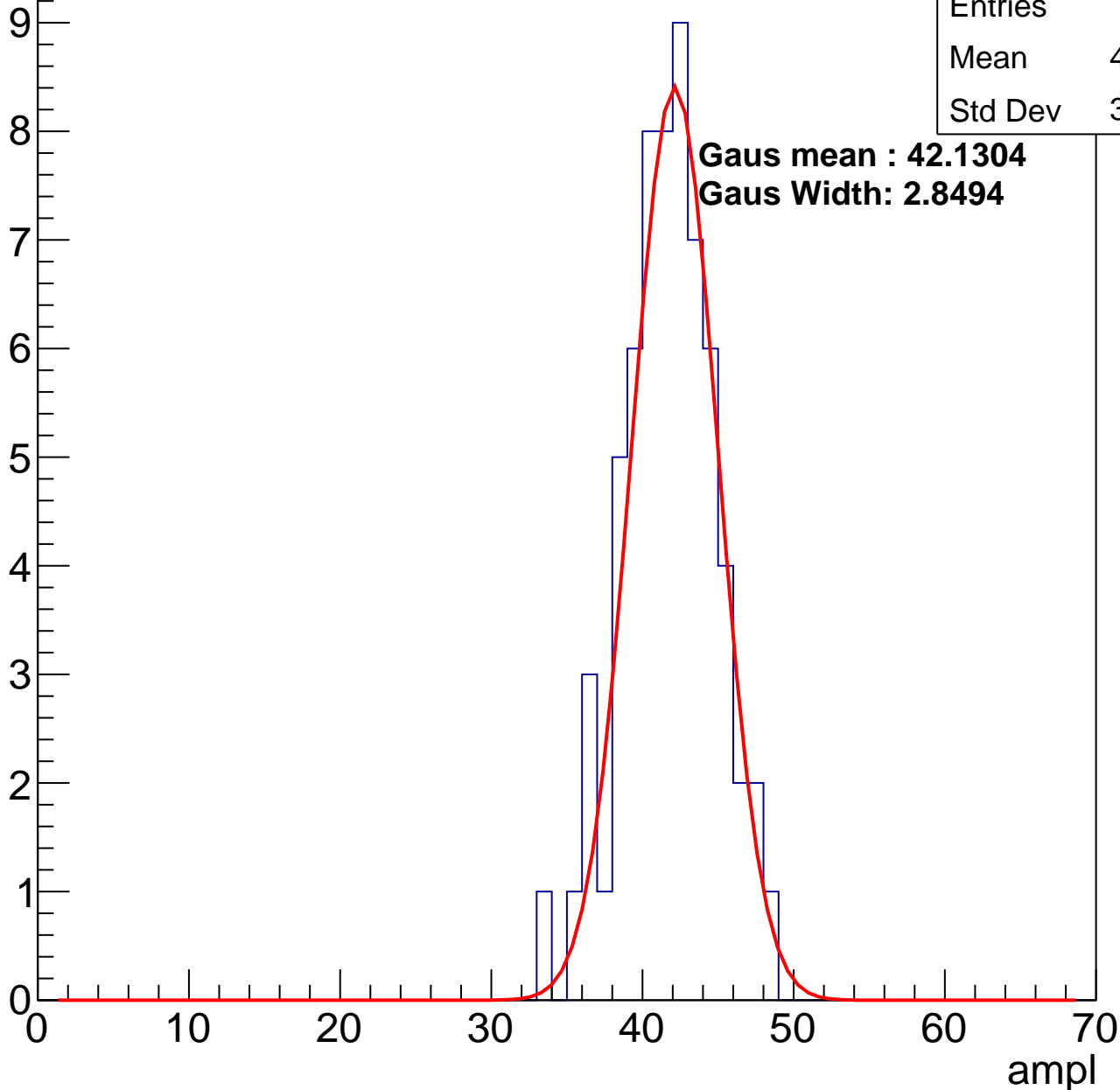
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.28
Std Dev	3.034

Gaus mean : 42.1304

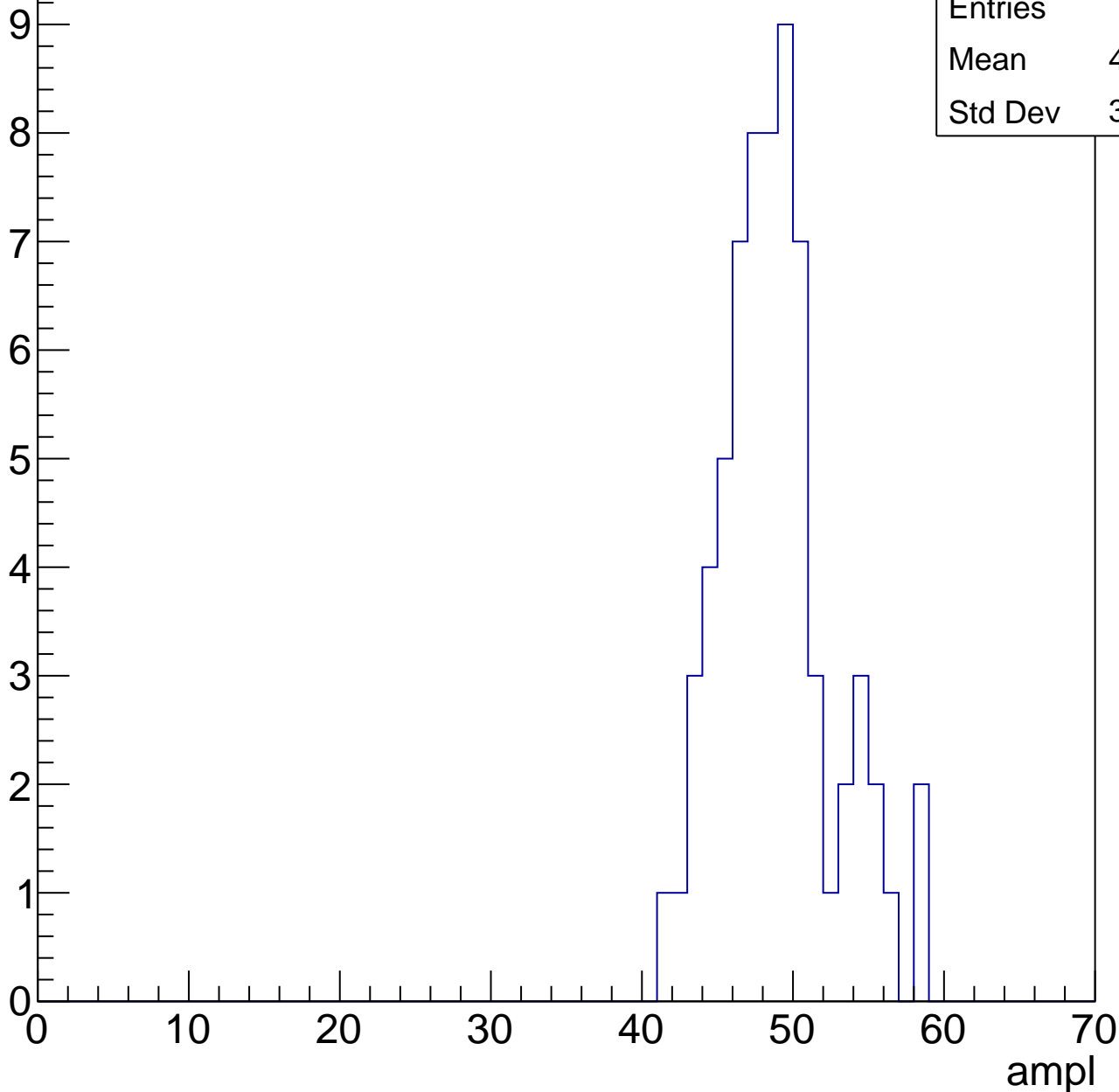
Gaus Width: 2.8494



B1L102S, U2-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

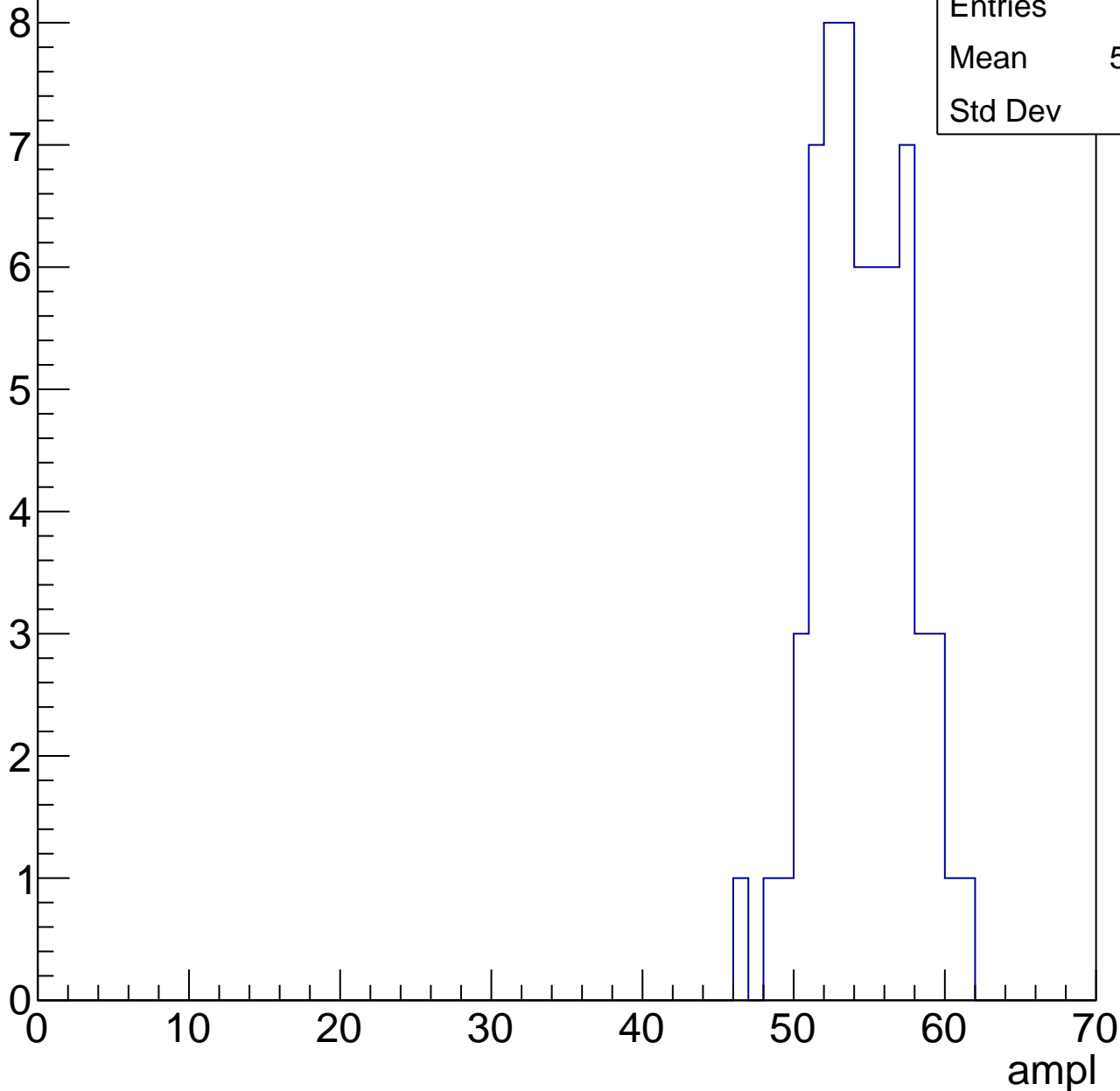


B1L102S, U2-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.05
Std Dev	3.05



B1L102S, U2-ch59, adc5

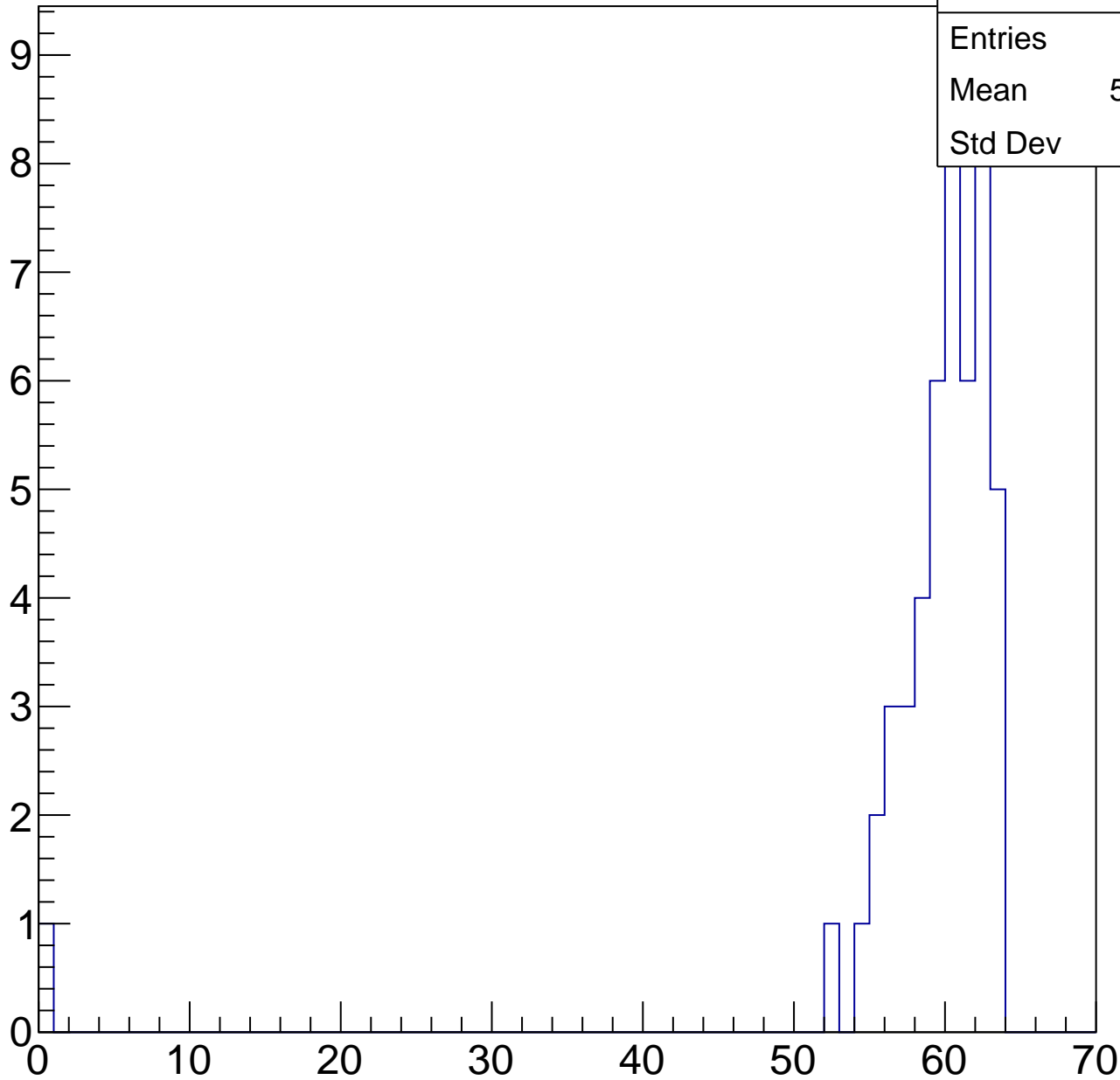
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.37
Std Dev	8.81

ampl

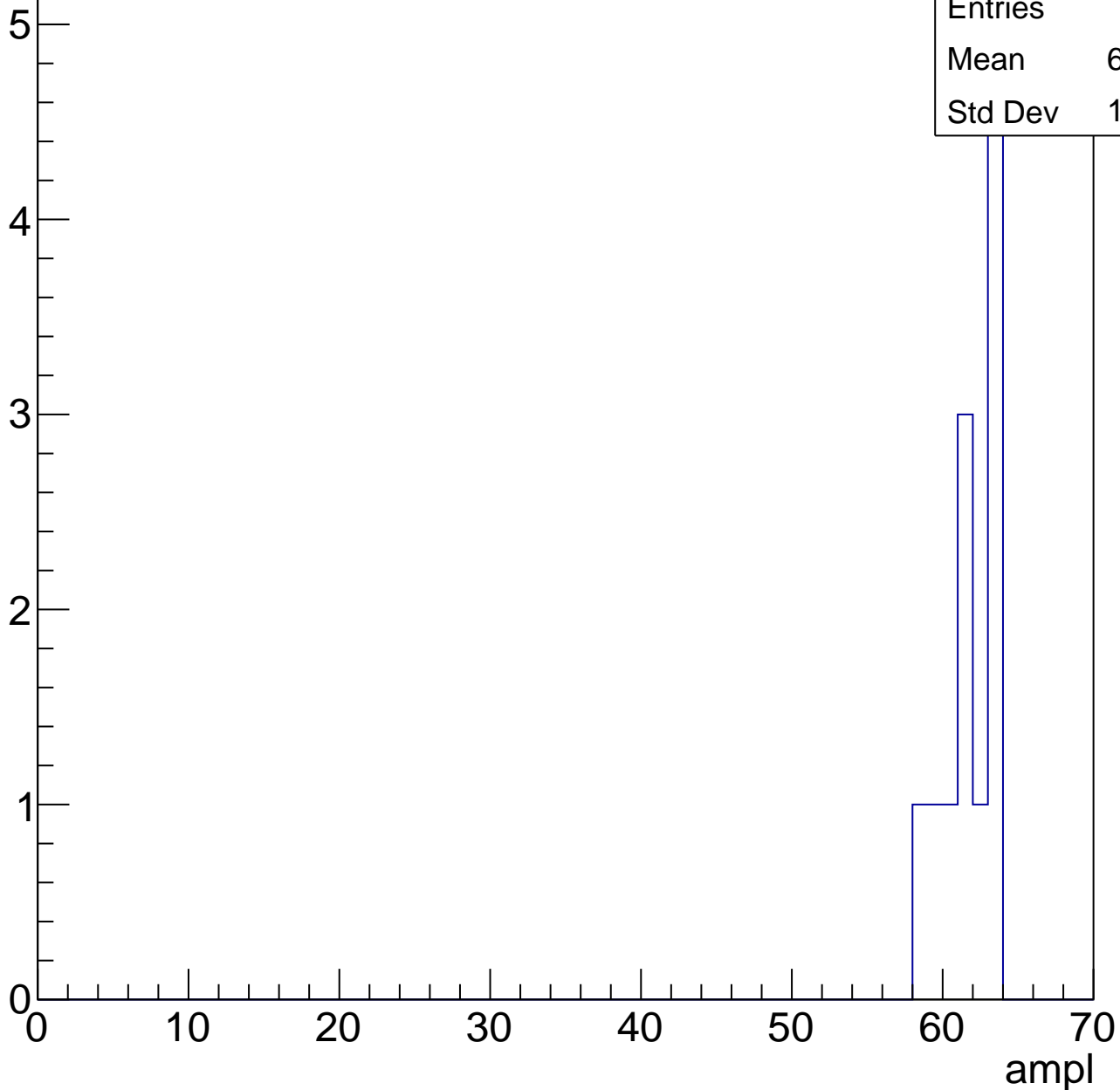


B1L102S, U2-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.42
Std Dev	1.656



B1L102S, U2-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch60, adc0

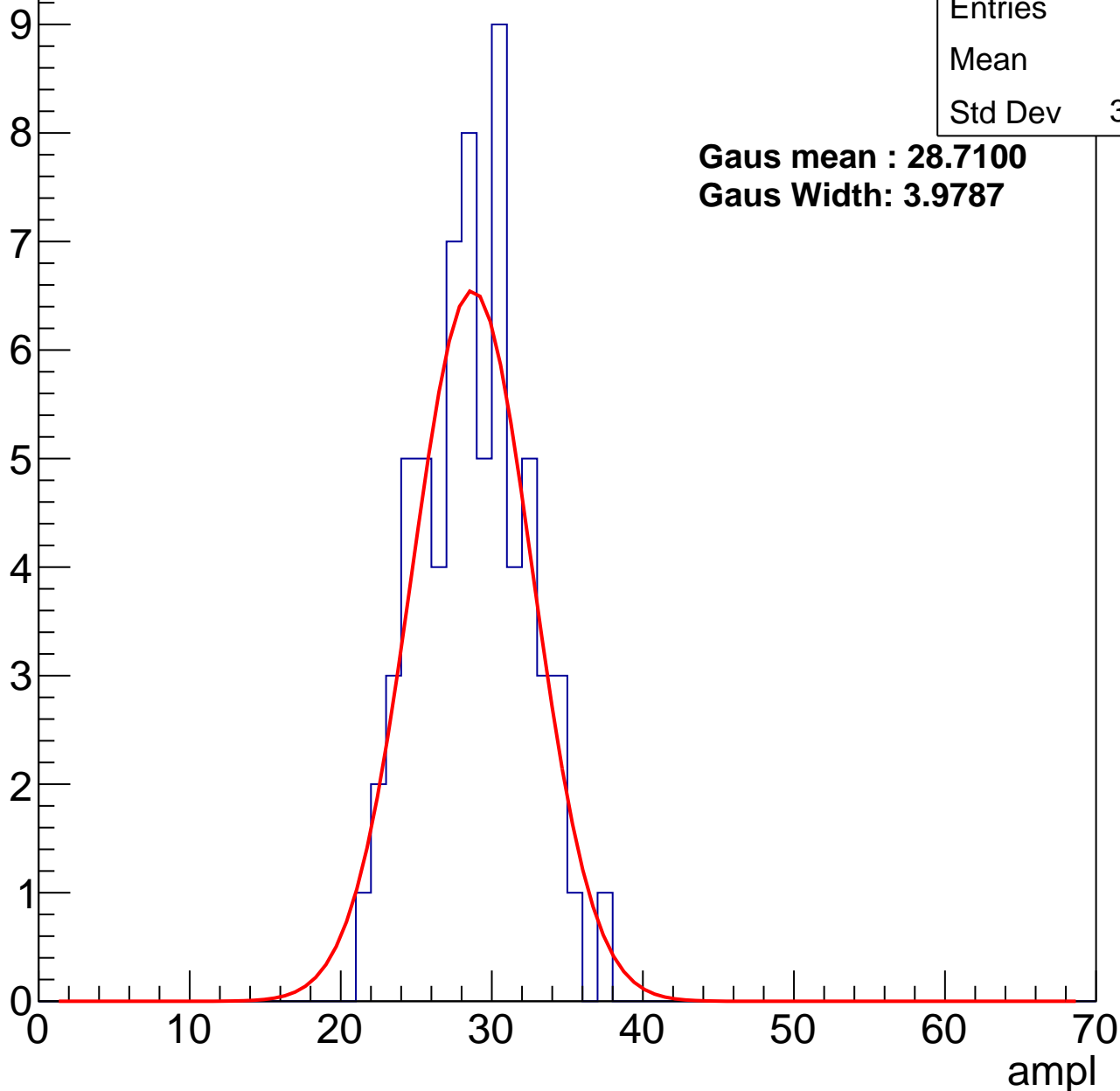
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.3
Std Dev	3.499

Gaus mean : 28.7100

Gaus Width: 3.9787



B1L102S, U2-ch60, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	35.85
Std Dev	3.832

Gaus mean : 36.6450

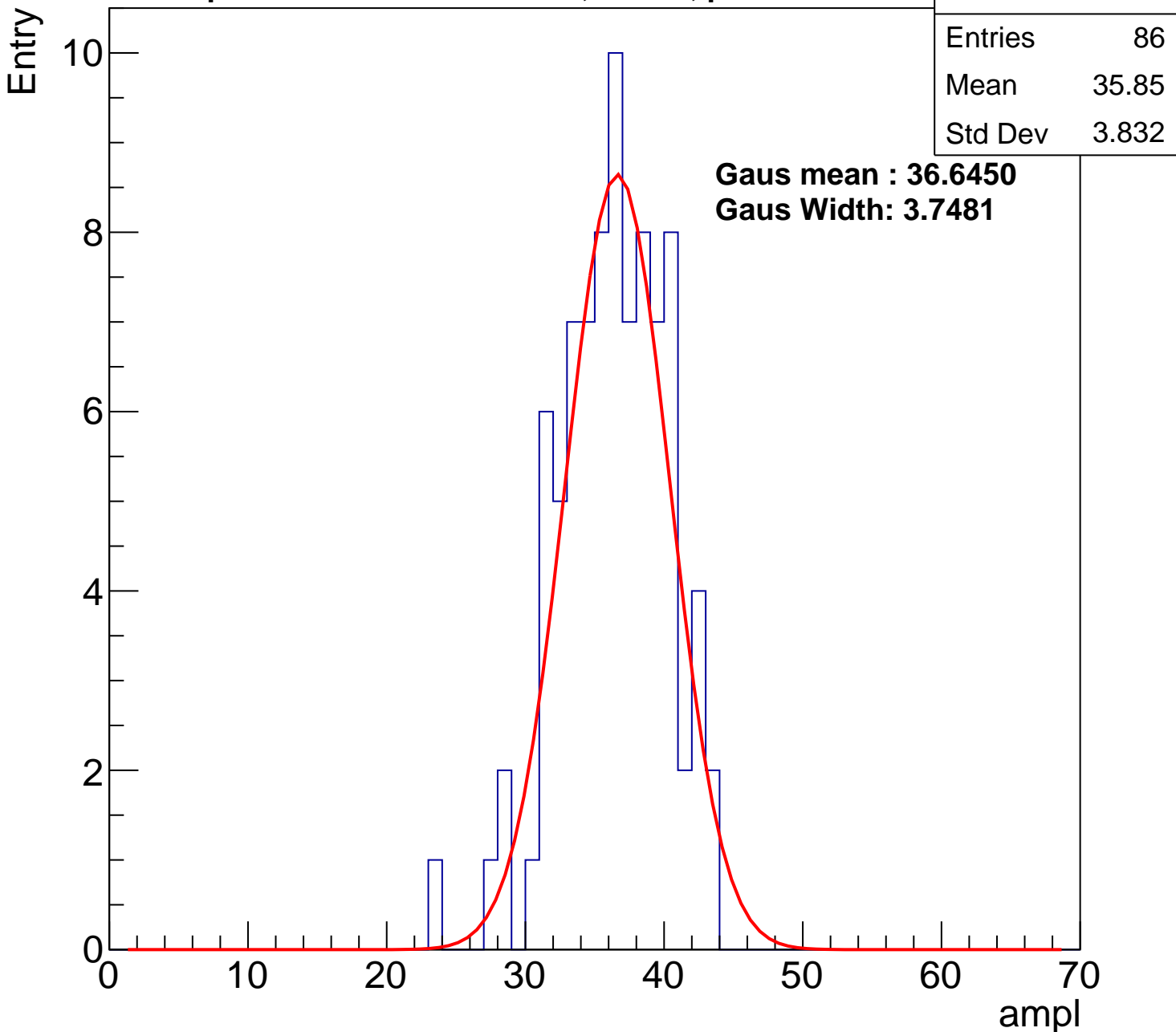
Gaus Width: 3.7481

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch60, adc2

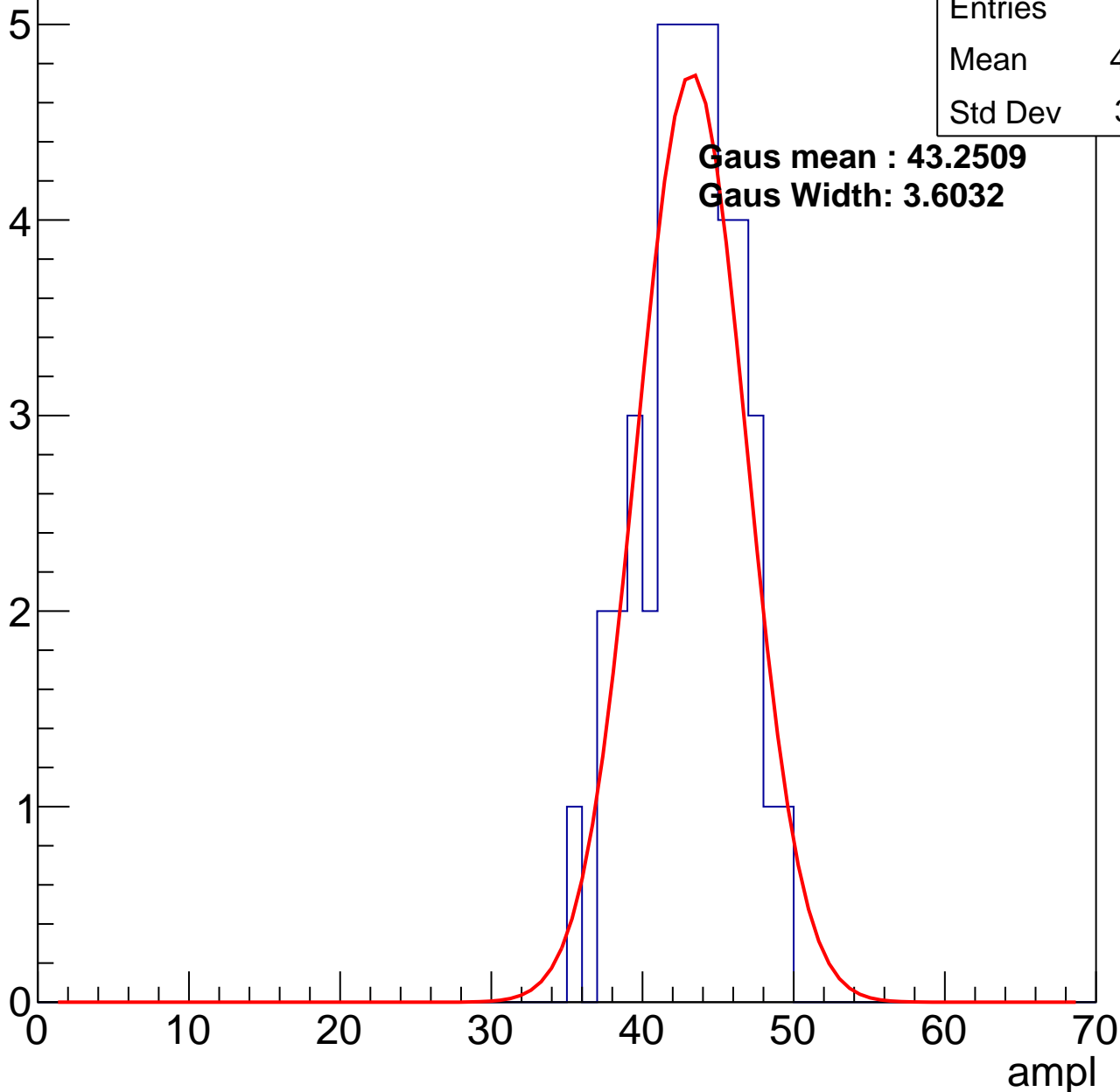
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	42.65
Std Dev	3.191

Gaus mean : 43.2509

Gaus Width: 3.6032



B1L102S, U2-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

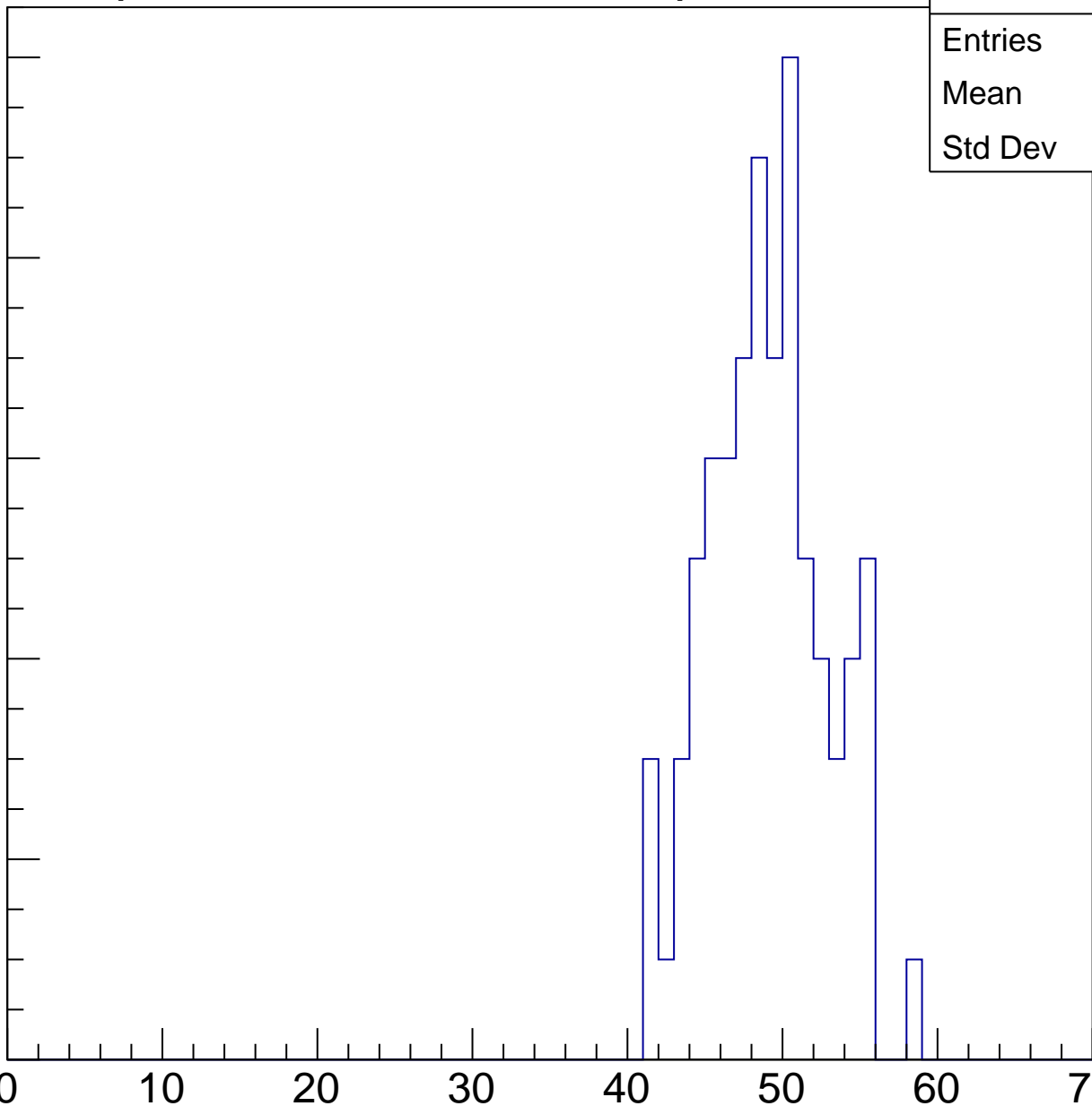
Entries	79
Mean	48.54
Std Dev	3.765

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

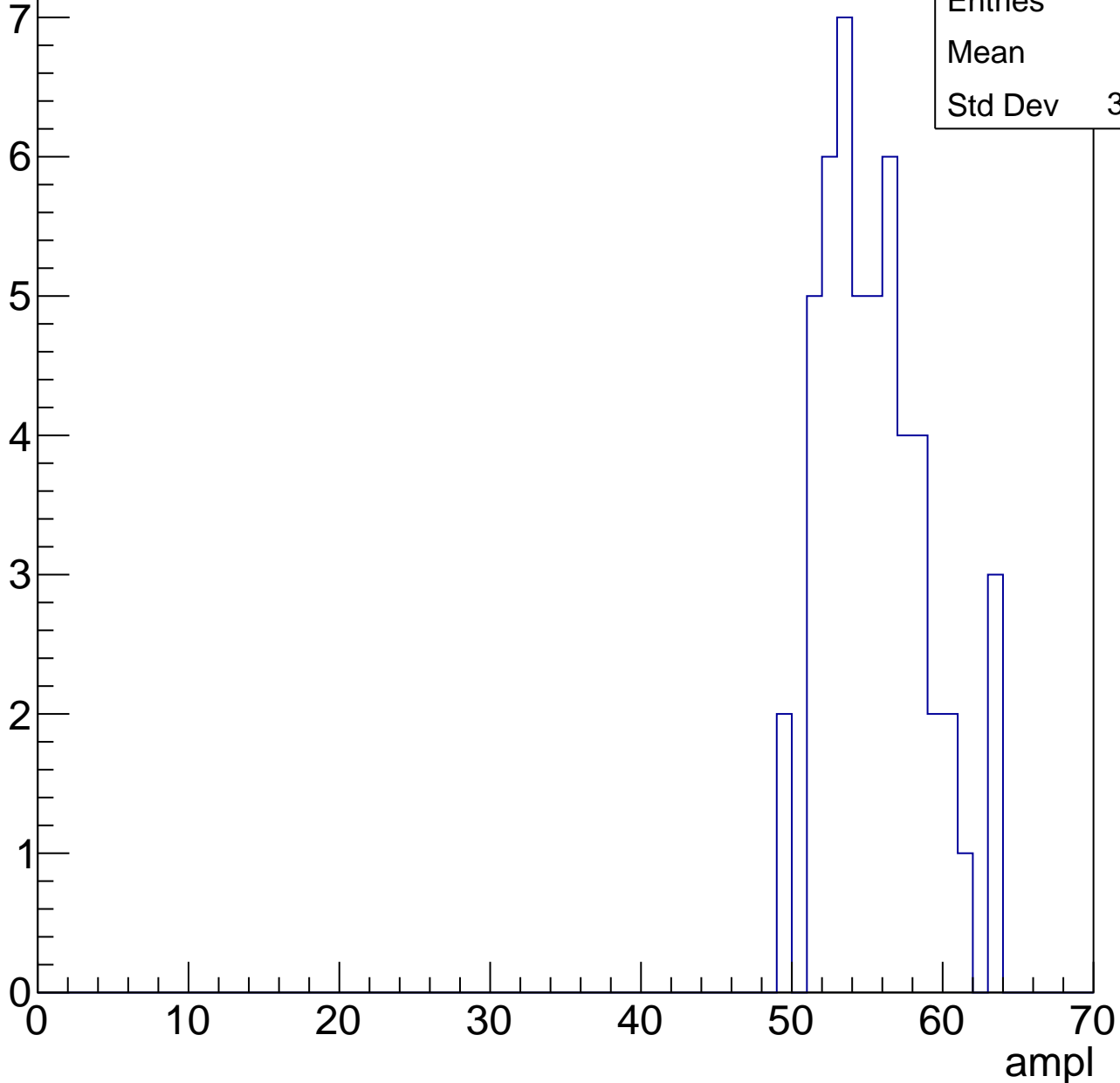


B1L102S, U2-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	55.1
Std Dev	3.415

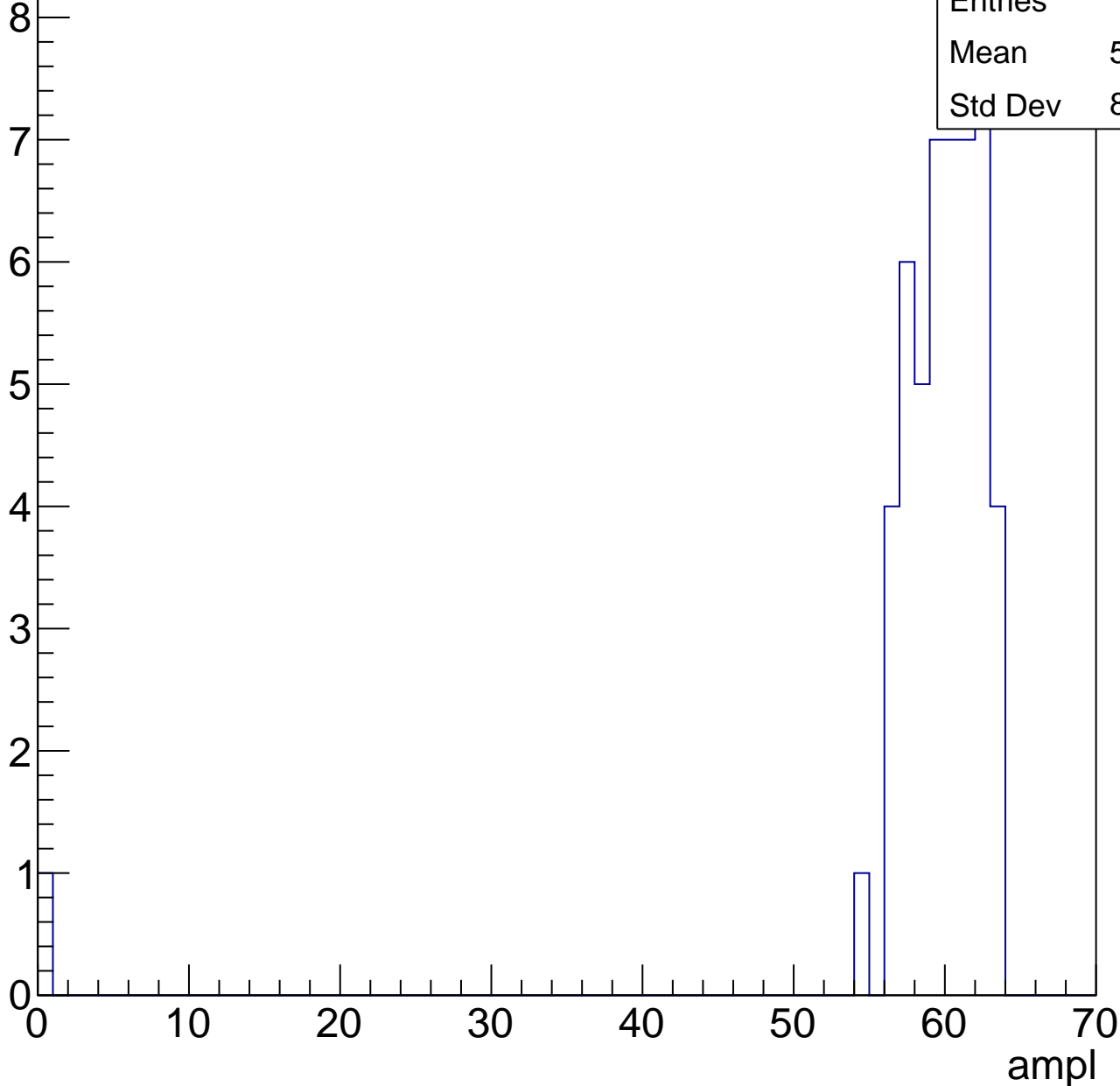


B1L102S, U2-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

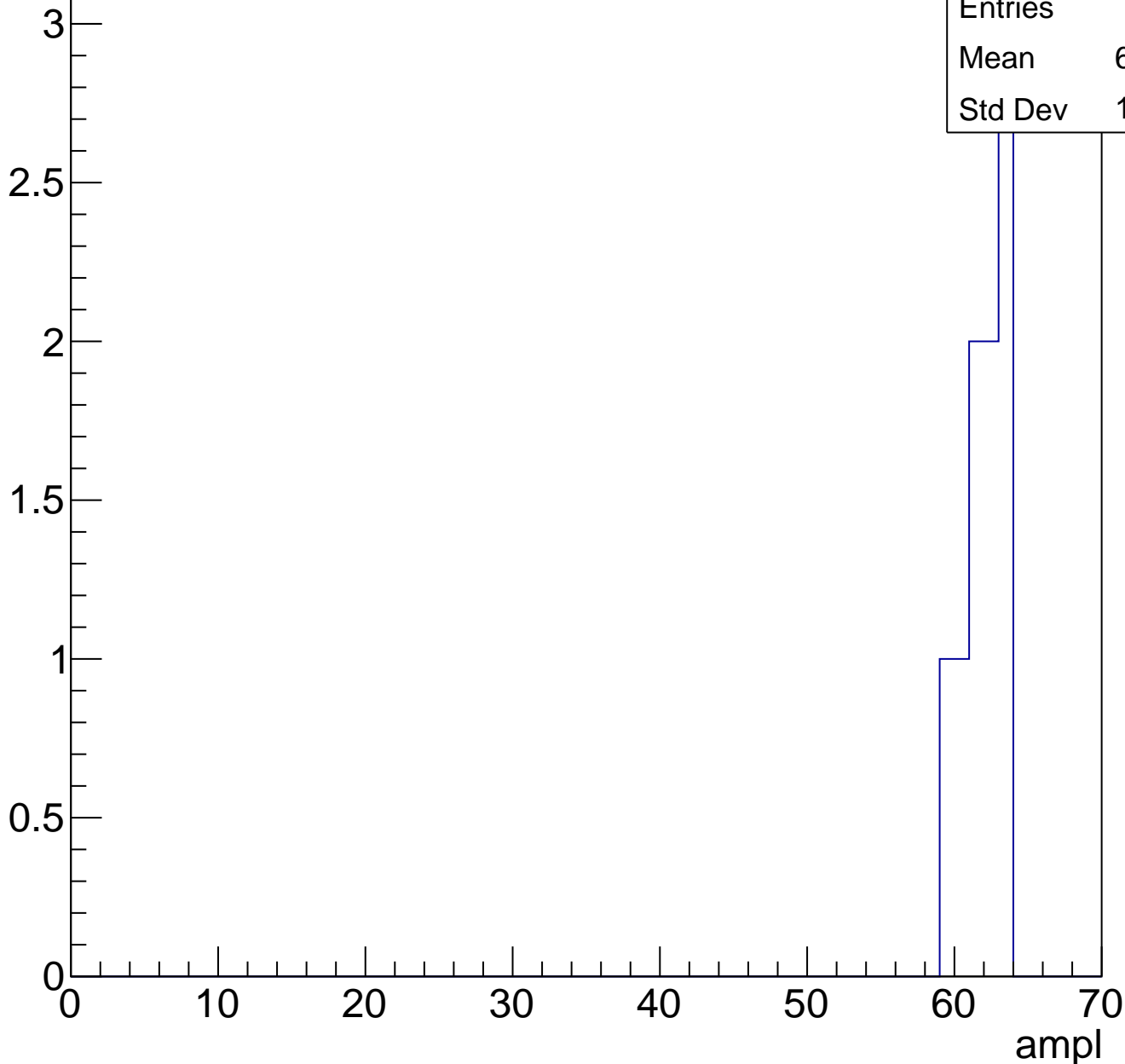
Entries	50
Mean	58.36
Std Dev	8.627



B1L102S, U2-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch61, adc0

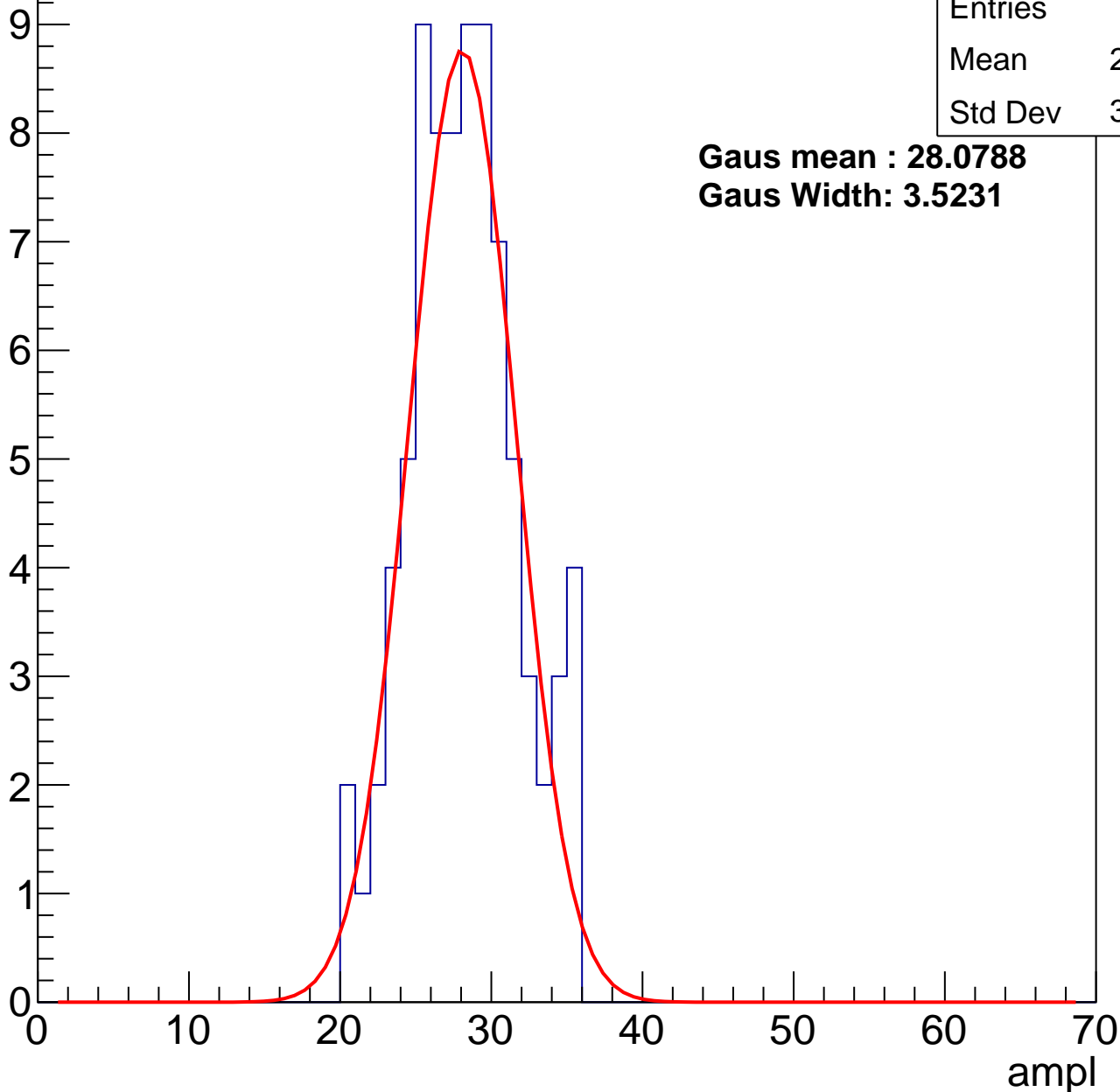
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.75
Std Dev	3.564

Gaus mean : 28.0788

Gaus Width: 3.5231



B1L102S, U2-ch61, adc1

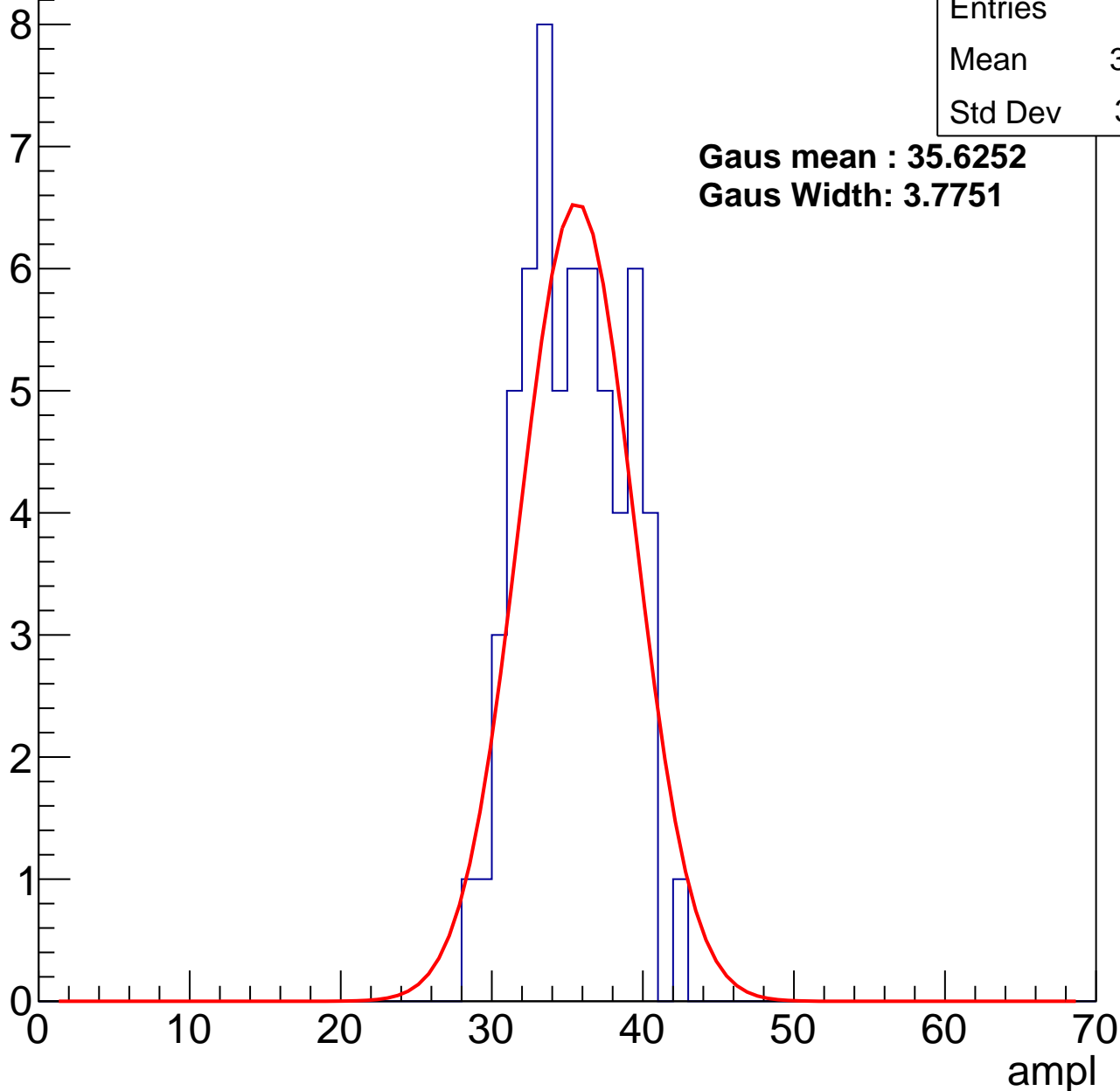
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.87
Std Dev	3.231

Gaus mean : 35.6252

Gaus Width: 3.7751



B1L102S, U2-ch61, adc2

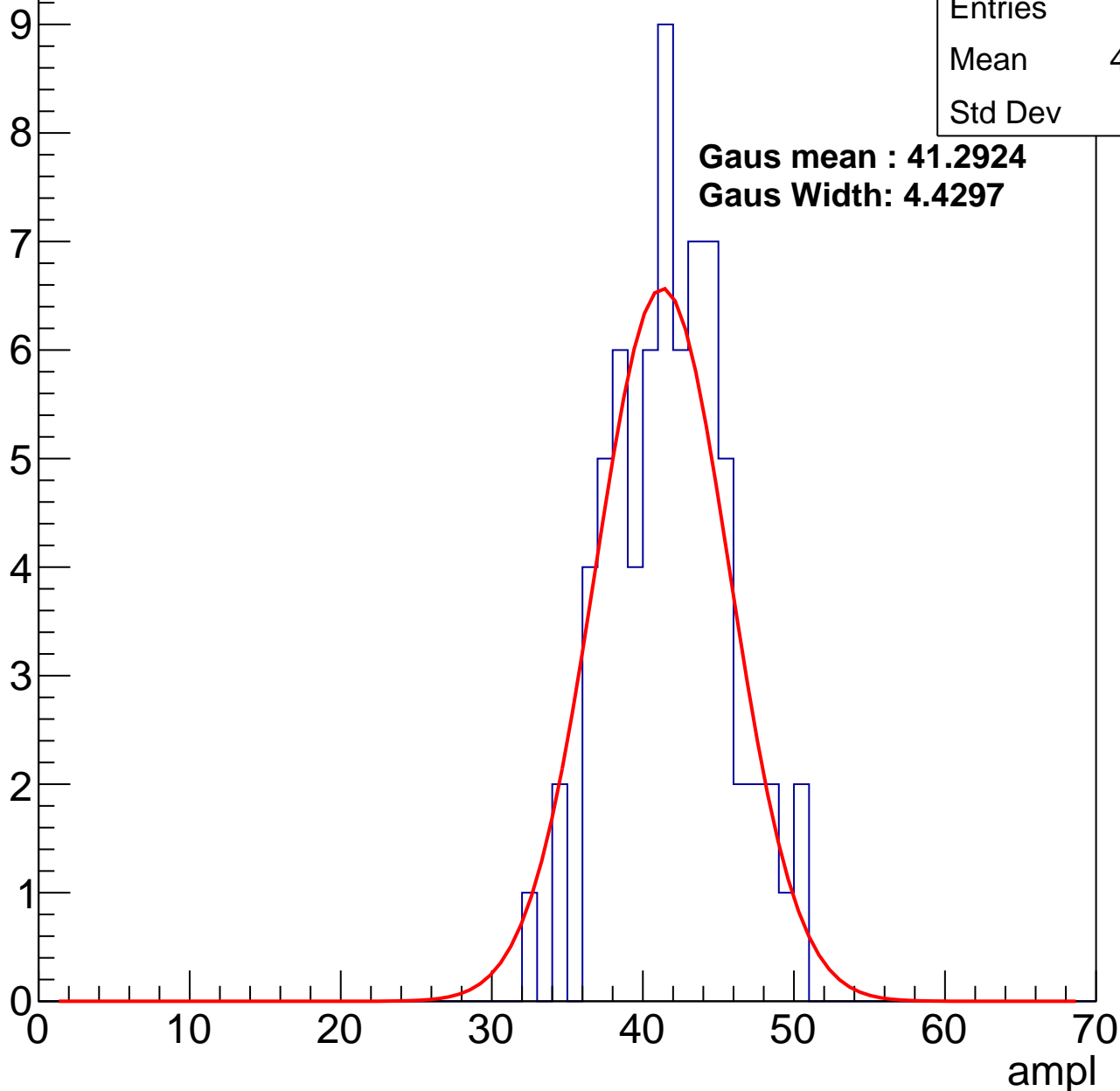
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.39
Std Dev	3.84

Gaus mean : 41.2924

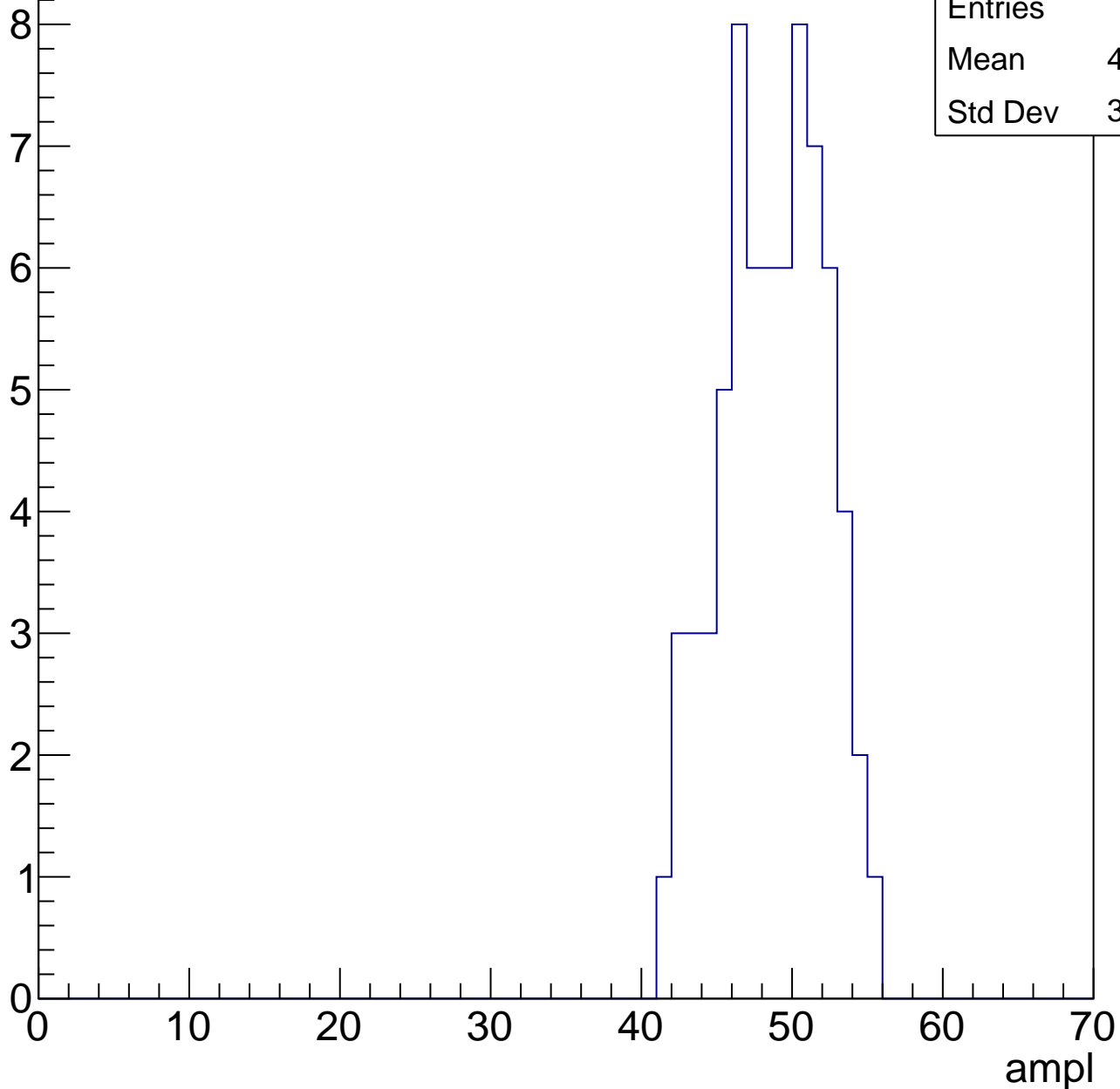
Gaus Width: 4.4297



B1L102S, U2-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

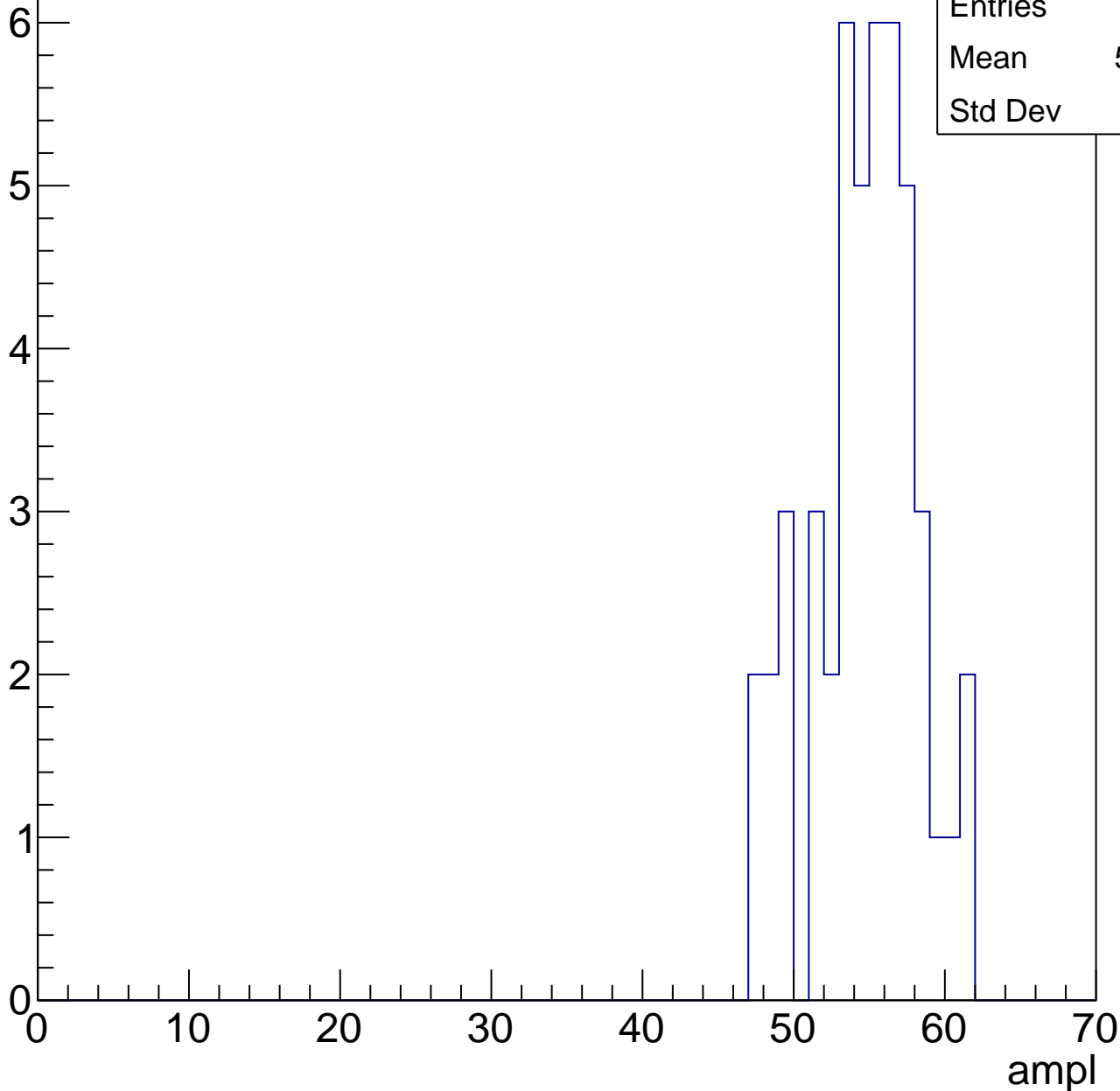


B1L102S, U2-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	54.21
Std Dev	3.47



B1L102S, U2-ch61, adc5

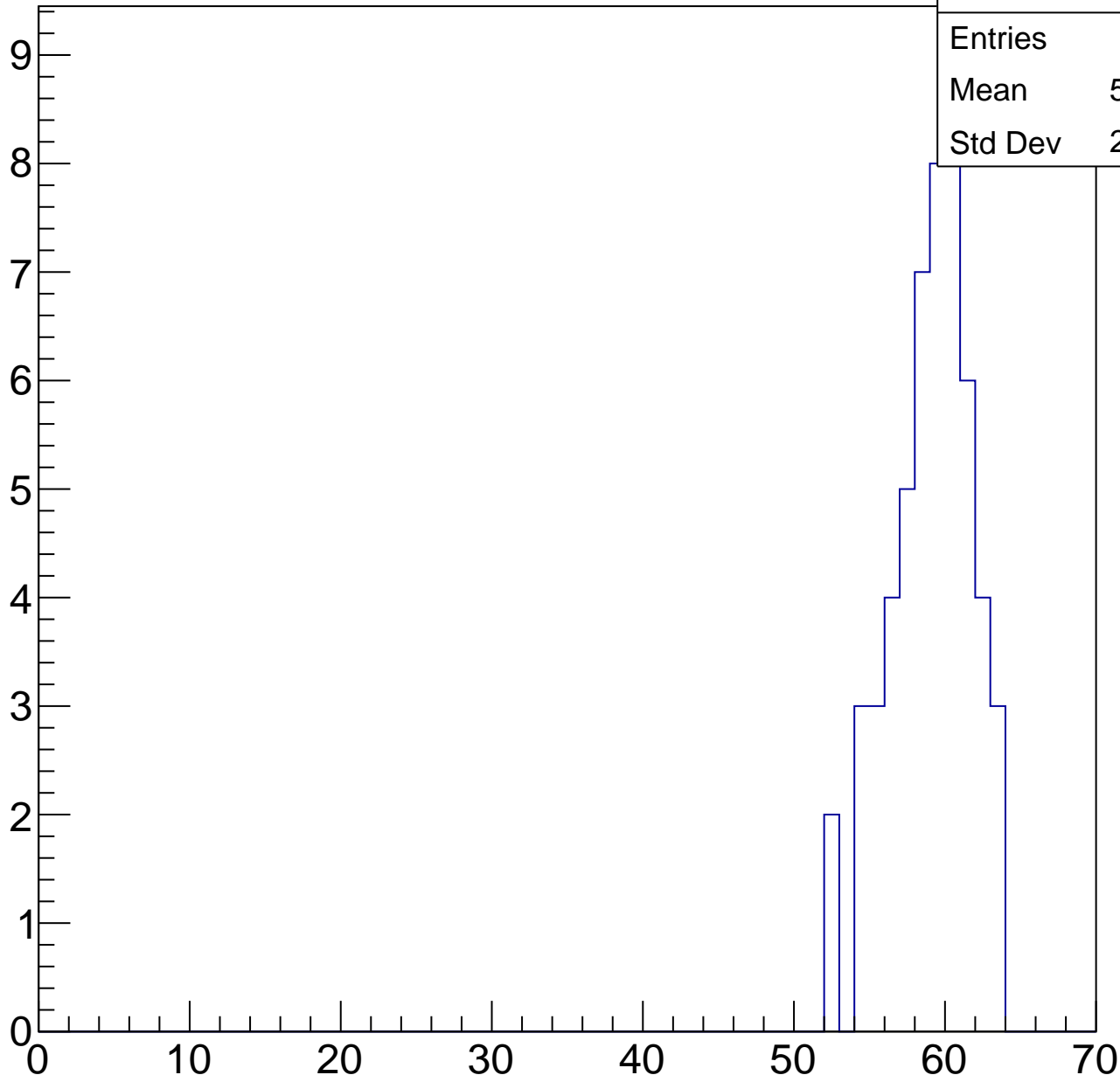
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.54
Std Dev	2.685

ampl

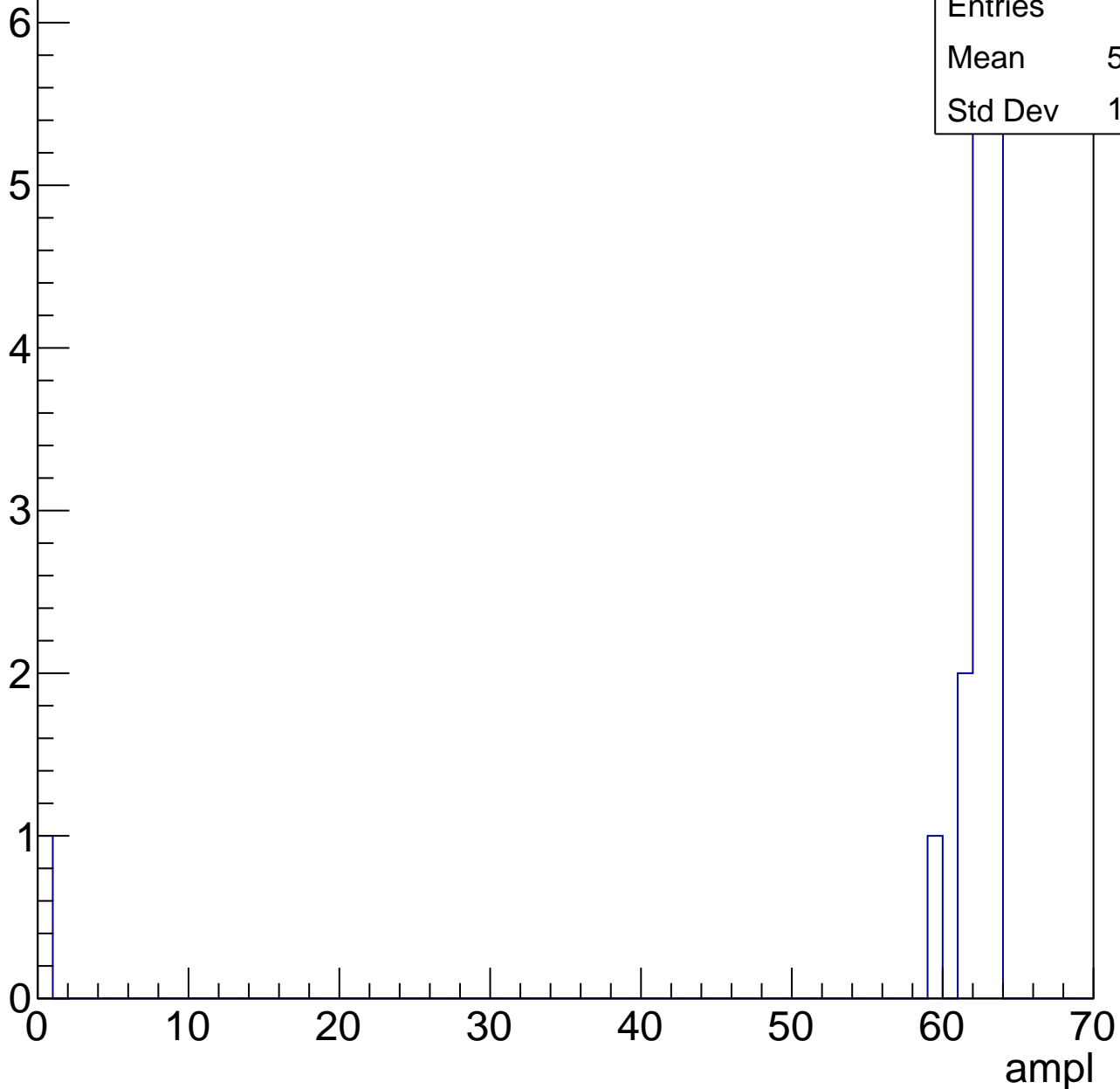


B1L102S, U2-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	58.19
Std Dev	15.06



B1L102S, U2-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch62, adc0

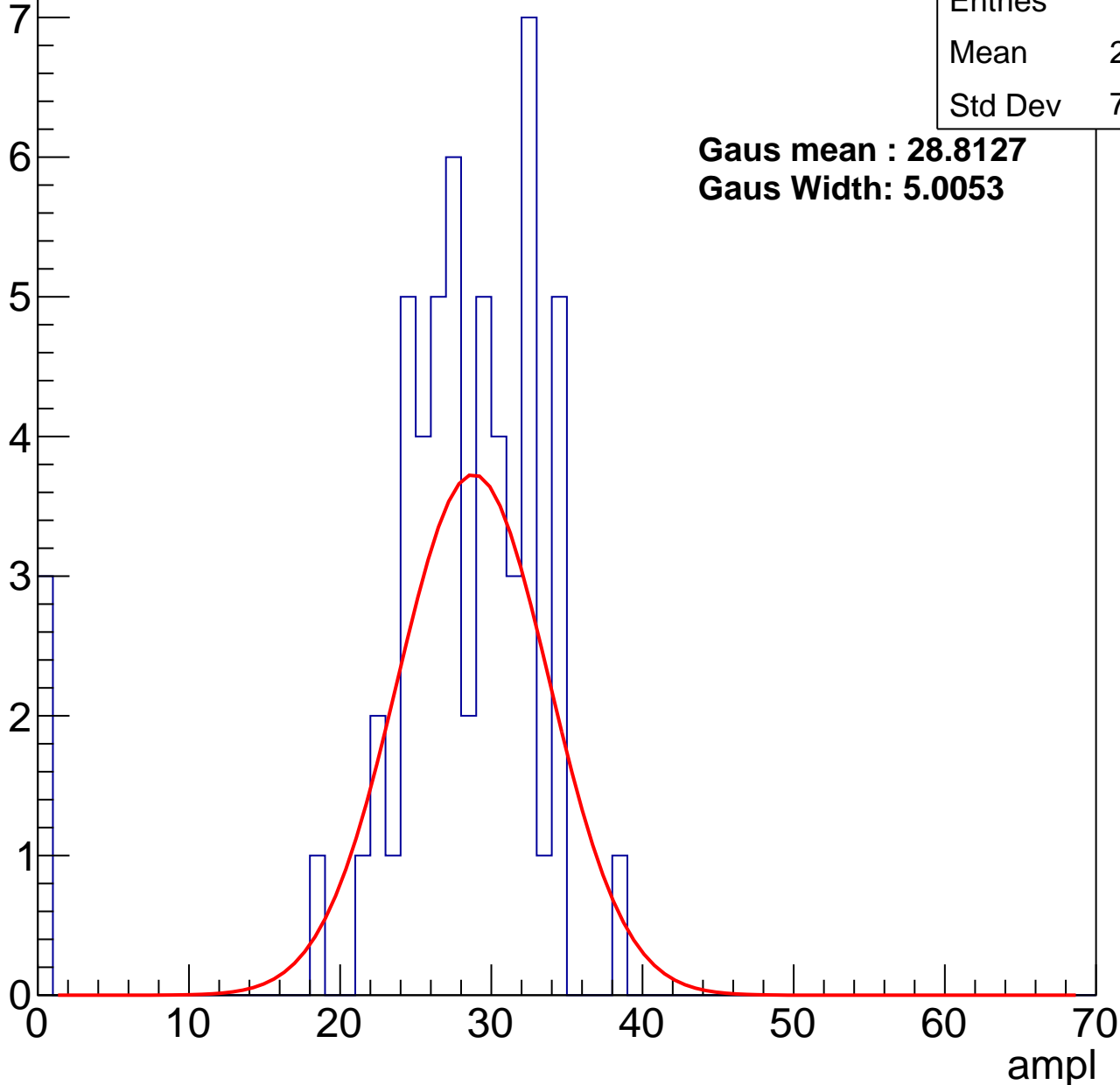
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	26.73
Std Dev	7.458

Gaus mean : 28.8127

Gaus Width: 5.0053



B1L102S, U2-ch62, adc1

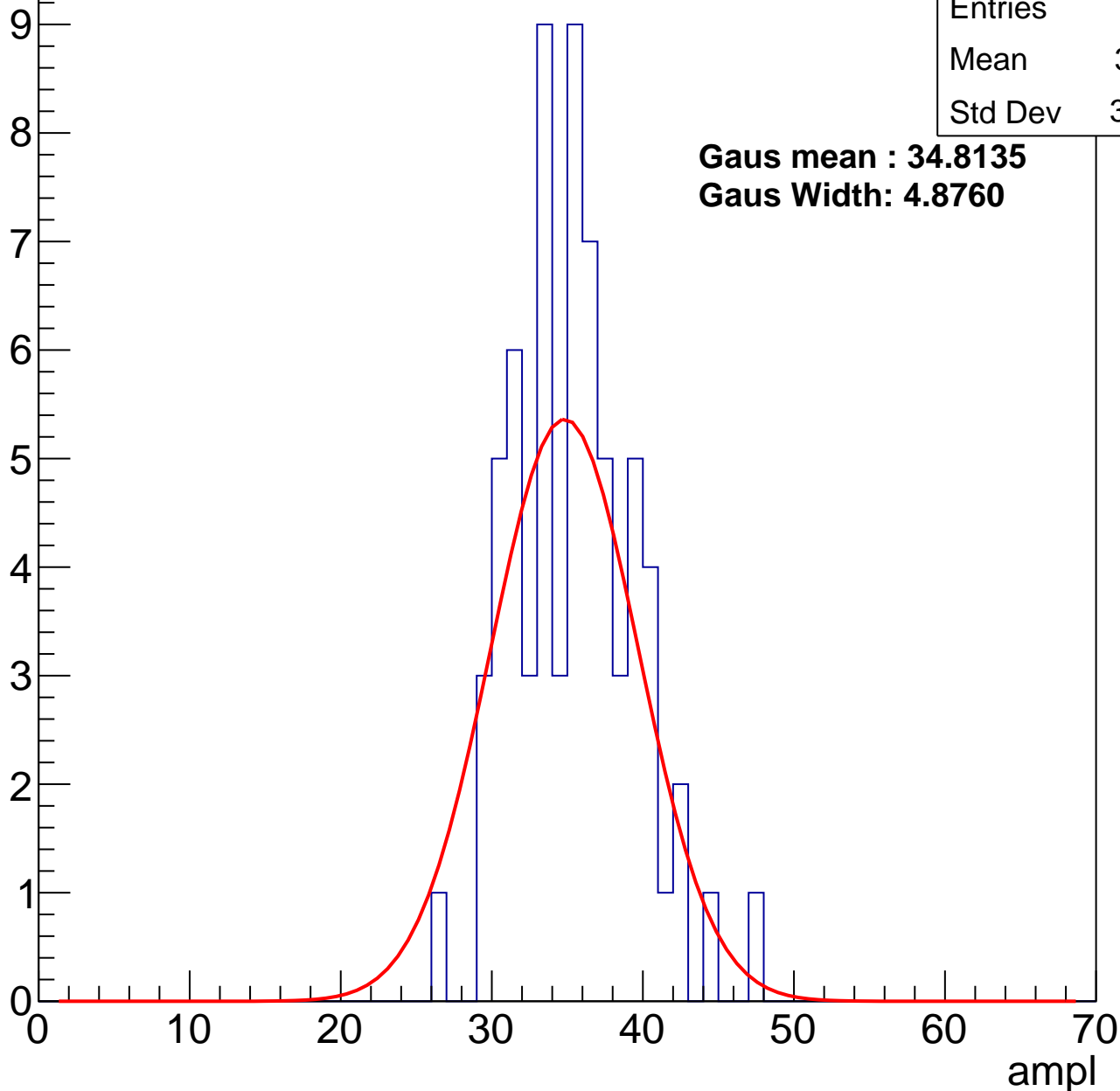
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.01
Std Dev	3.965

Gaus mean : 34.8135

Gaus Width: 4.8760



B1L102S, U2-ch62, adc2

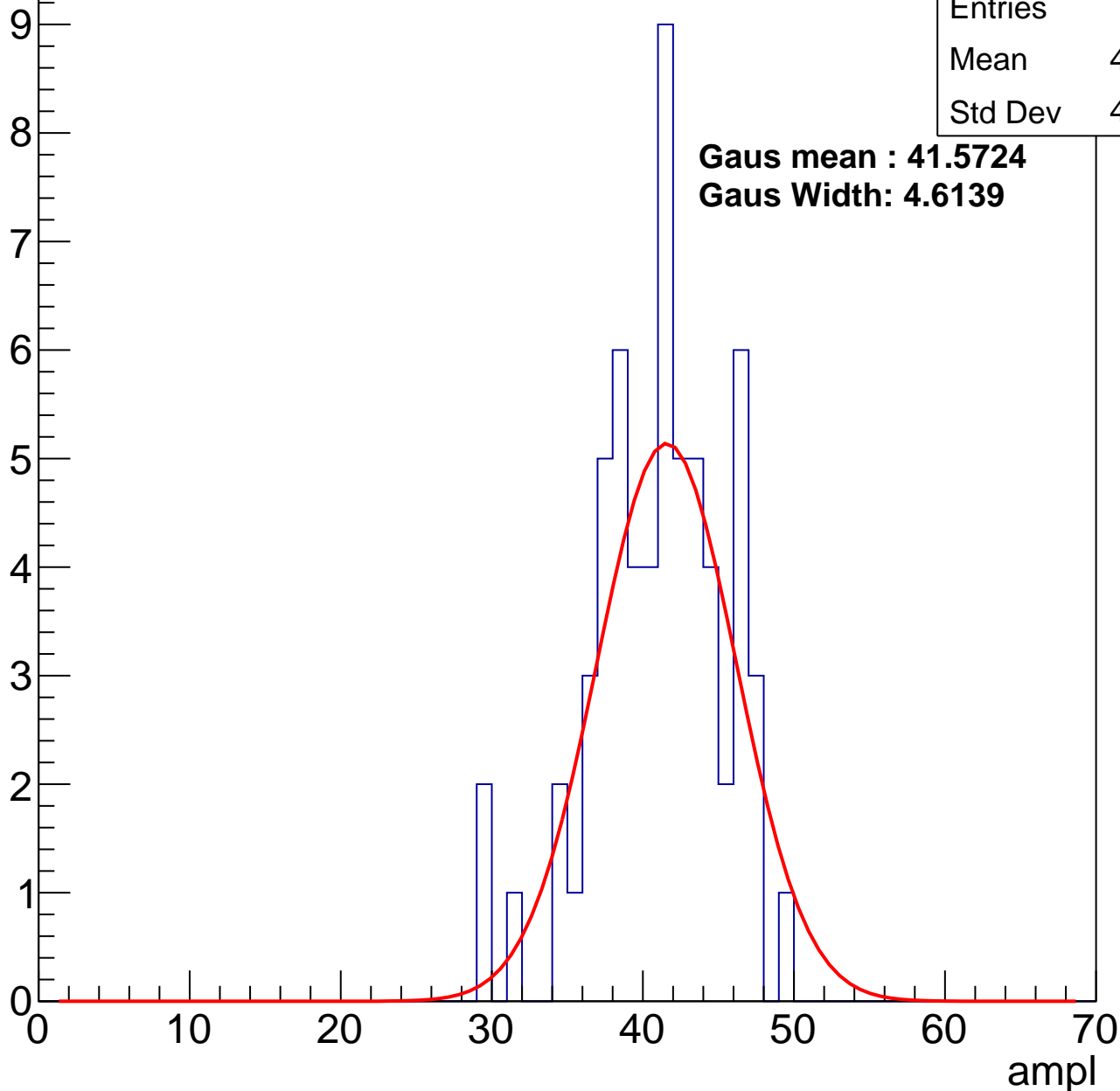
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	40.56
Std Dev	4.279

Gaus mean : 41.5724

Gaus Width: 4.6139

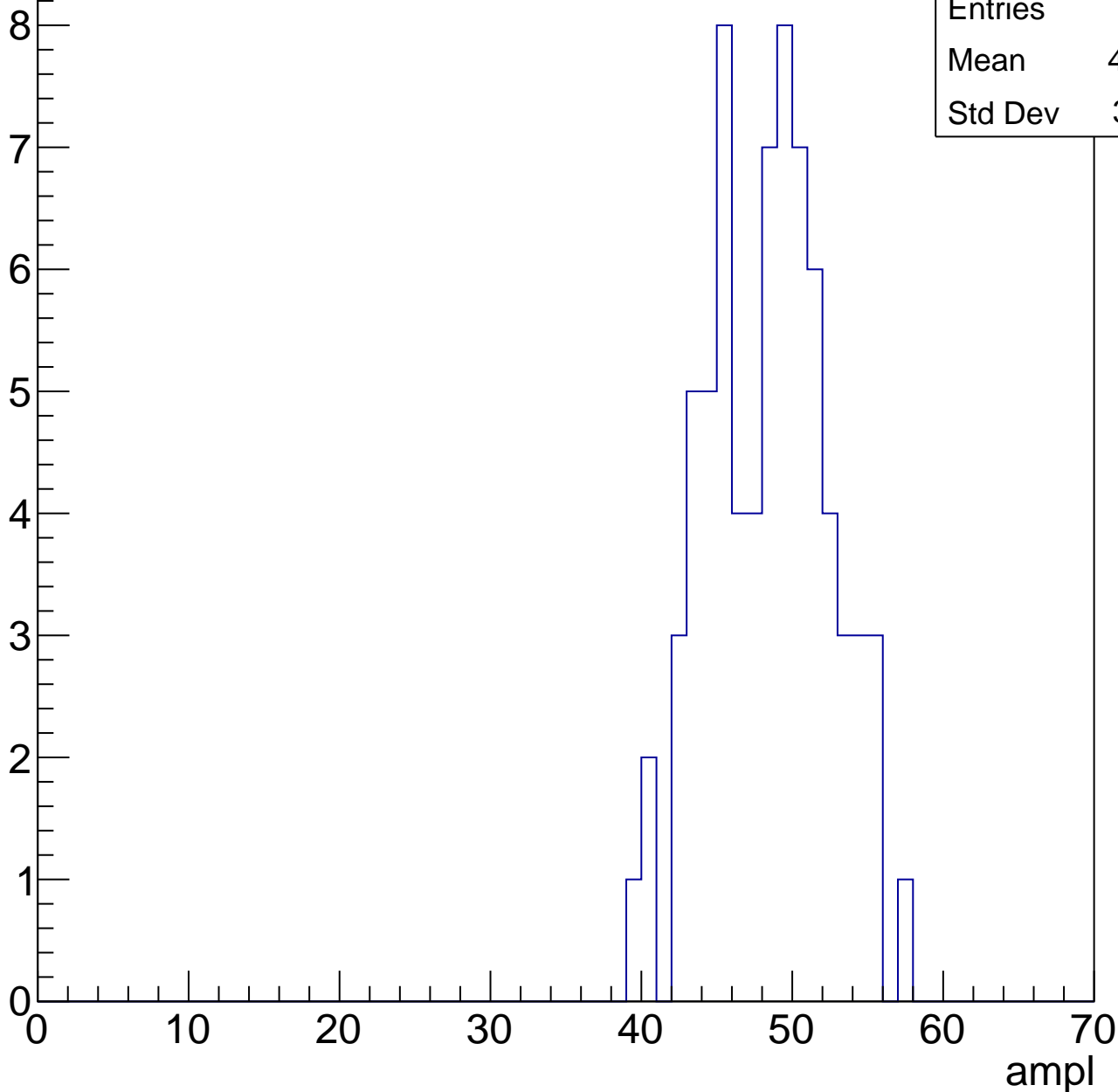


B1L102S, U2-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.93
Std Dev	3.991

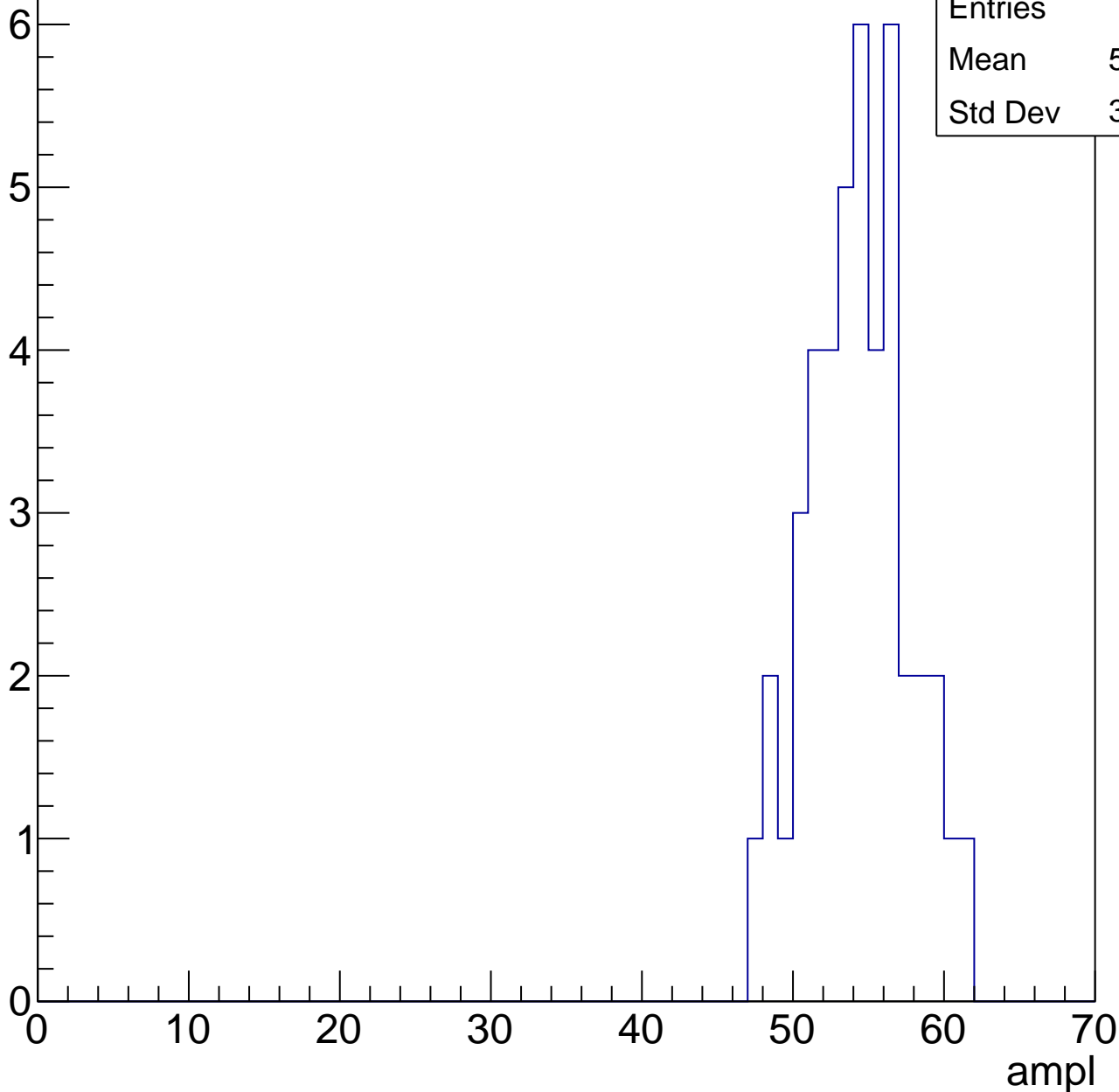


B1L102S, U2-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

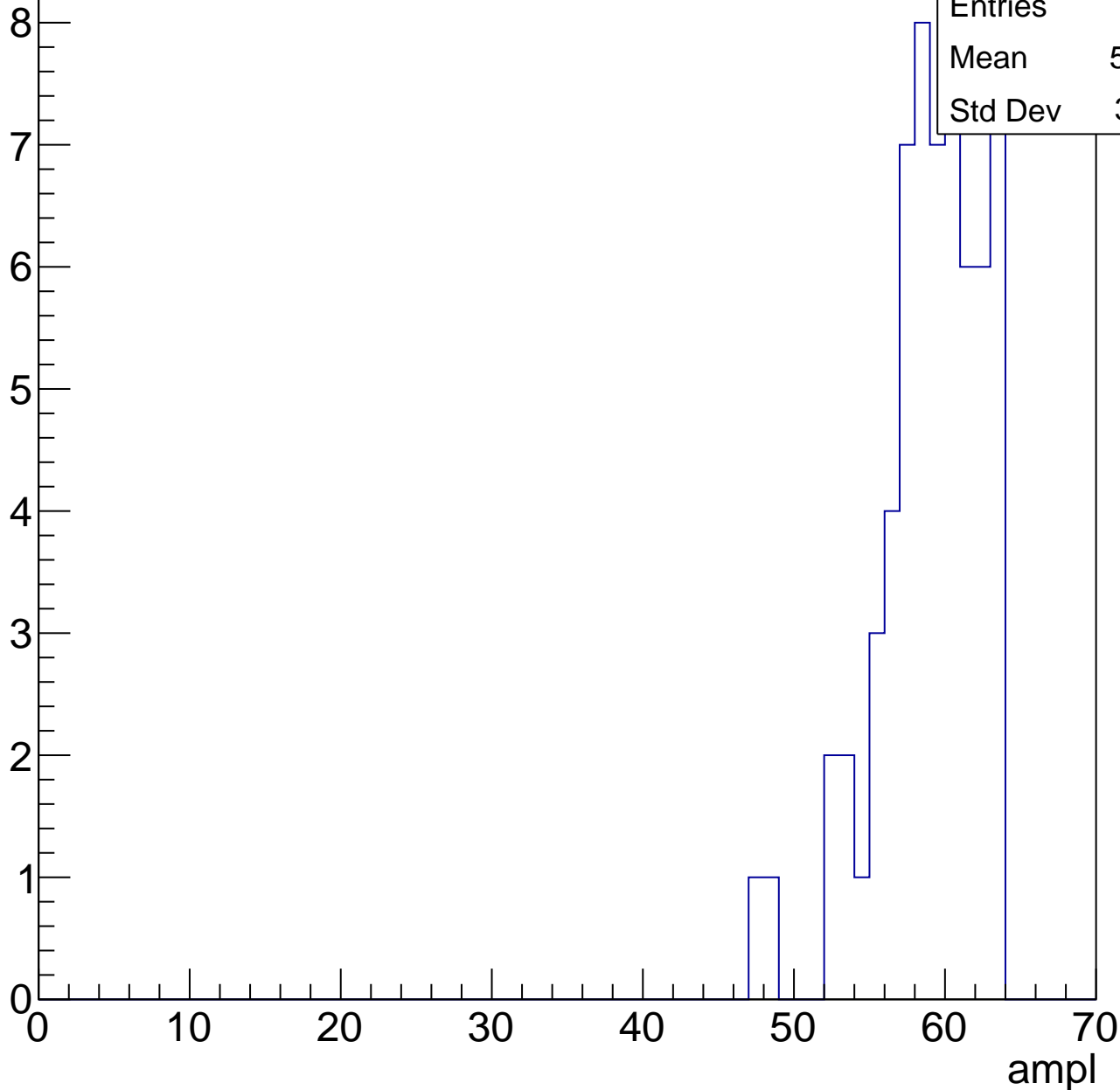
Entries	44
Mean	53.82
Std Dev	3.242



B1L102S, U2-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	64
Mean	58.53
Std Dev	3.491

B1L102S, U2-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

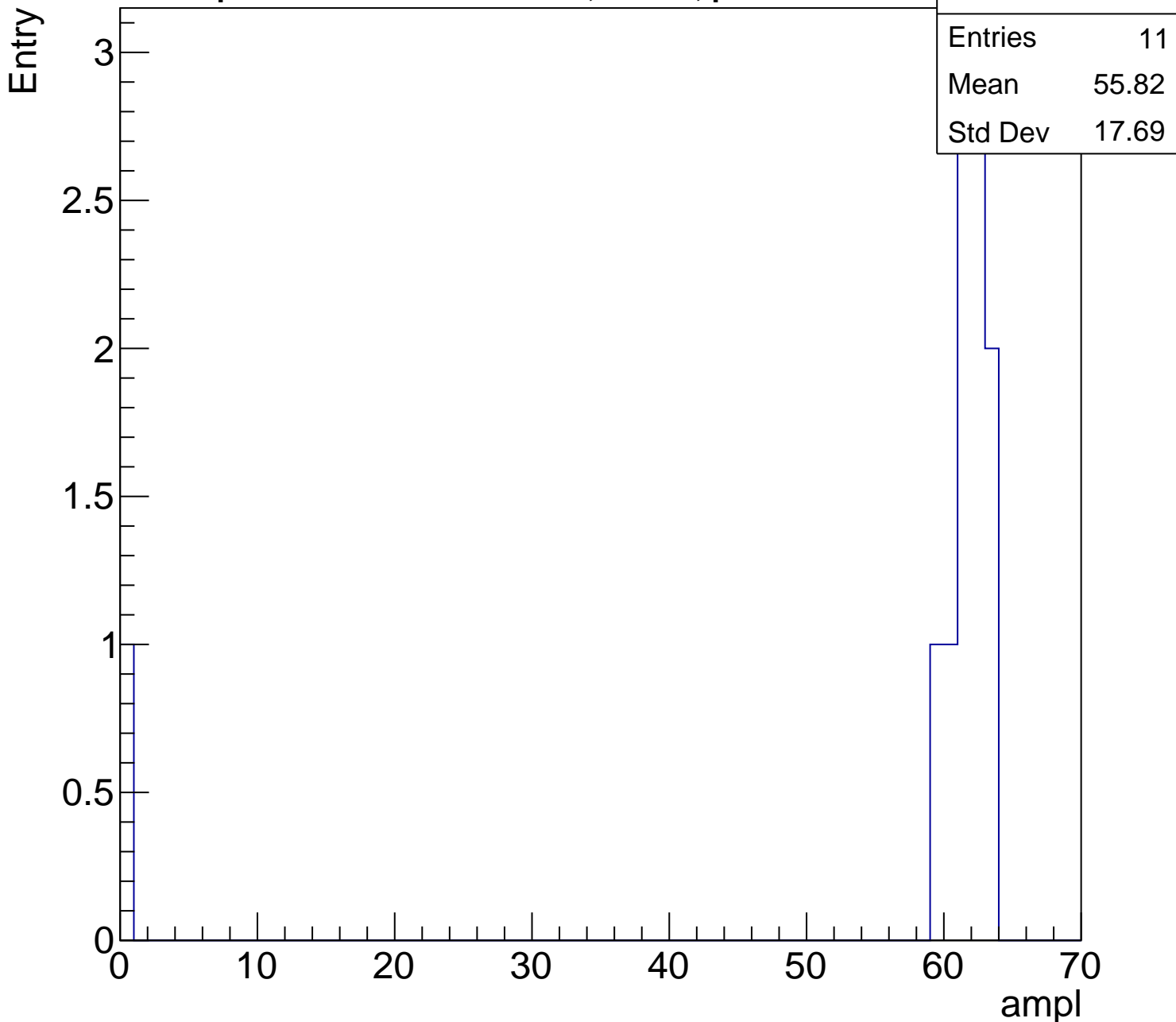
Mean

55.82

Std Dev

17.69

ampl



B1L102S, U2-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch63, adc0

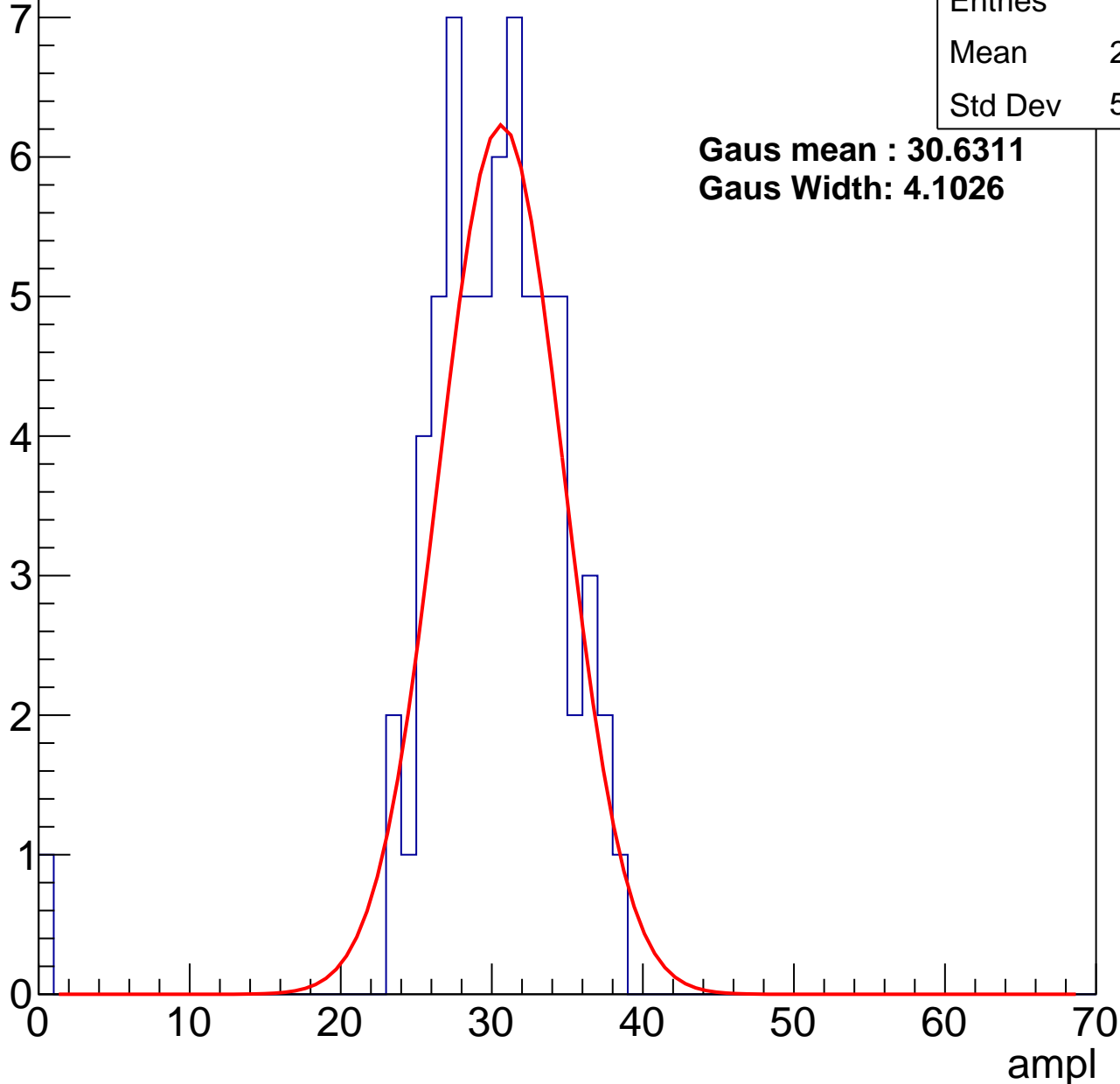
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.64
Std Dev	5.172

Gaus mean : 30.6311

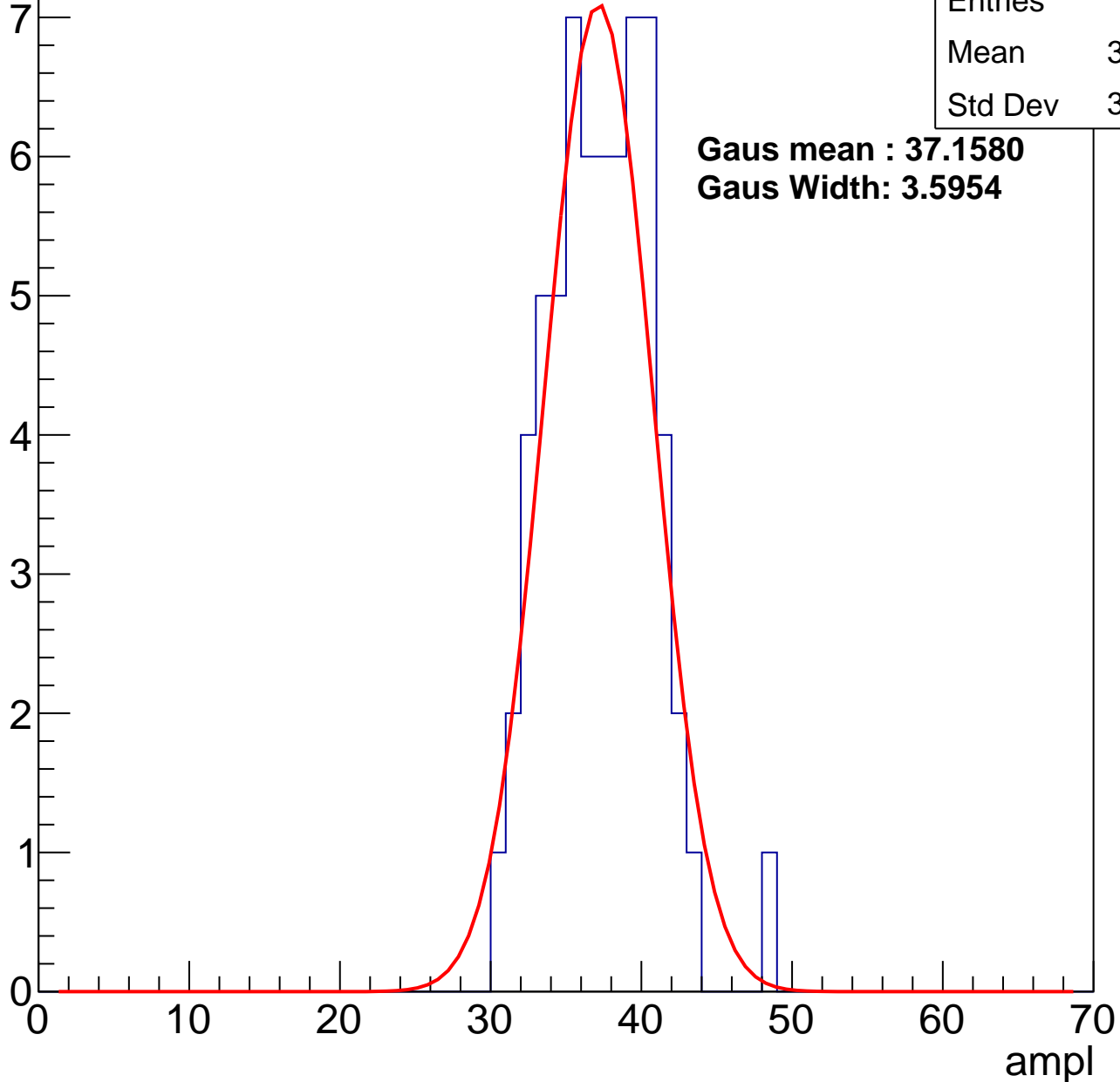
Gaus Width: 4.1026



B1L102S, U2-ch63, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch63, adc2

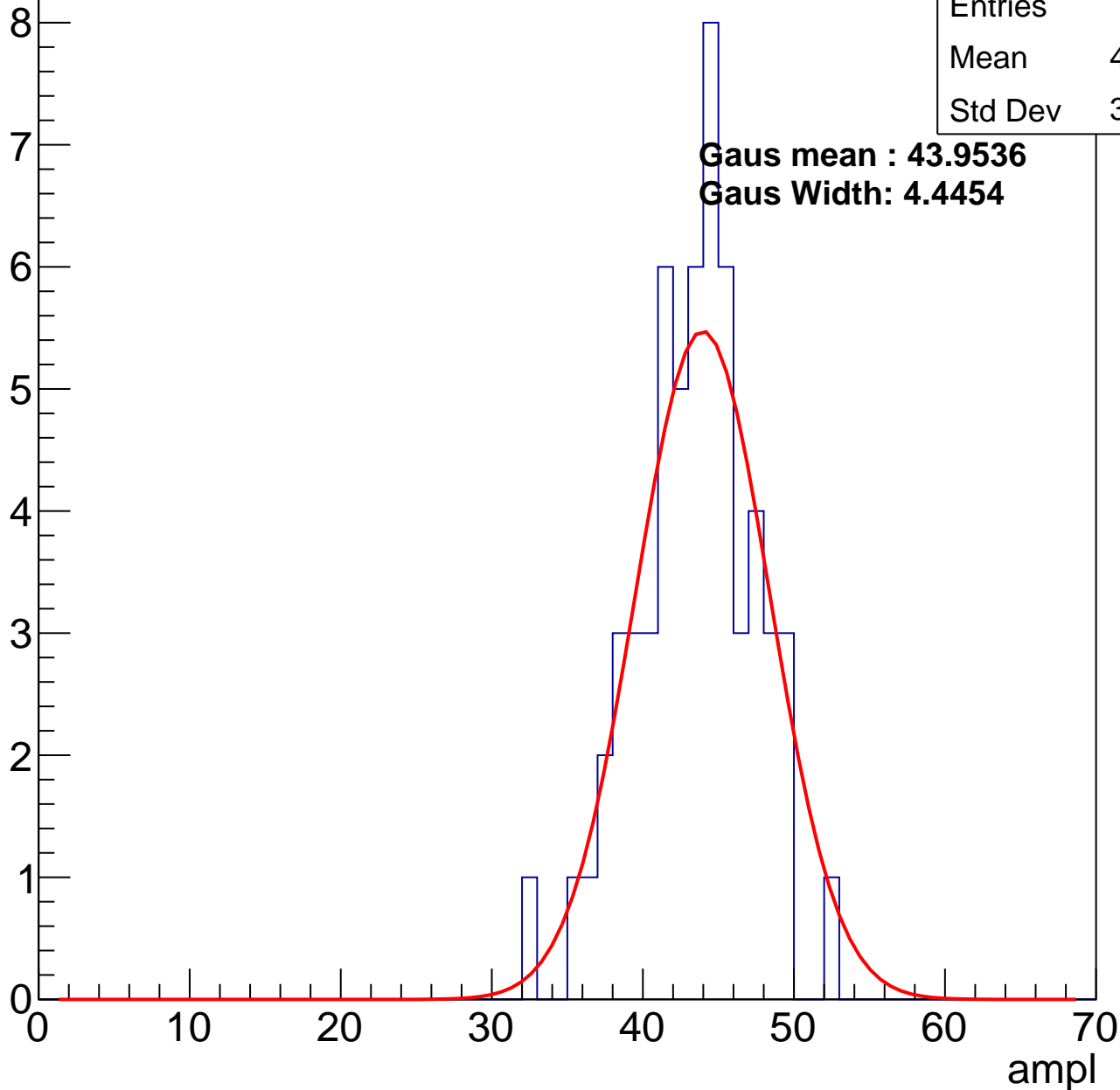
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.93
Std Dev	3.844

Gaus mean : 43.9536

Gaus Width: 4.4454

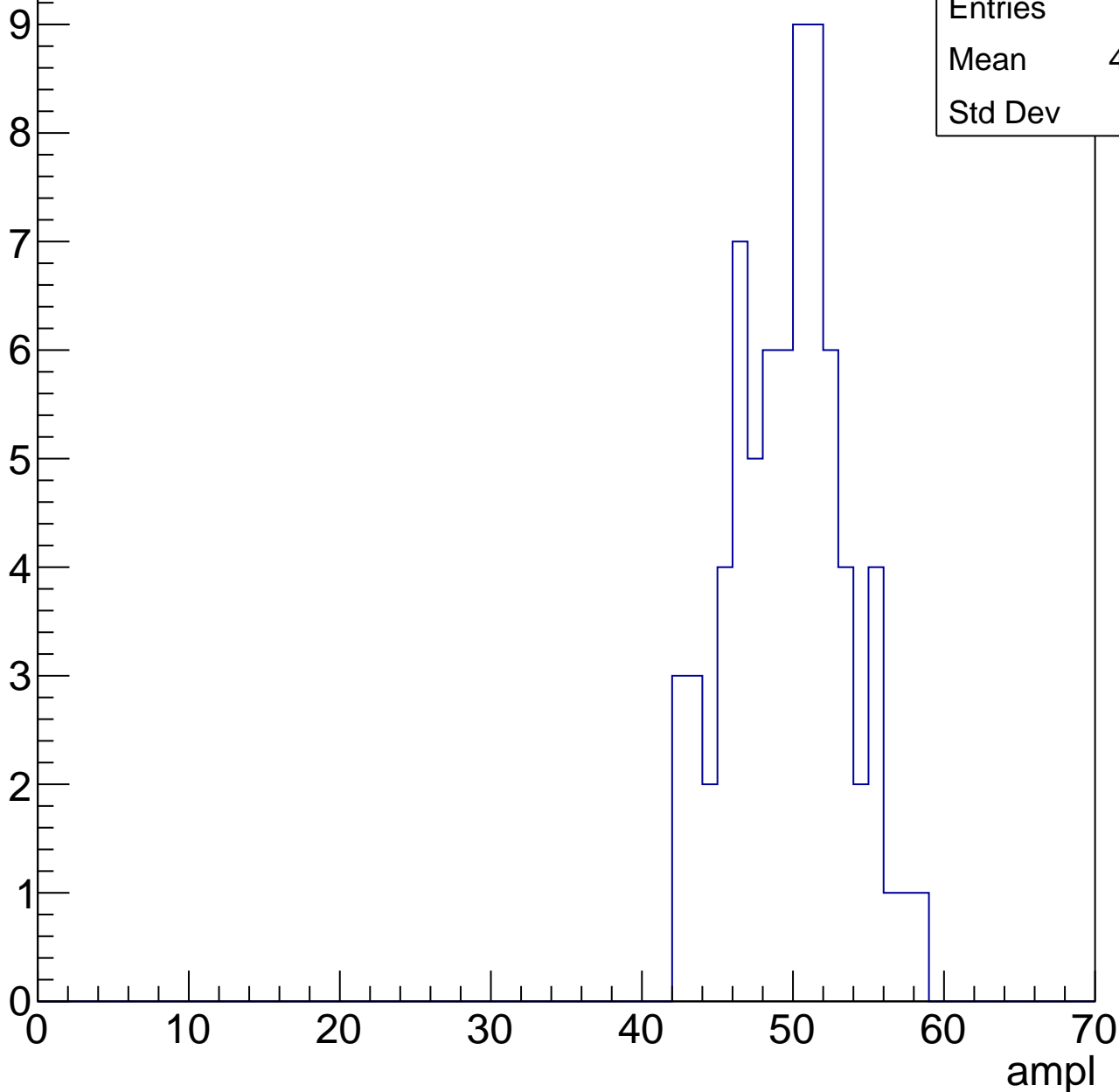


B1L102S, U2-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.23
Std Dev	3.71

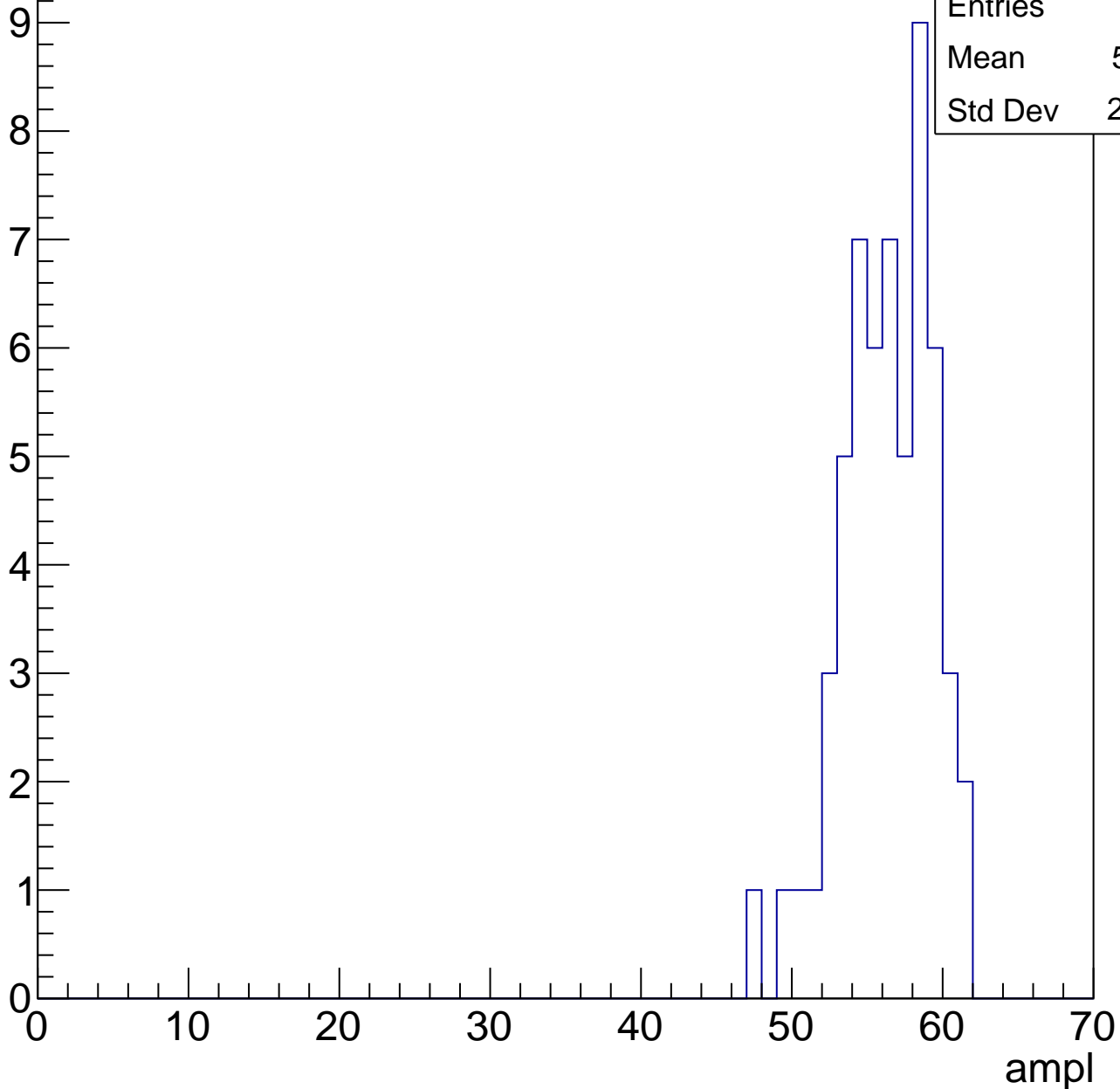


B1L102S, U2-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.81
Std Dev	2.988

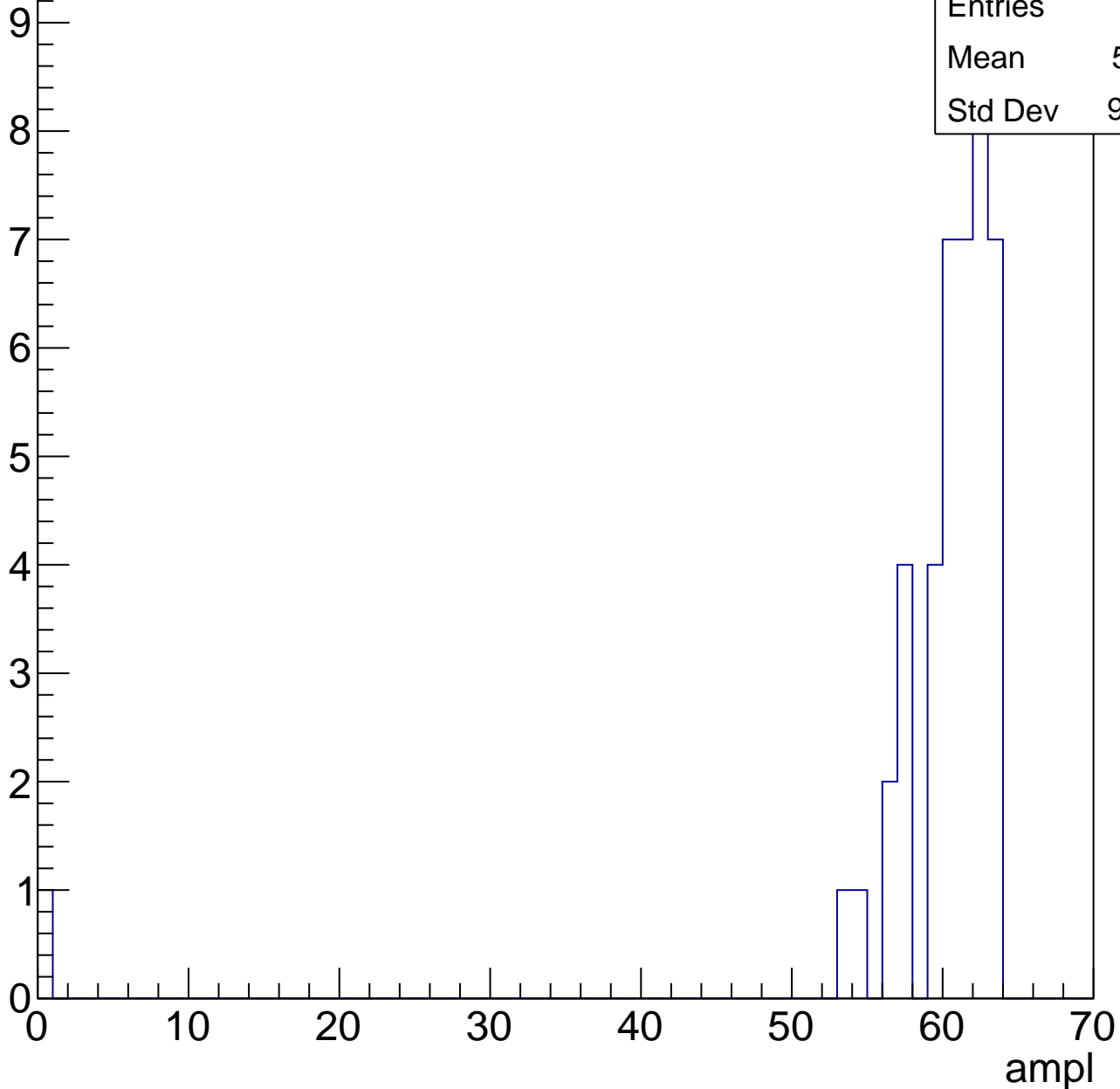


B1L102S, U2-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.81
Std Dev	9.404



B1L102S, U2-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L102S, U2-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L102S, U2-ch64, adc0

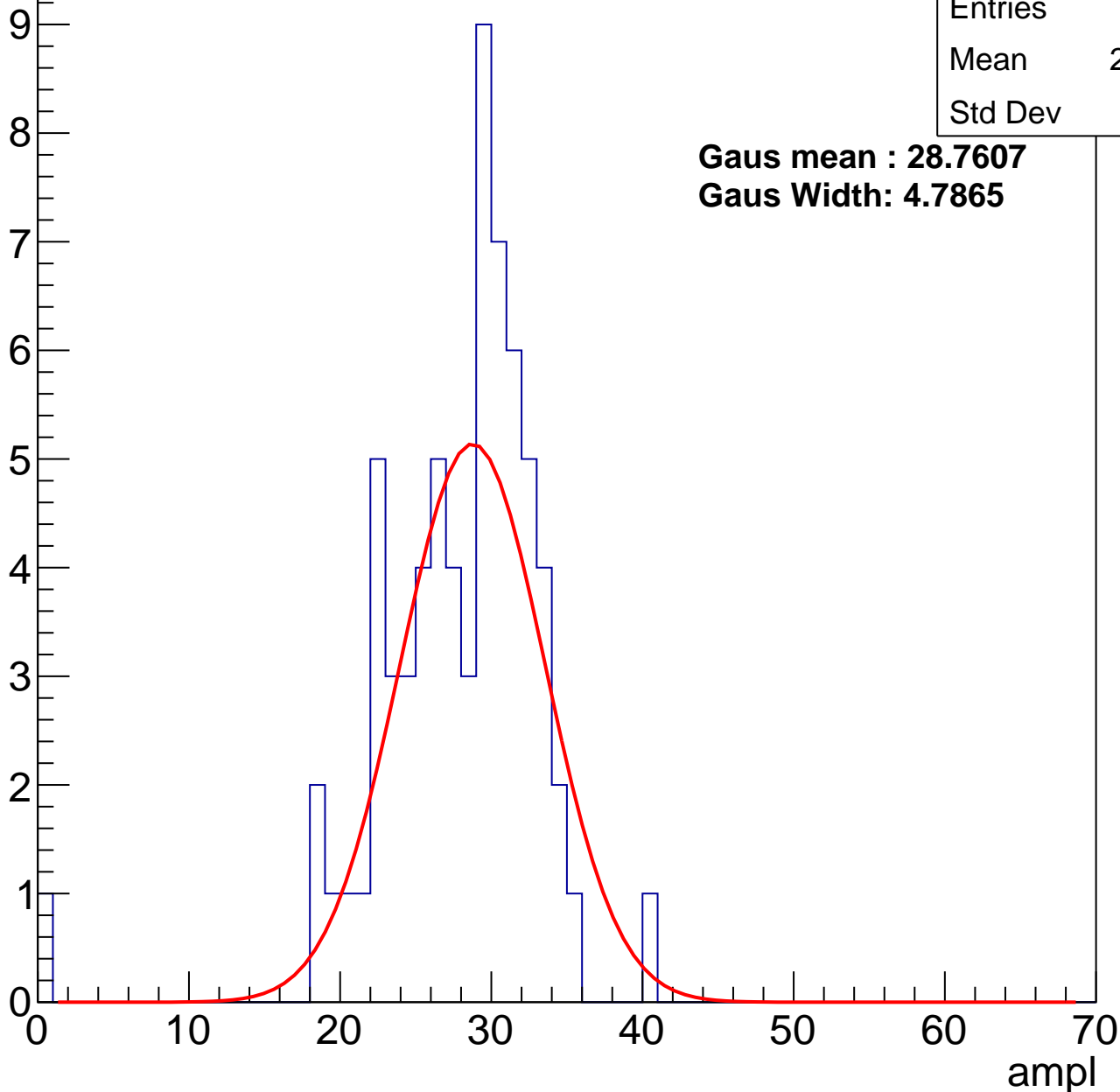
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.37
Std Dev	5.49

Gaus mean : 28.7607

Gaus Width: 4.7865



B1L102S, U2-ch64, adc1

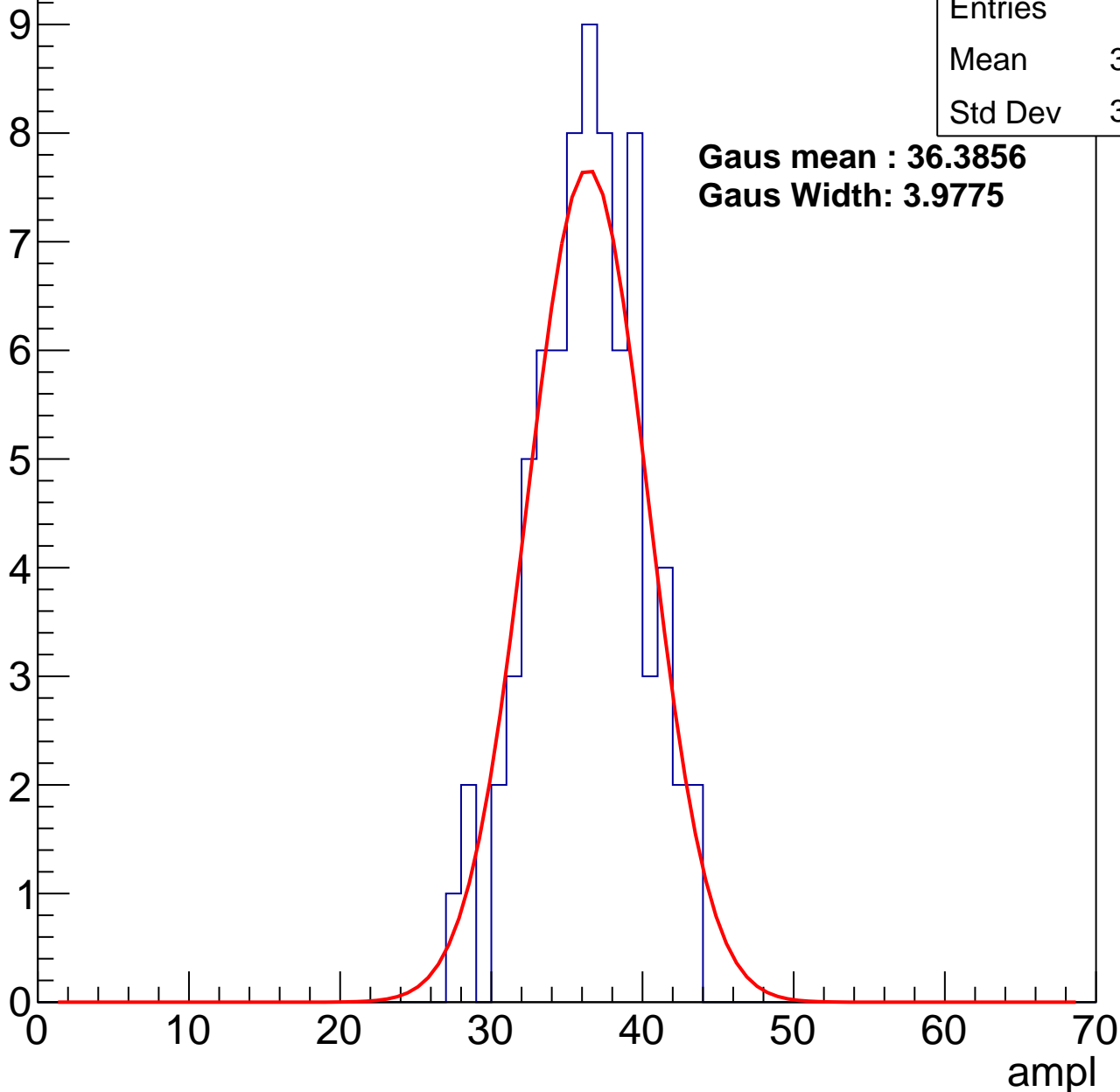
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.89
Std Dev	3.546

Gaus mean : 36.3856

Gaus Width: 3.9775



B1L102S, U2-ch64, adc2

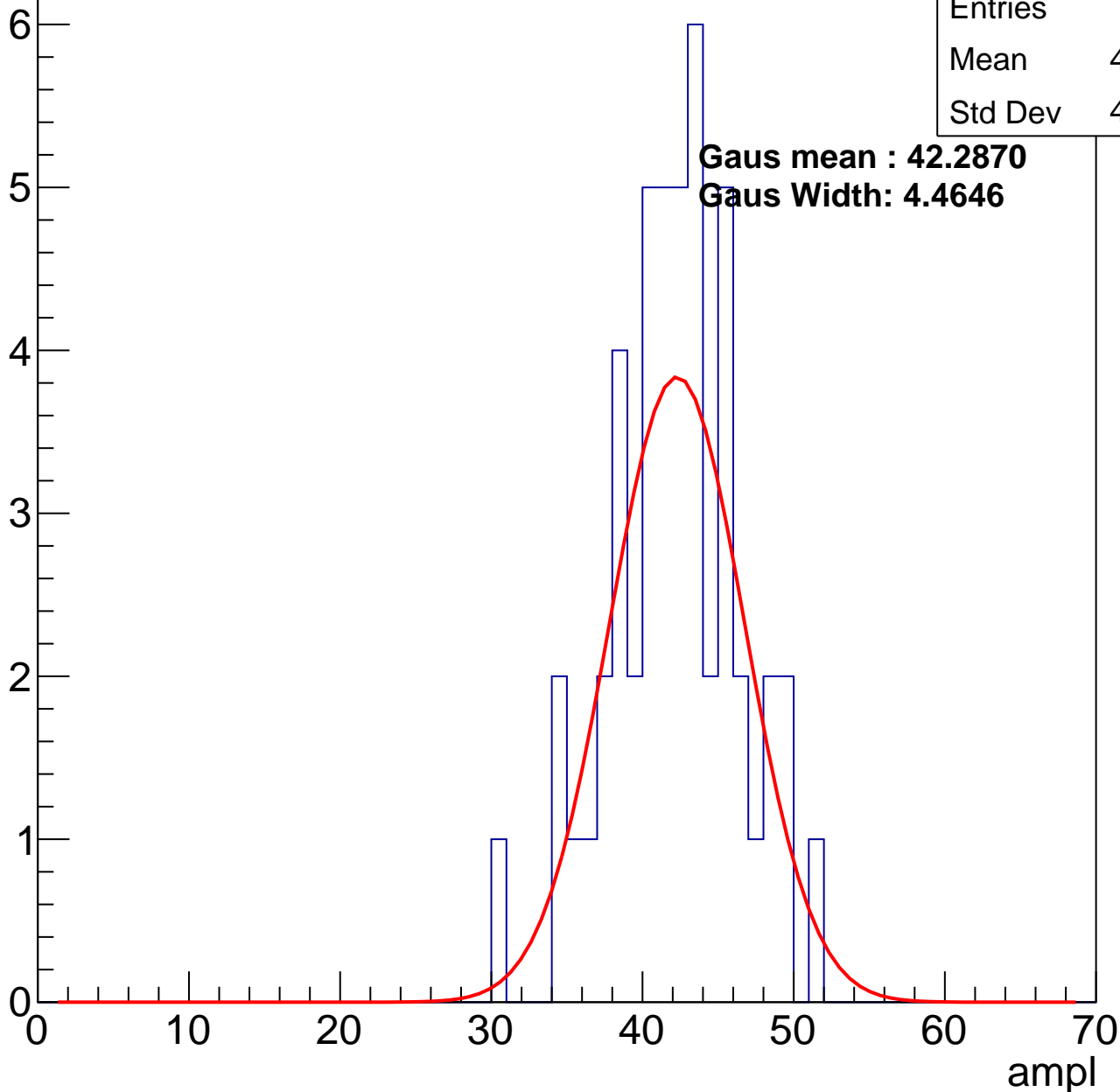
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.69
Std Dev	4.229

Gaus mean : 42.2870

Gaus Width: 4.4646

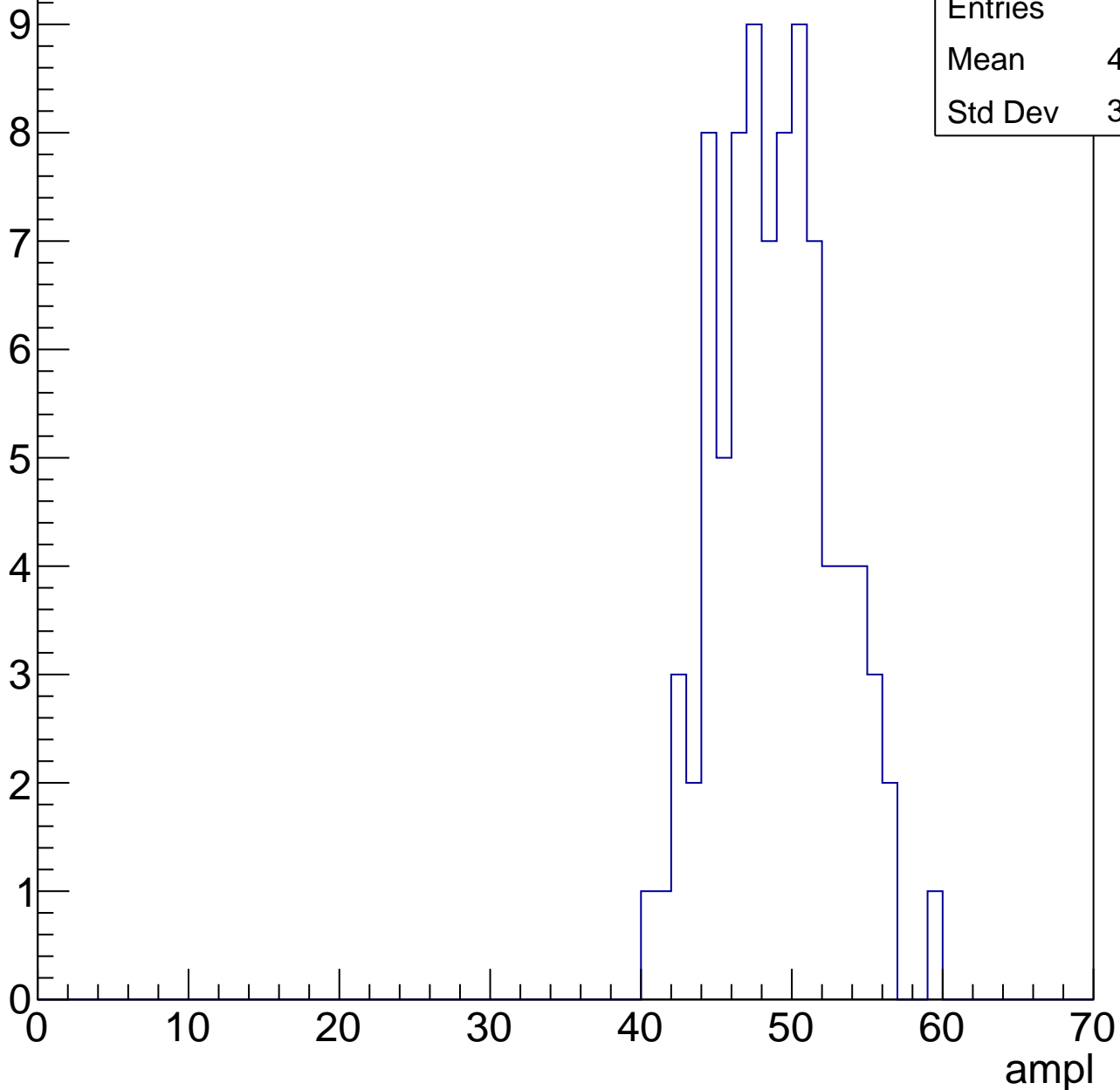


B1L102S, U2-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	48.47
Std Dev	3.854

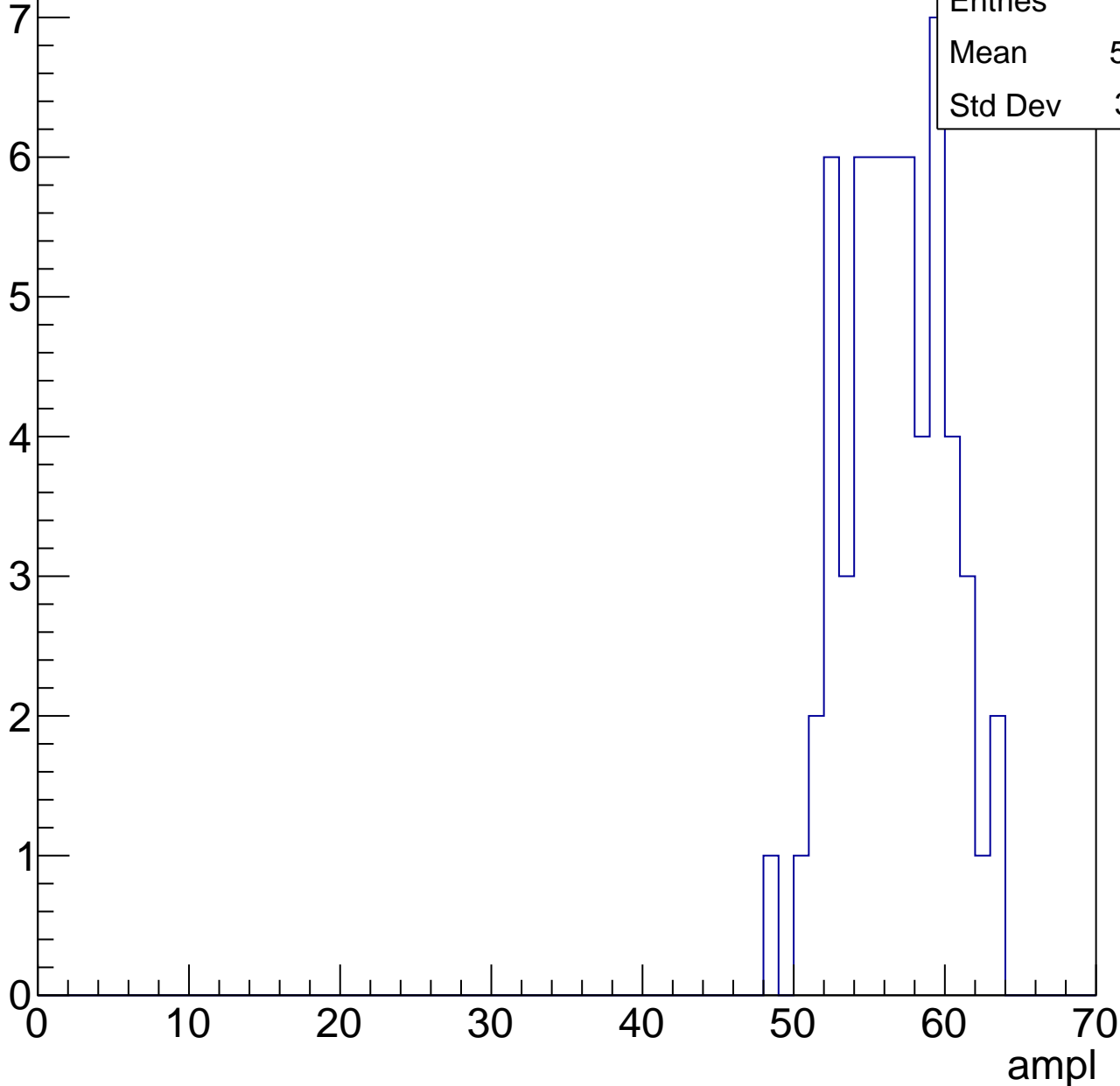


B1L102S, U2-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.19
Std Dev	3.371

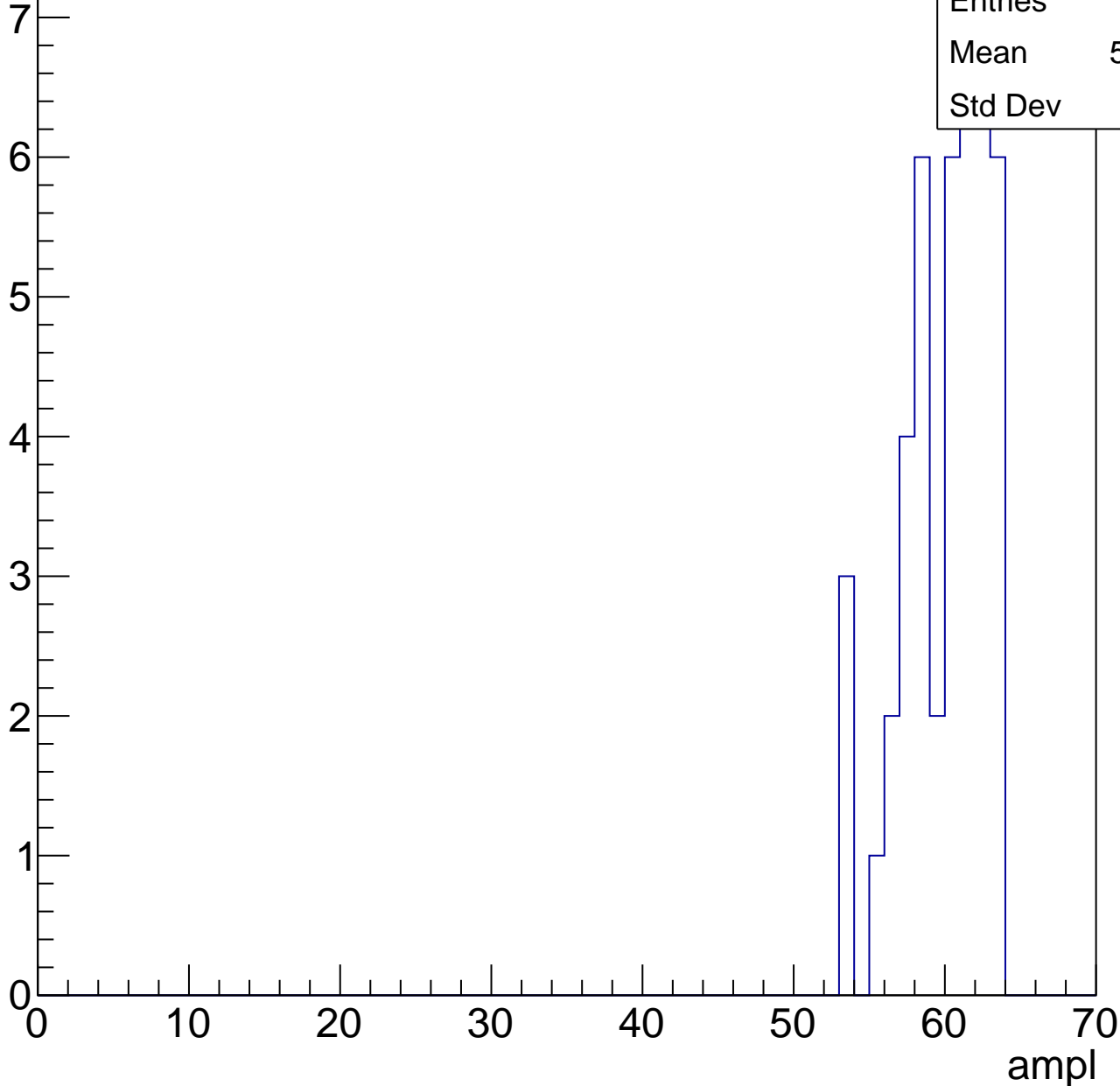


B1L102S, U2-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

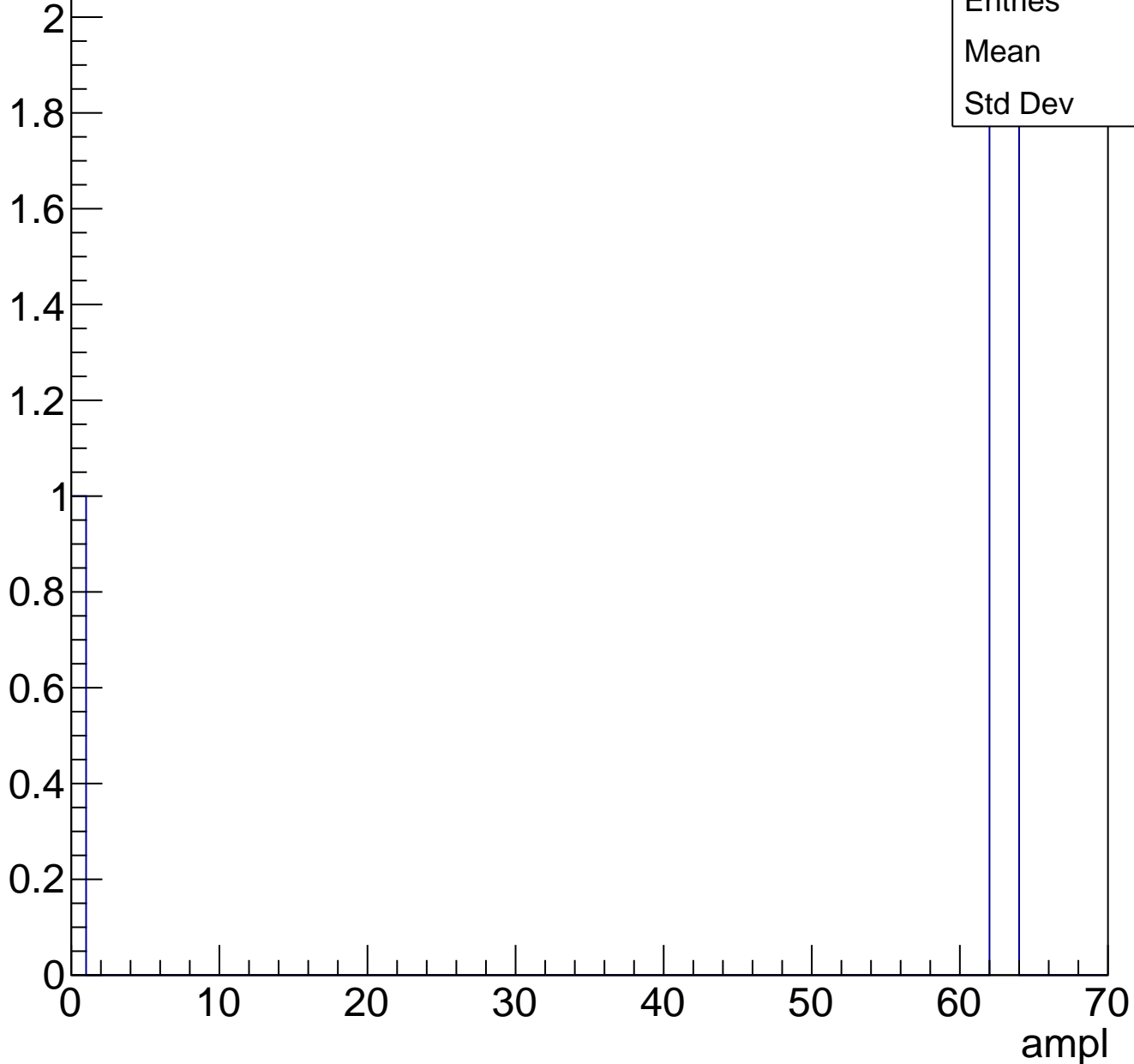
Entries	44
Mean	59.52
Std Dev	2.8



B1L102S, U2-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U2-ch65, adc0

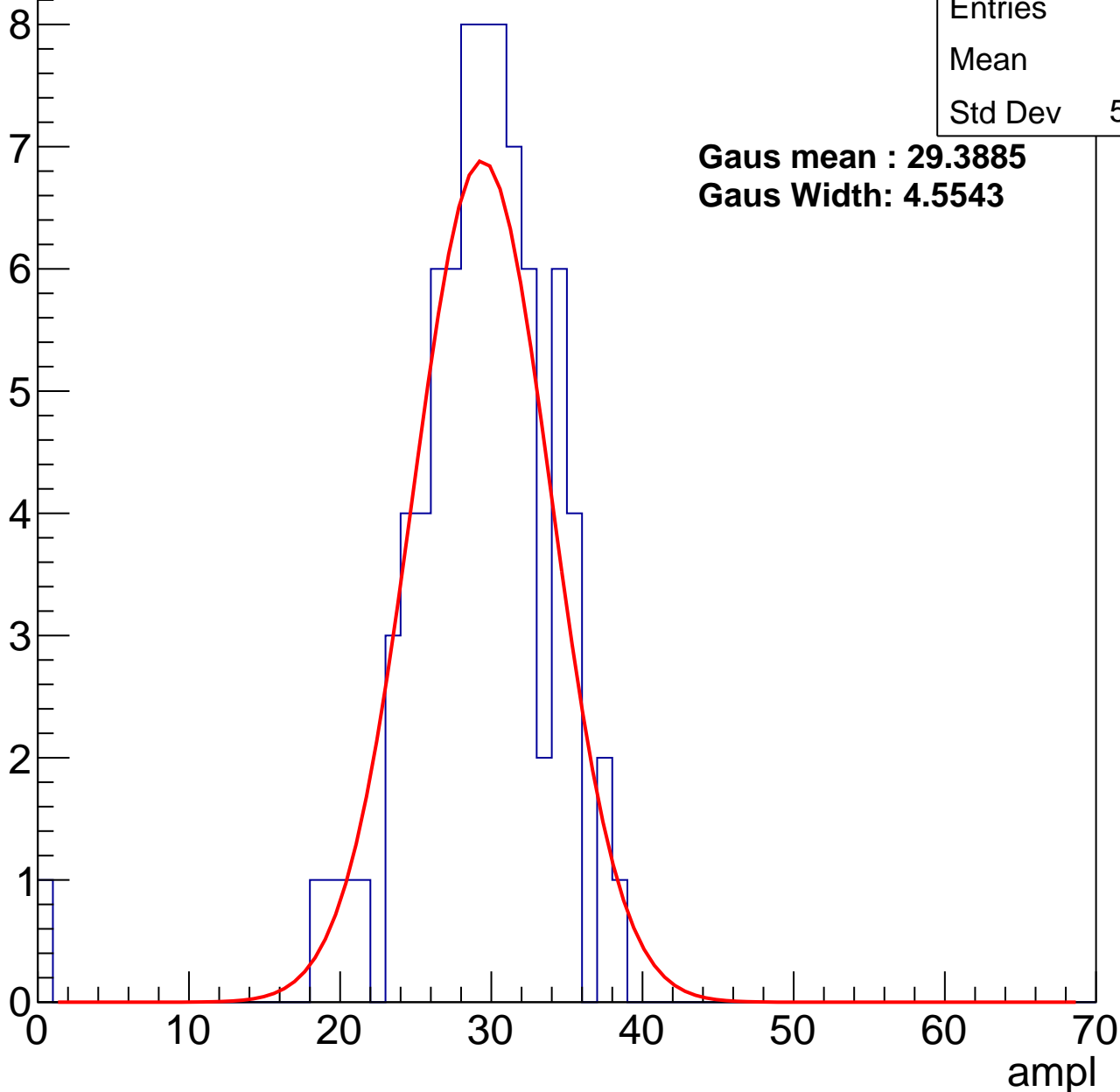
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	28.6
Std Dev	5.217

Gaus mean : 29.3885

Gaus Width: 4.5543



B1L102S, U2-ch65, adc1

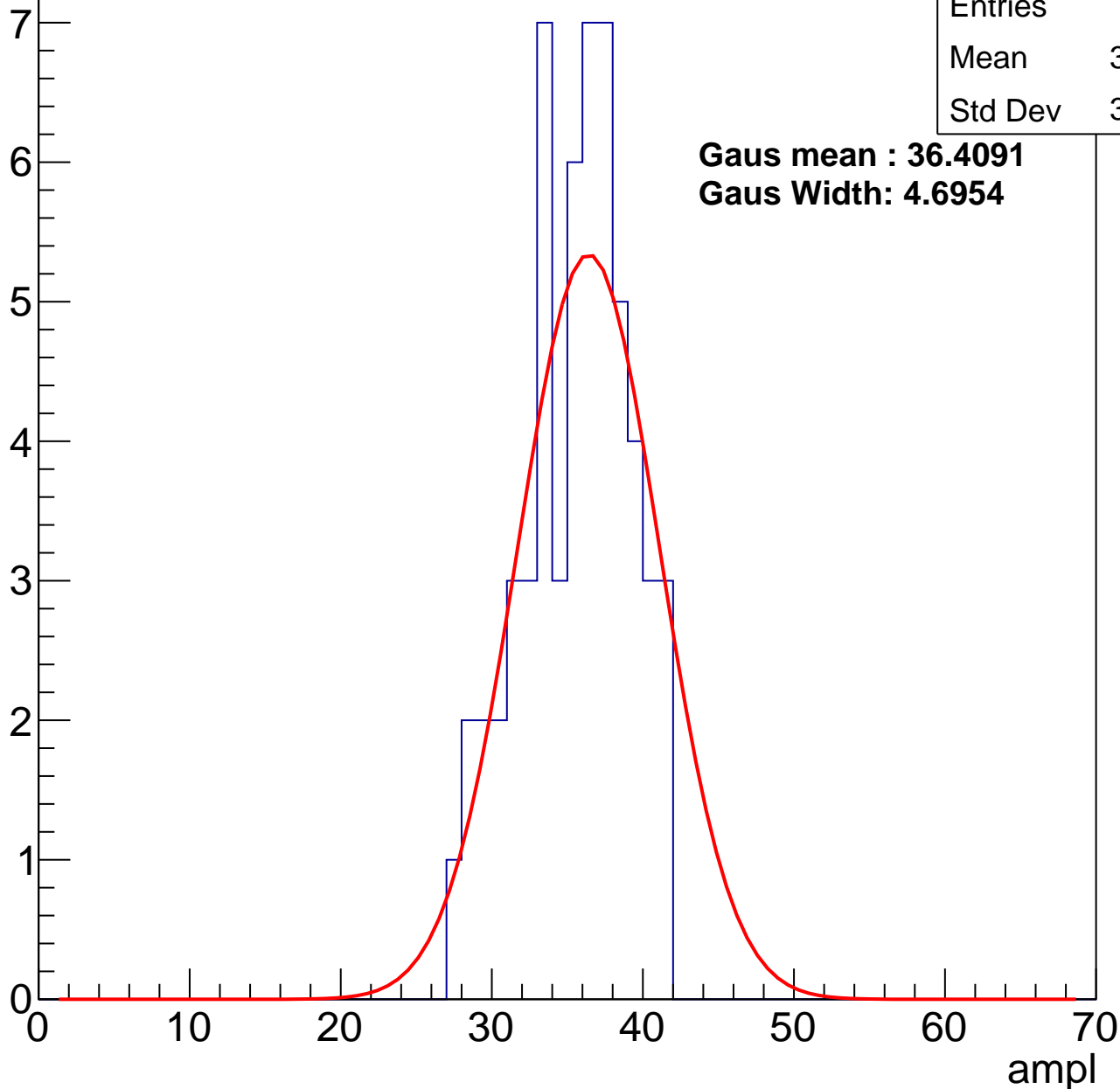
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.05
Std Dev	3.516

Gaus mean : 36.4091

Gaus Width: 4.6954



B1L102S, U2-ch65, adc2

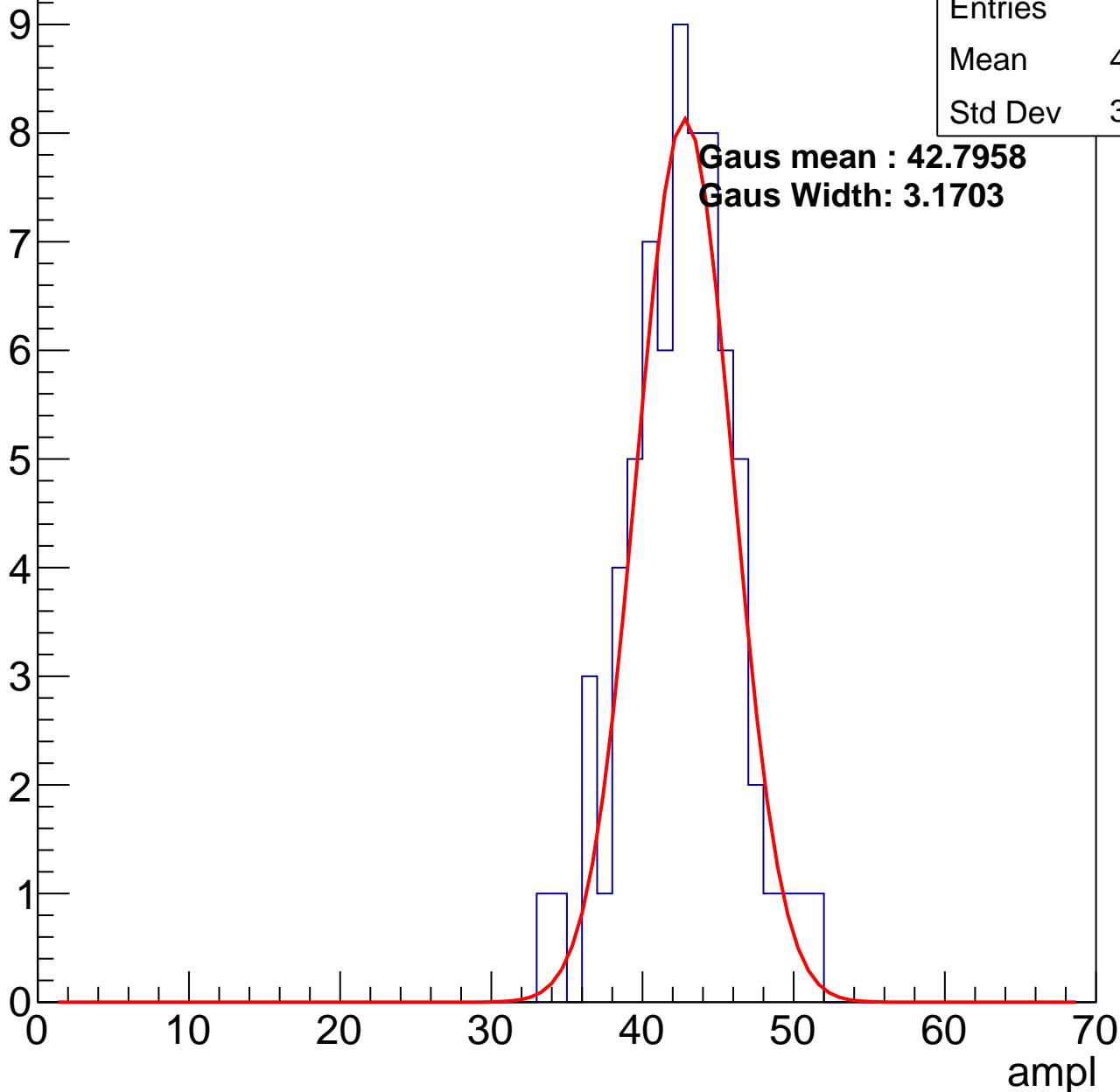
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.16
Std Dev	3.532

Gaus mean : 42.7958

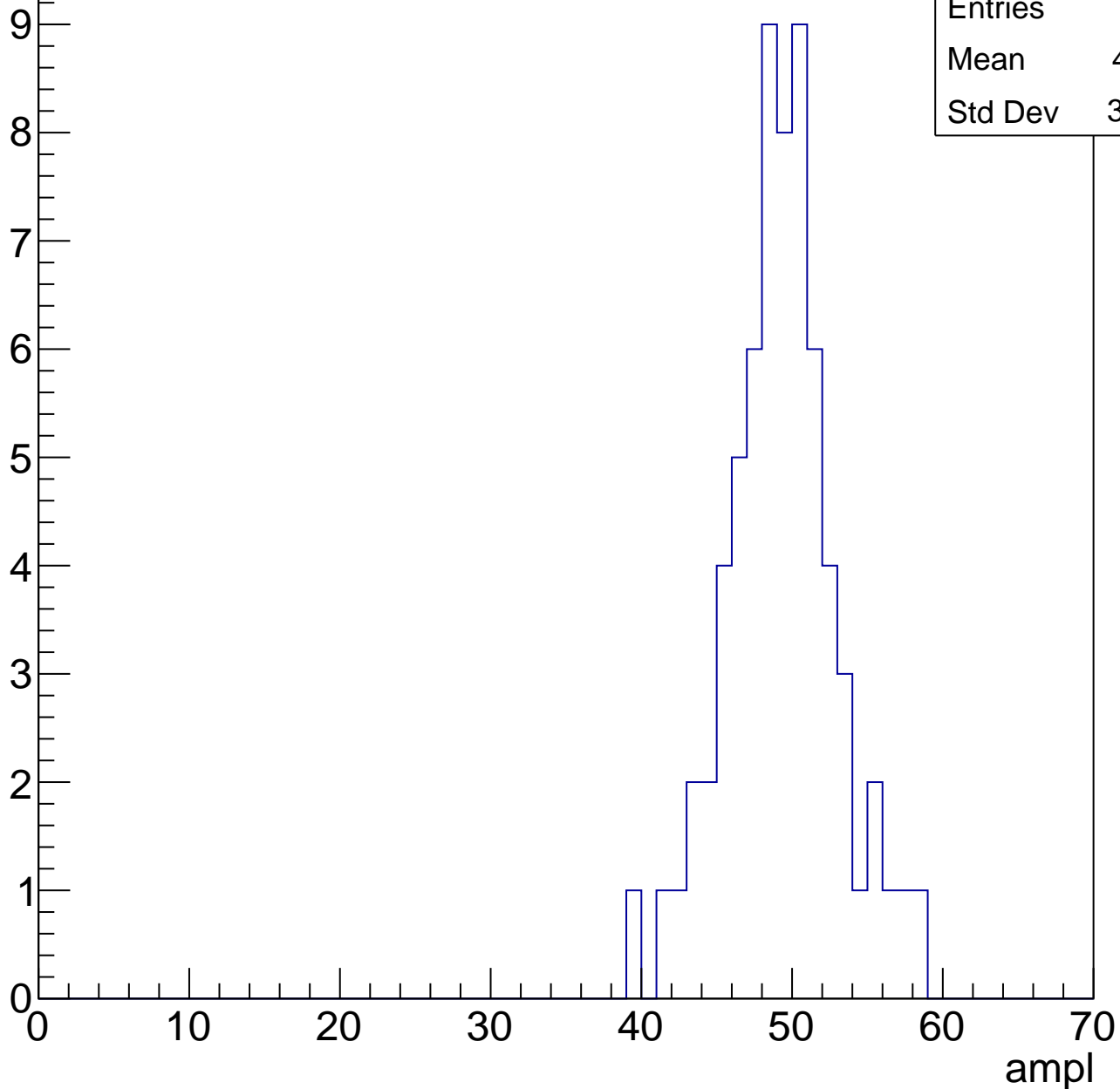
Gaus Width: 3.1703



B1L102S, U2-ch65, adc3

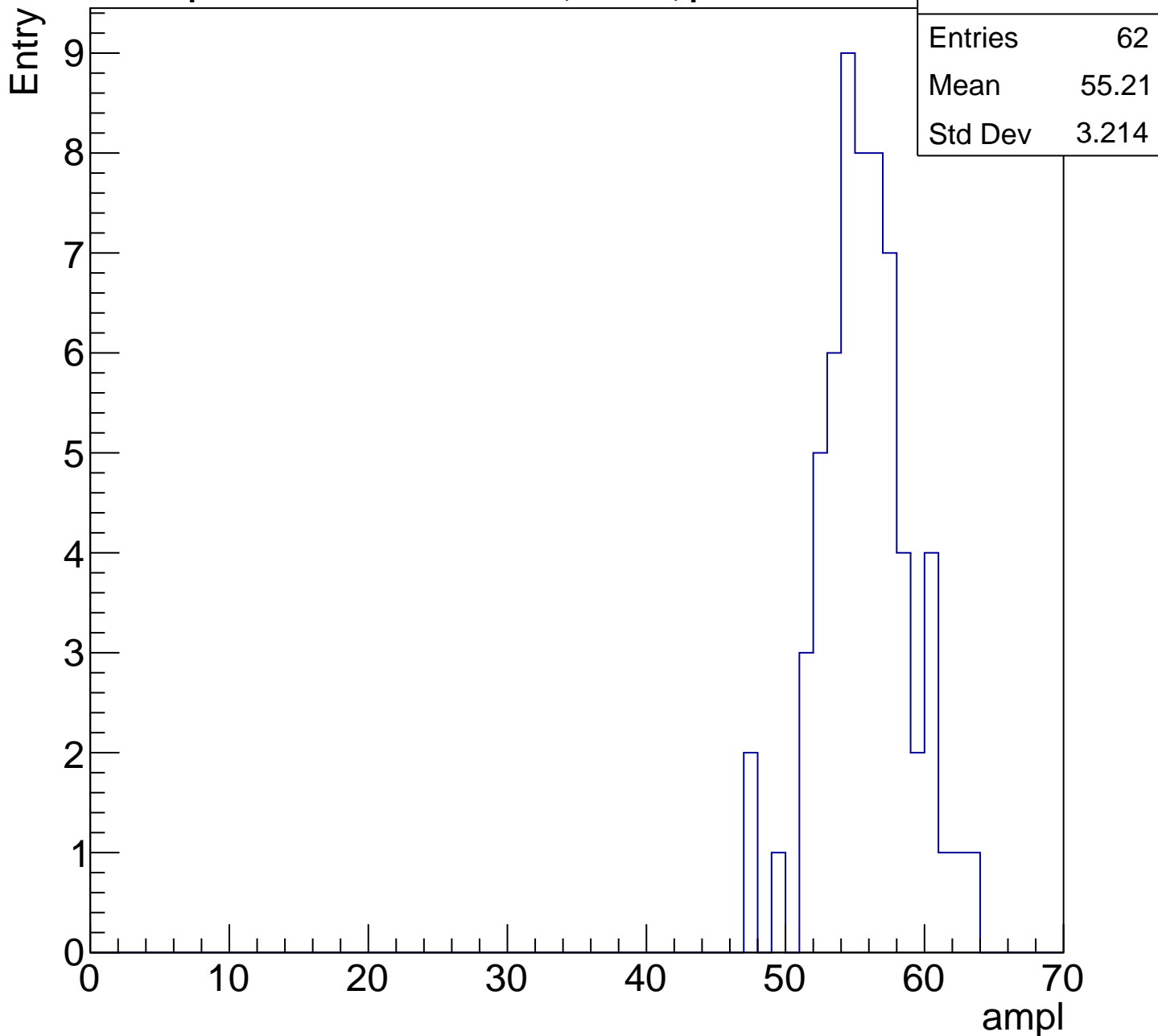
calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch65, adc5

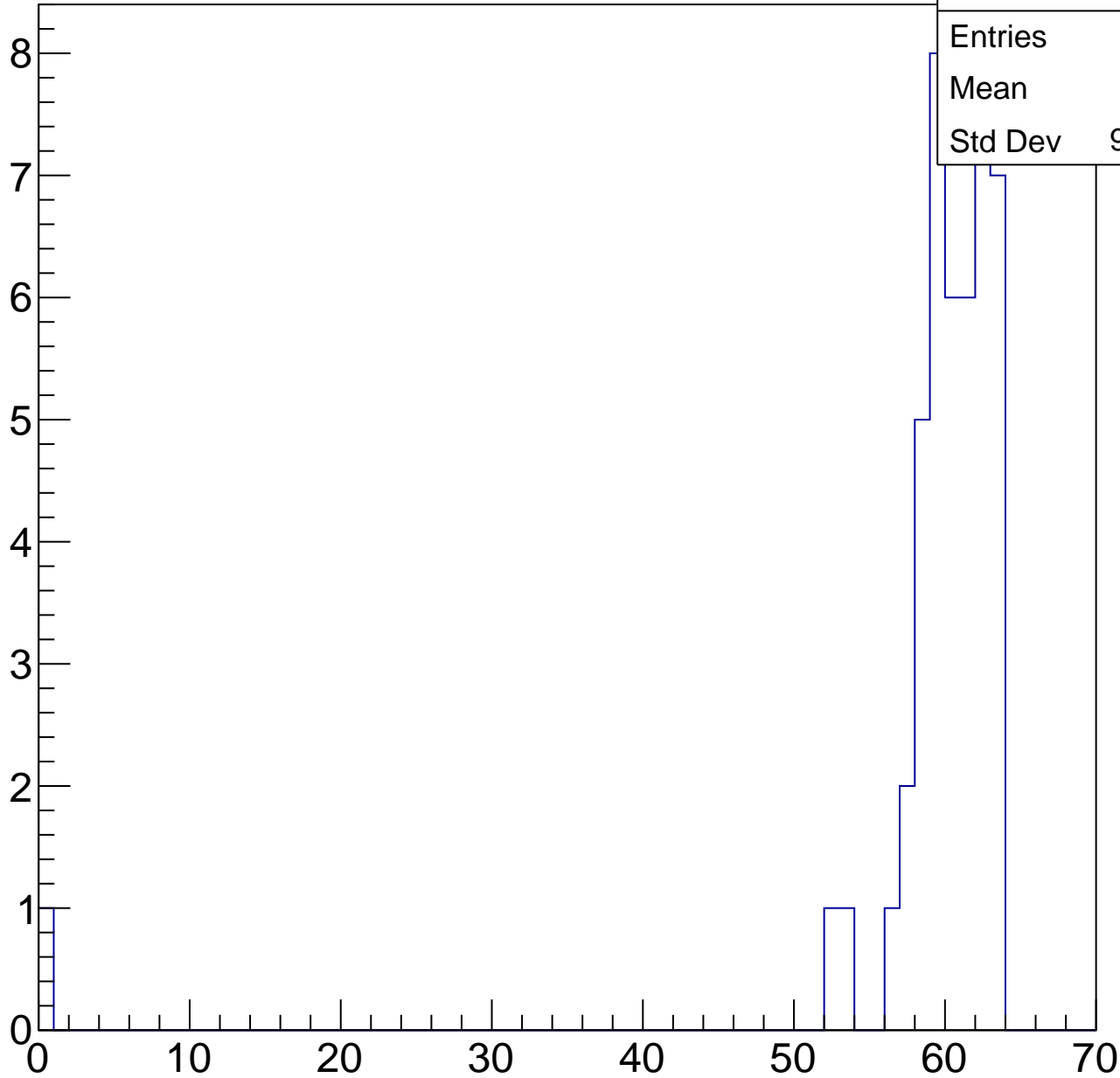
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.7
Std Dev	9.086

ampl



B1L102S, U2-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

6

Mean

61.83

Std Dev

0.8975

B1L102S, U2-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch66, adc0

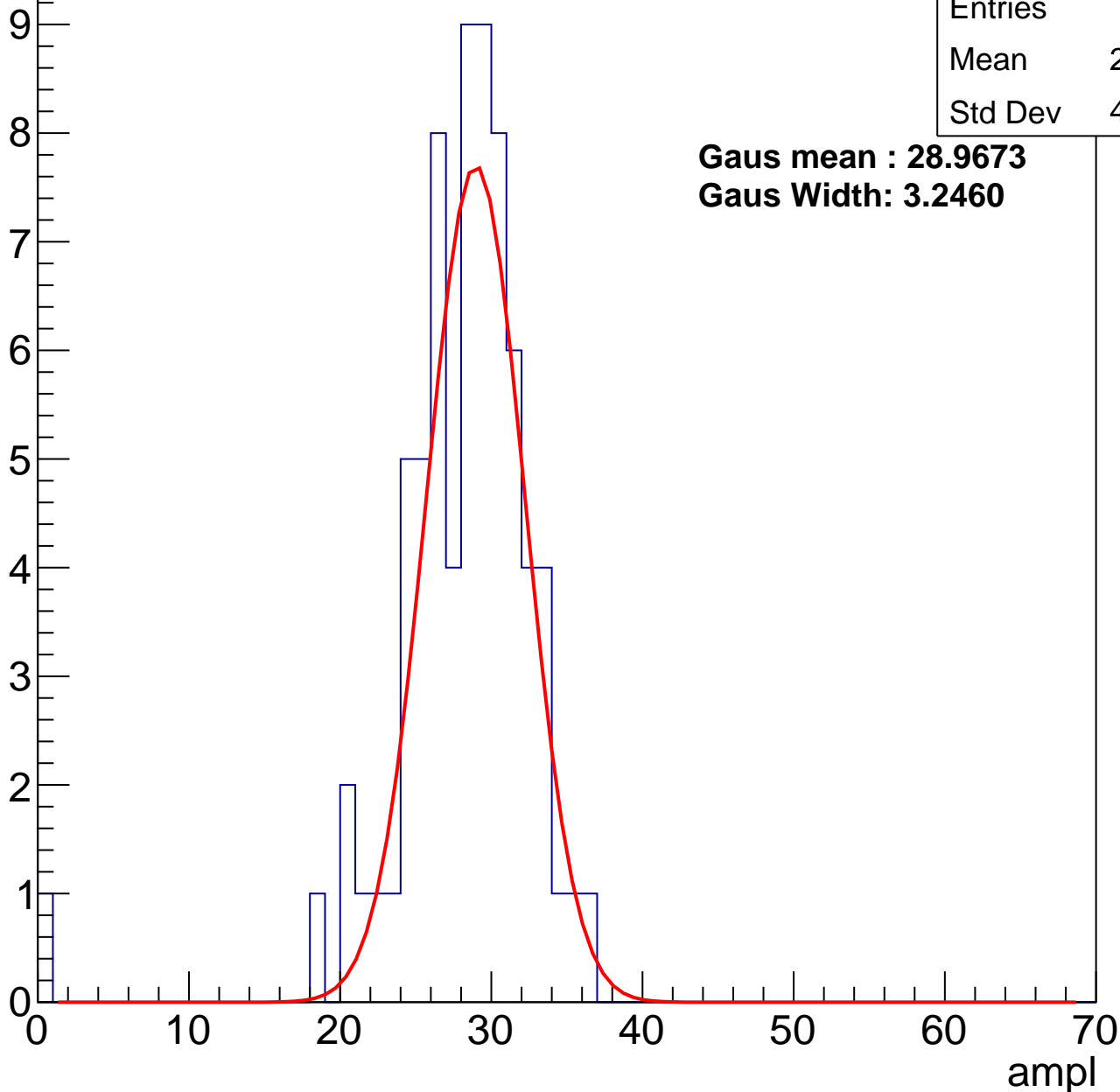
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	27.62
Std Dev	4.832

Gaus mean : 28.9673

Gaus Width: 3.2460



B1L102S, U2-ch66, adc1

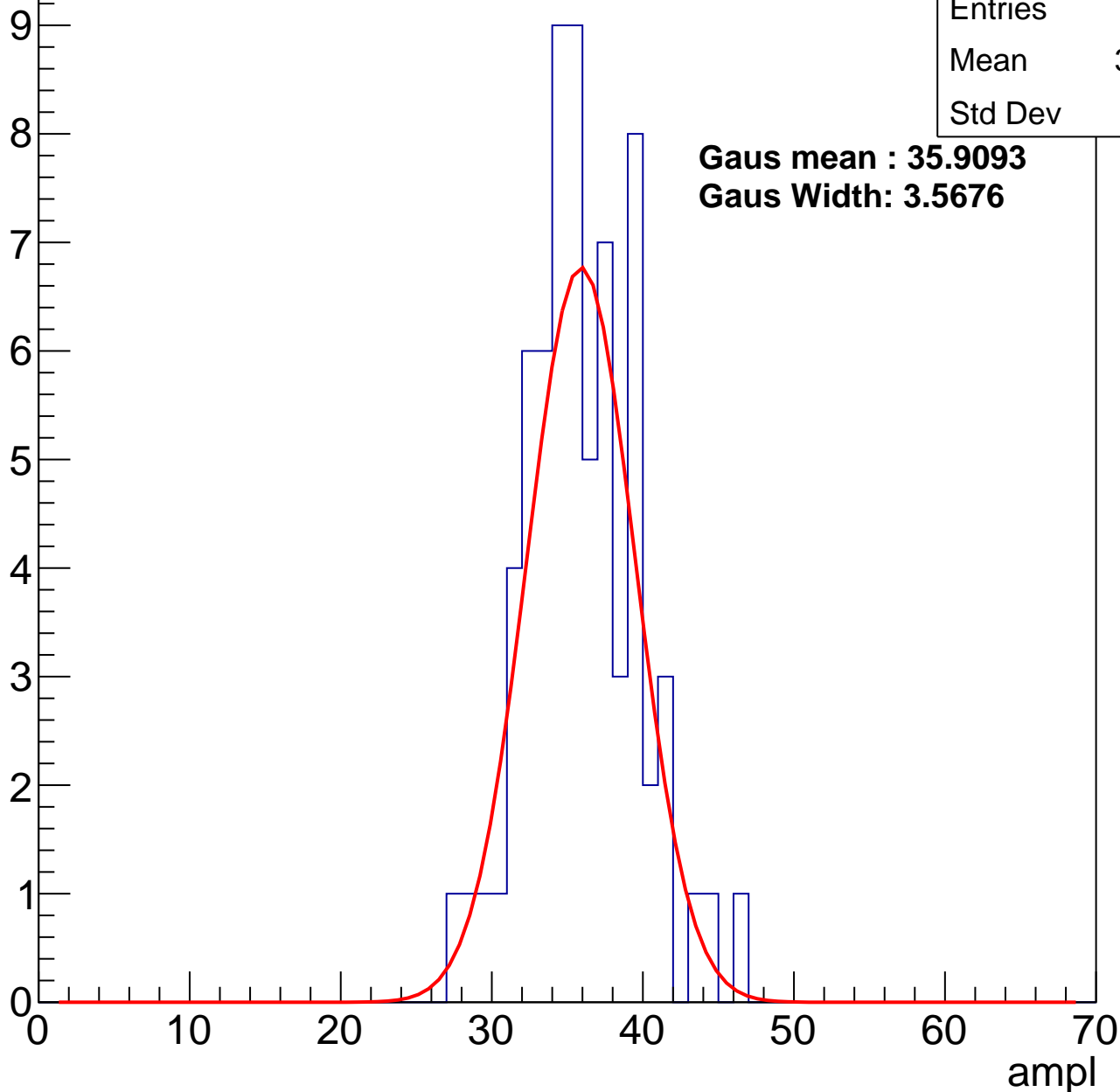
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.51
Std Dev	3.65

Gaus mean : 35.9093

Gaus Width: 3.5676



B1L102S, U2-ch66, adc2

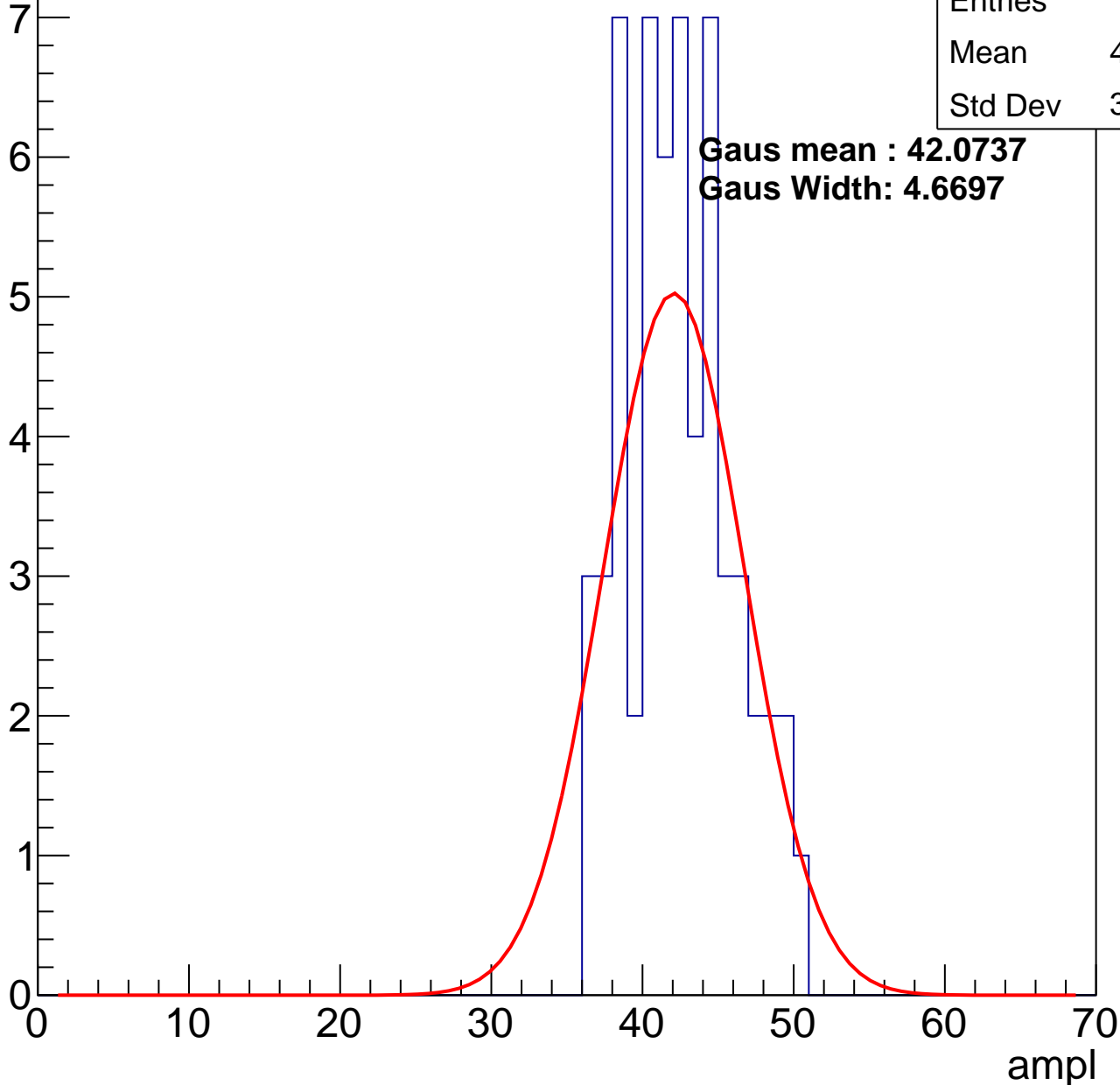
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.93
Std Dev	3.536

Gaus mean : 42.0737

Gaus Width: 4.6697

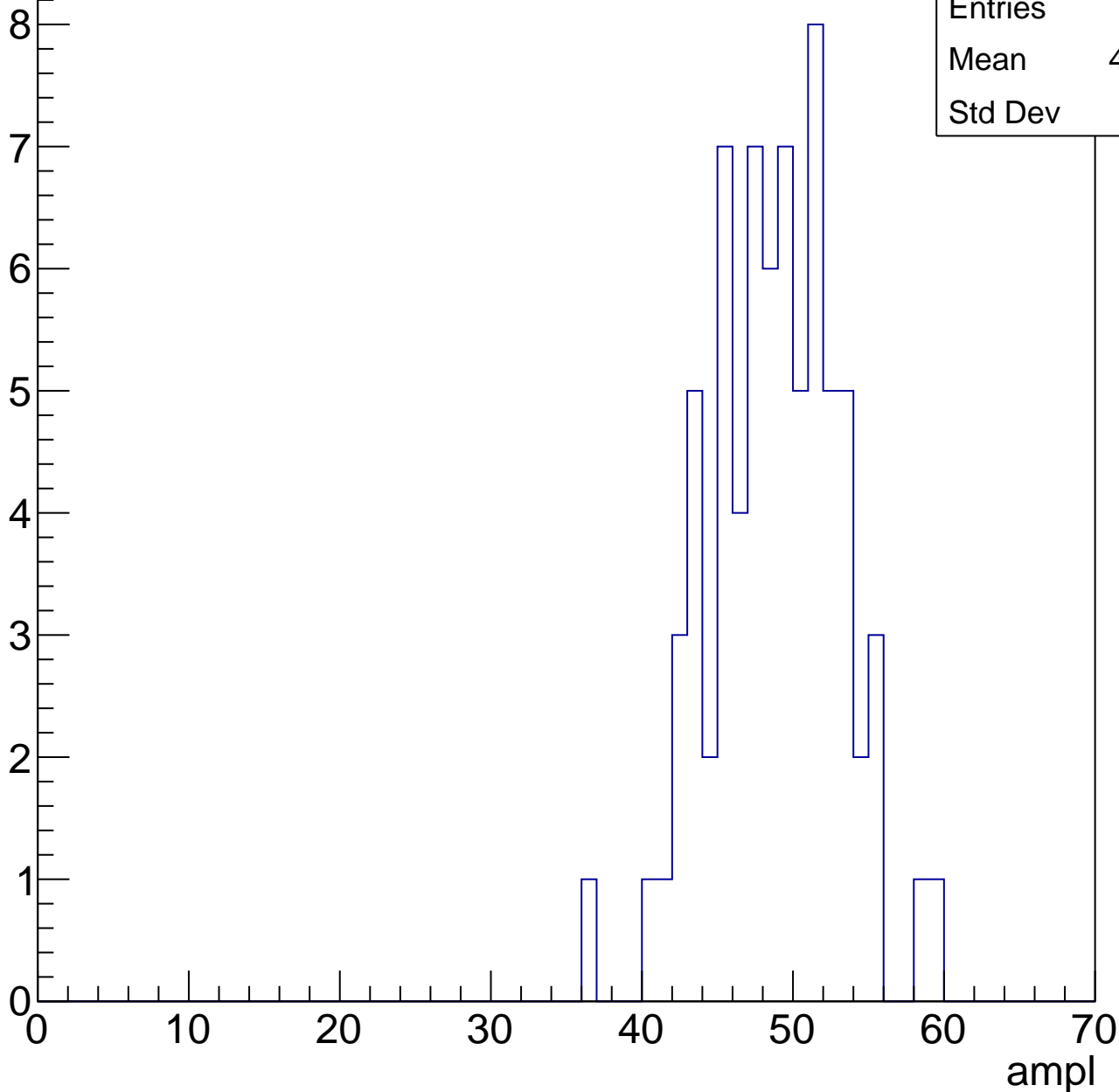


B1L102S, U2-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.35
Std Dev	4.26

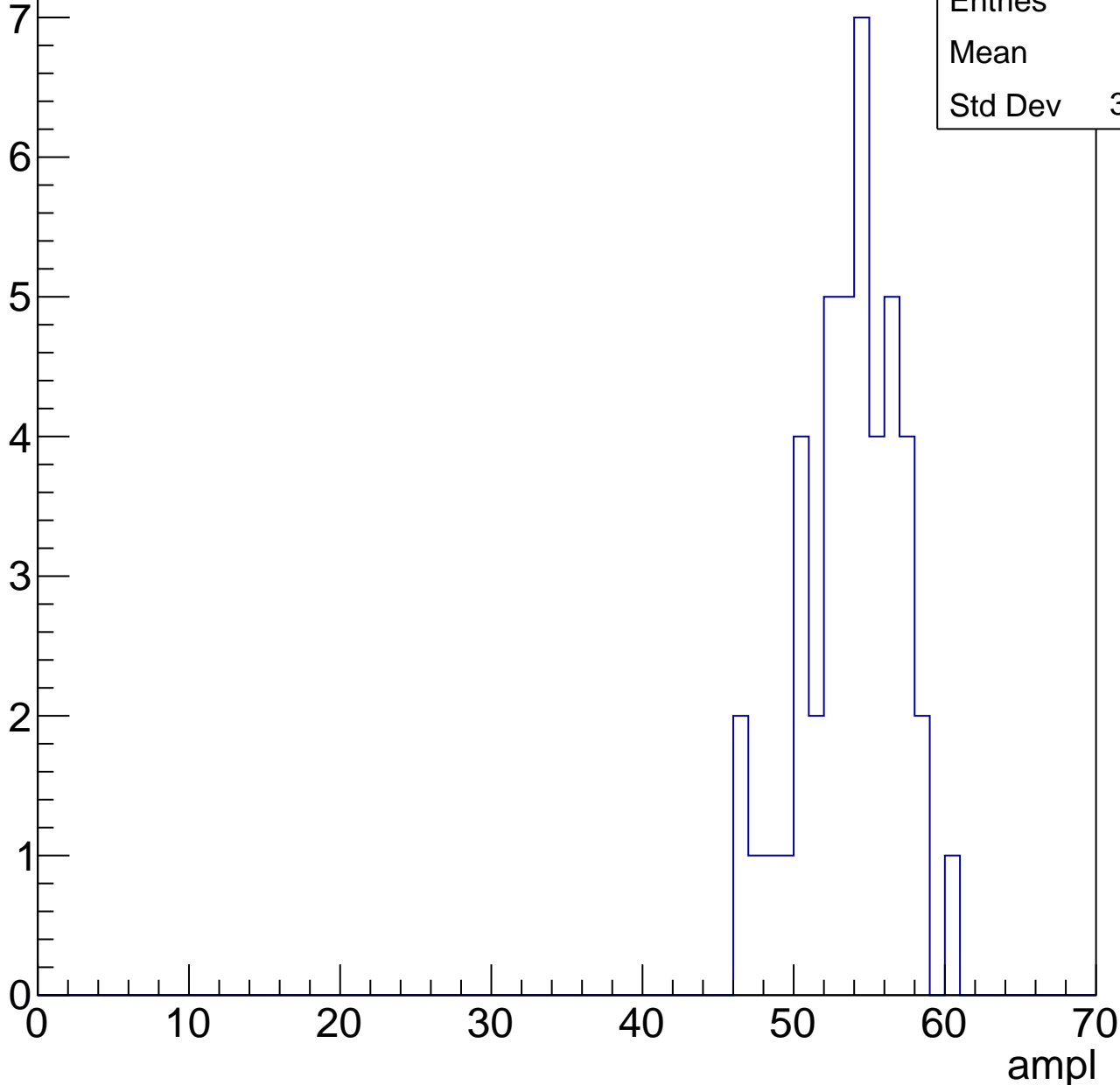


B1L102S, U2-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

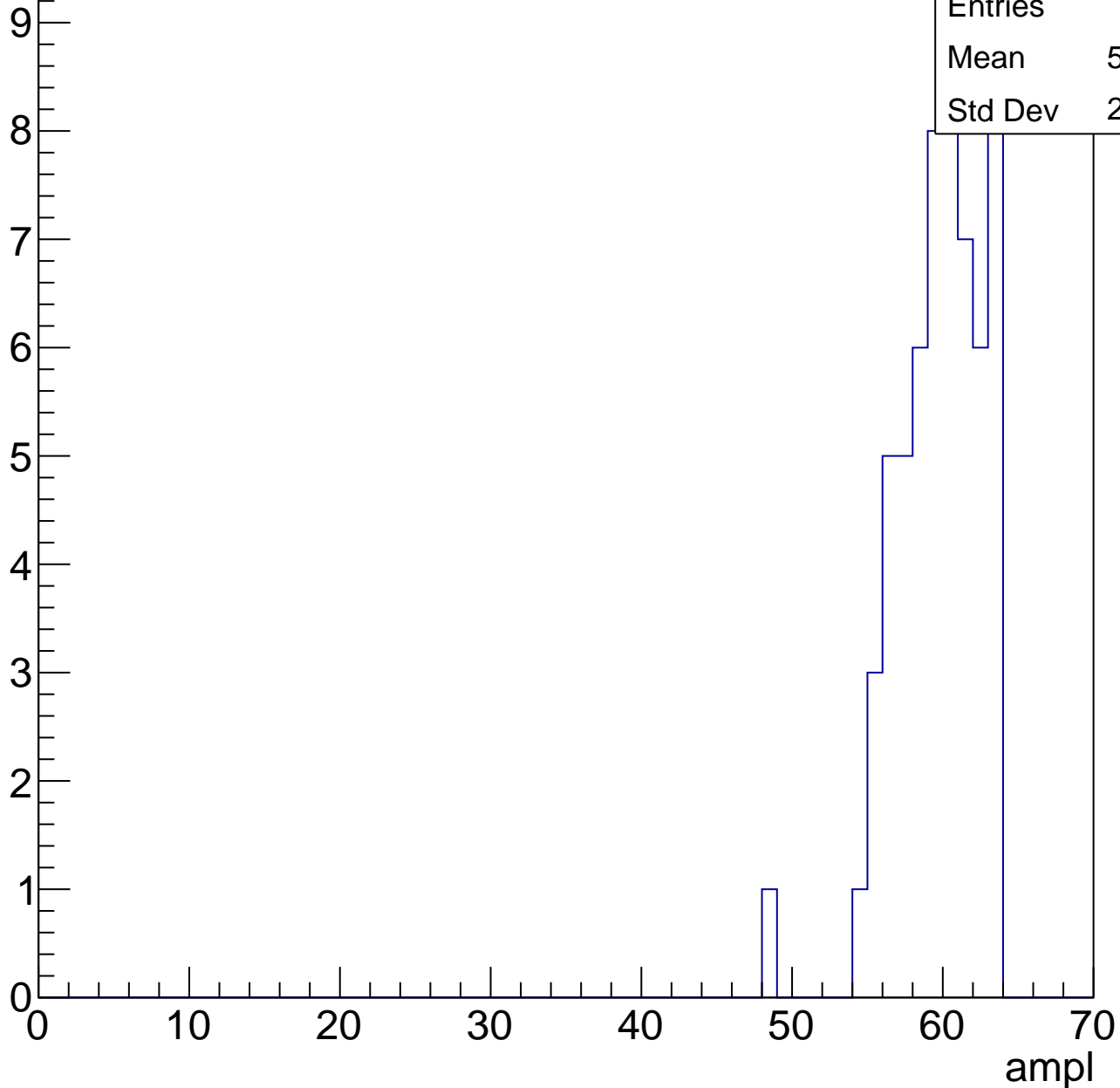
Entries	44
Mean	53.3
Std Dev	3.202



B1L102S, U2-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

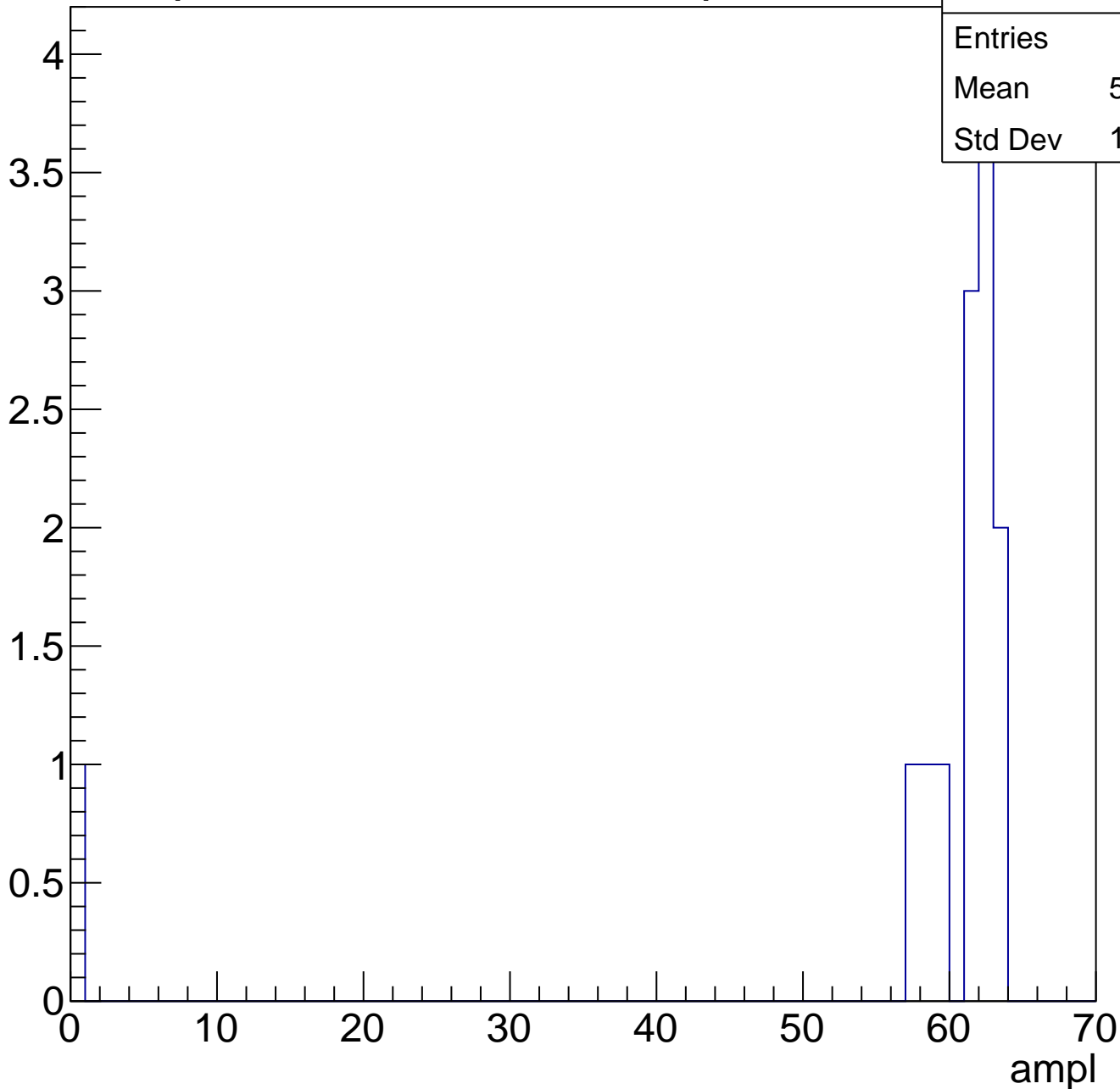


Entries	59
Mean	59.24
Std Dev	2.854

B1L102S, U2-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch67, adc0

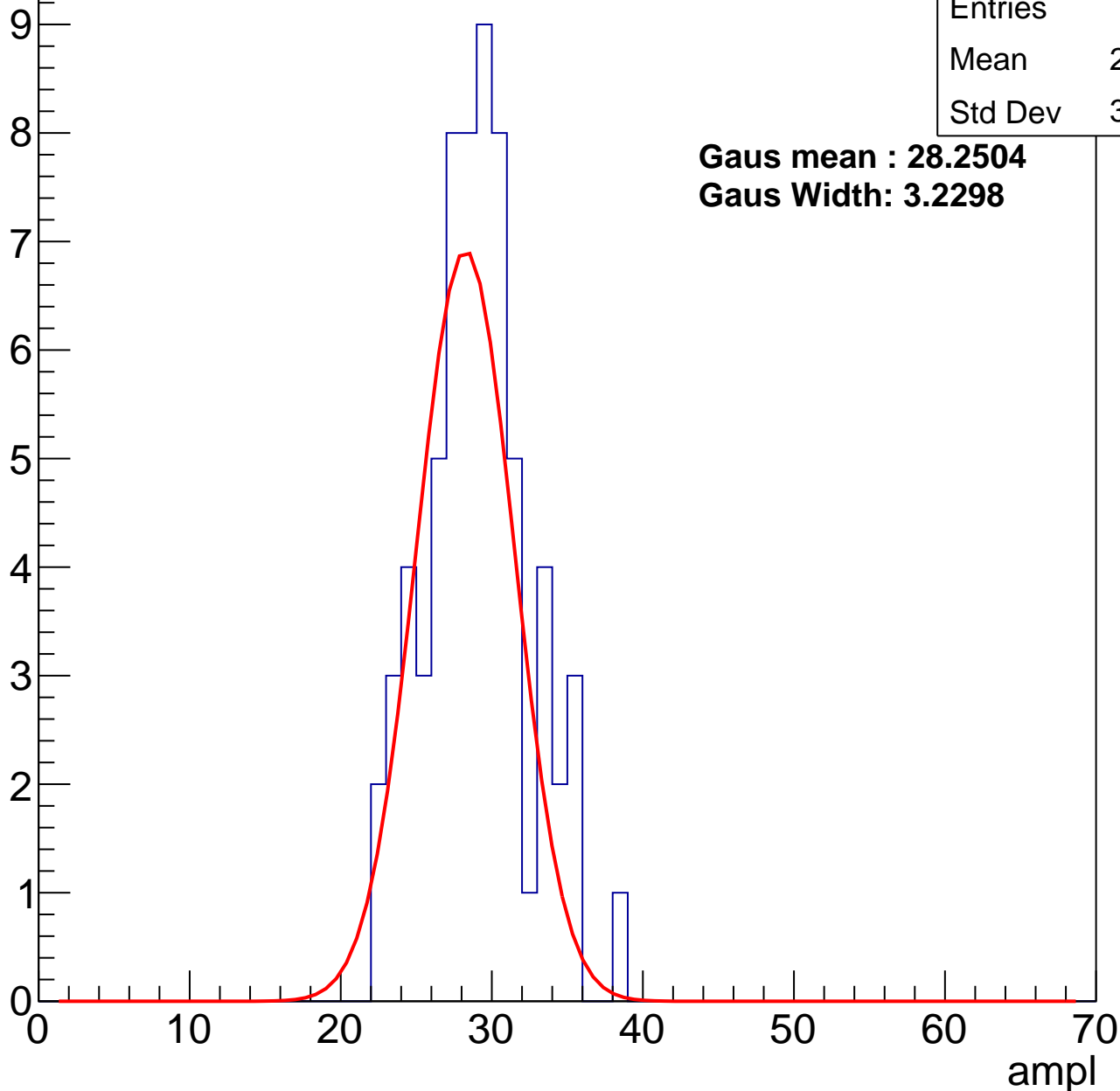
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.56
Std Dev	3.407

Gaus mean : 28.2504

Gaus Width: 3.2298



B1L102S, U2-ch67, adc1

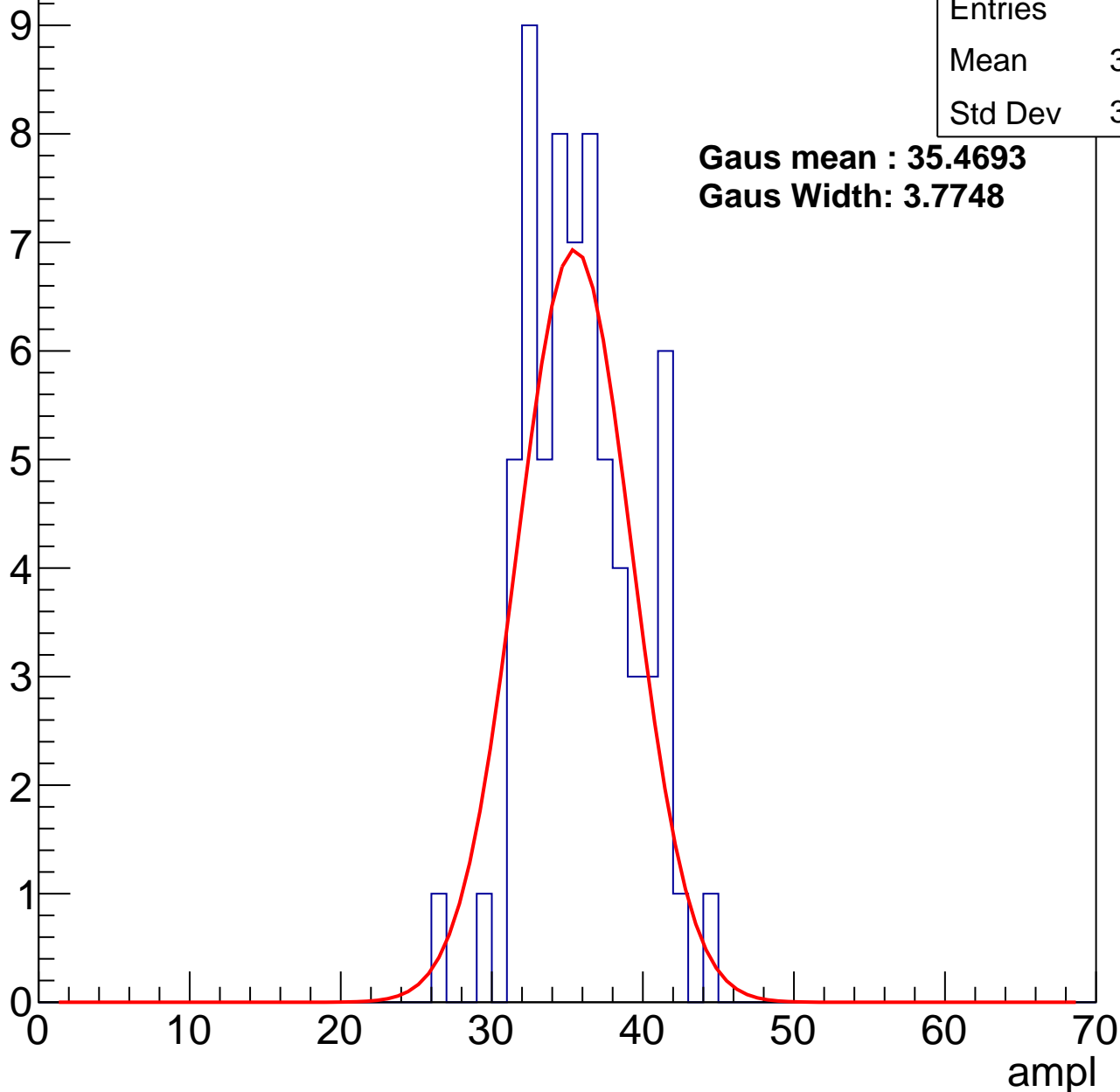
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.43
Std Dev	3.512

Gaus mean : 35.4693

Gaus Width: 3.7748



B1L102S, U2-ch67, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	42.68
Std Dev	3.815

Gaus mean : 42.6928

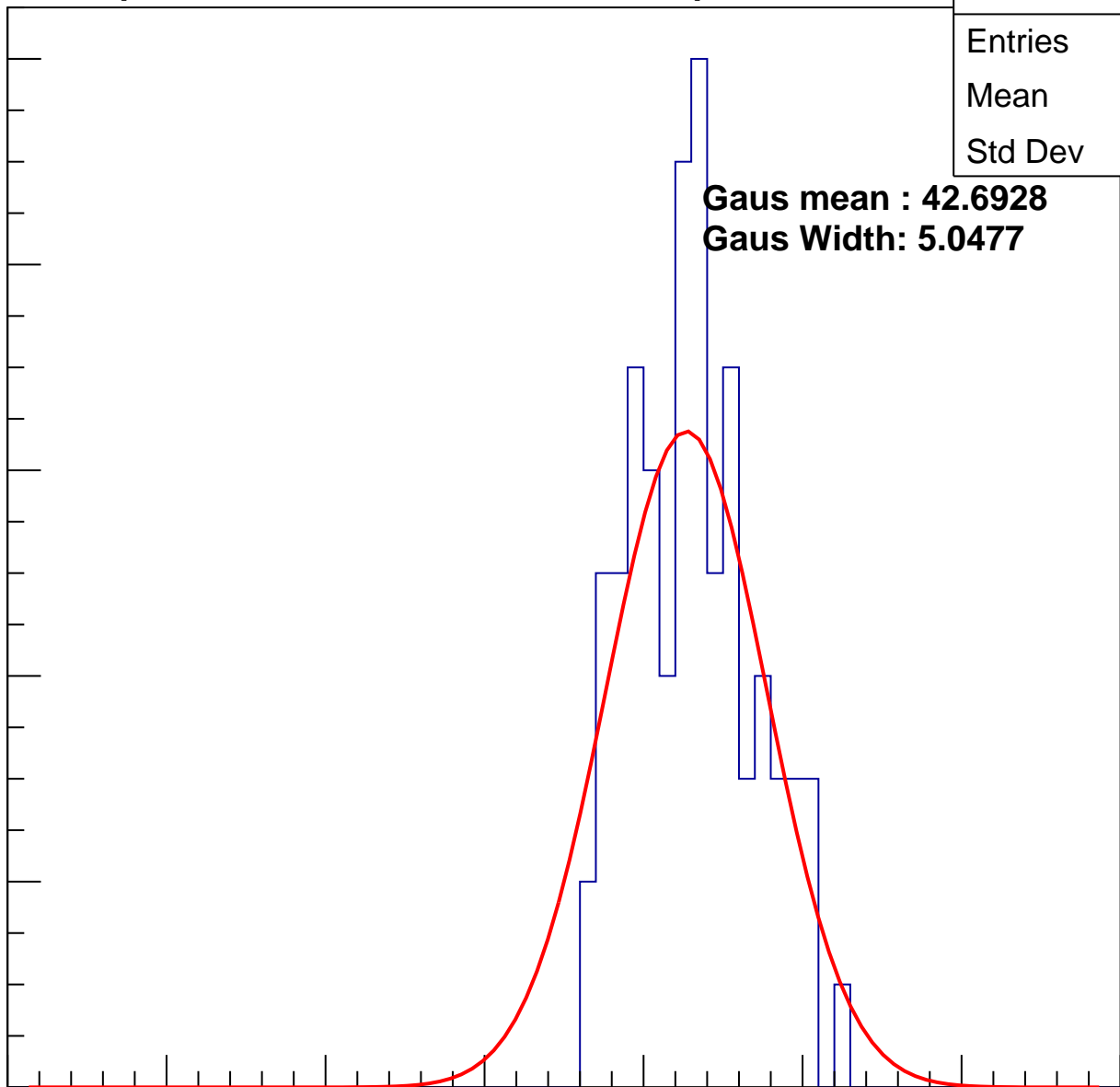
Gaus Width: 5.0477

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

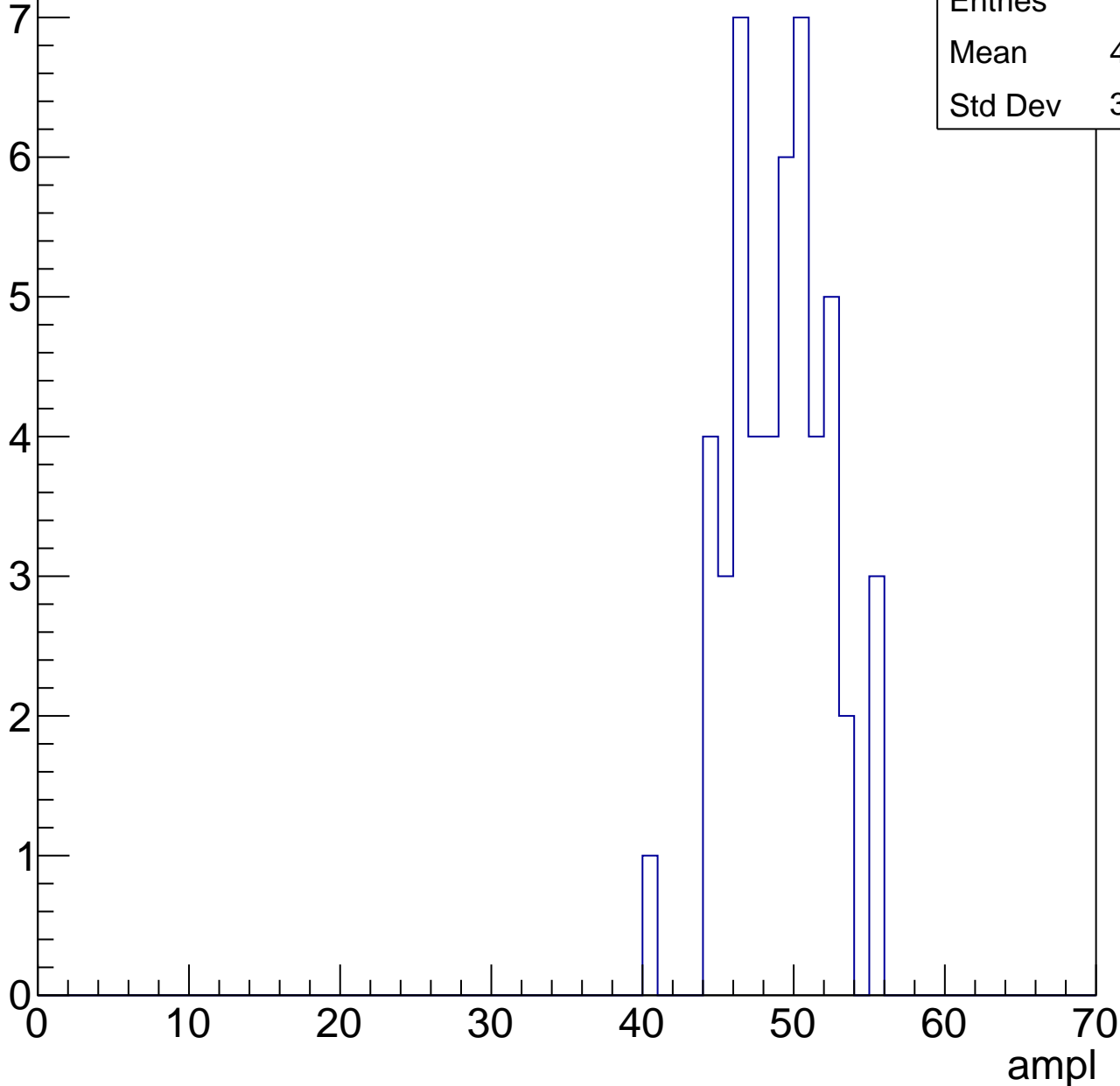


B1L102S, U2-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.64
Std Dev	3.205

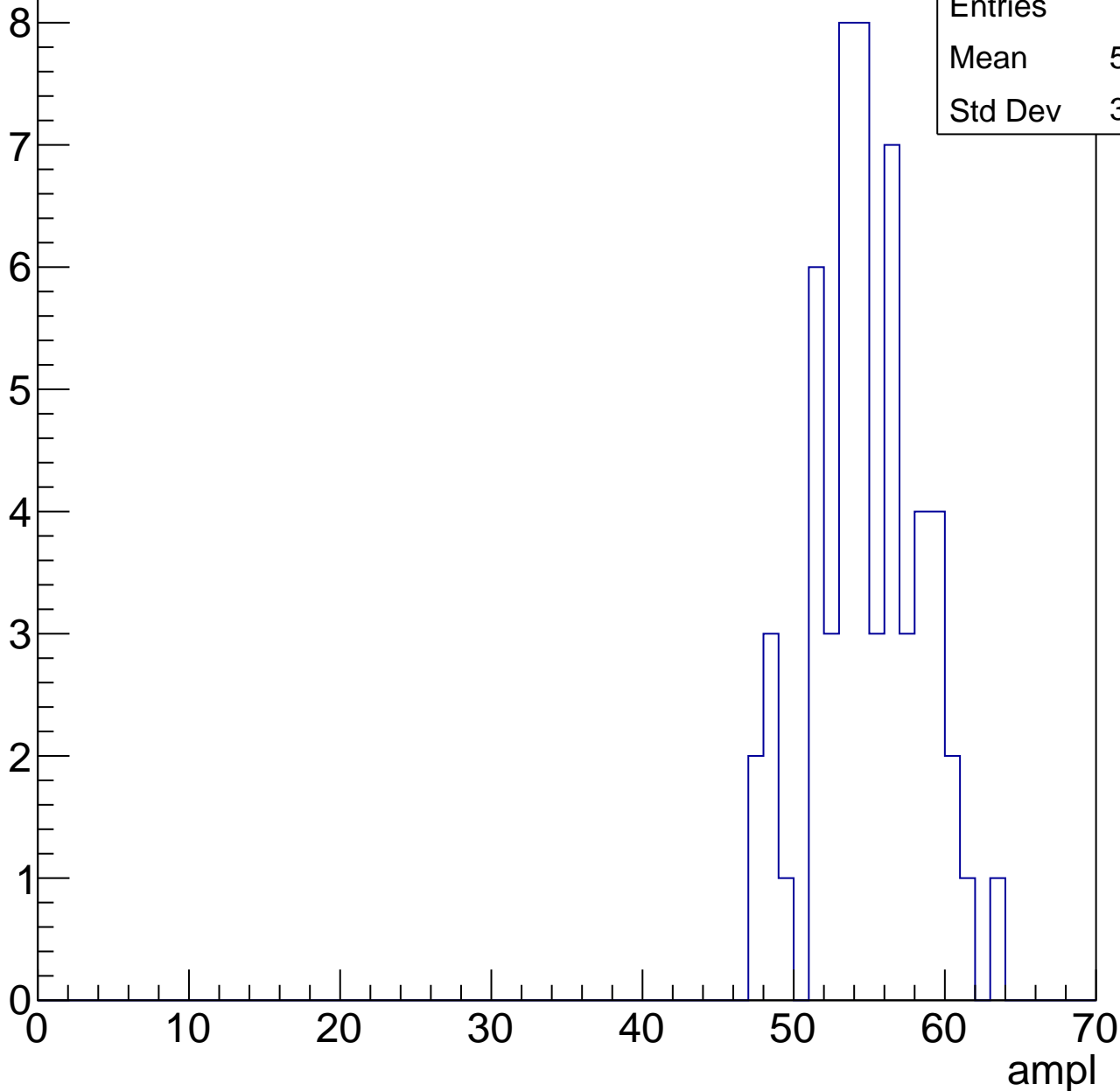


B1L102S, U2-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

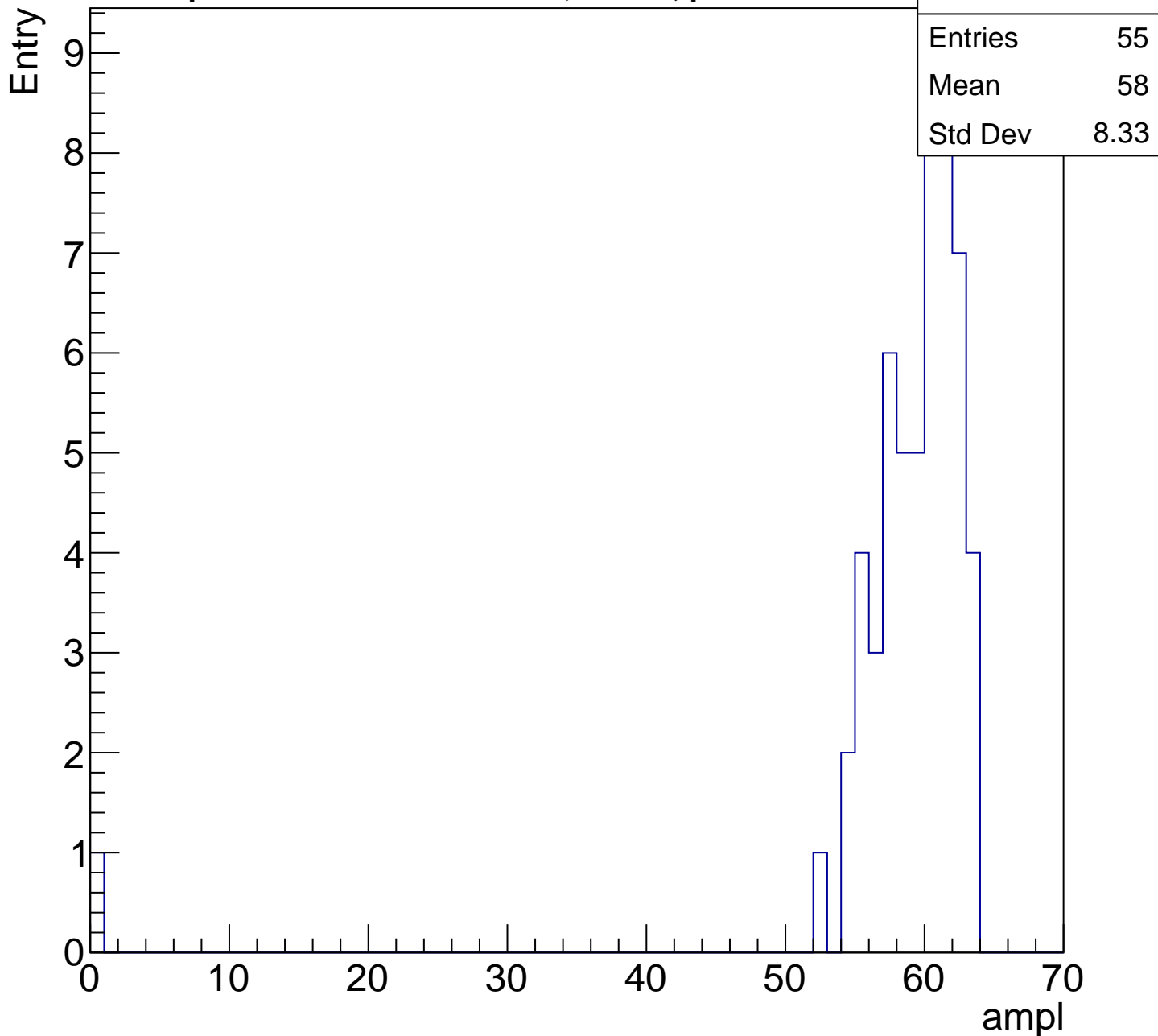
Entry

Entries	56
Mean	54.38
Std Dev	3.588



B1L102S, U2-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

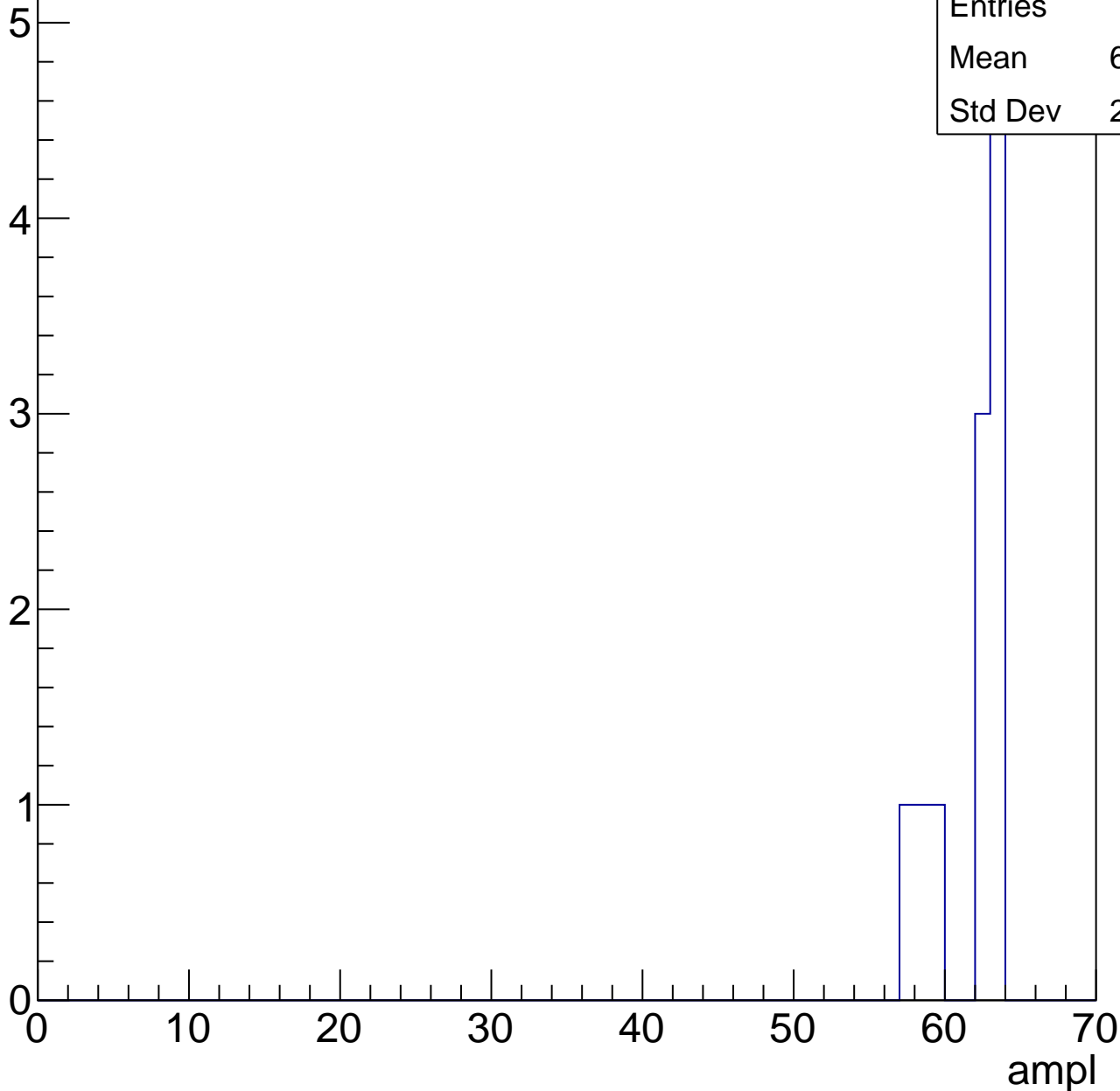


B1L102S, U2-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.36
Std Dev	2.144



B1L102S, U2-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch68, adc0

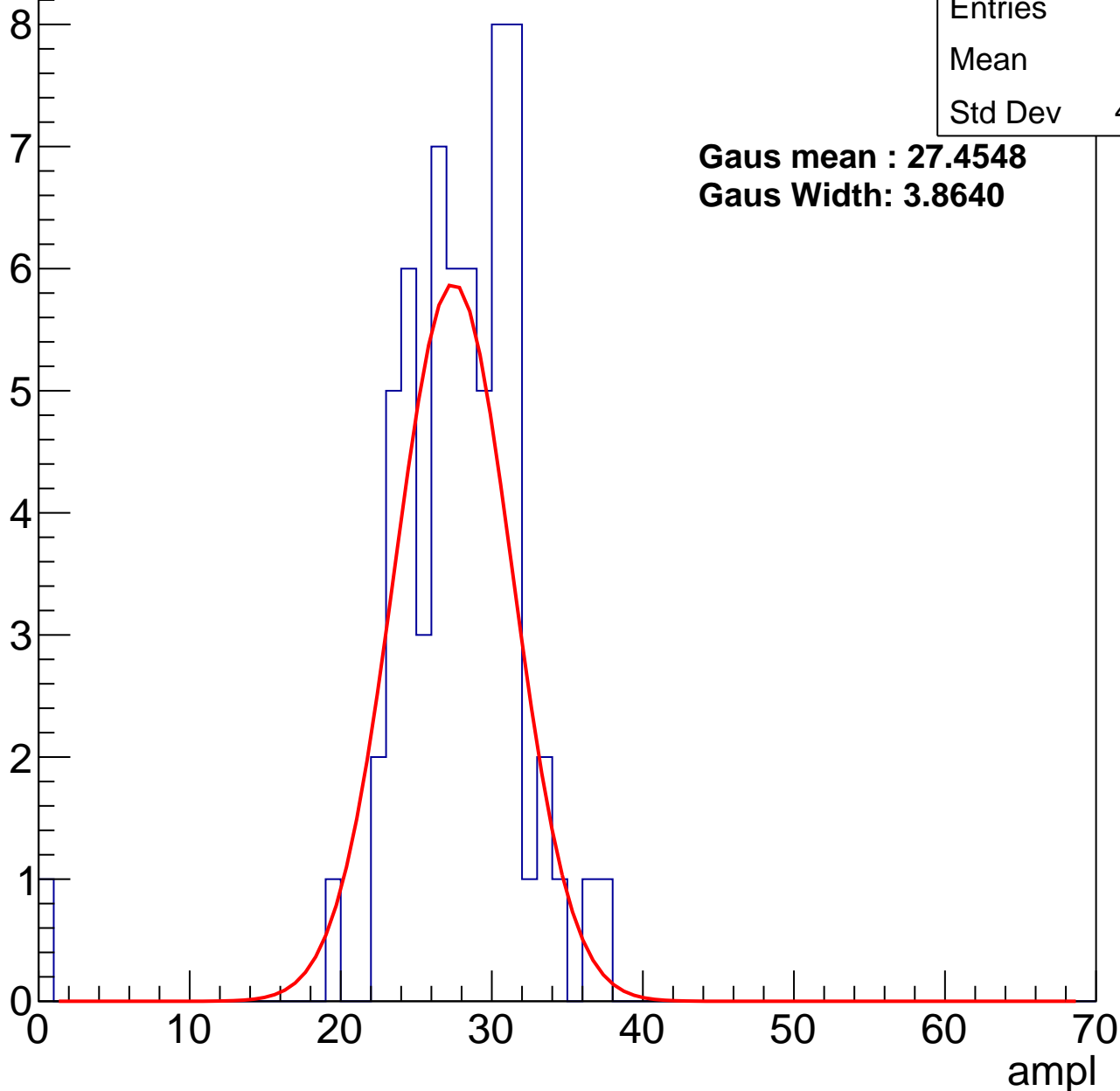
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.3
Std Dev	4.911

Gaus mean : 27.4548

Gaus Width: 3.8640



B1L102S, U2-ch68, adc1

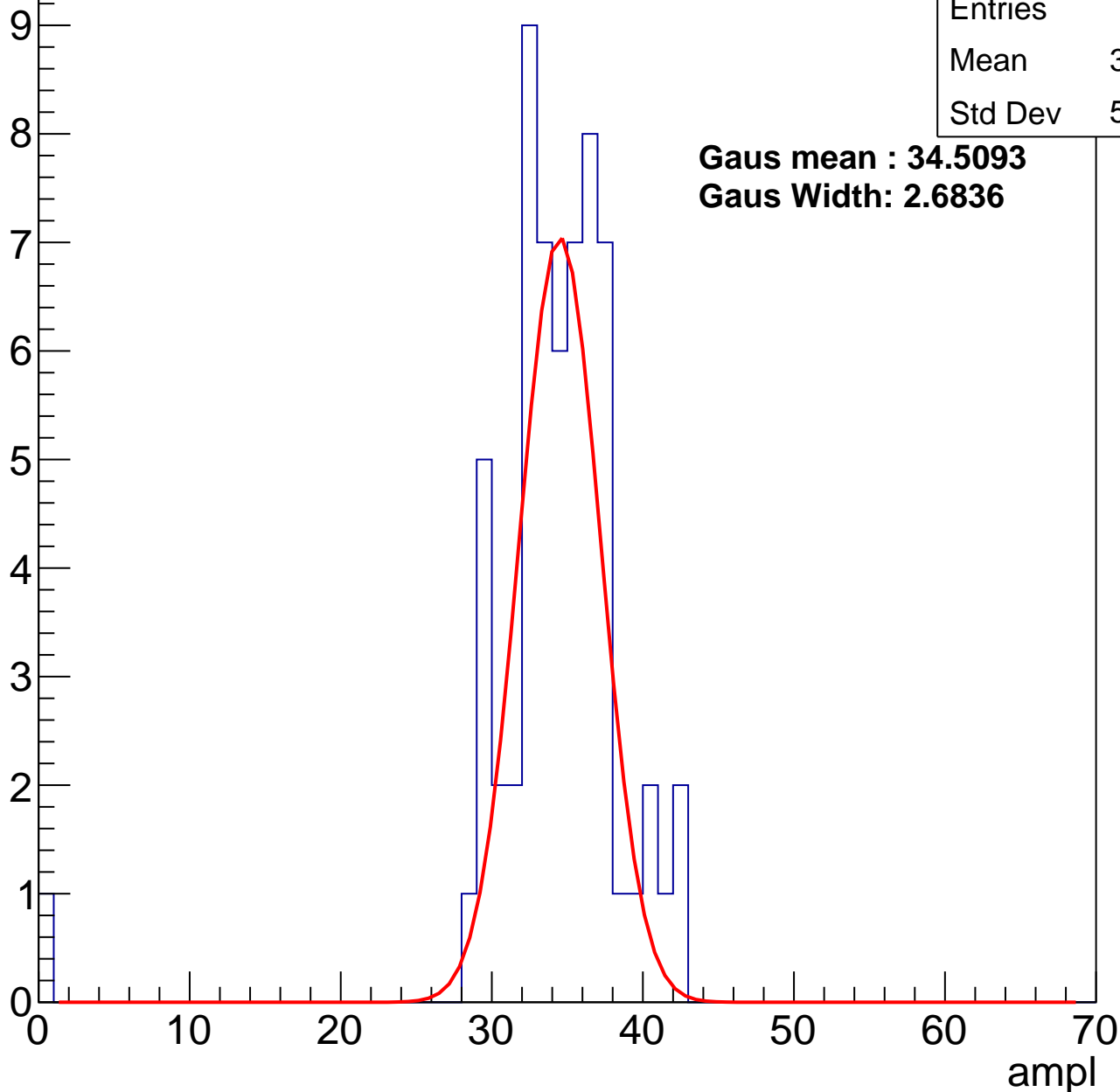
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.74
Std Dev	5.385

Gaus mean : 34.5093

Gaus Width: 2.6836



B1L102S, U2-ch68, adc2

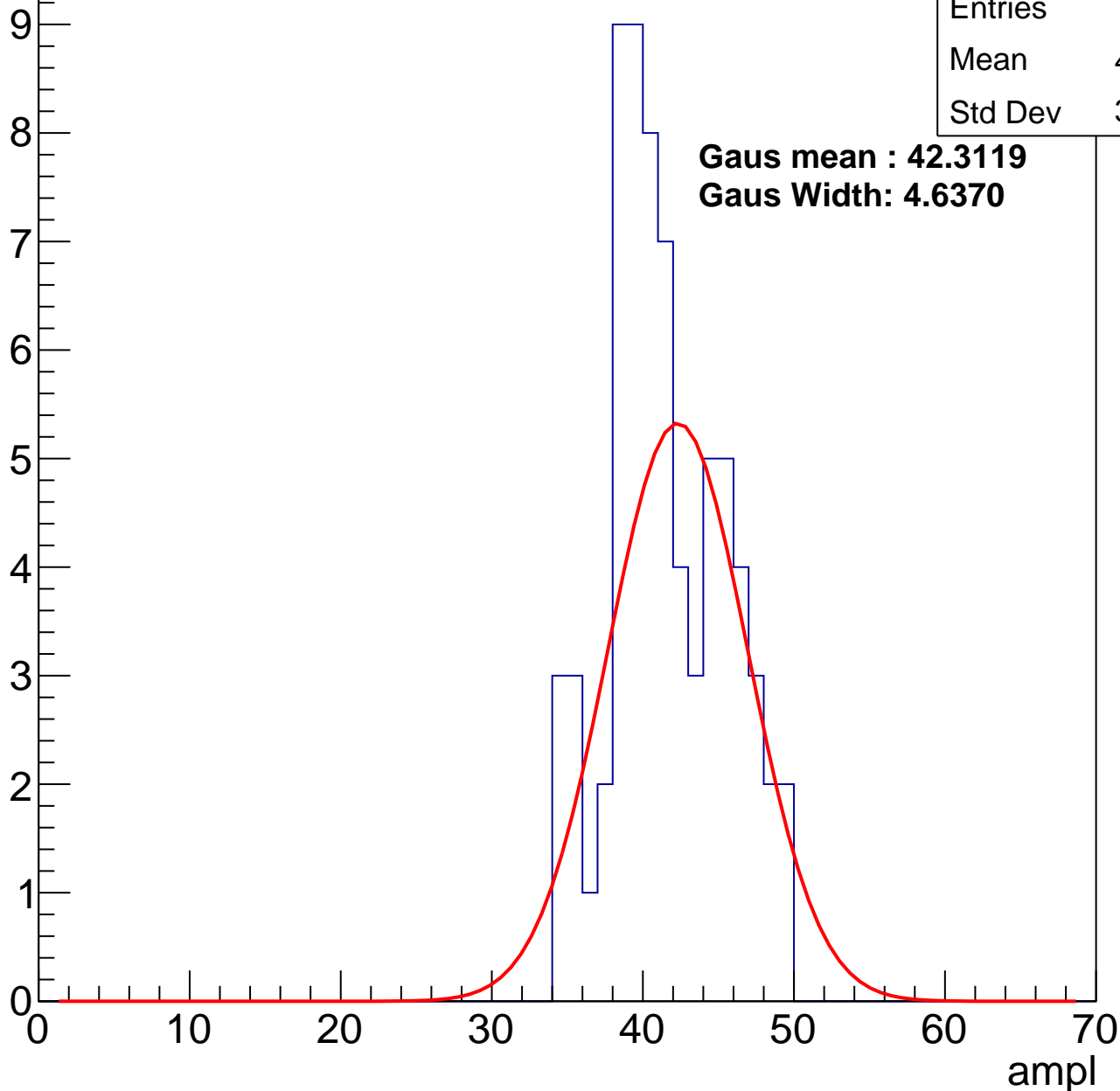
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.11
Std Dev	3.801

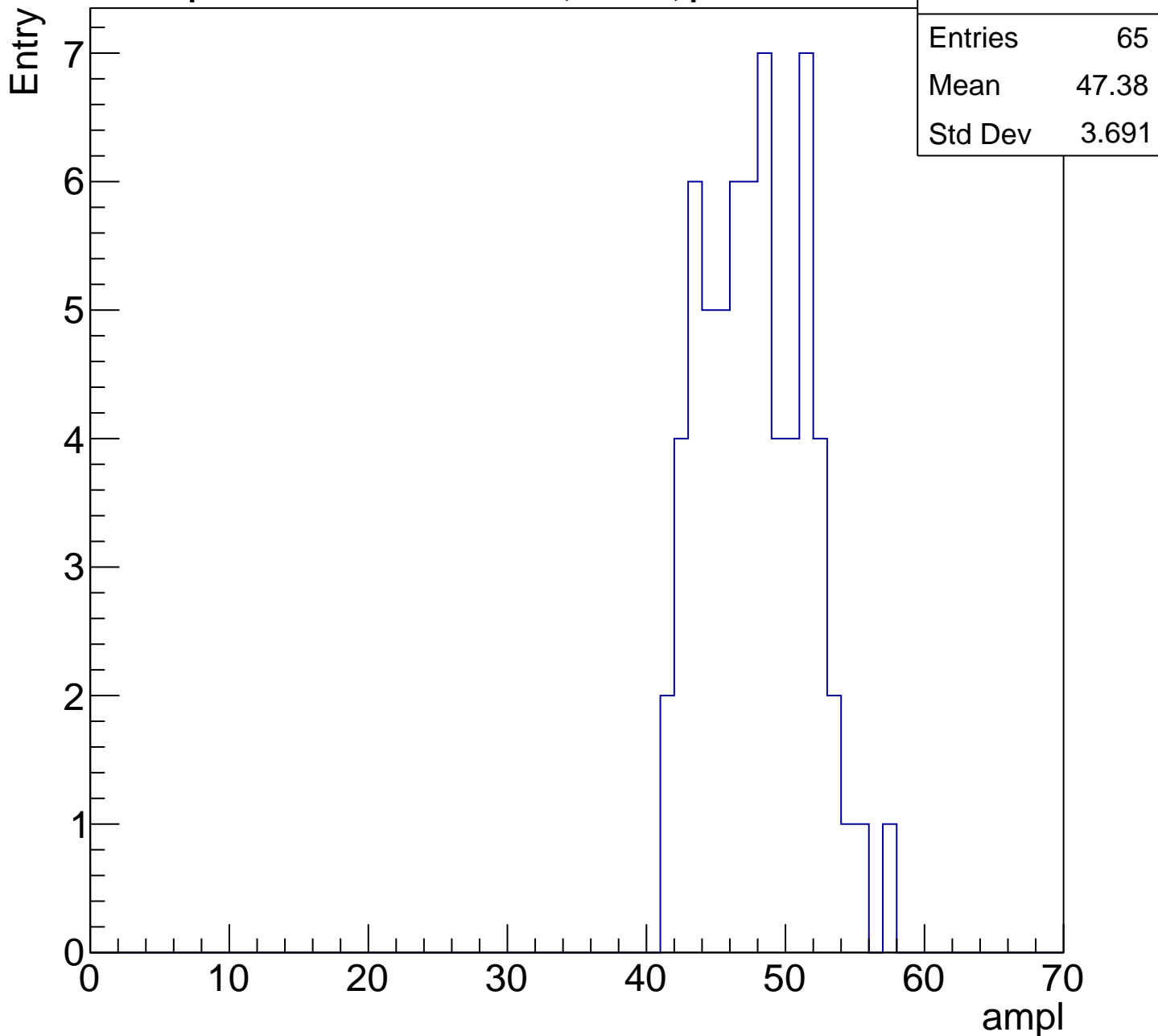
Gaus mean : 42.3119

Gaus Width: 4.6370



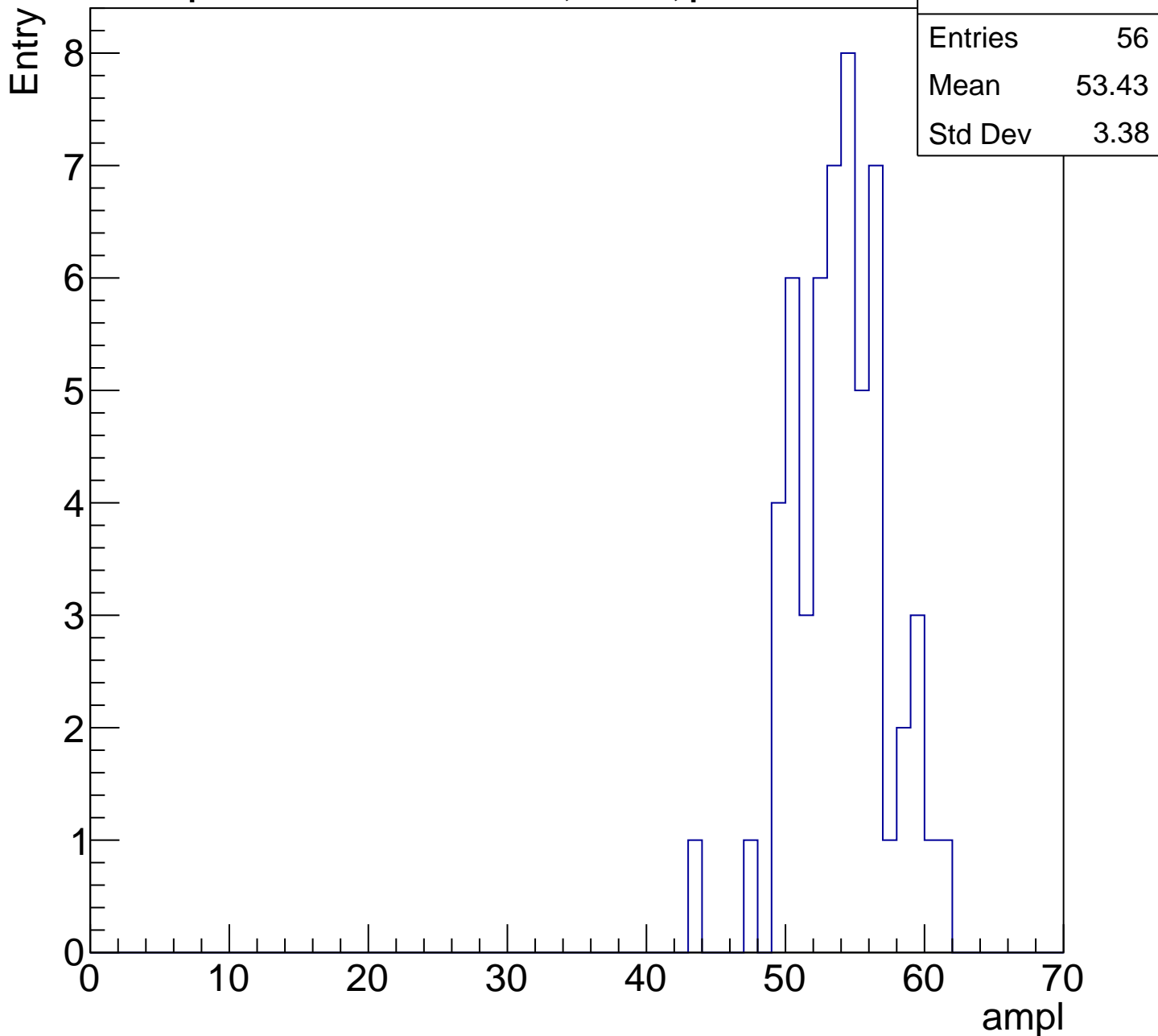
B1L102S, U2-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch68, adc4

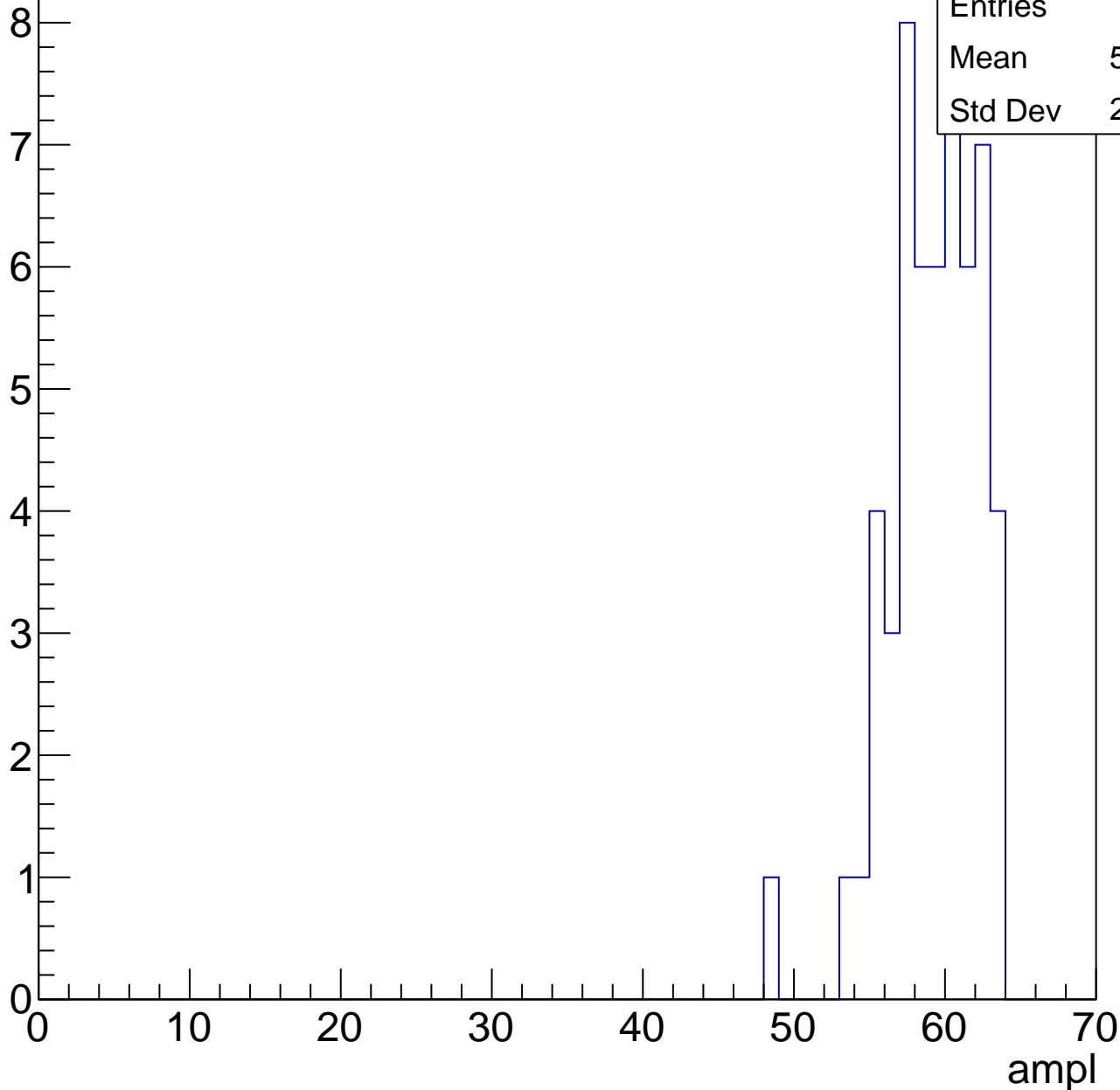
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

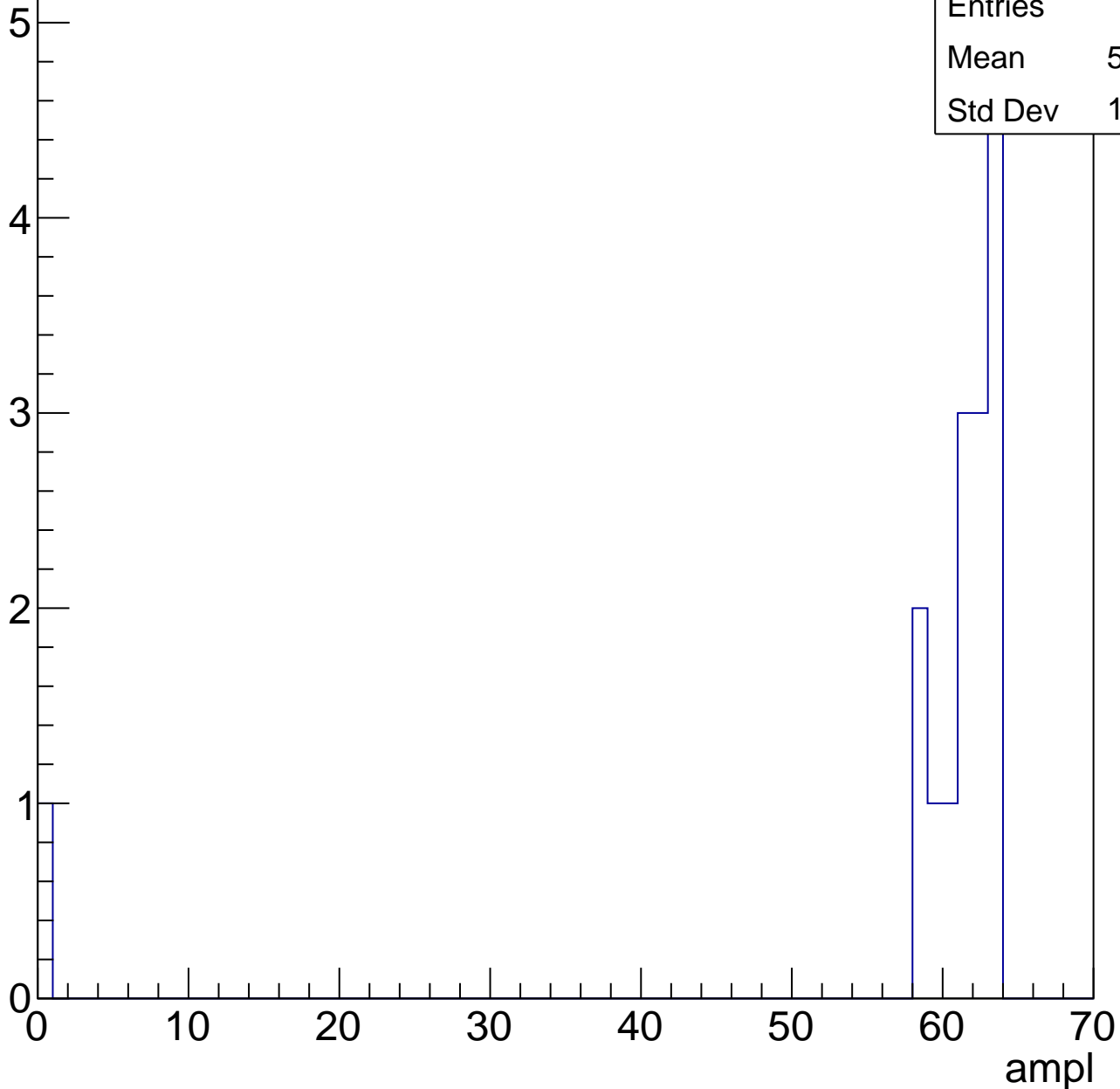


B1L102S, U2-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

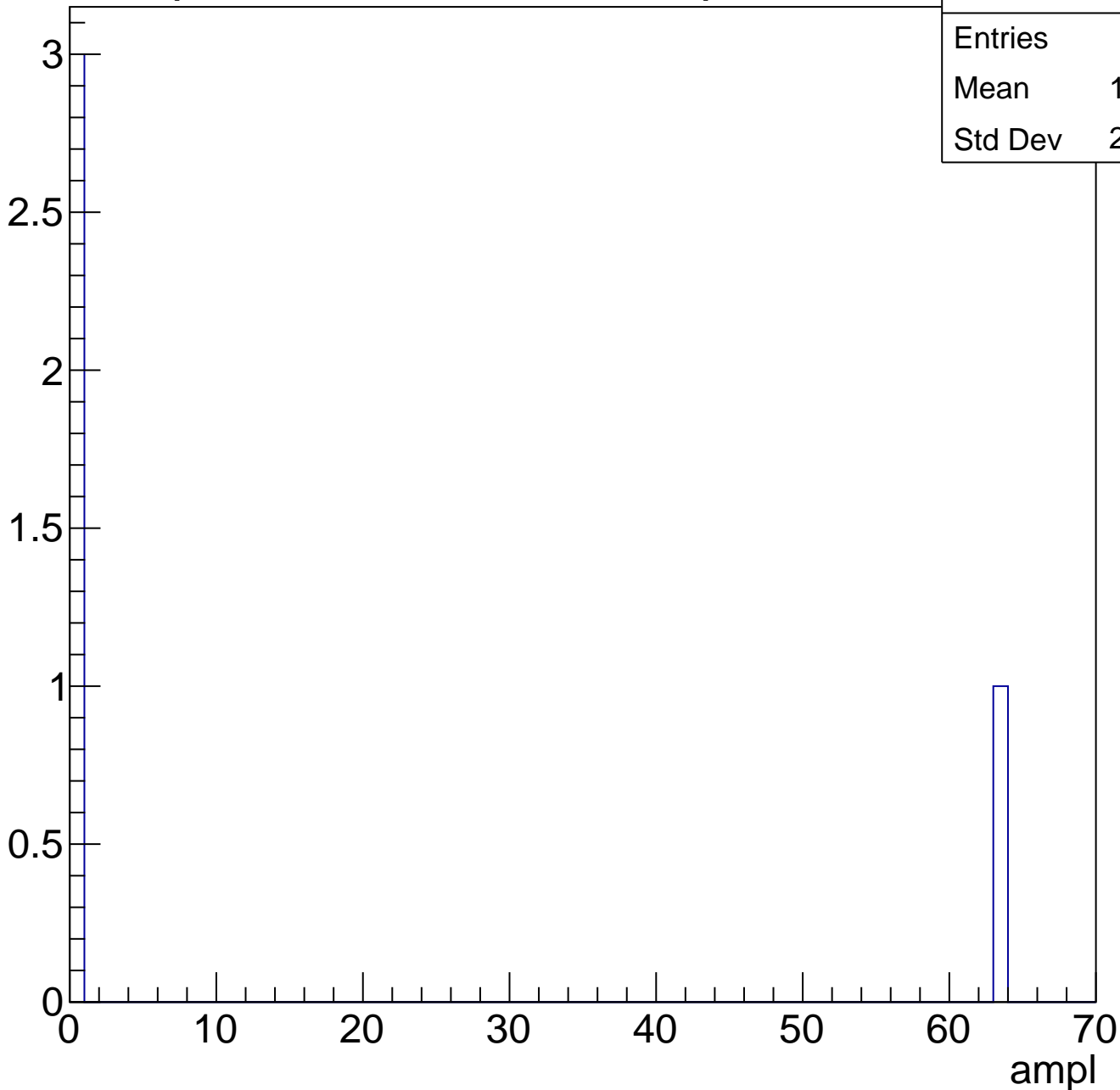
Entries	16
Mean	57.44
Std Dev	14.92



B1L102S, U2-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch69, adc0

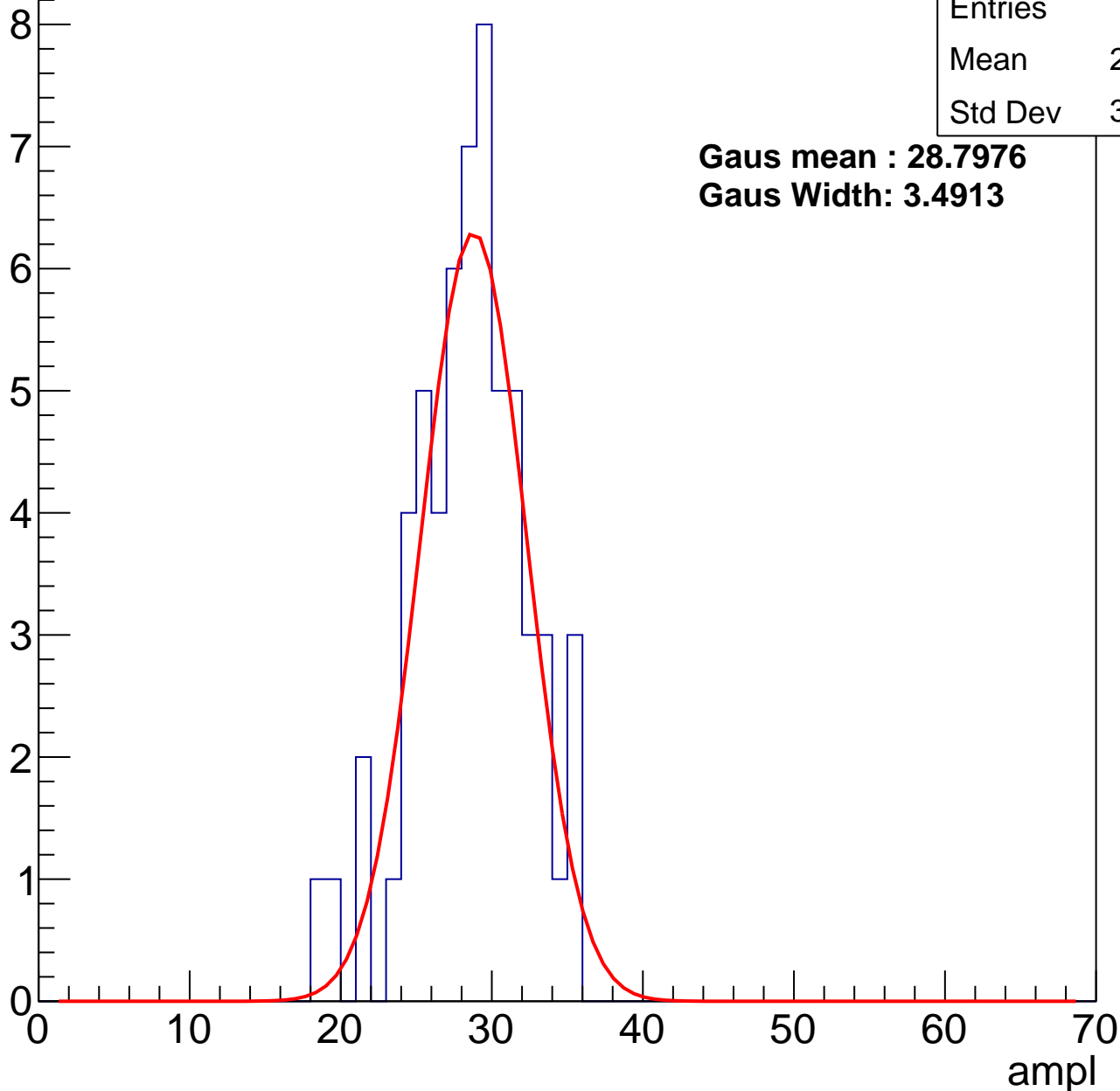
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.07
Std Dev	3.714

Gaus mean : 28.7976

Gaus Width: 3.4913



B1L102S, U2-ch69, adc1

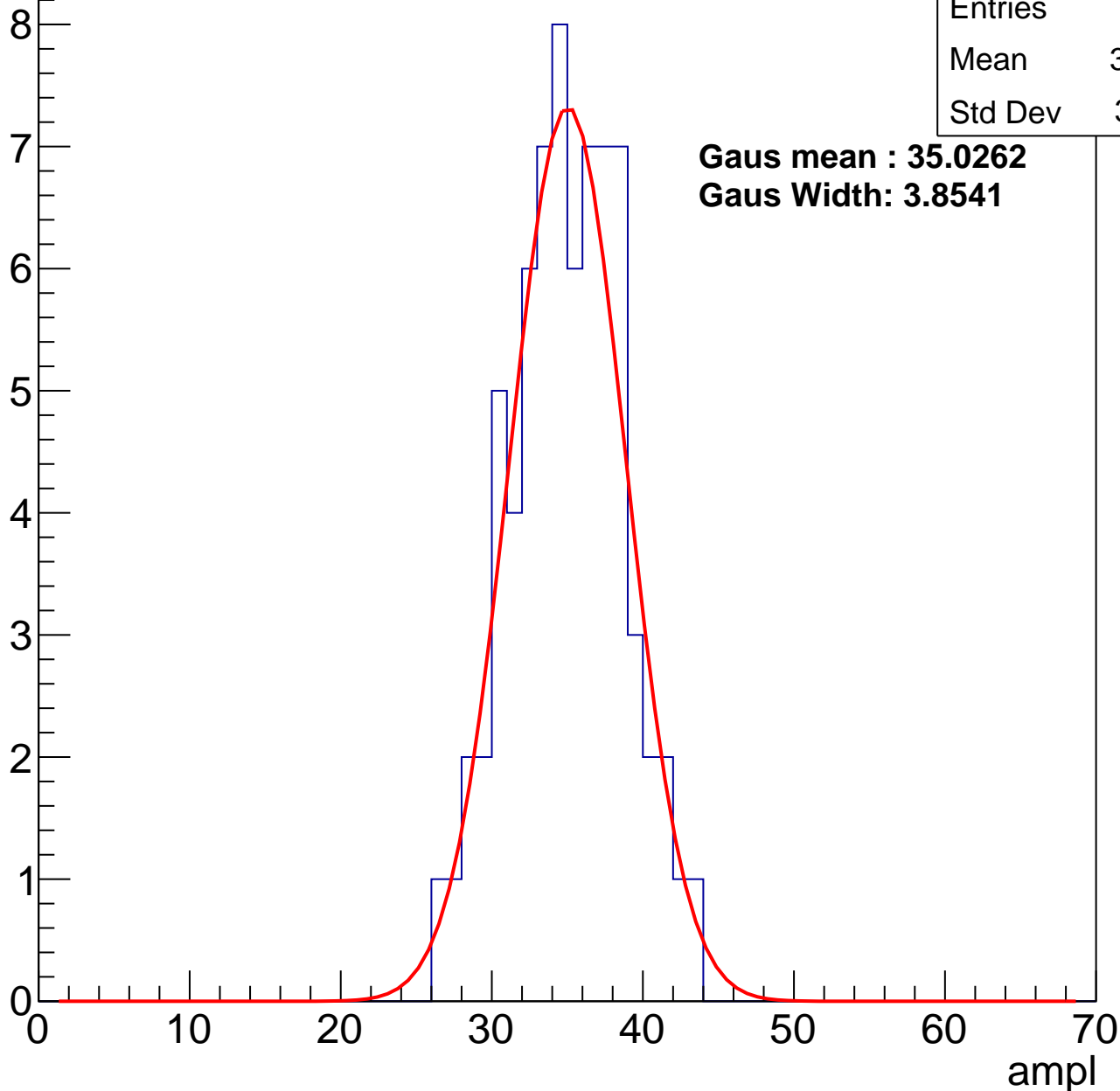
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.54
Std Dev	3.651

Gaus mean : 35.0262

Gaus Width: 3.8541



B1L102S, U2-ch69, adc2

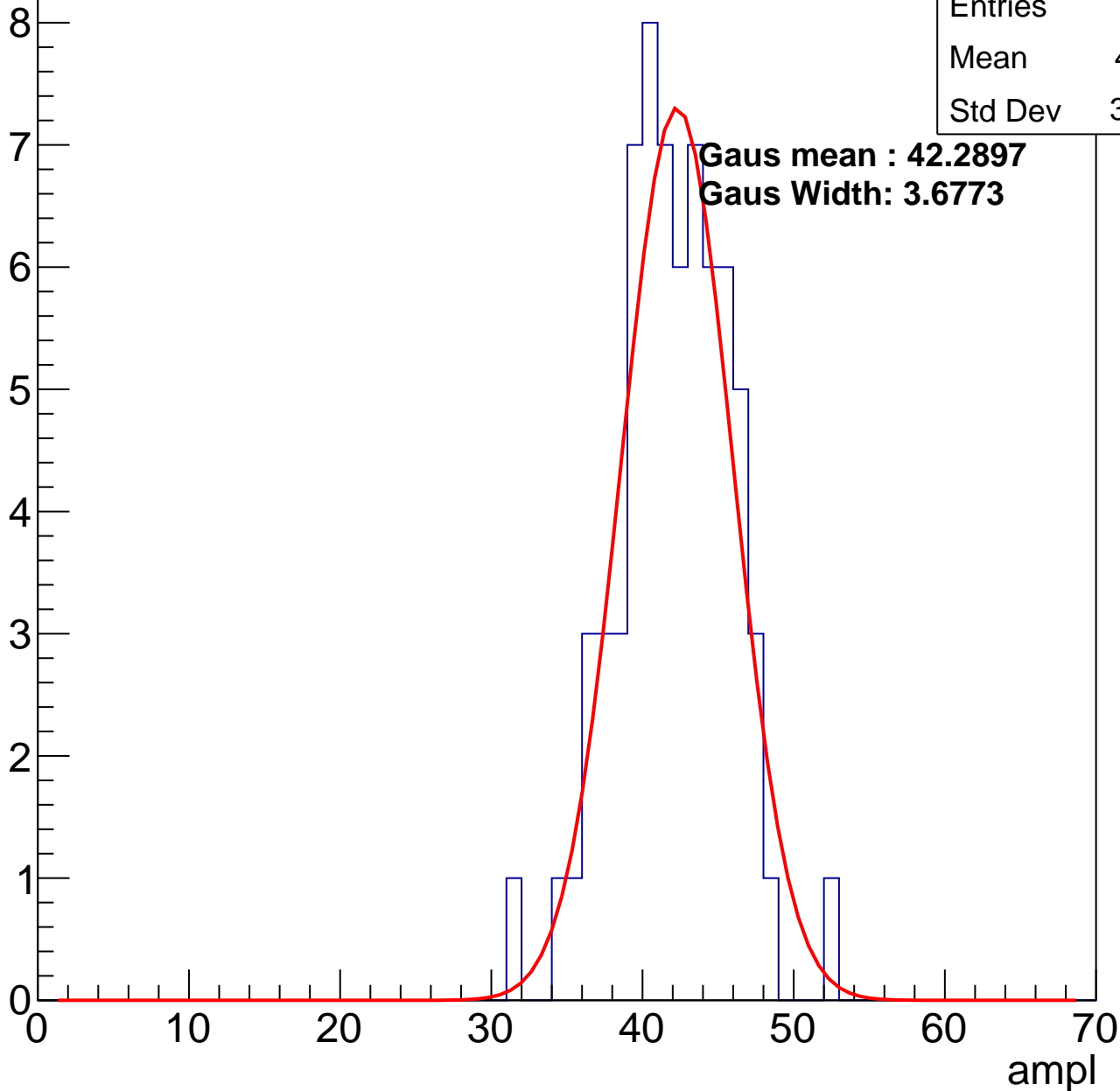
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.61
Std Dev	3.676

Gaus mean : 42.2897

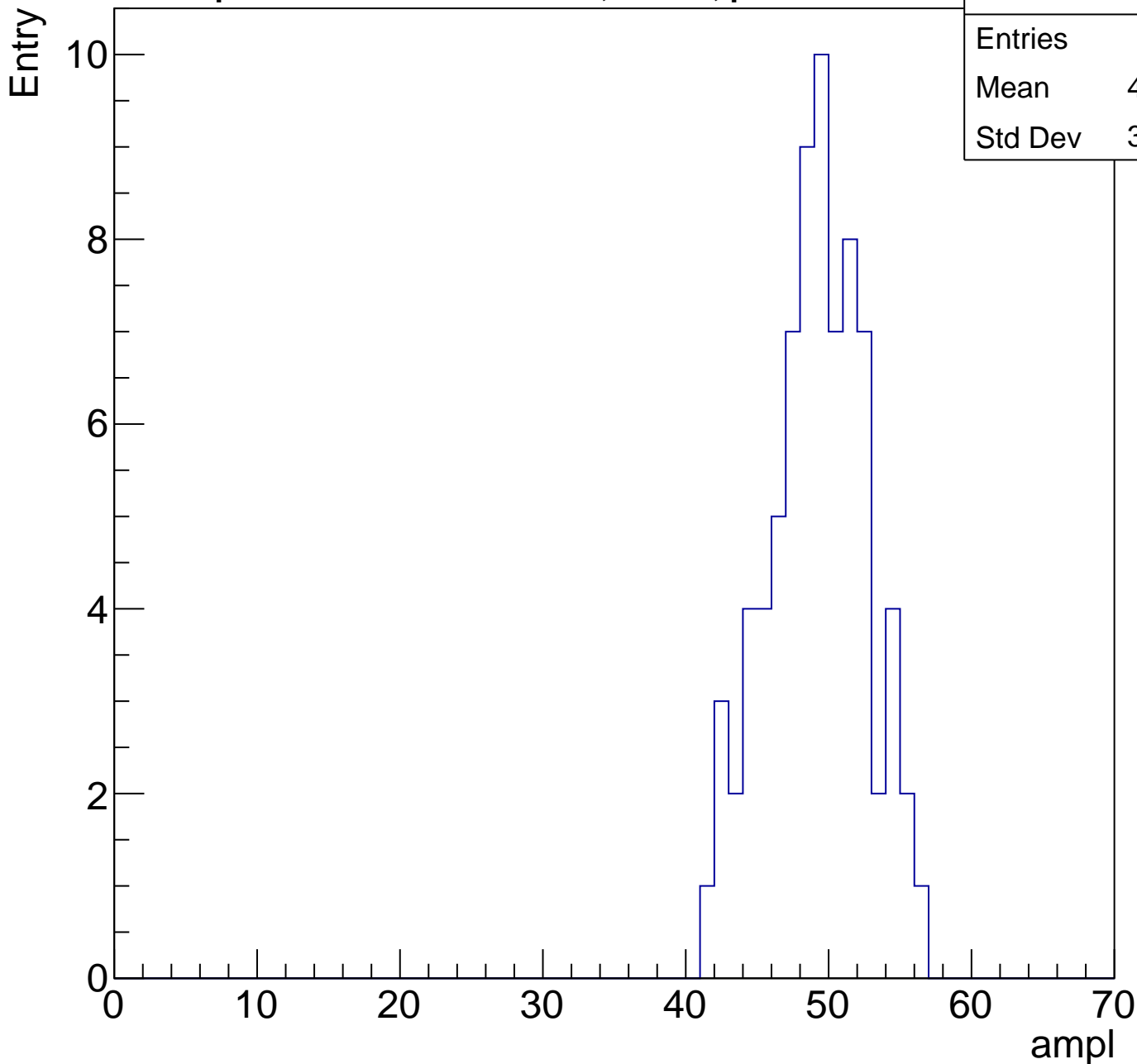
Gaus Width: 3.6773



B1L102S, U2-ch69, adc3

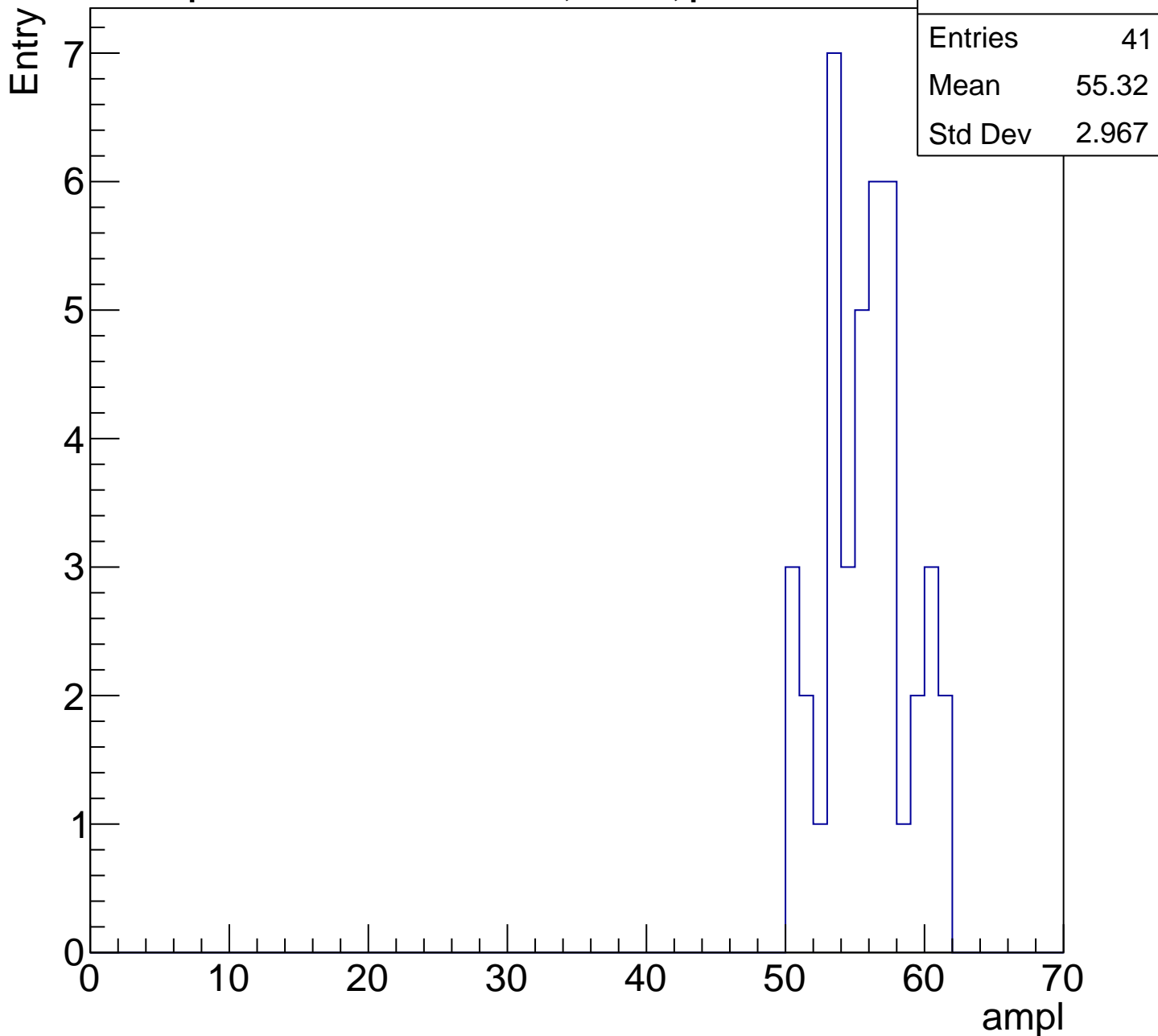
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	48.68
Std Dev	3.396



B1L102S, U2-ch69, adc4

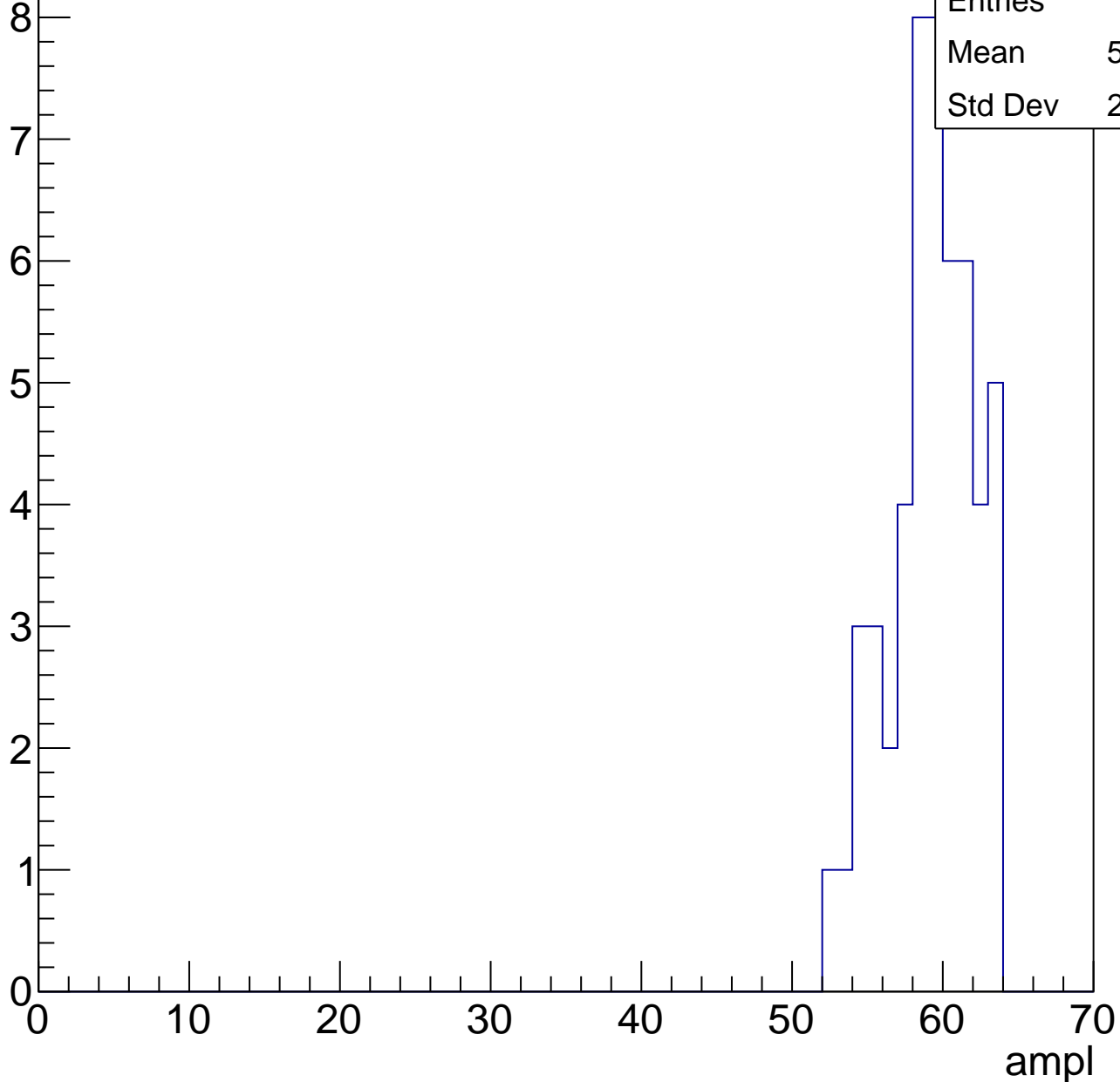
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

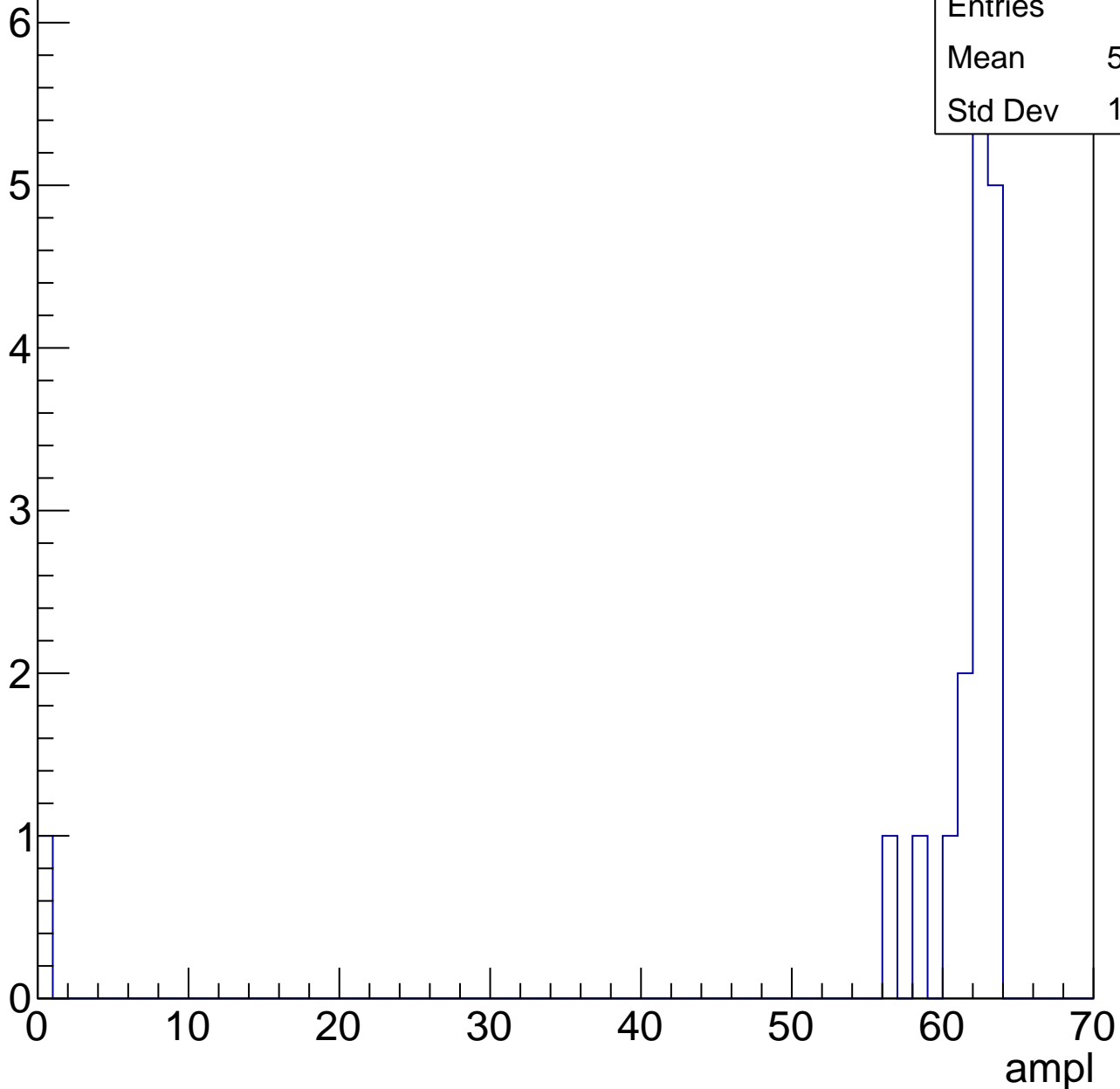


B1L102S, U2-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.82
Std Dev	14.57



B1L102S, U2-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch70, adc0

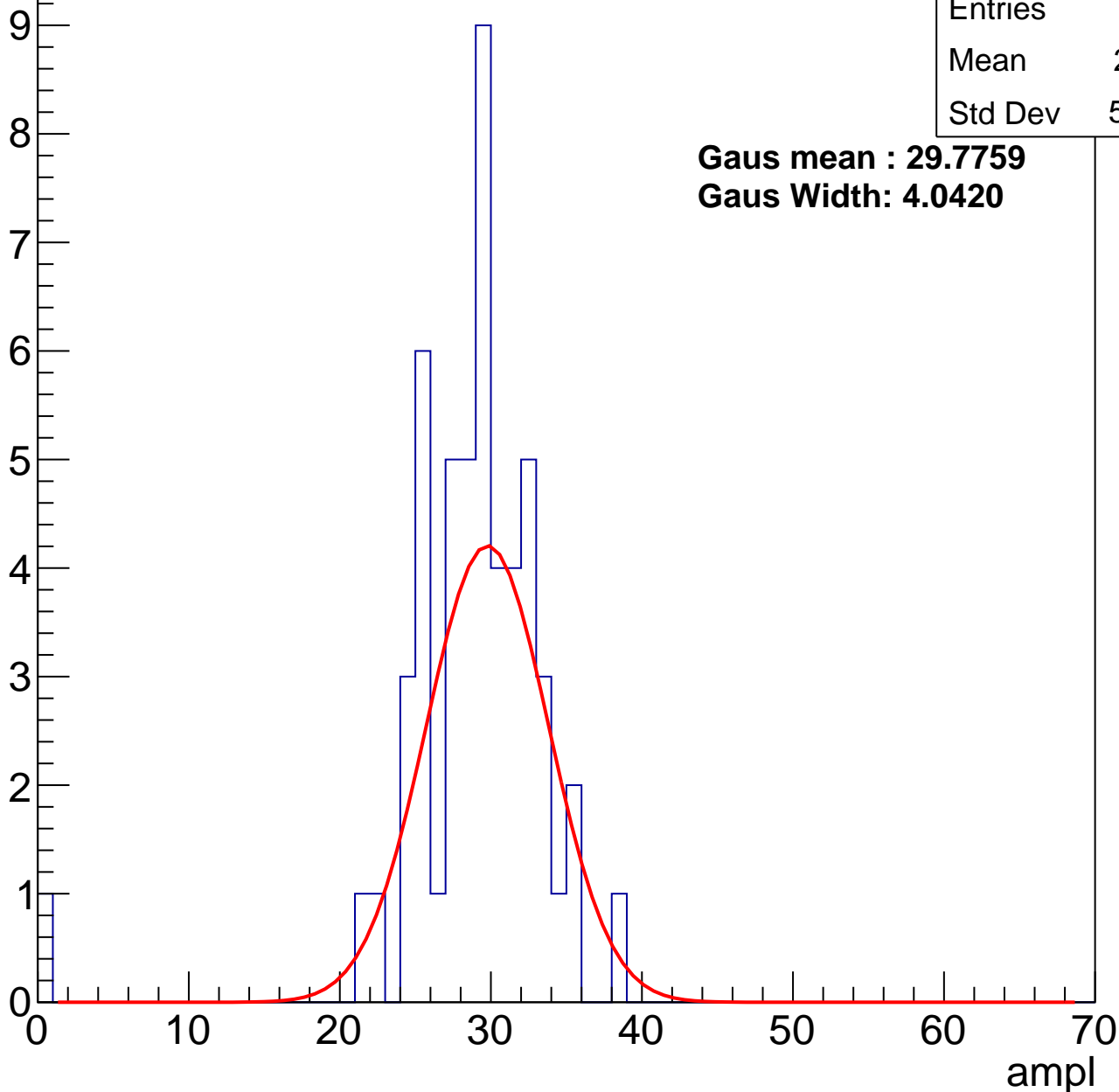
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	28.31
Std Dev	5.235

Gaus mean : 29.7759

Gaus Width: 4.0420



B1L102S, U2-ch70, adc1

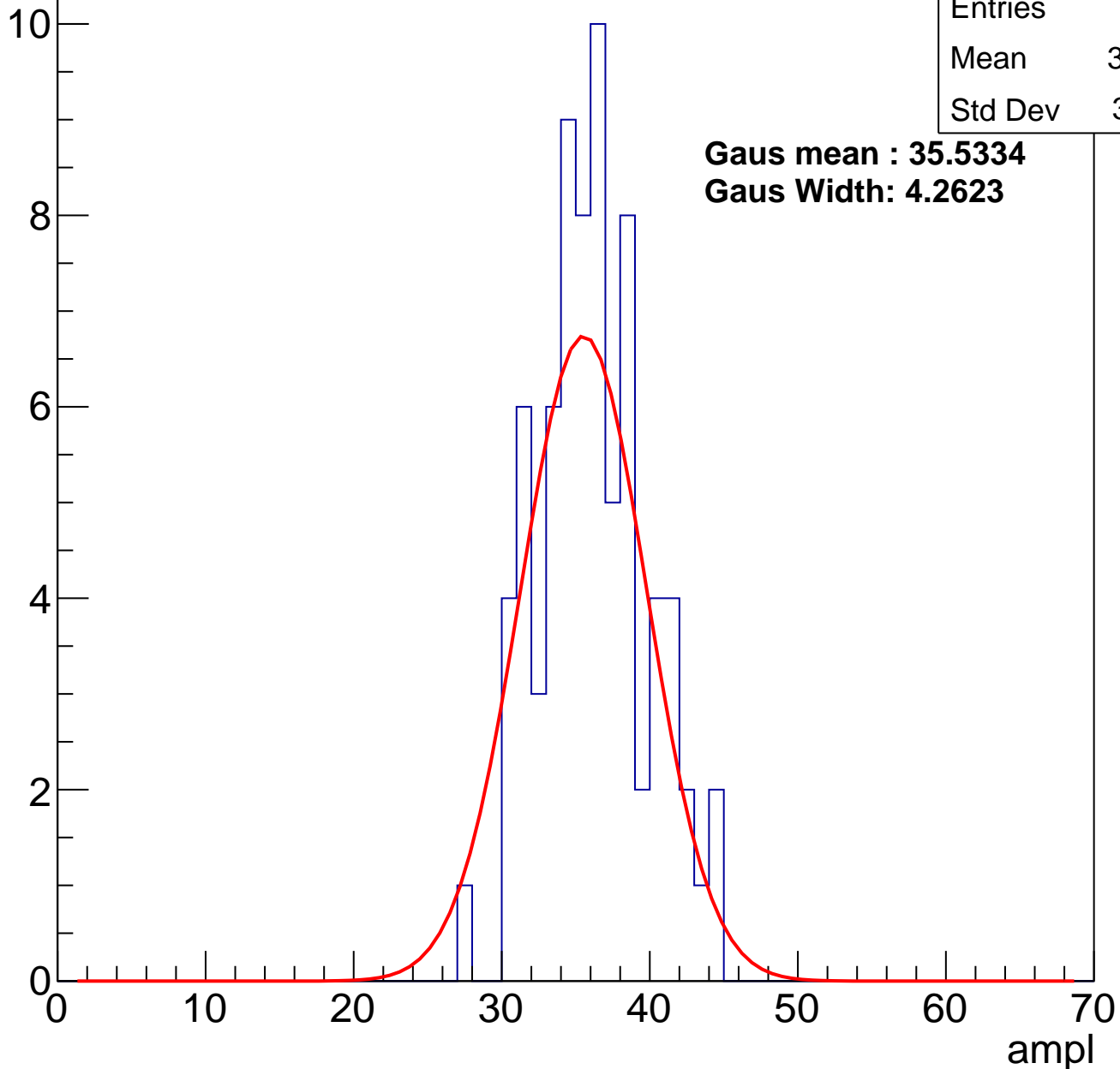
calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	35.72
Std Dev	3.621

Gaus mean : 35.5334

Gaus Width: 4.2623

Entry



B1L102S, U2-ch70, adc2

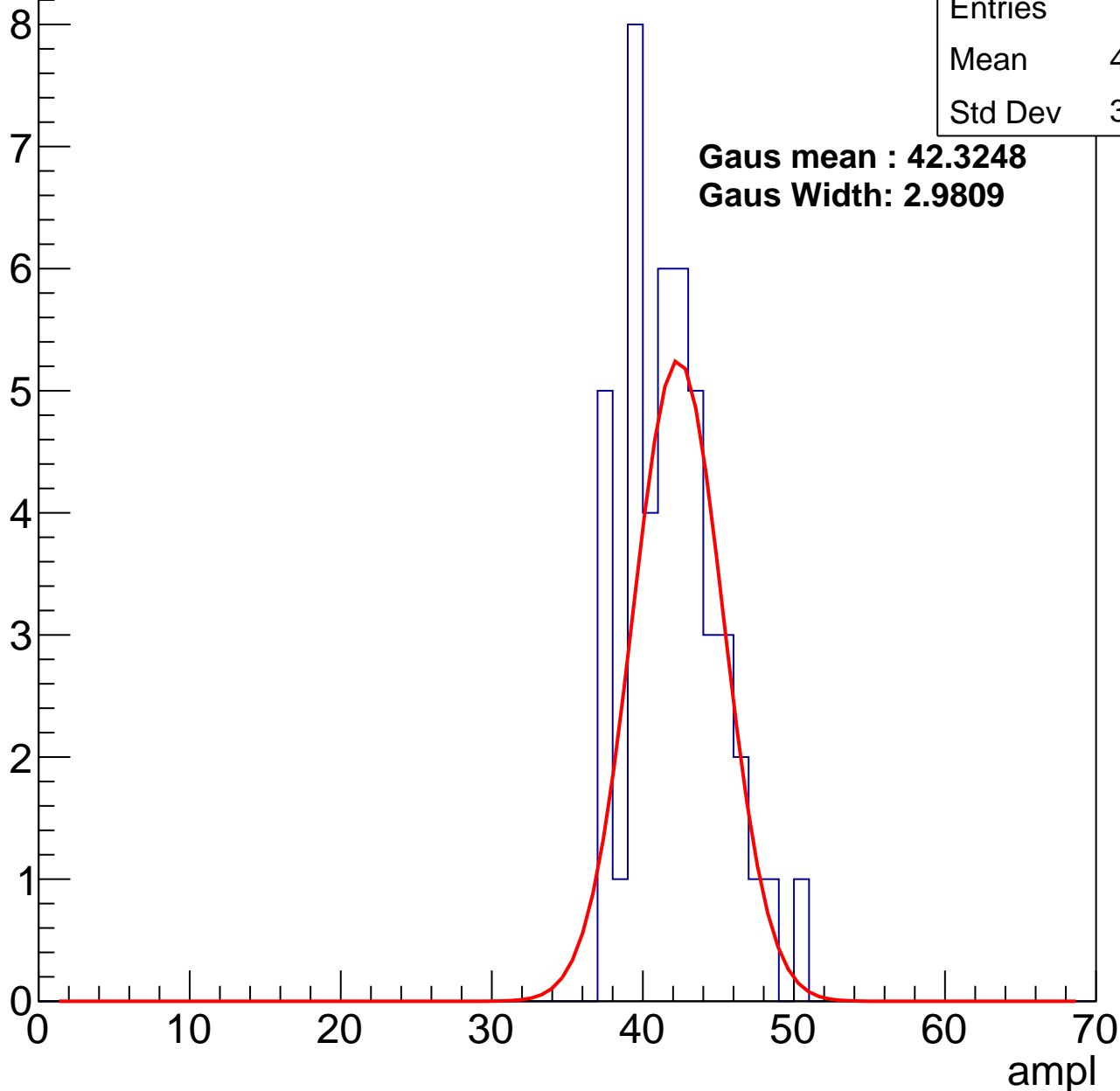
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	41.57
Std Dev	3.048

Gaus mean : 42.3248

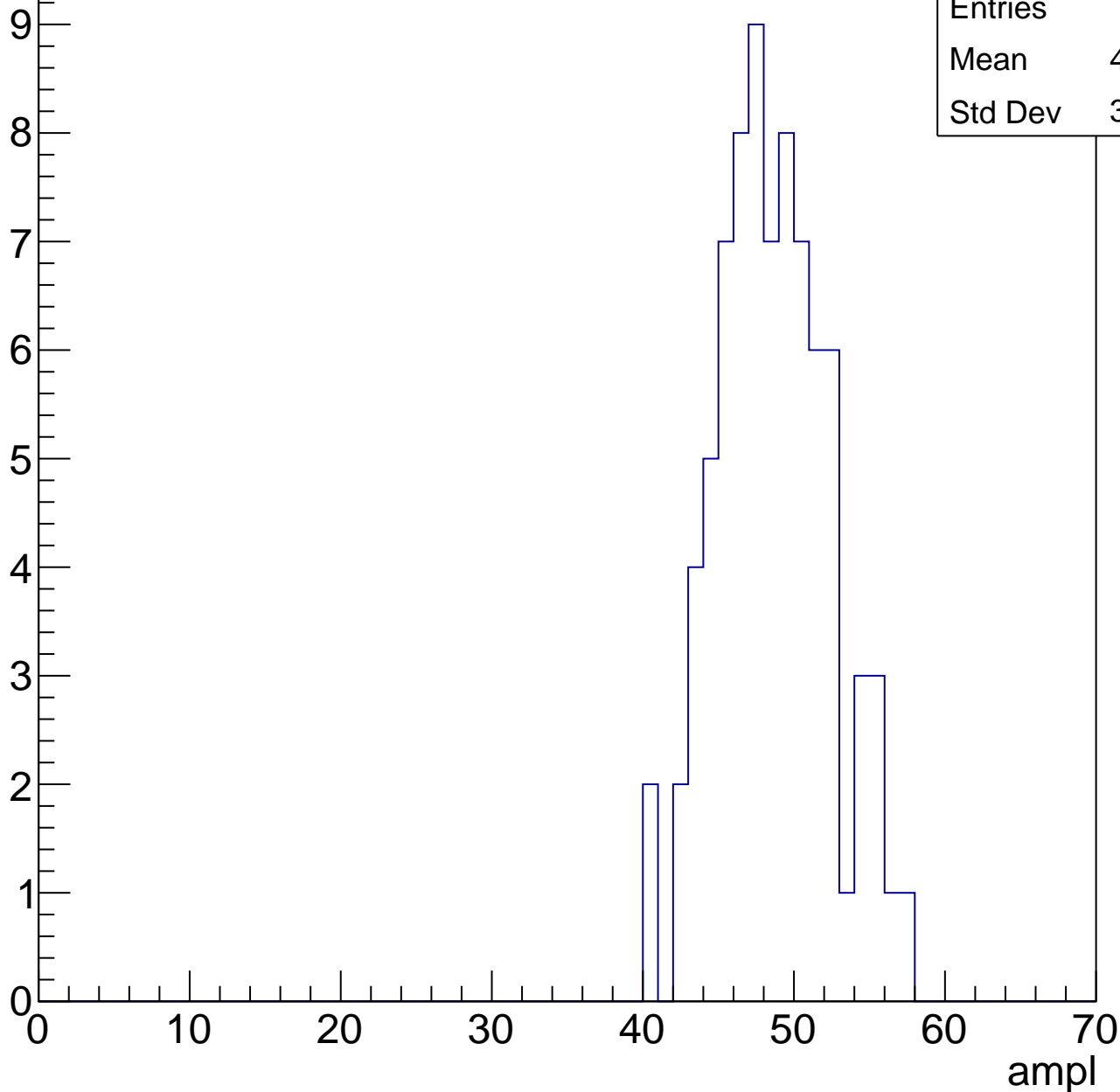
Gaus Width: 2.9809



B1L102S, U2-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

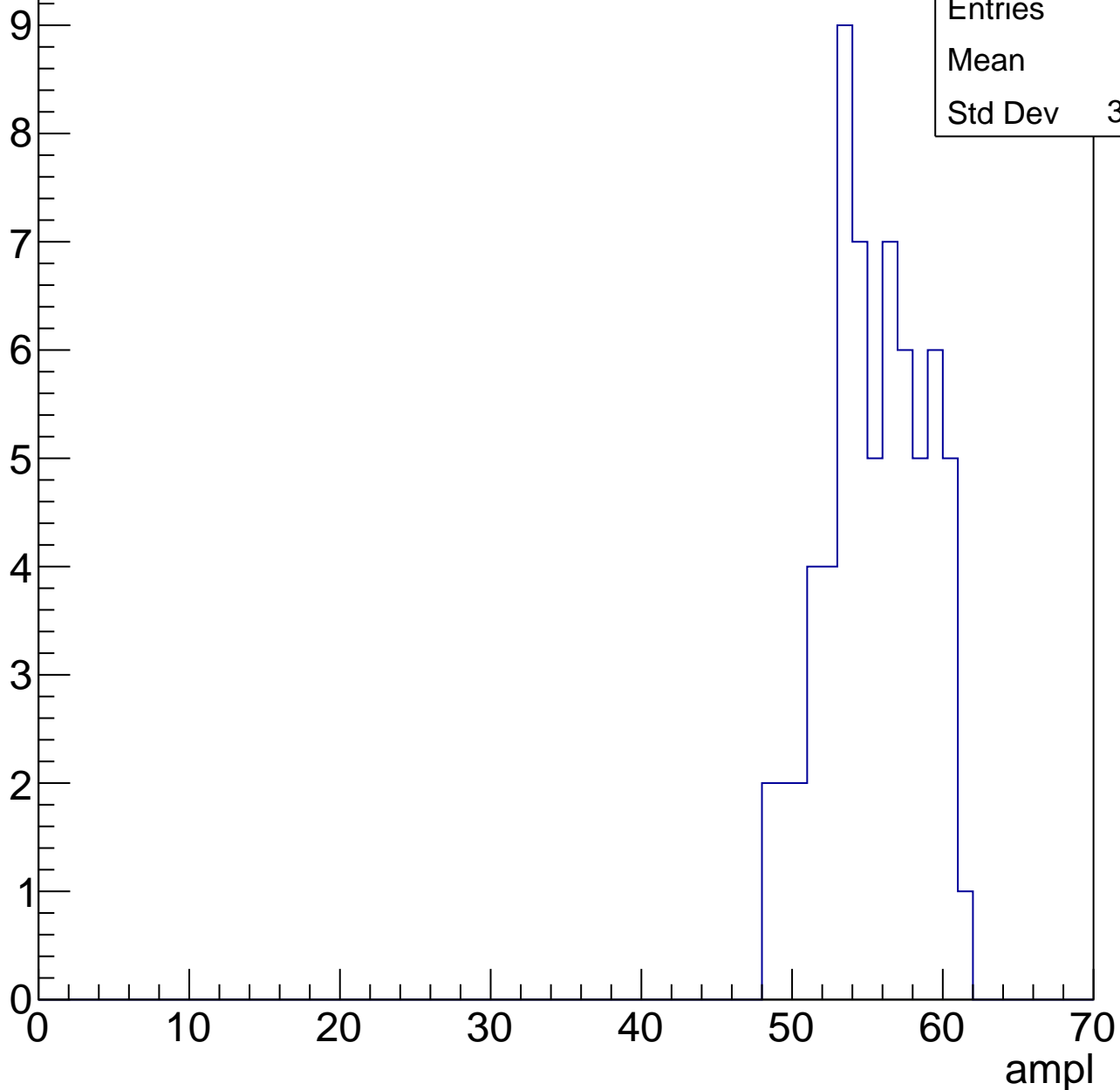
Entry



B1L102S, U2-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

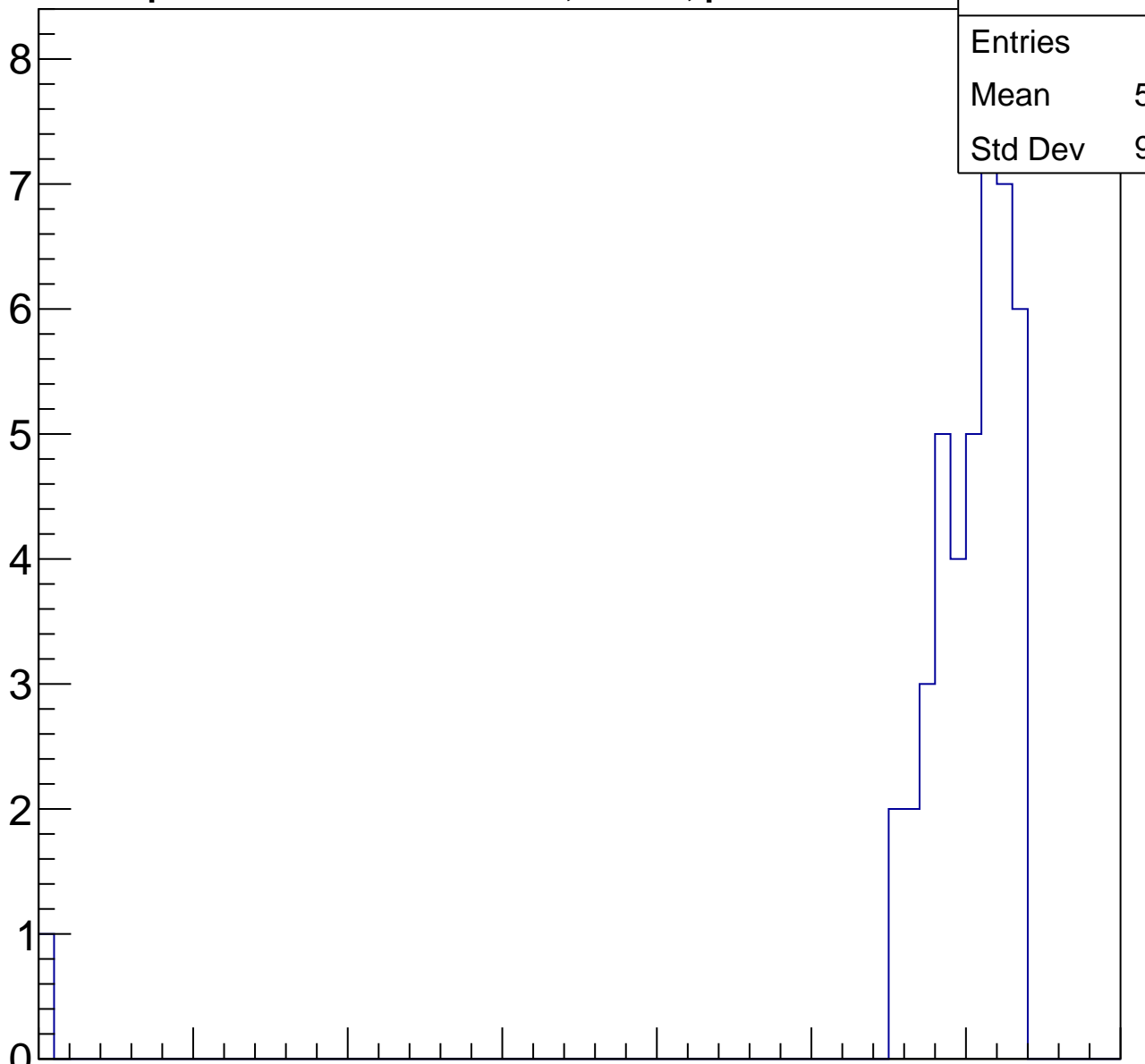
Entry

Entries	43
Mean	58.58
Std Dev	9.322

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

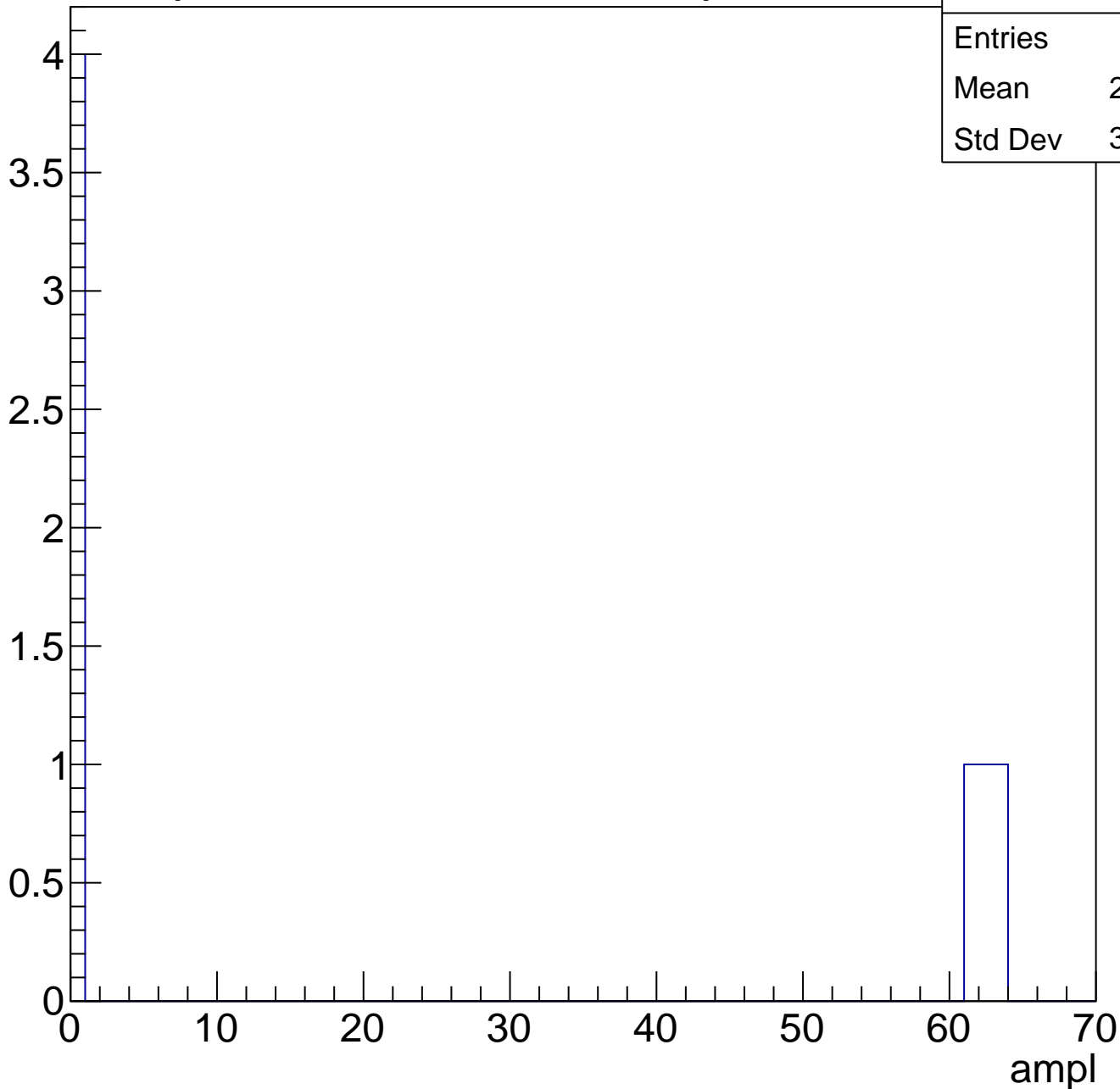
Entry



B1L102S, U2-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch71, adc0

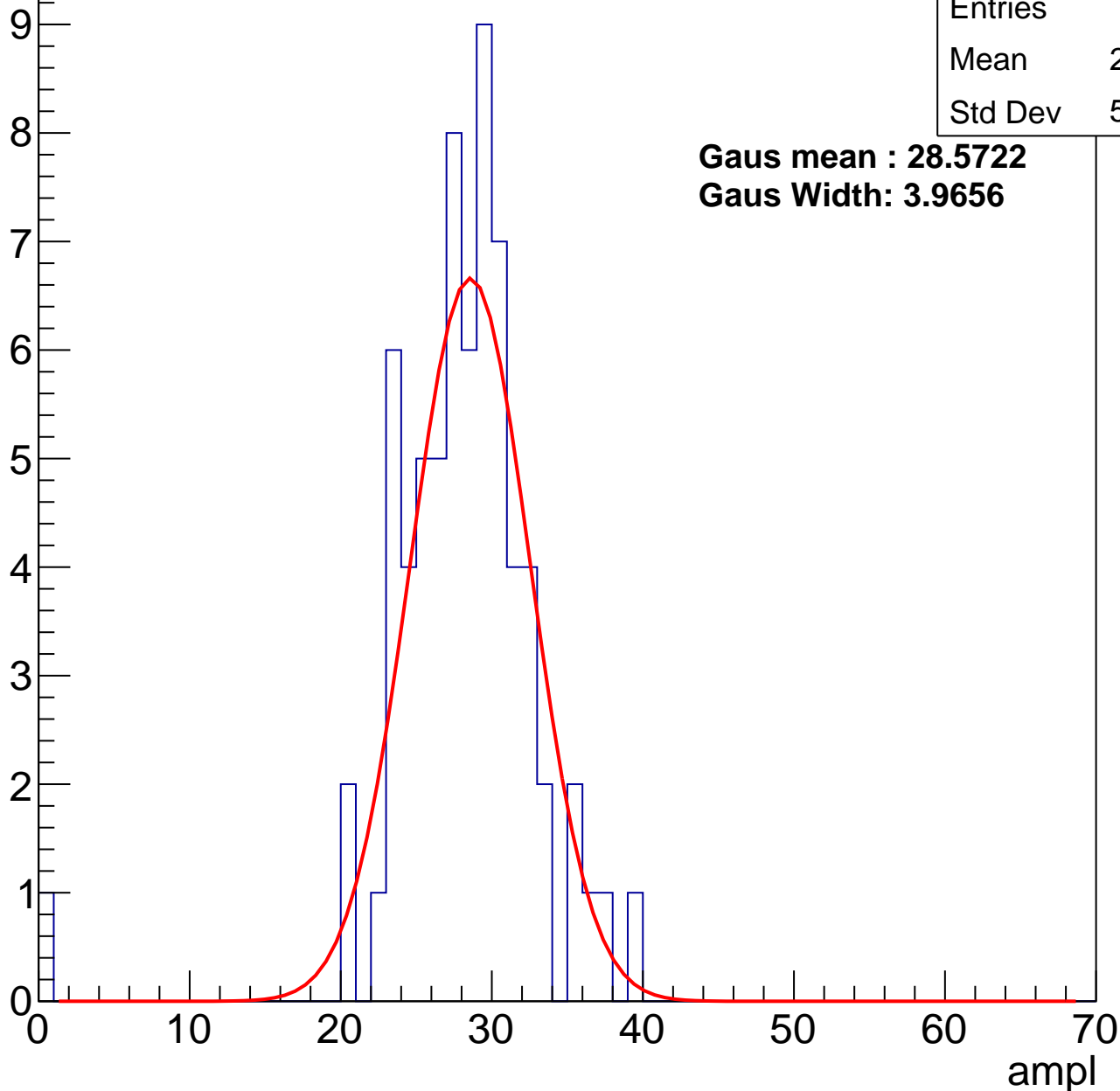
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.62
Std Dev	5.074

Gaus mean : 28.5722

Gaus Width: 3.9656



B1L102S, U2-ch71, adc1

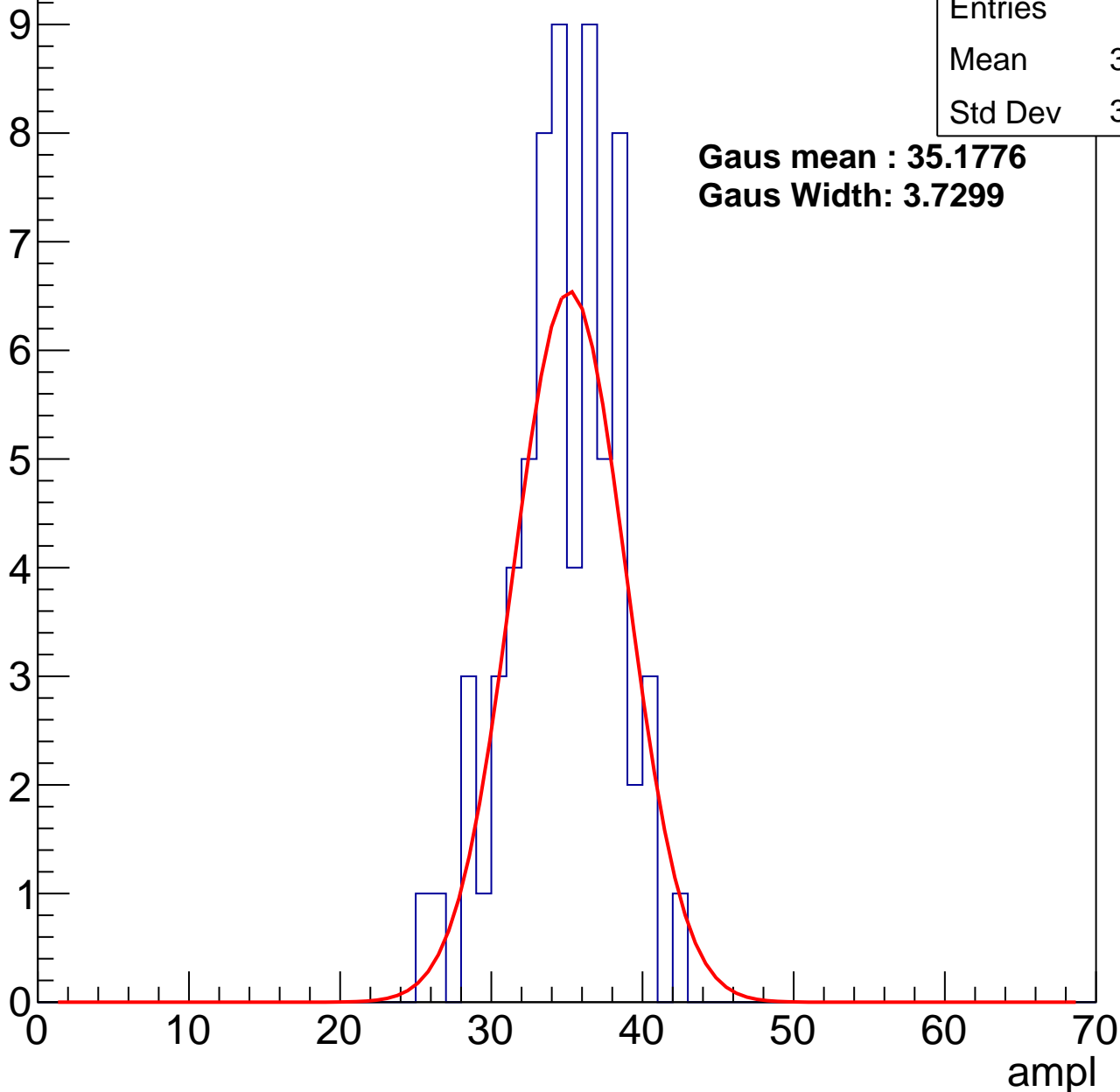
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.34
Std Dev	3.492

Gaus mean : 35.1776

Gaus Width: 3.7299



B1L102S, U2-ch71, adc2

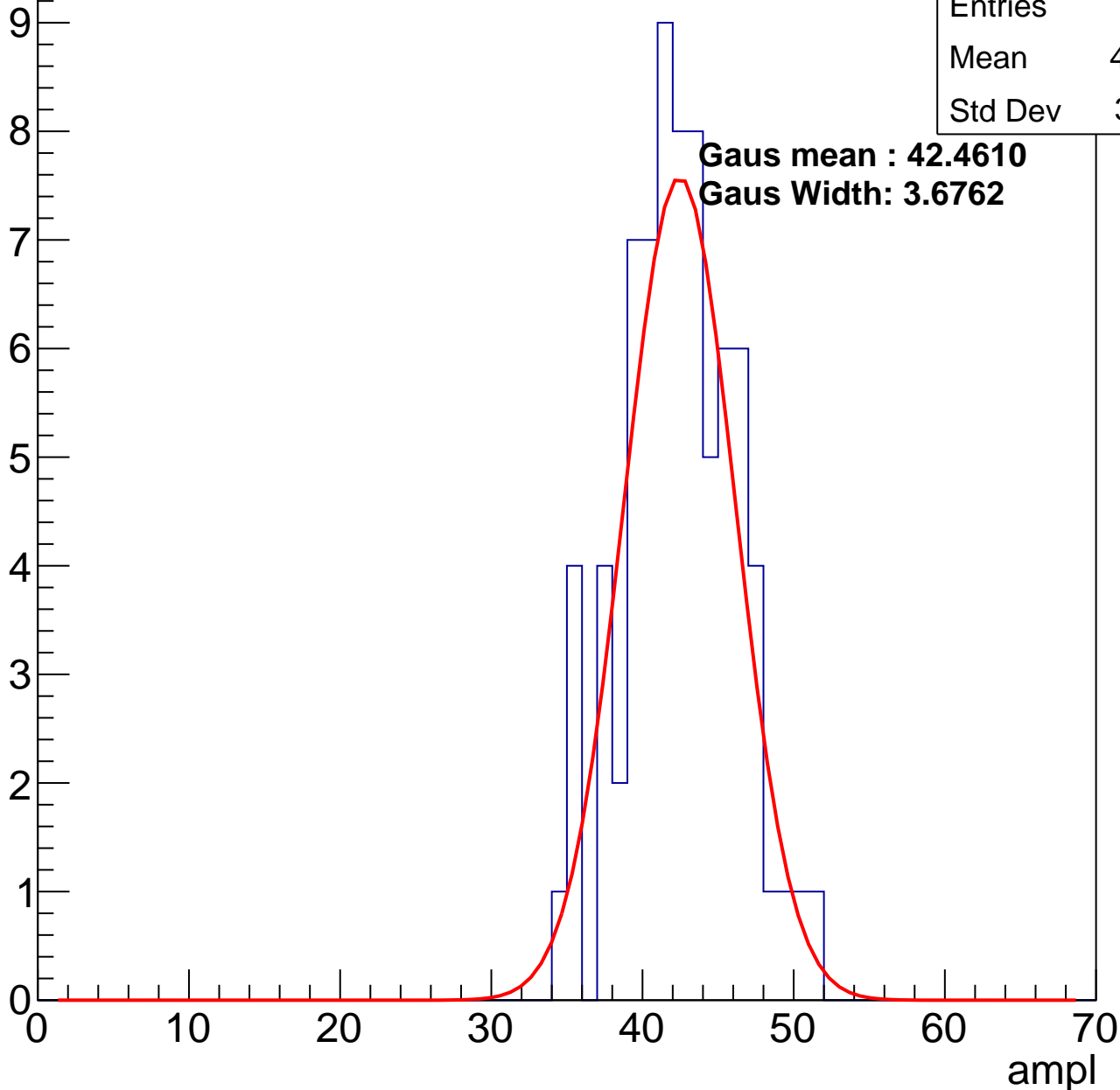
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	42.03
Std Dev	3.651

Gaus mean : 42.4610

Gaus Width: 3.6762

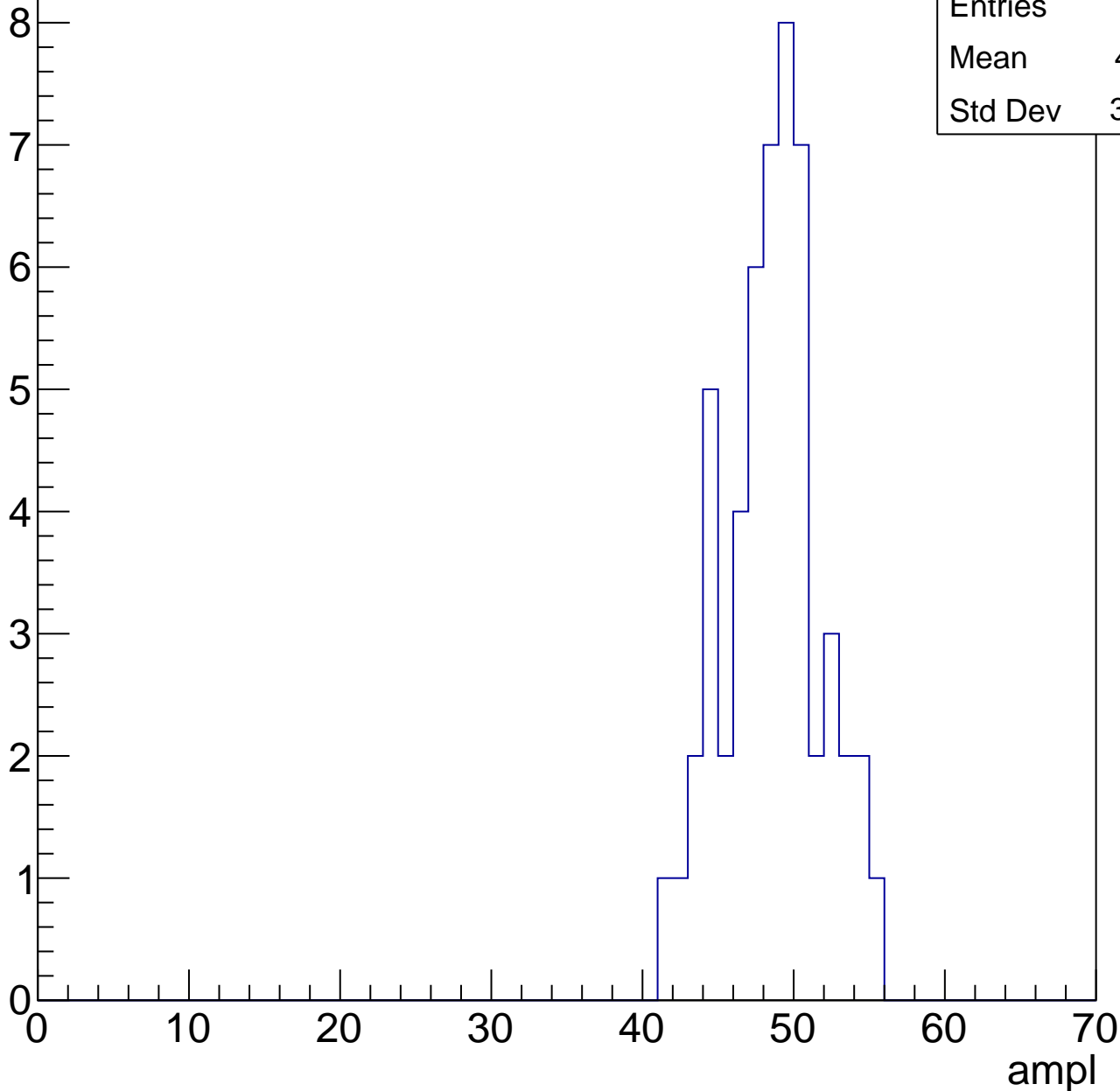


B1L102S, U2-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.11
Std Dev	3.154

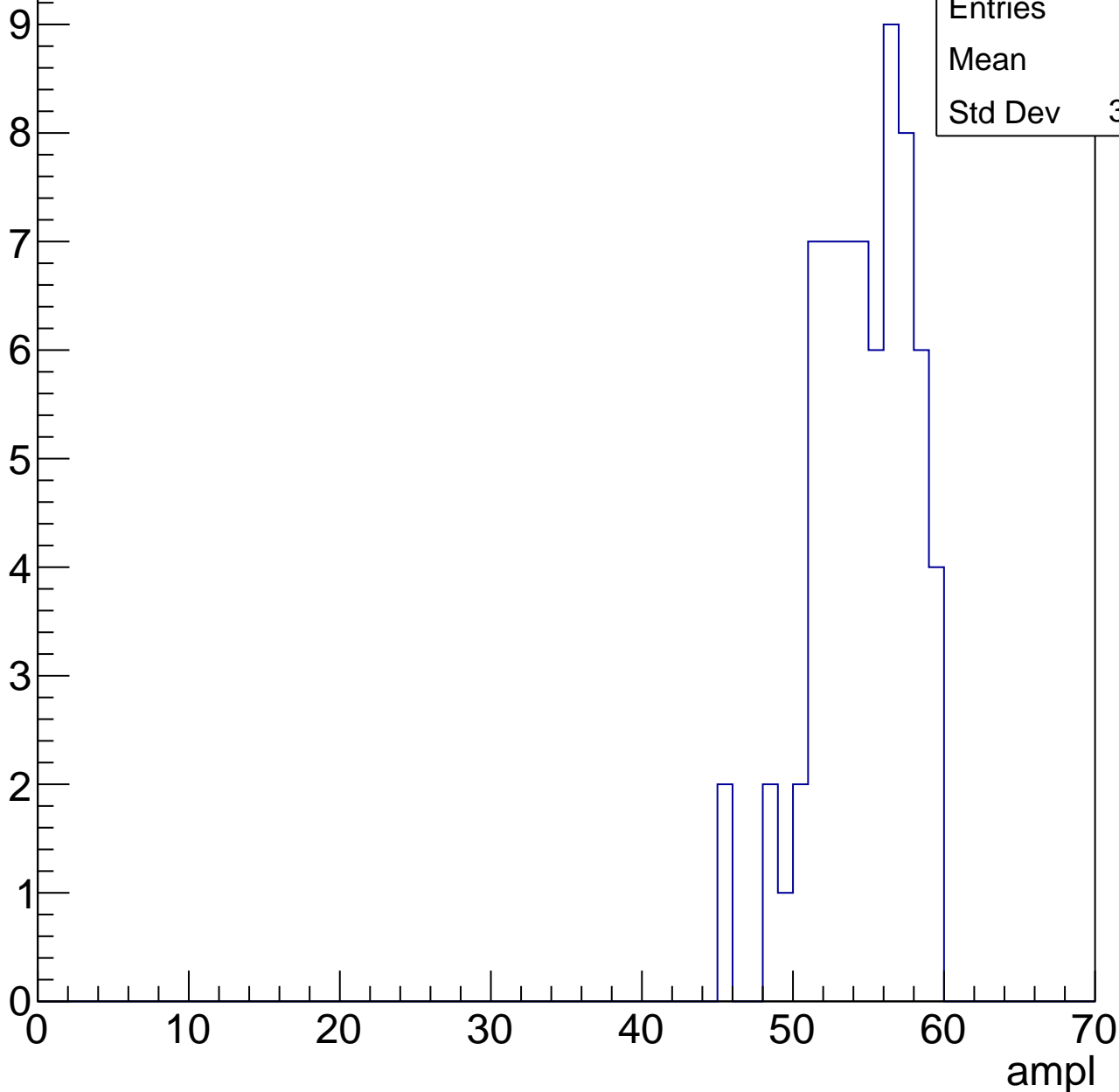


B1L102S, U2-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.1
Std Dev	3.204

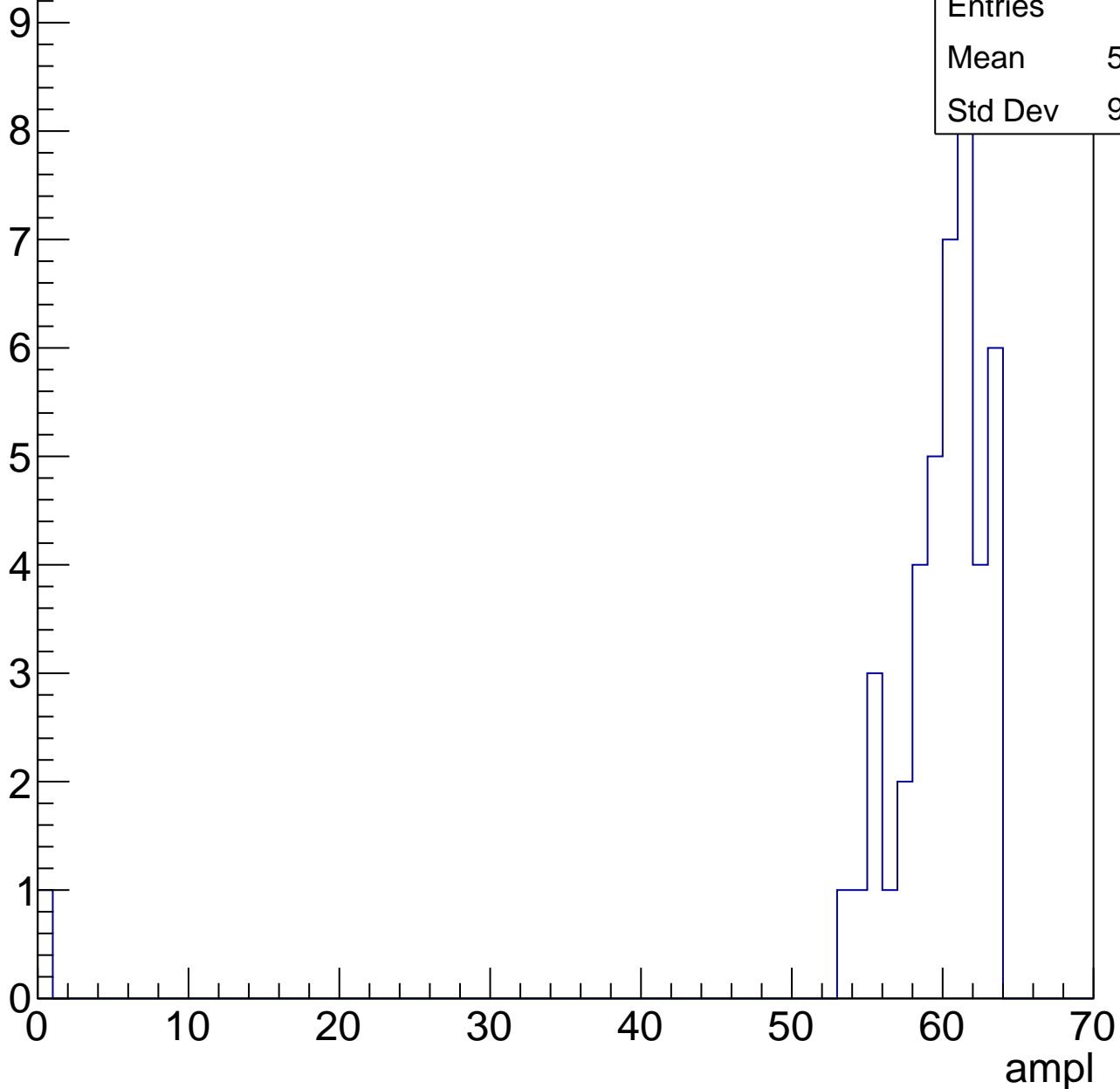


B1L102S, U2-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.27
Std Dev	9.248

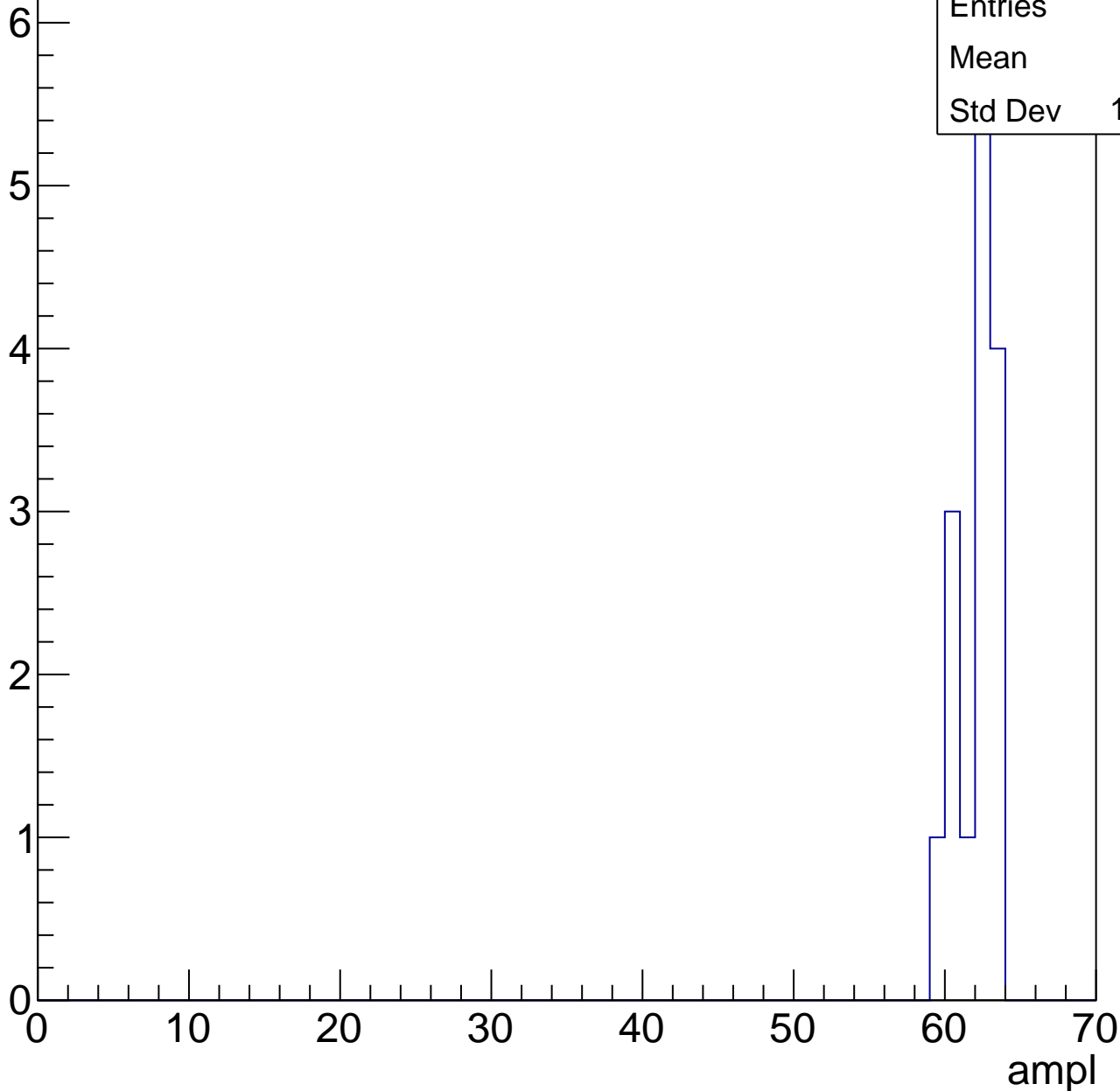


B1L102S, U2-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.6
Std Dev	1.254



B1L102S, U2-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch72, adc0

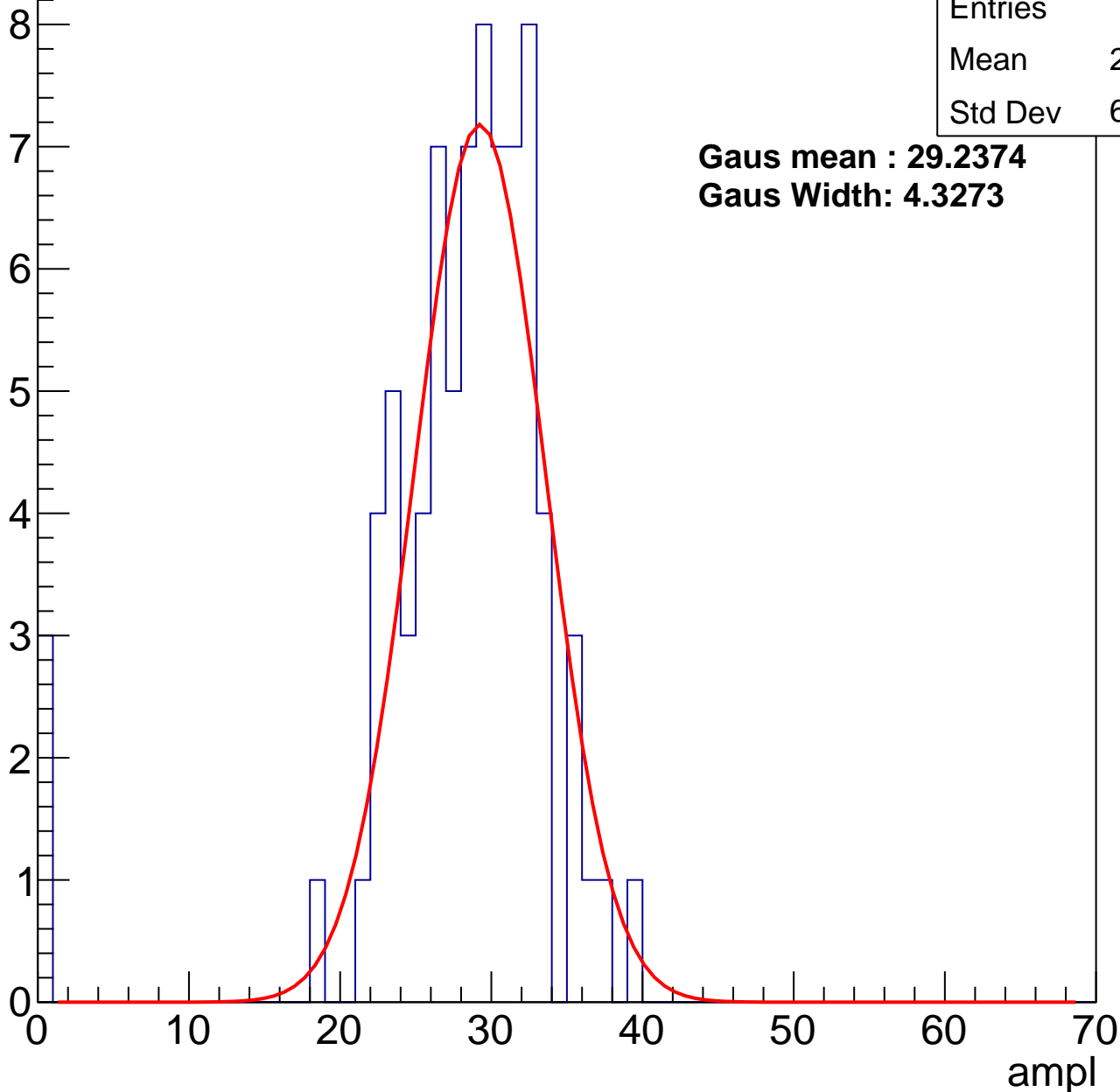
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	27.39
Std Dev	6.705

Gaus mean : 29.2374

Gaus Width: 4.3273



B1L102S, U2-ch72, adc1

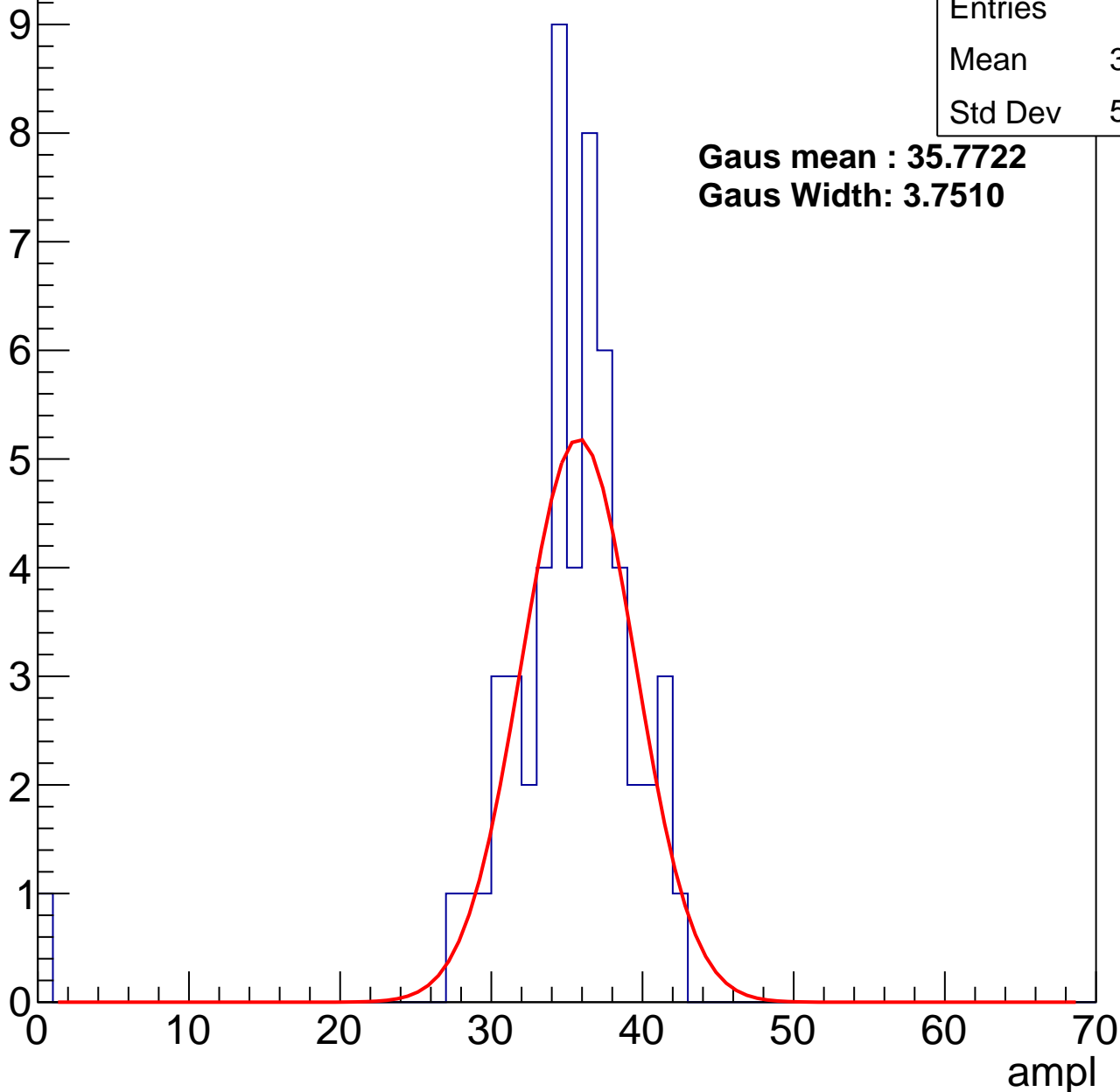
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	34.44
Std Dev	5.768

Gaus mean : 35.7722

Gaus Width: 3.7510



B1L102S, U2-ch72, adc2

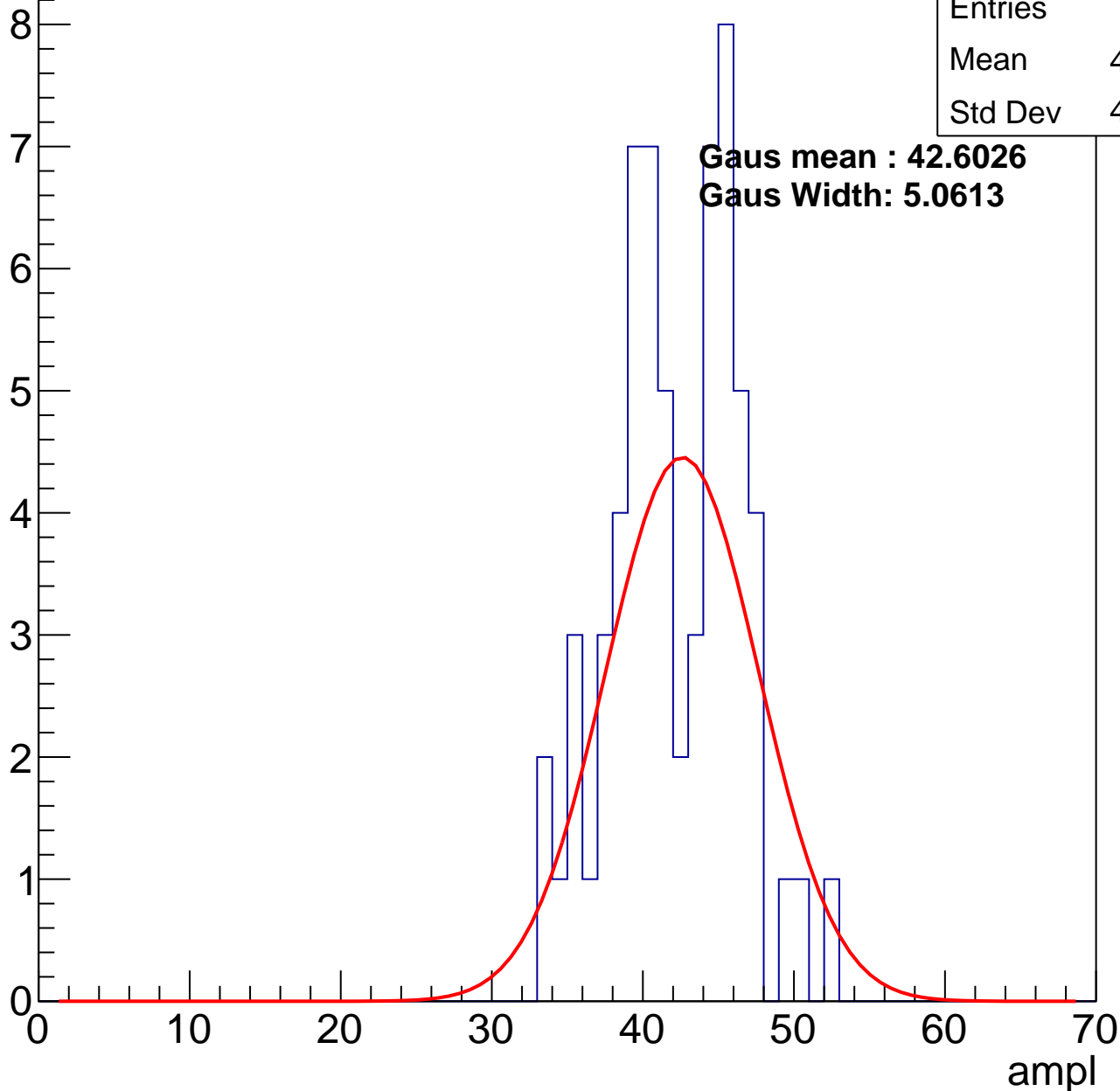
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.72
Std Dev	4.179

Gaus mean : 42.6026

Gaus Width: 5.0613

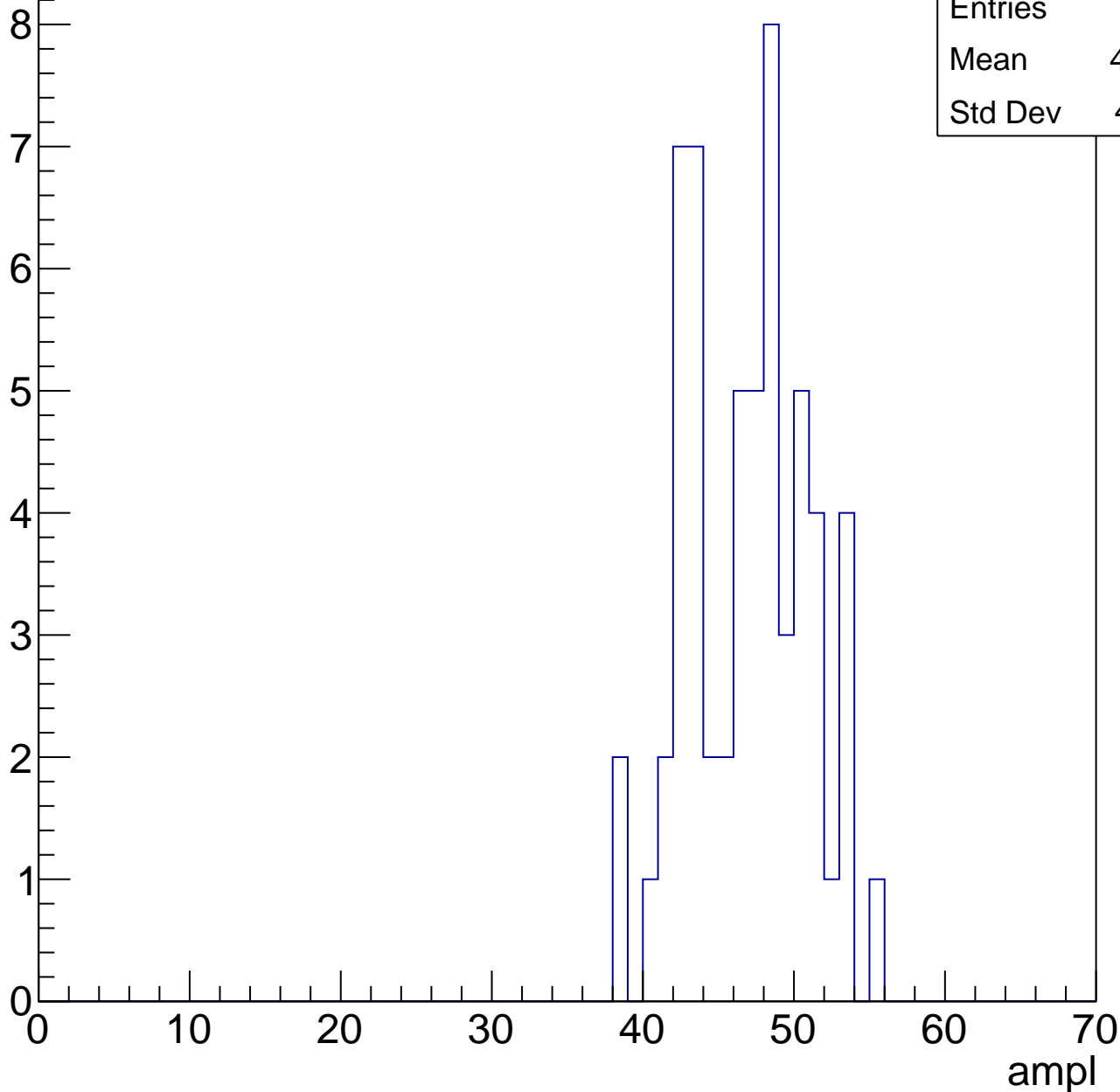


B1L102S, U2-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	46.44
Std Dev	4.001

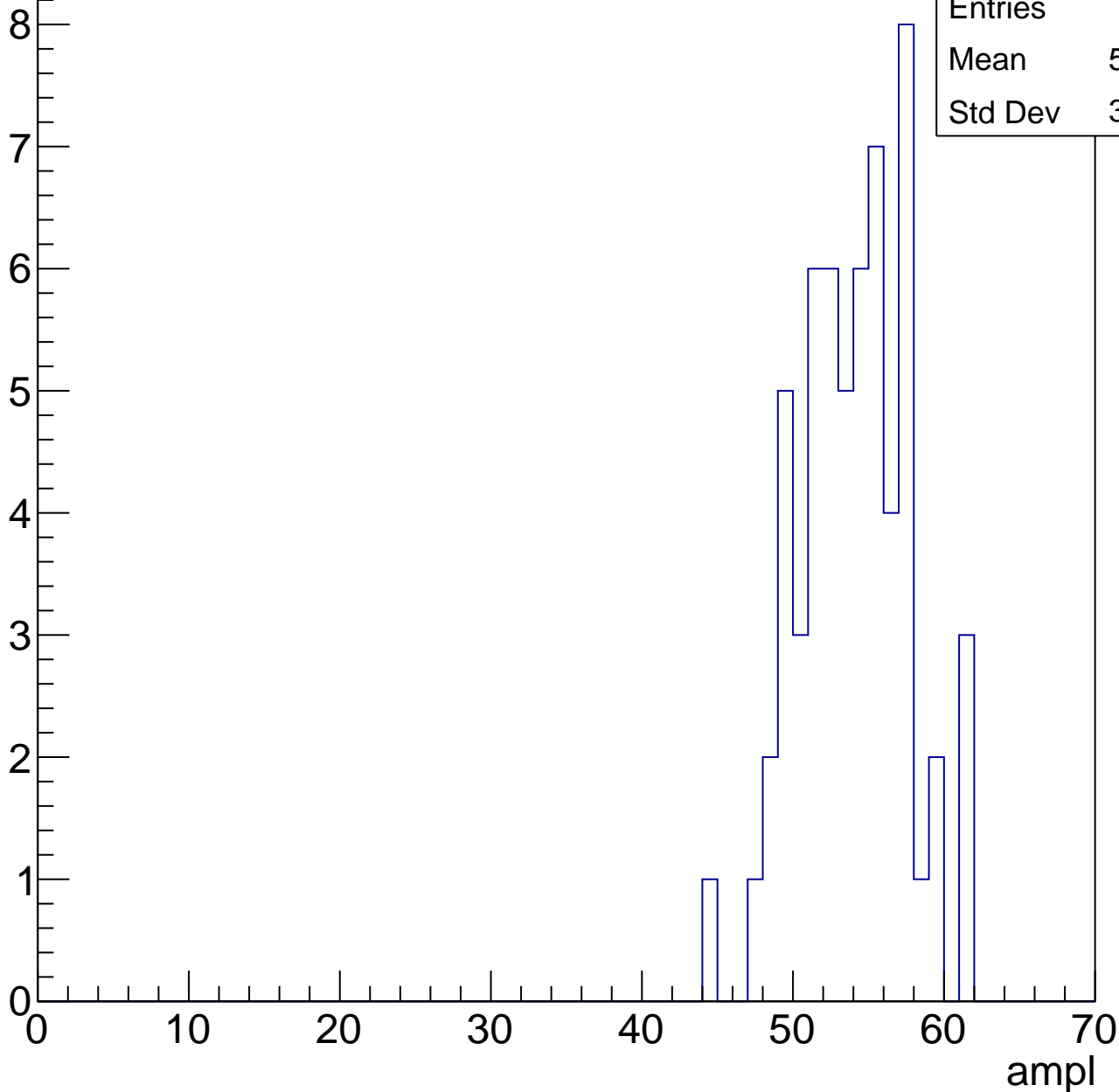


B1L102S, U2-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.55
Std Dev	3.589



B1L102S, U2-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 58.11

Std Dev 3.391

ampl

0

10

20

30

40

50

60

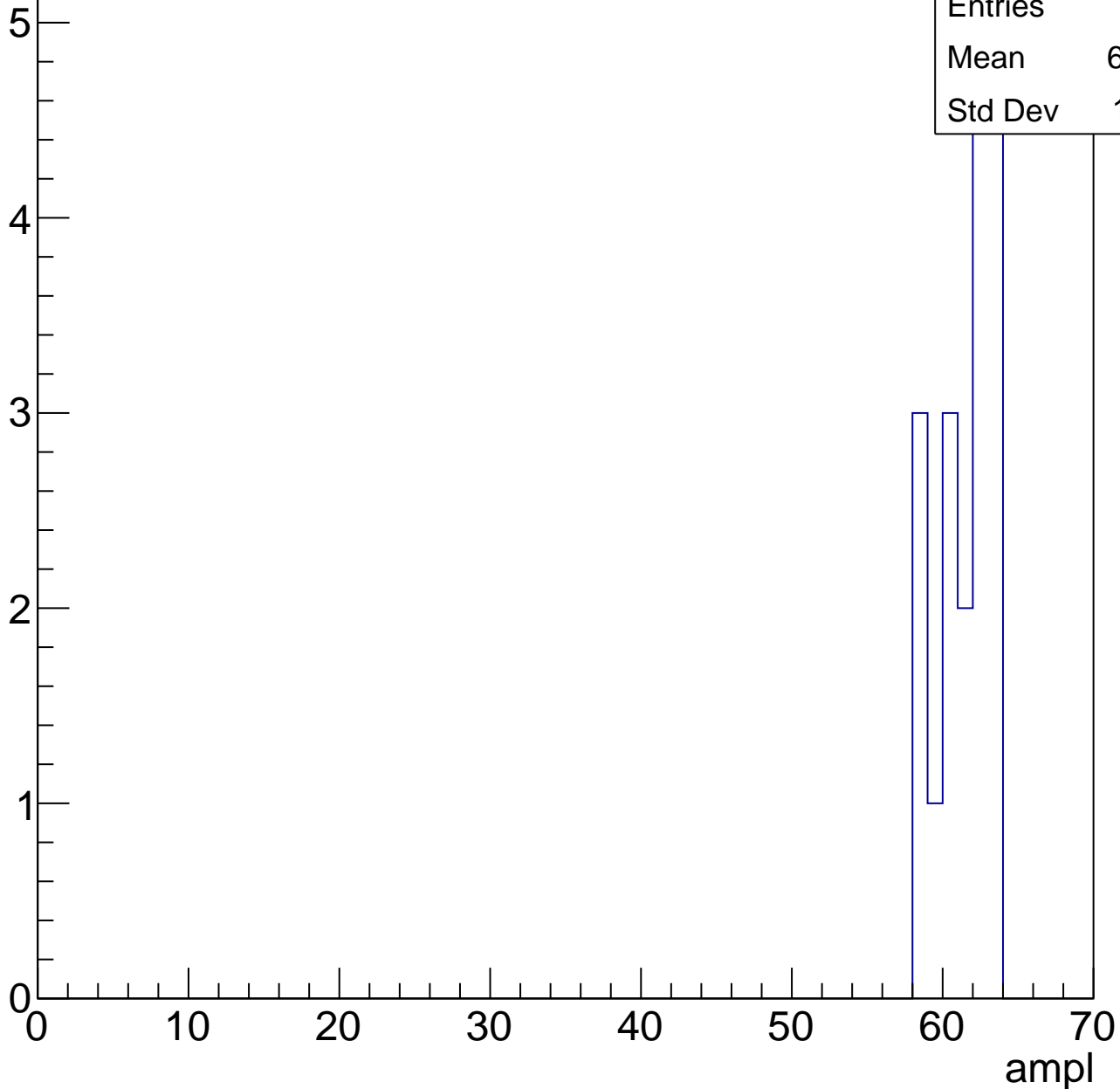
70

B1L102S, U2-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.05
Std Dev	1.761



B1L102S, U2-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch73, adc0

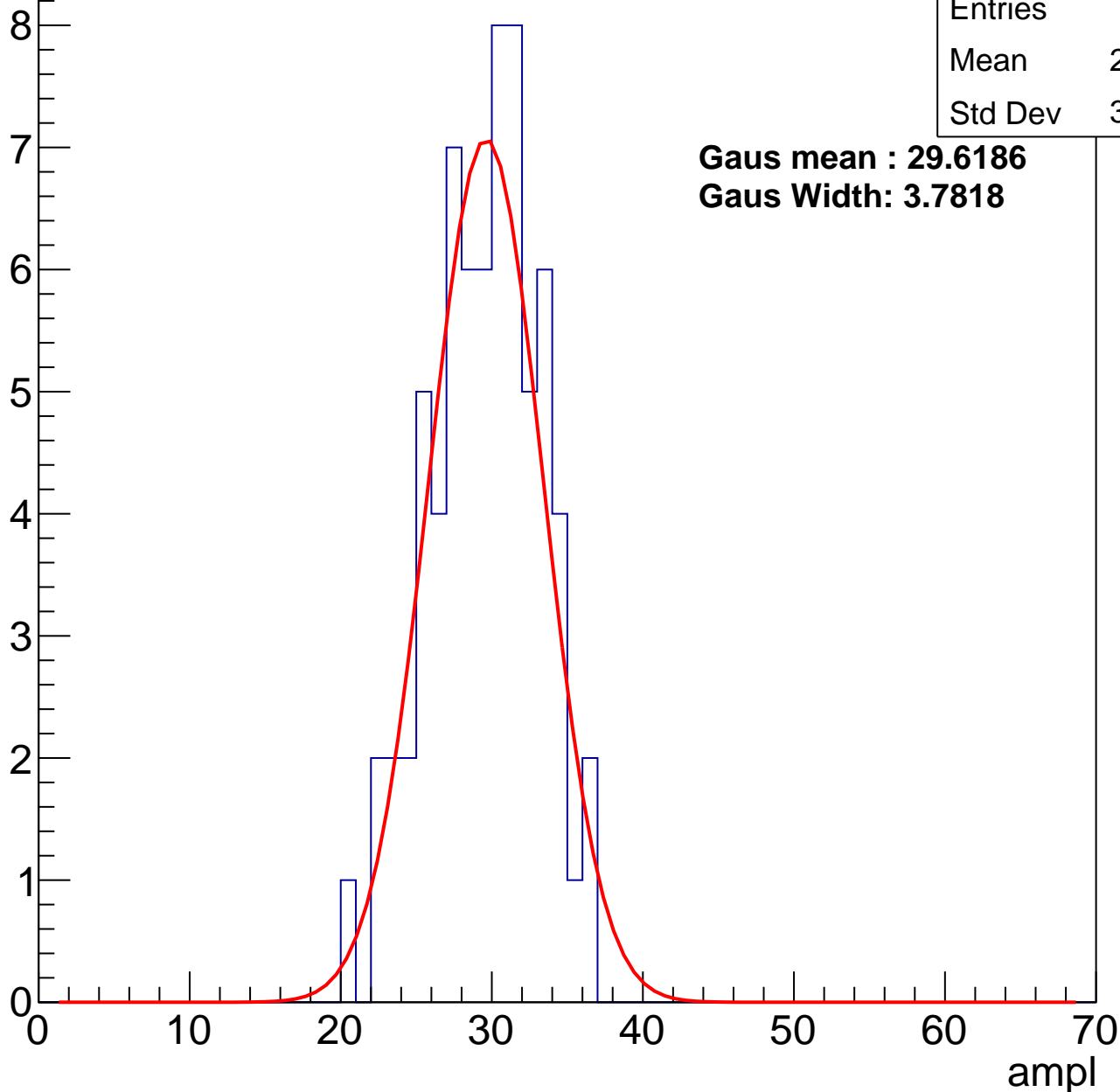
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.09
Std Dev	3.542

Gaus mean : 29.6186

Gaus Width: 3.7818



B1L102S, U2-ch73, adc1

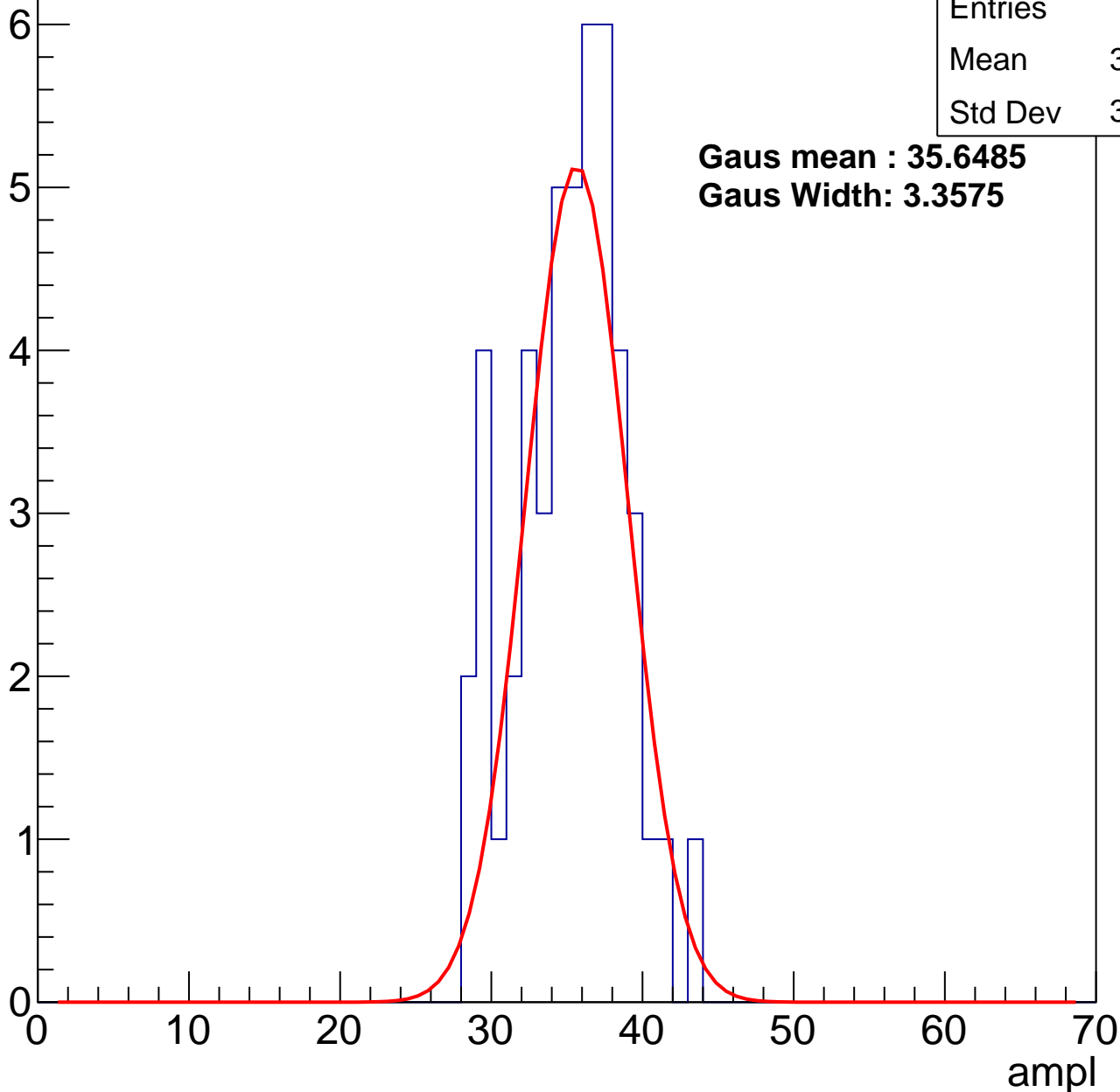
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	34.73
Std Dev	3.498

Gaus mean : 35.6485

Gaus Width: 3.3575



B1L102S, U2-ch73, adc2

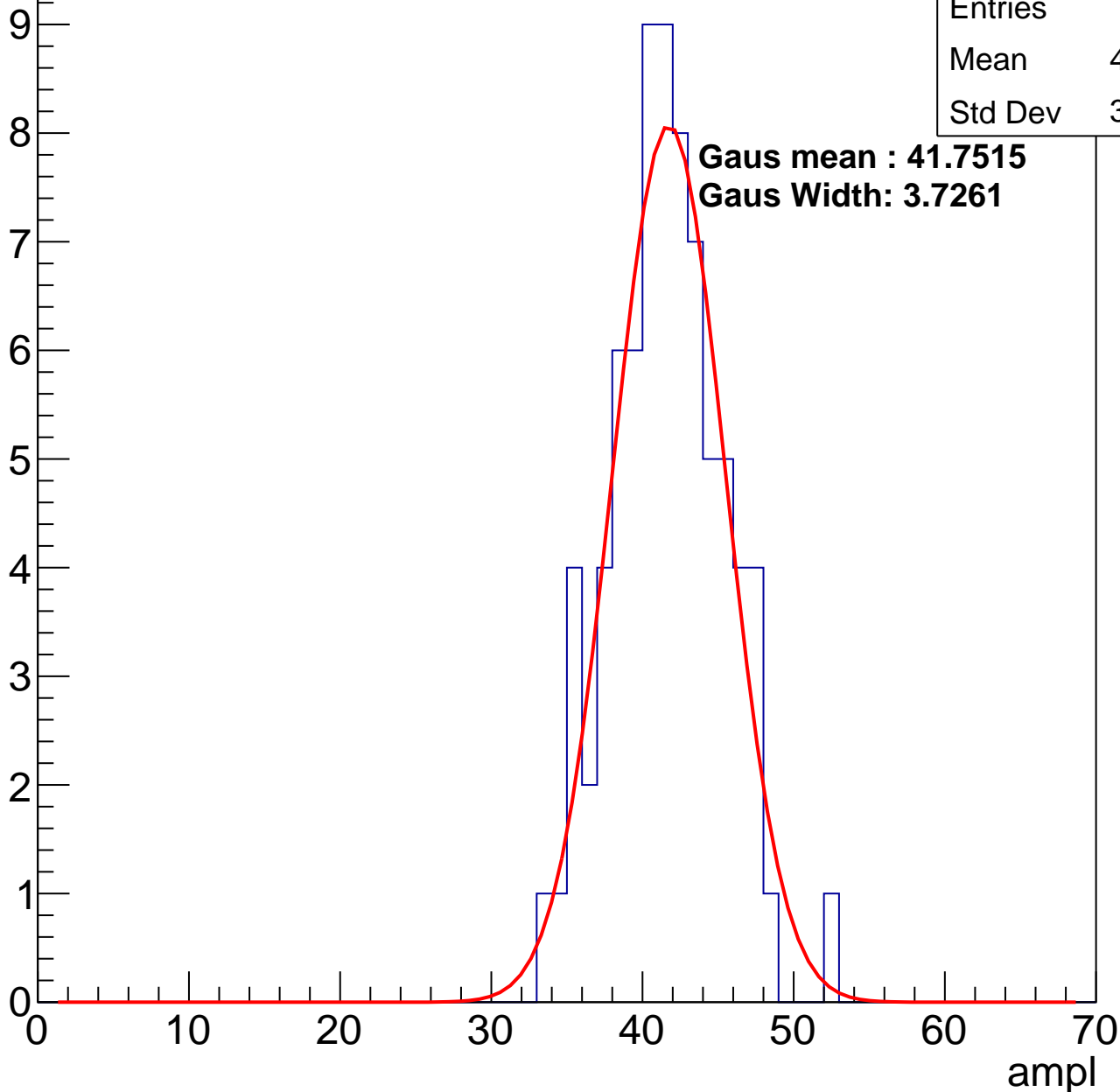
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.19
Std Dev	3.658

Gaus mean : 41.7515

Gaus Width: 3.7261

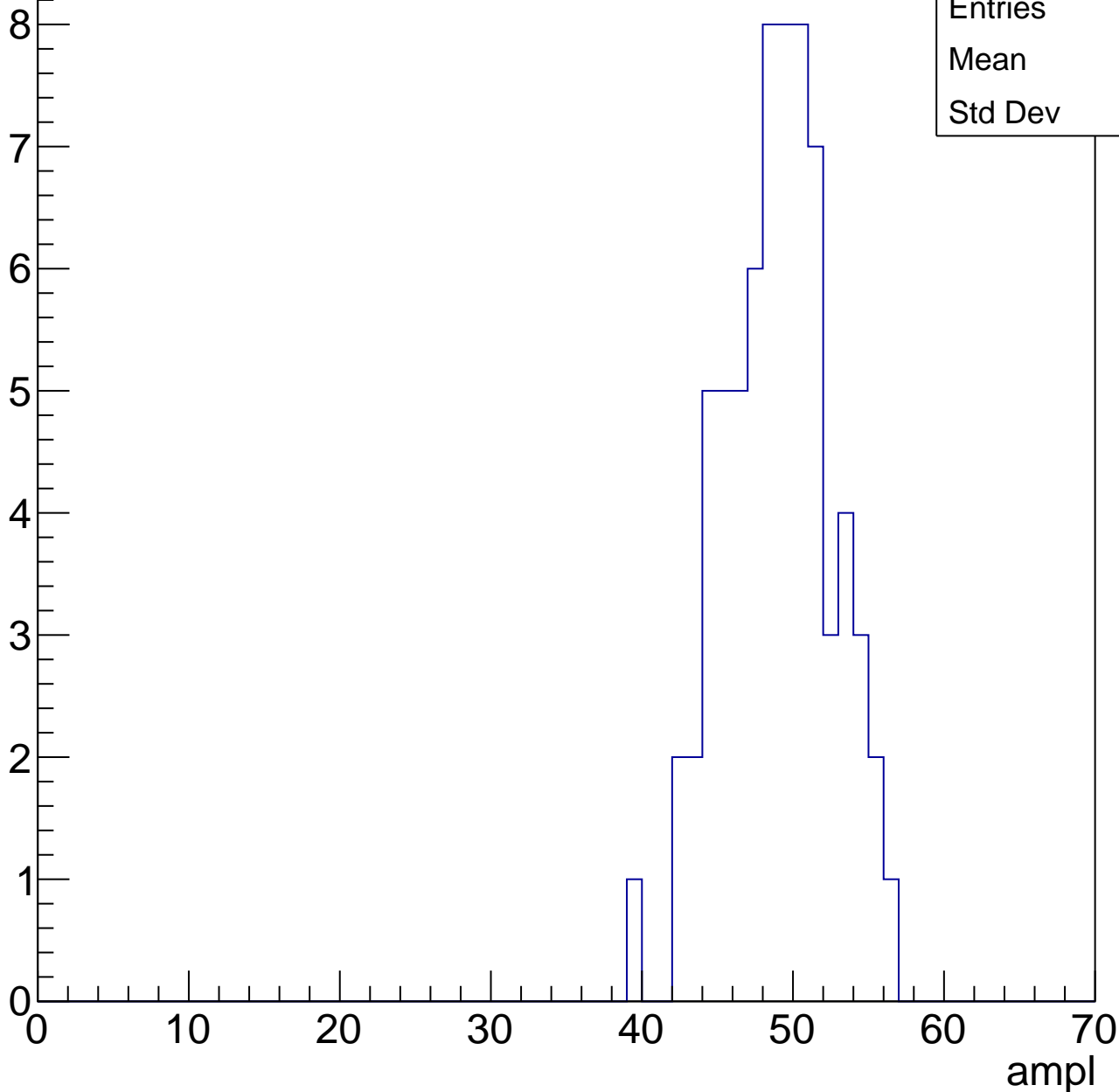


B1L102S, U2-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

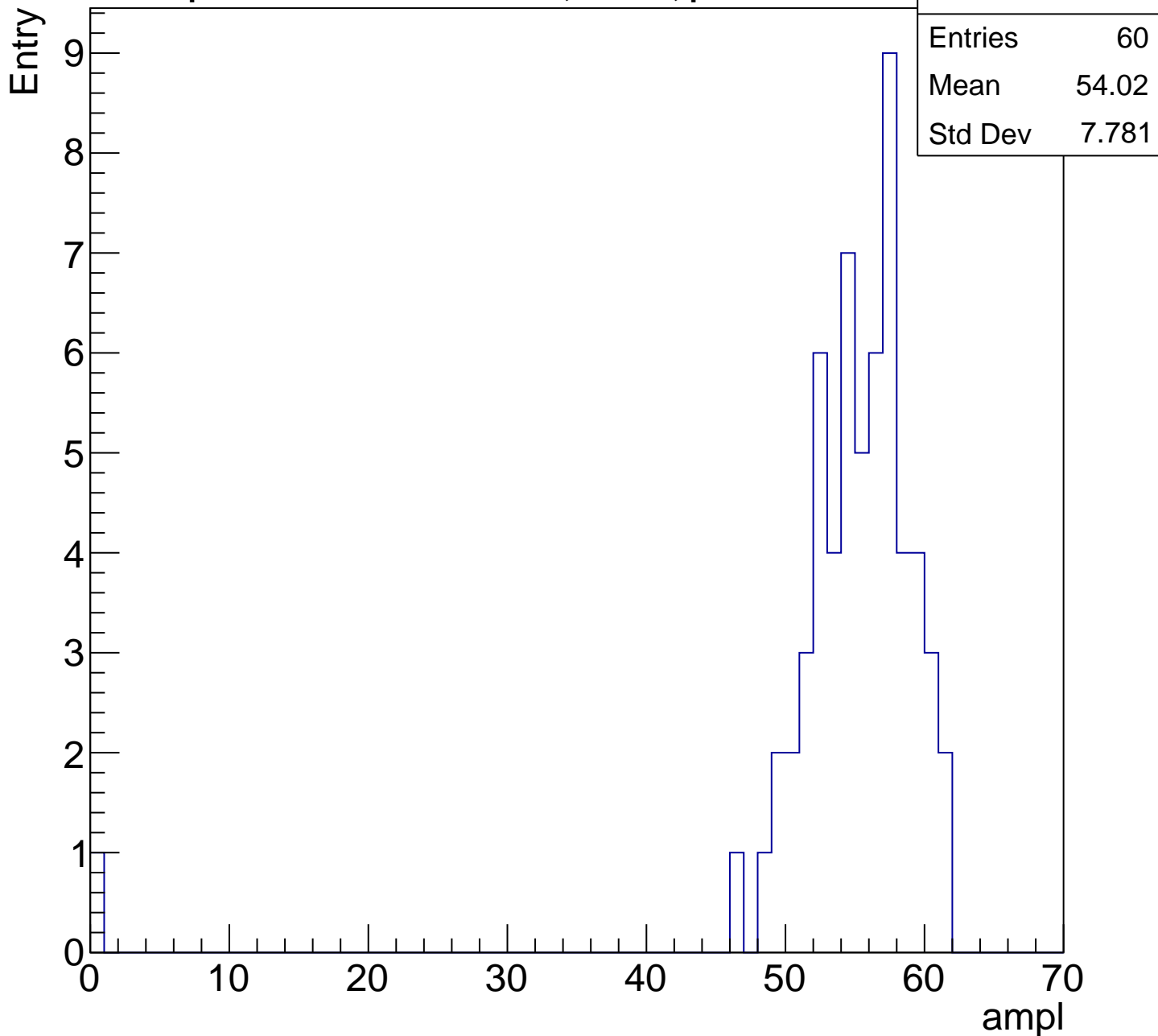
Entry

Entries	70
Mean	48.5
Std Dev	3.5



B1L102S, U2-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

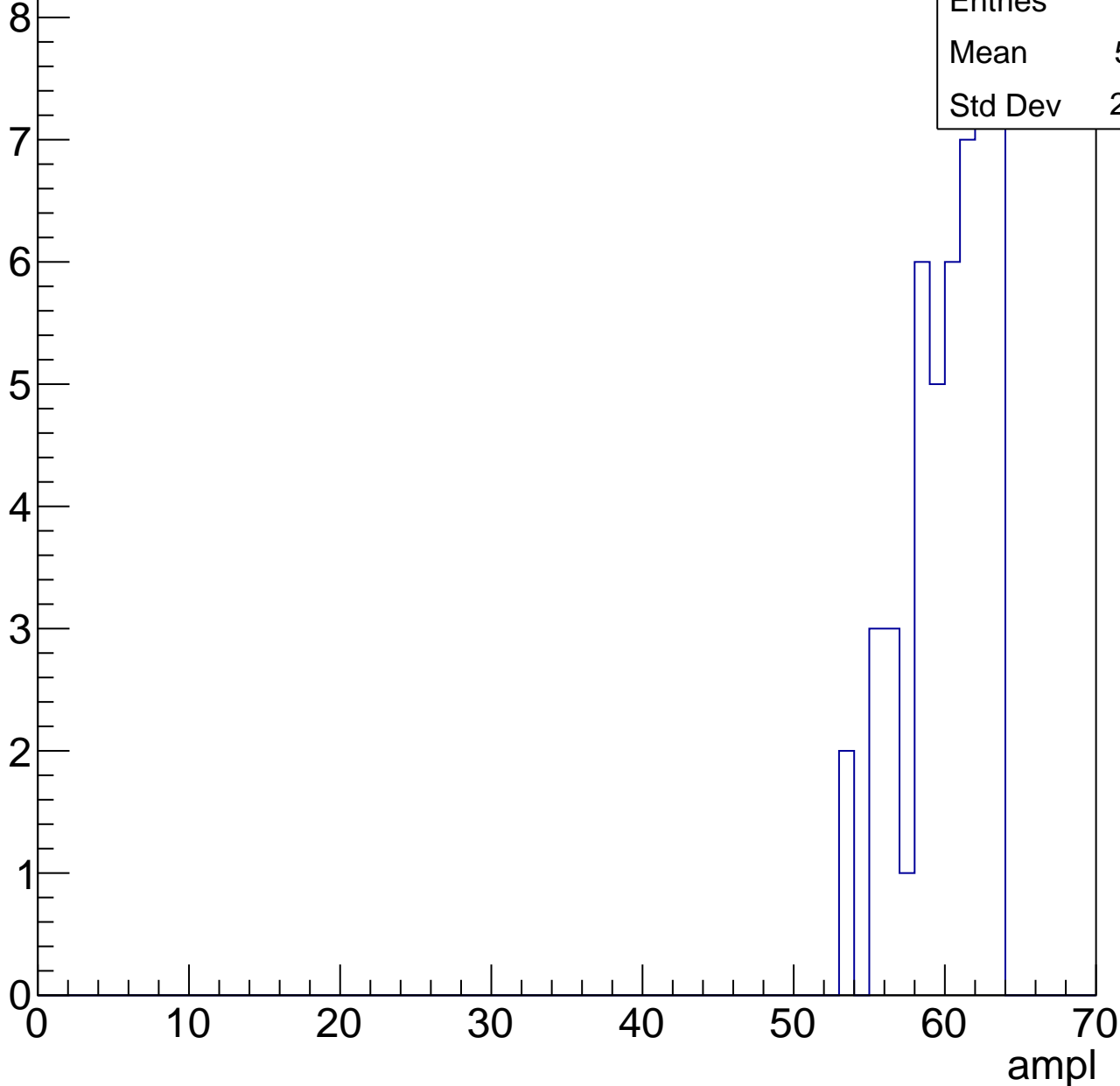


B1L102S, U2-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

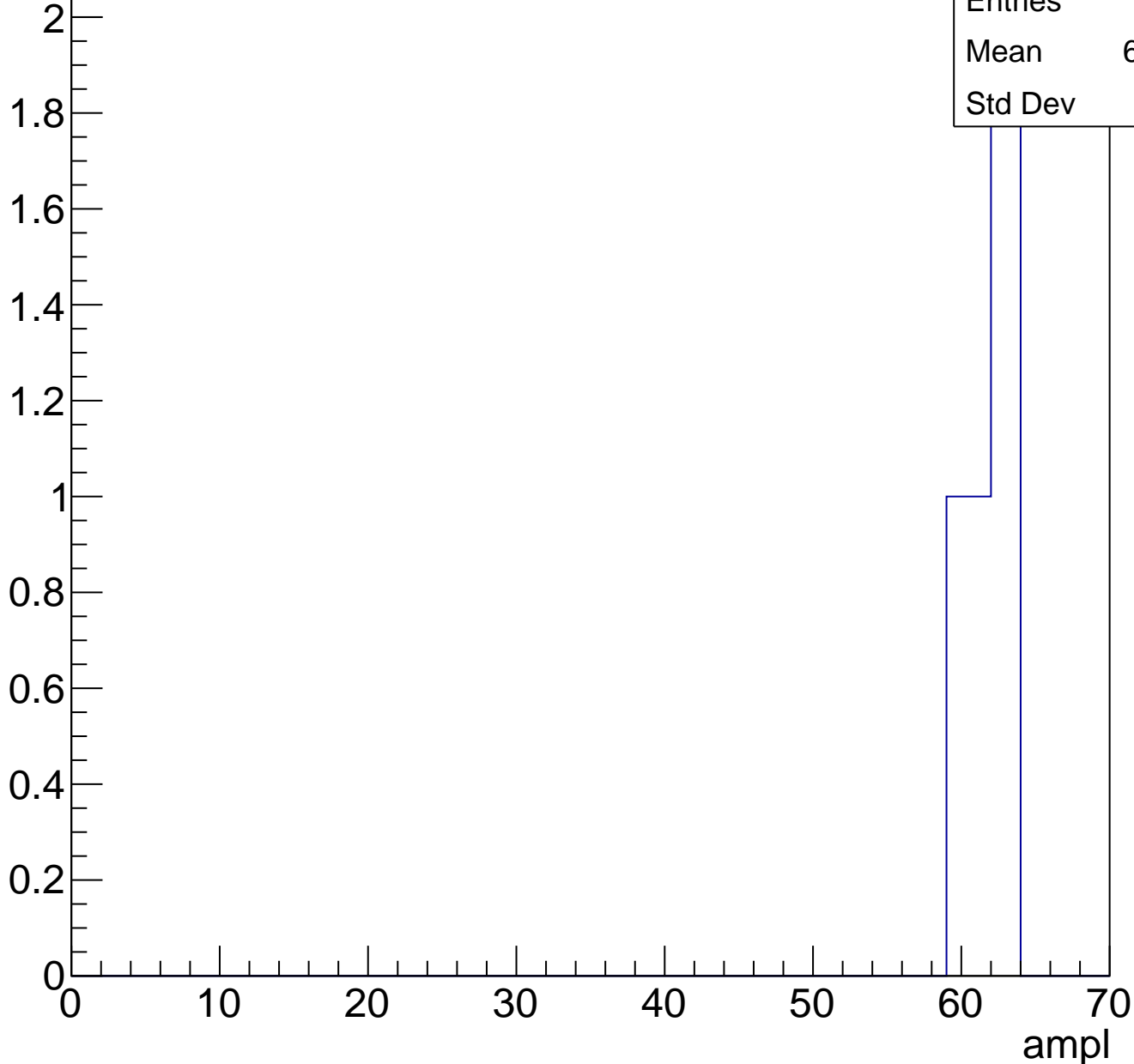
Entries	49
Mean	59.71
Std Dev	2.733



B1L102S, U2-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch74, adc0

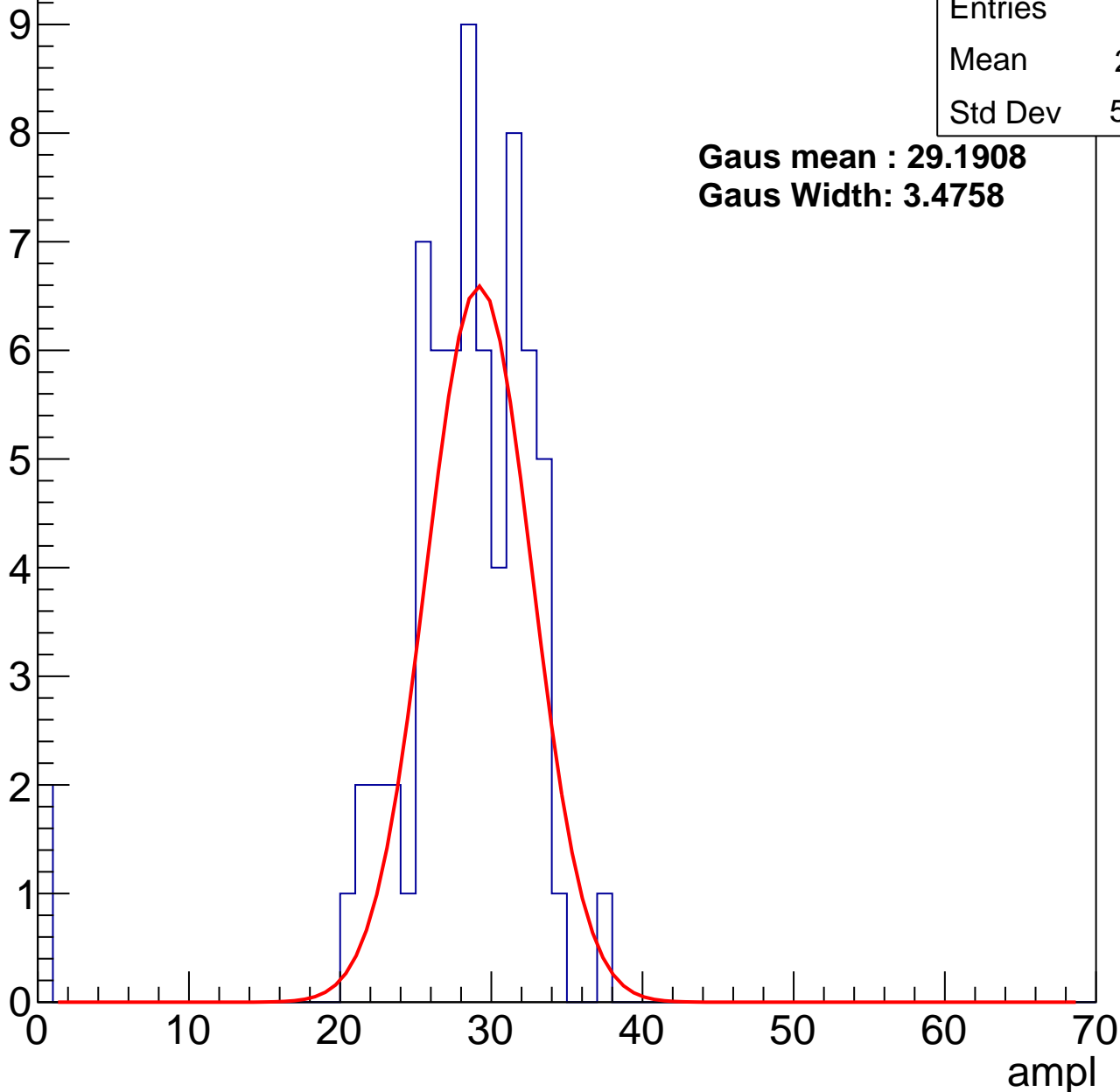
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.41
Std Dev	5.859

Gaus mean : 29.1908

Gaus Width: 3.4758



B1L102S, U2-ch74, adc1

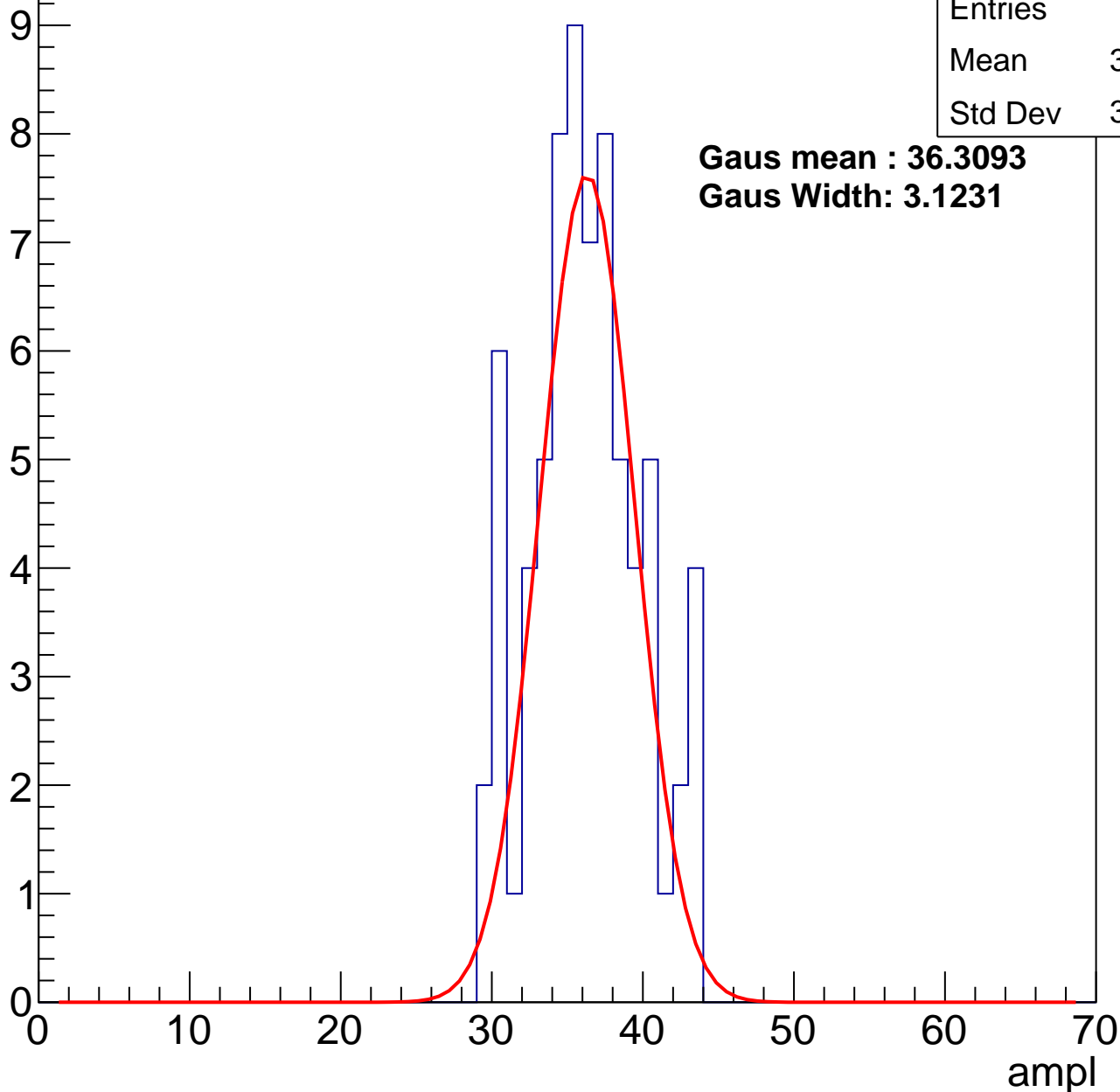
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	35.77
Std Dev	3.604

Gaus mean : 36.3093

Gaus Width: 3.1231



B1L102S, U2-ch74, adc2

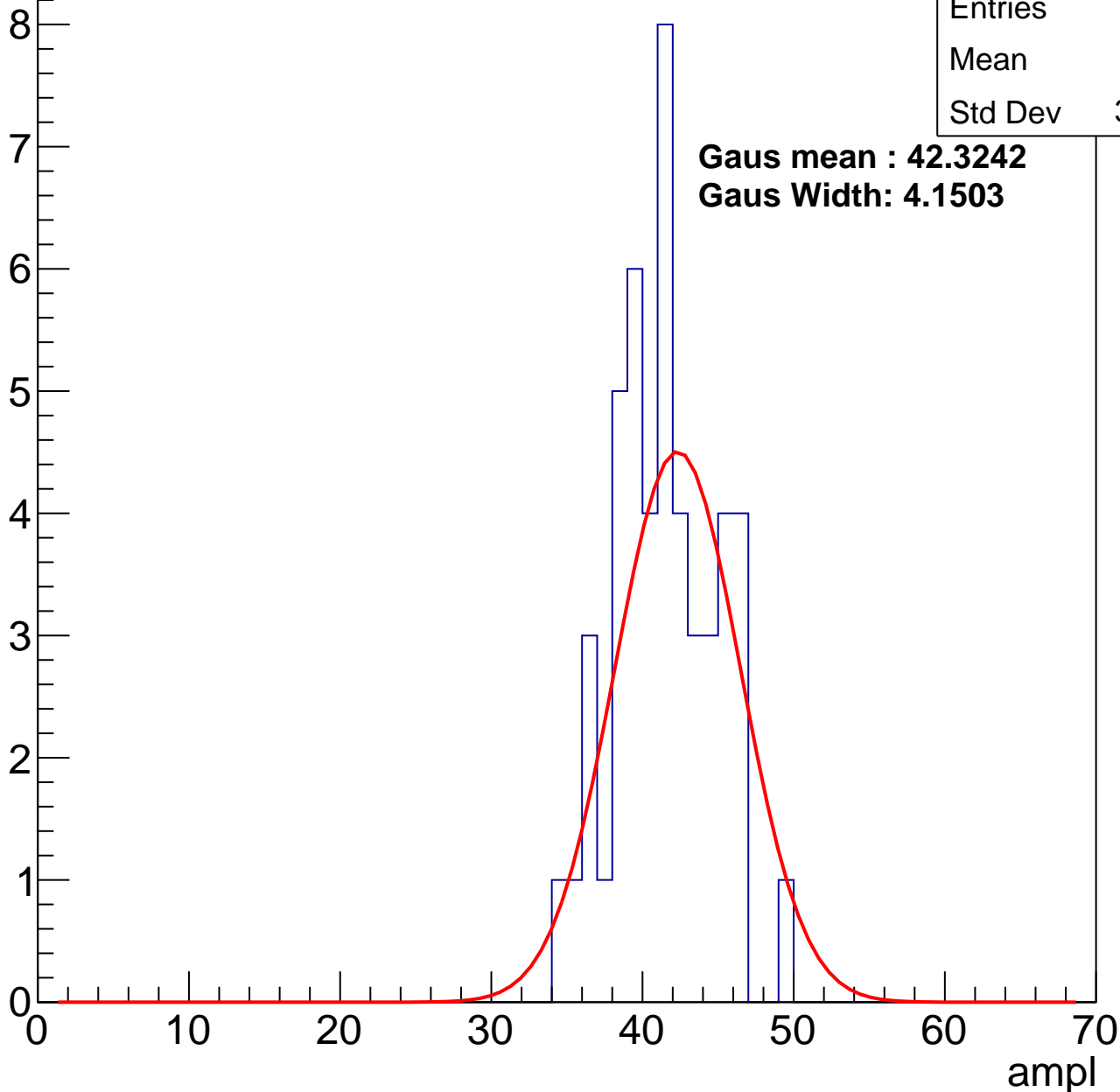
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	41
Std Dev	3.291

Gaus mean : 42.3242

Gaus Width: 4.1503

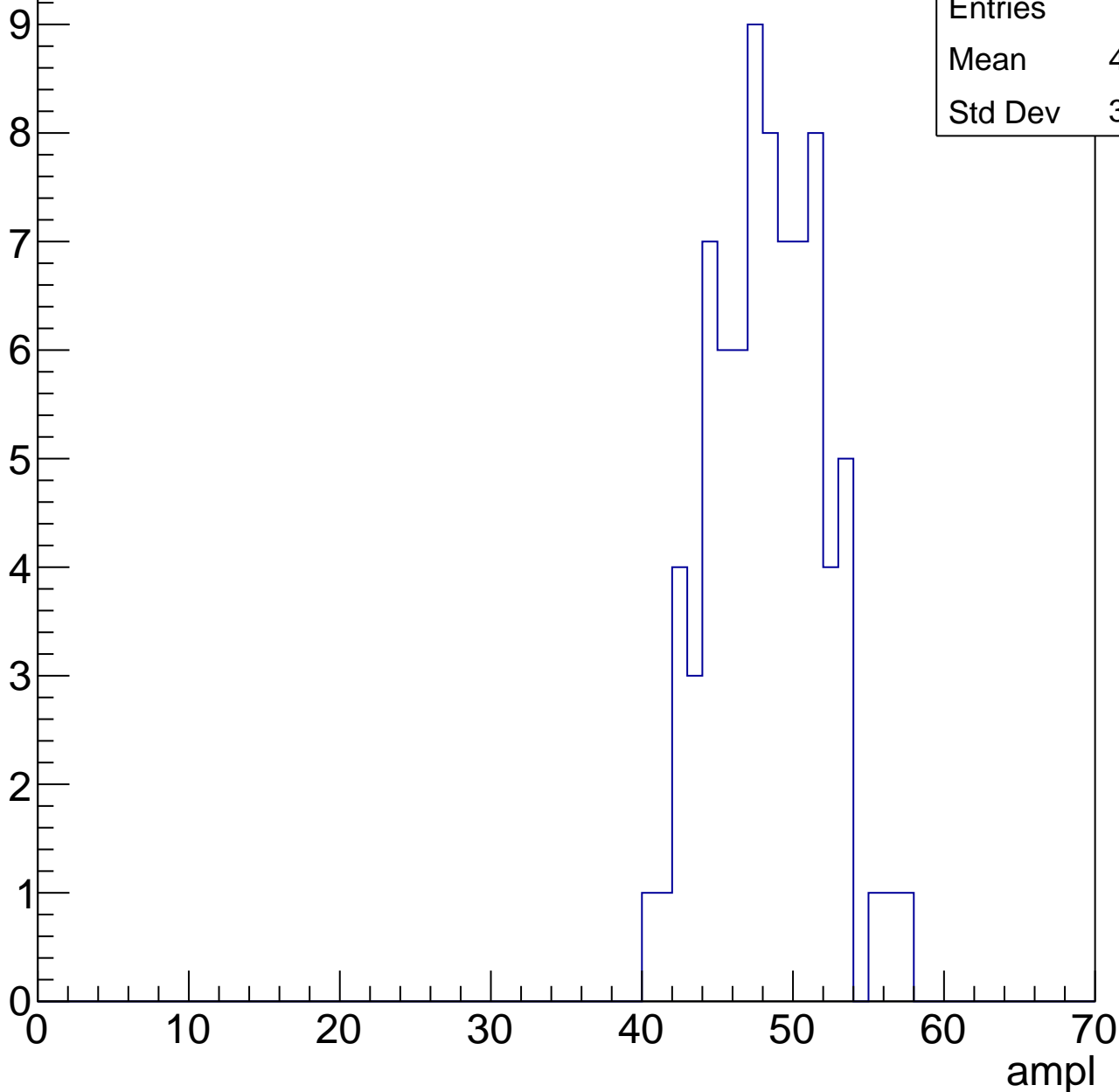


B1L102S, U2-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	47.86
Std Dev	3.592

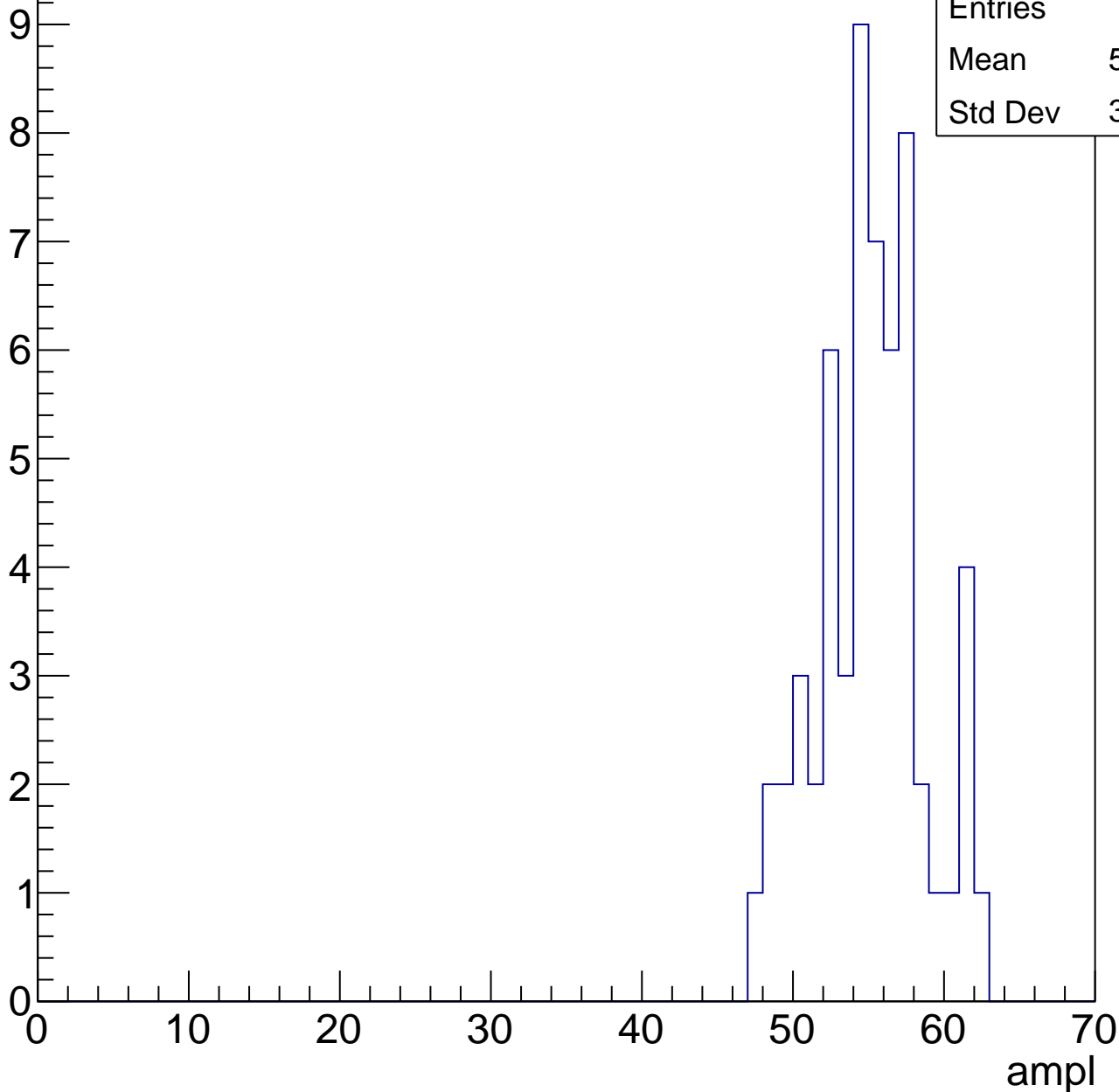


B1L102S, U2-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.62
Std Dev	3.468

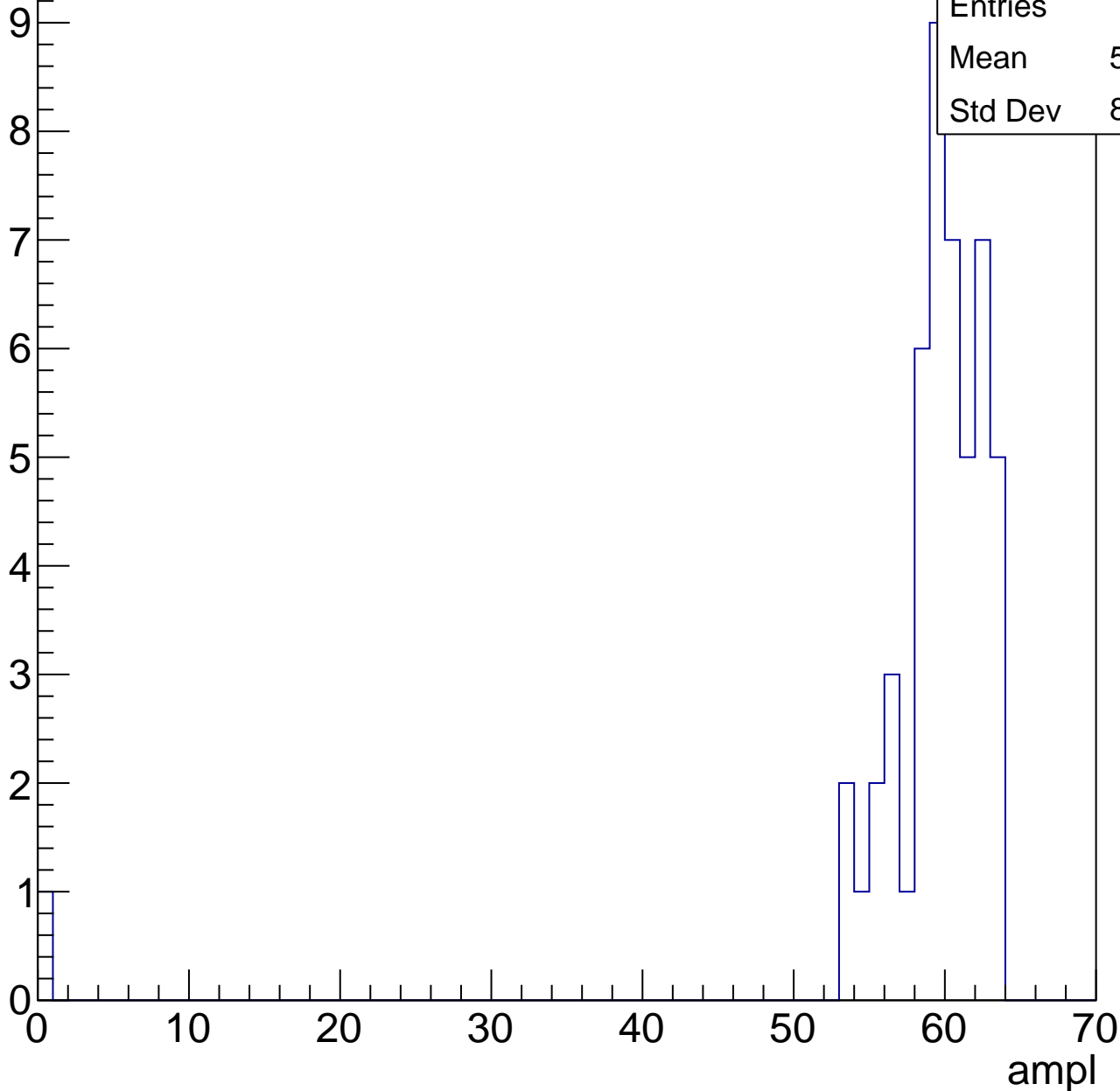


B1L102S, U2-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

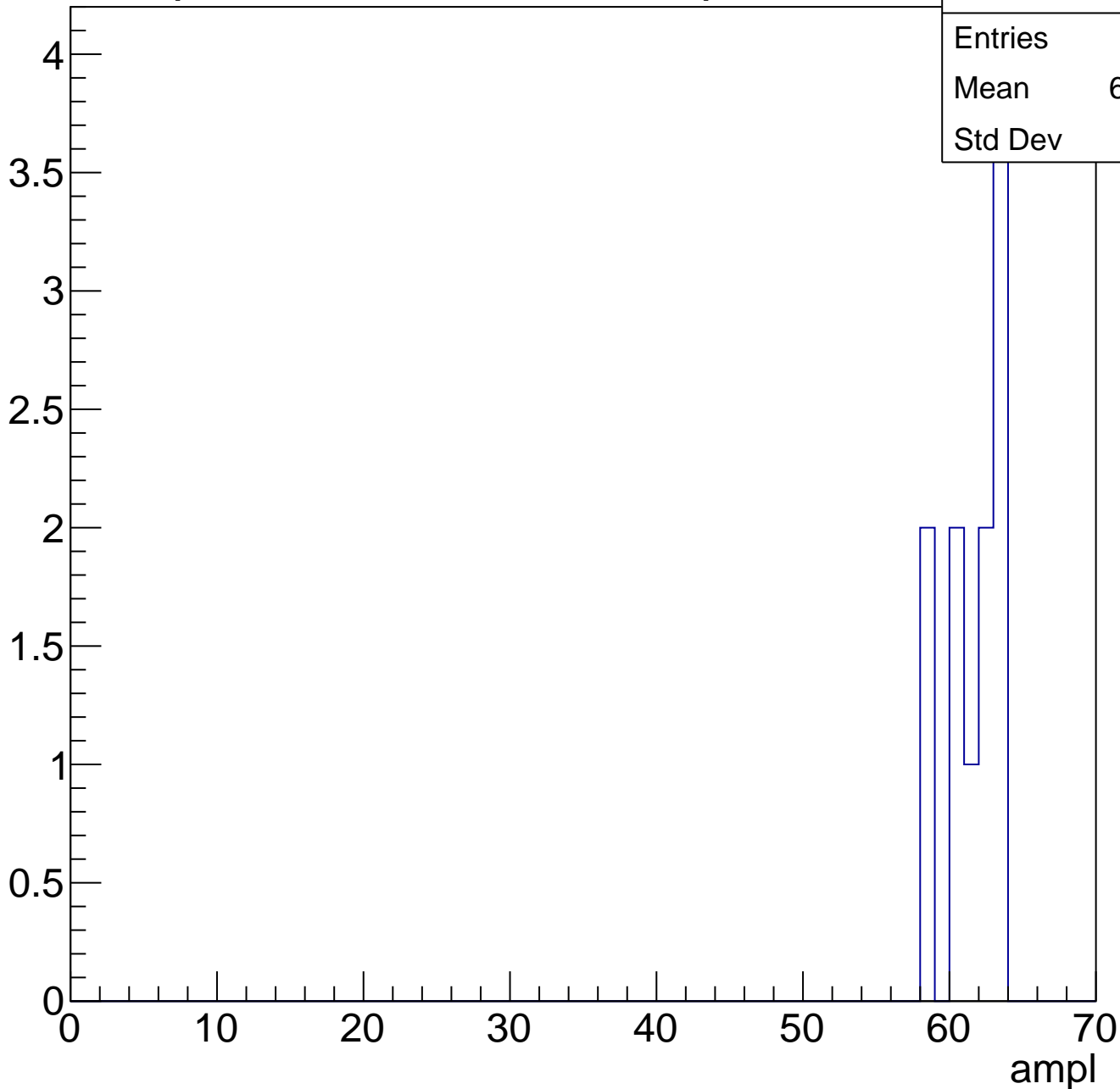
Entries	49
Mean	58.12
Std Dev	8.782



B1L102S, U2-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch75, adc0

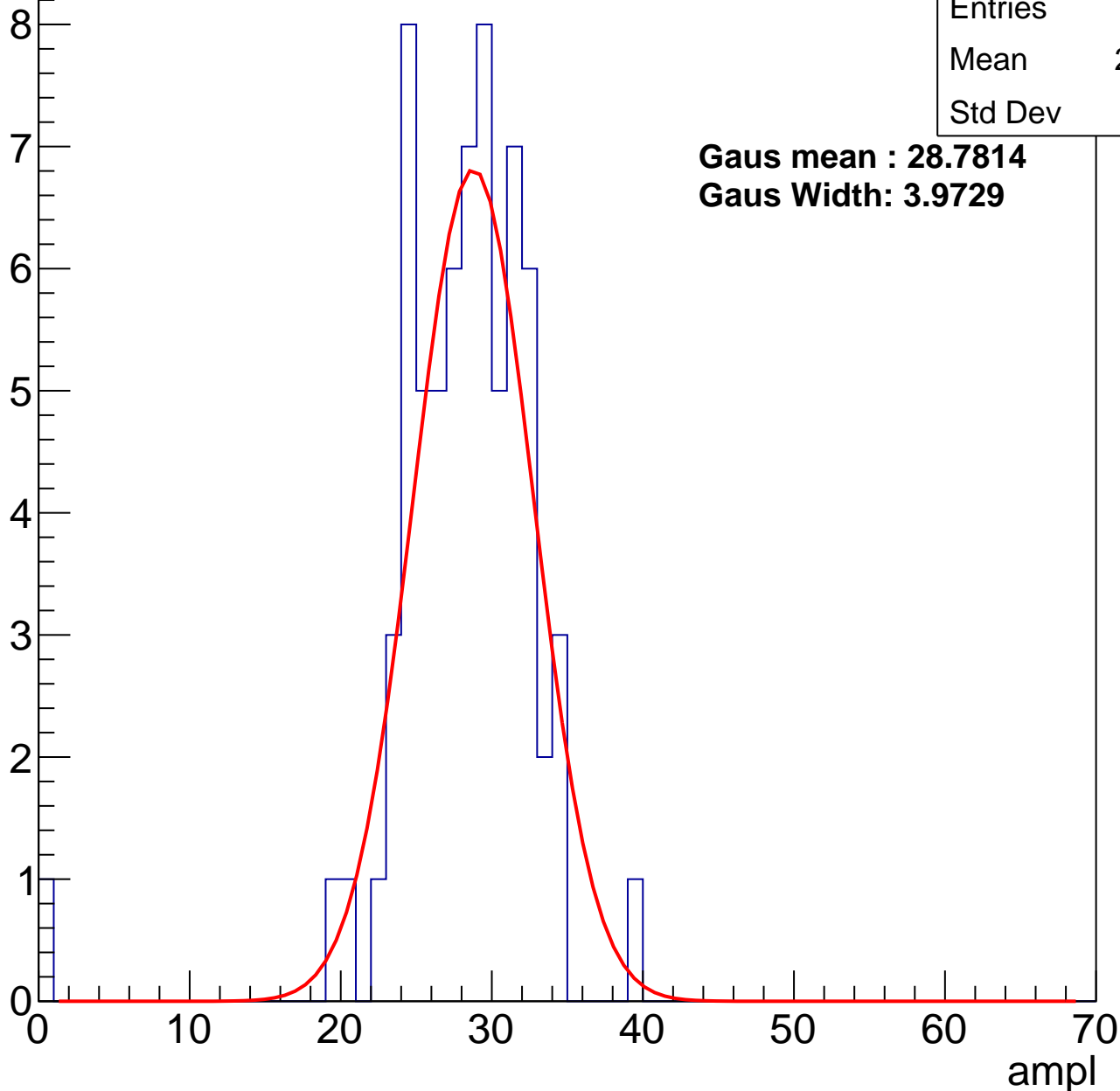
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.61
Std Dev	4.92

Gaus mean : 28.7814

Gaus Width: 3.9729



B1L102S, U2-ch75, adc1

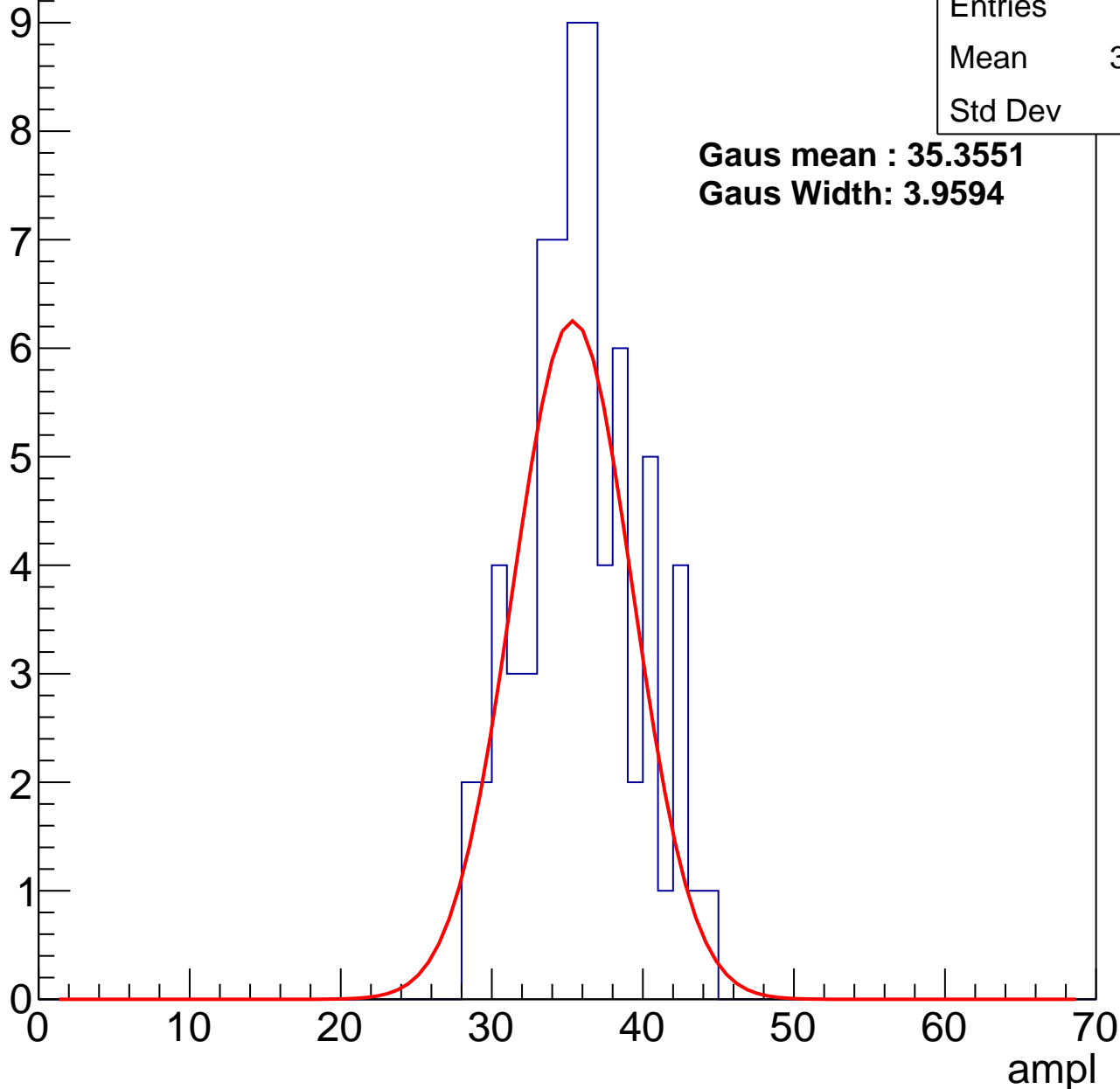
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.44
Std Dev	3.74

Gaus mean : 35.3551

Gaus Width: 3.9594



B1L102S, U2-ch75, adc2

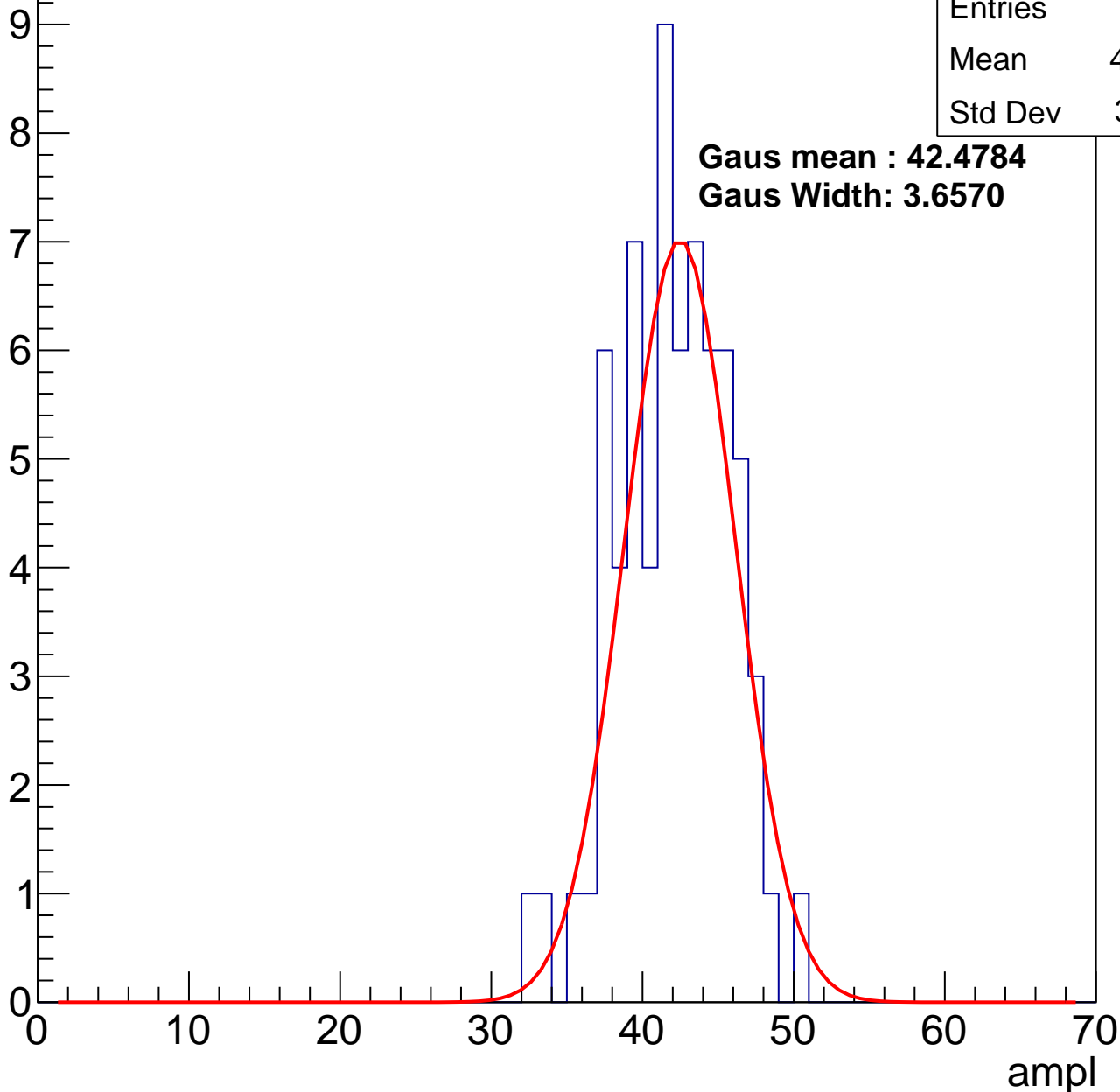
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.57
Std Dev	3.601

Gaus mean : 42.4784

Gaus Width: 3.6570

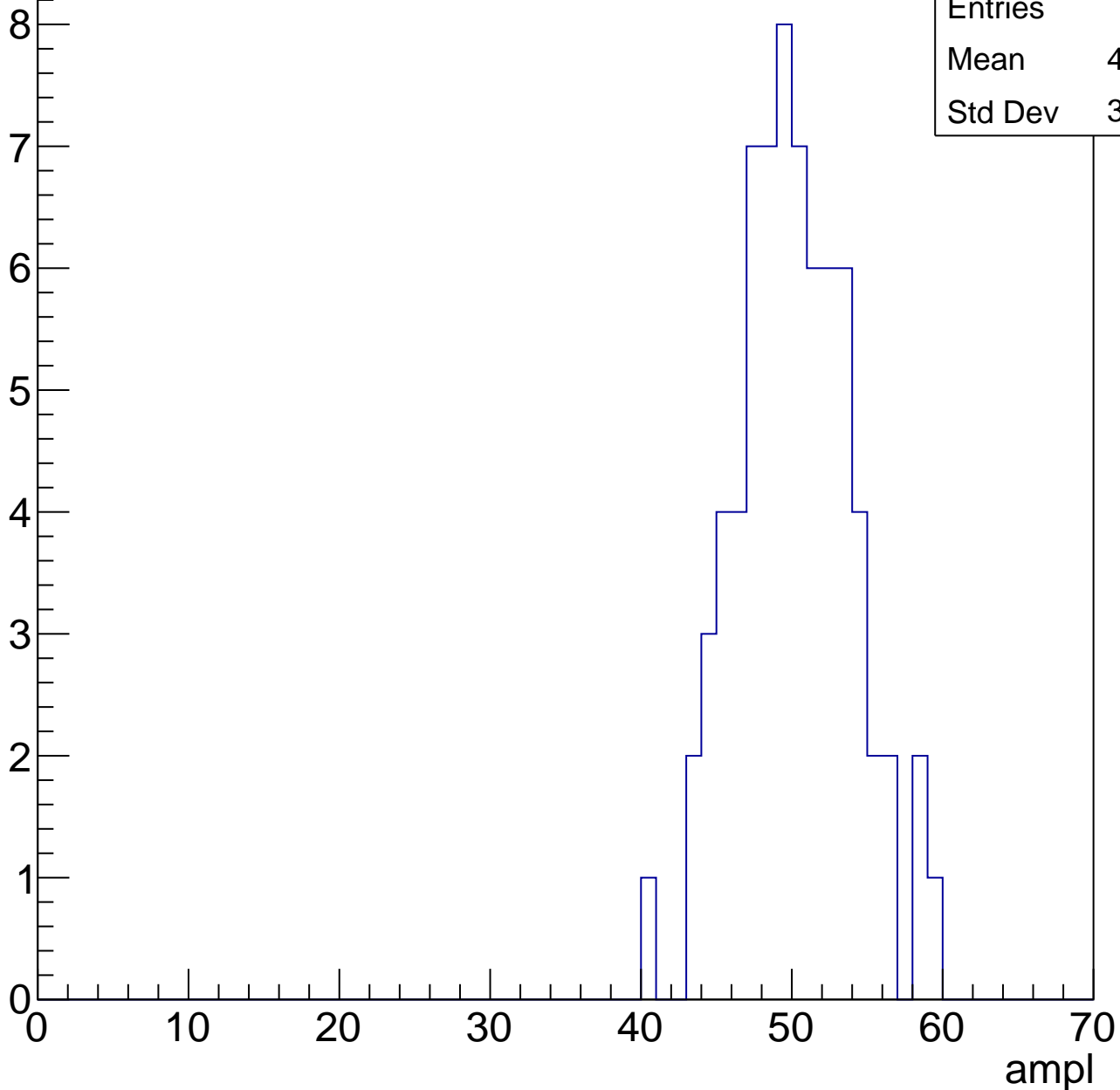


B1L102S, U2-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

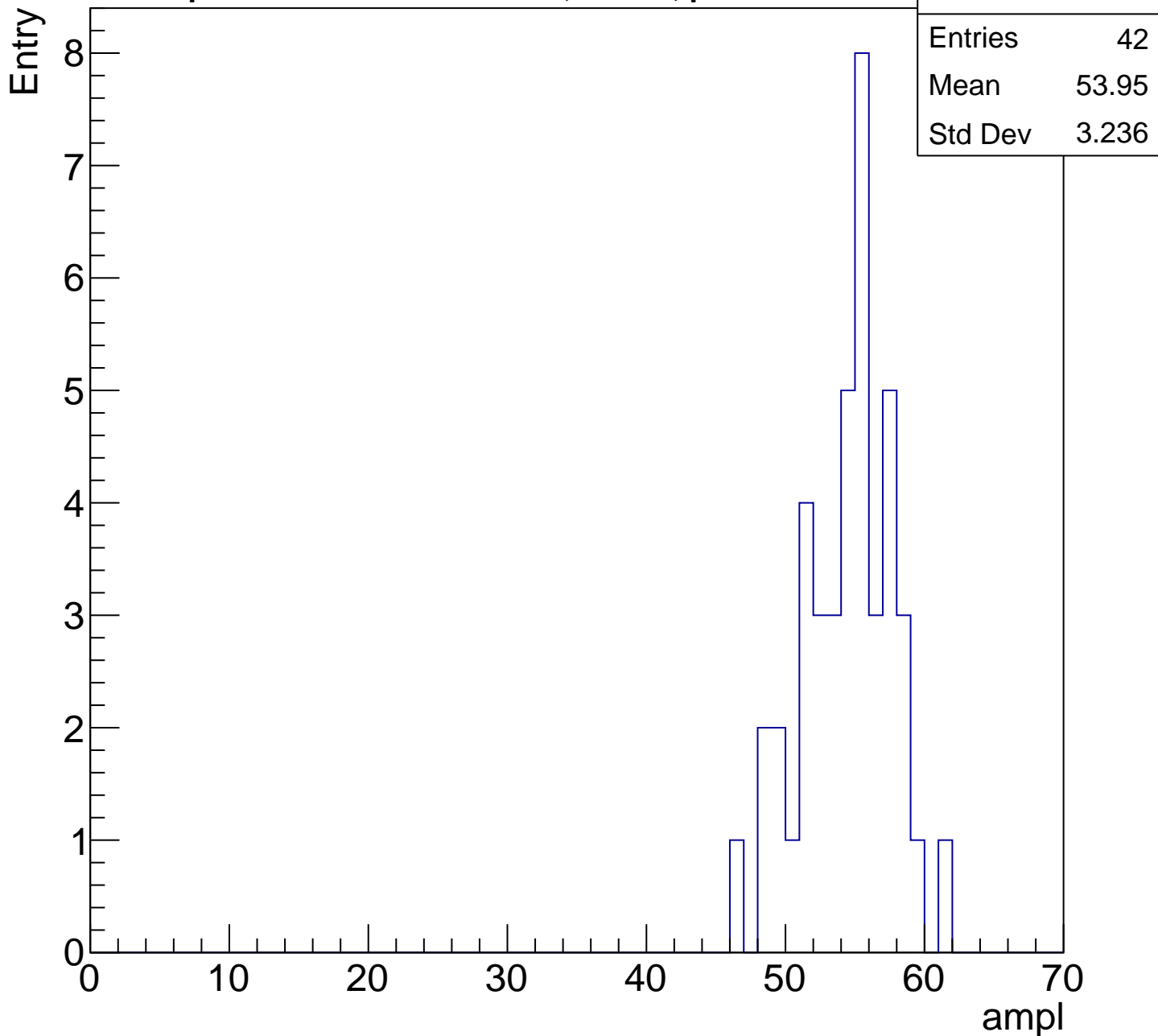
Entry

Entries	72
Mean	49.69
Std Dev	3.803



B1L102S, U2-ch75, adc4

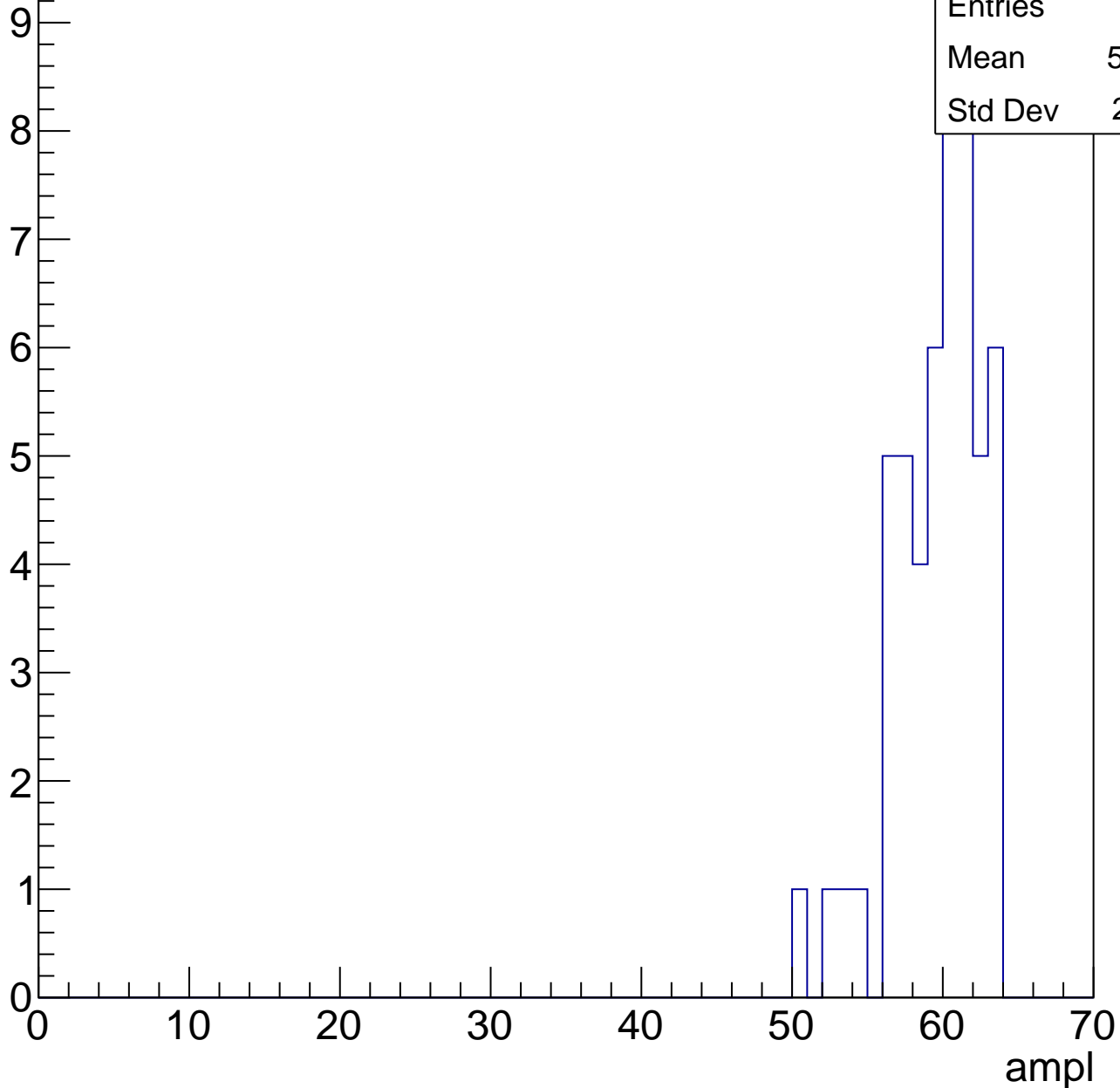
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

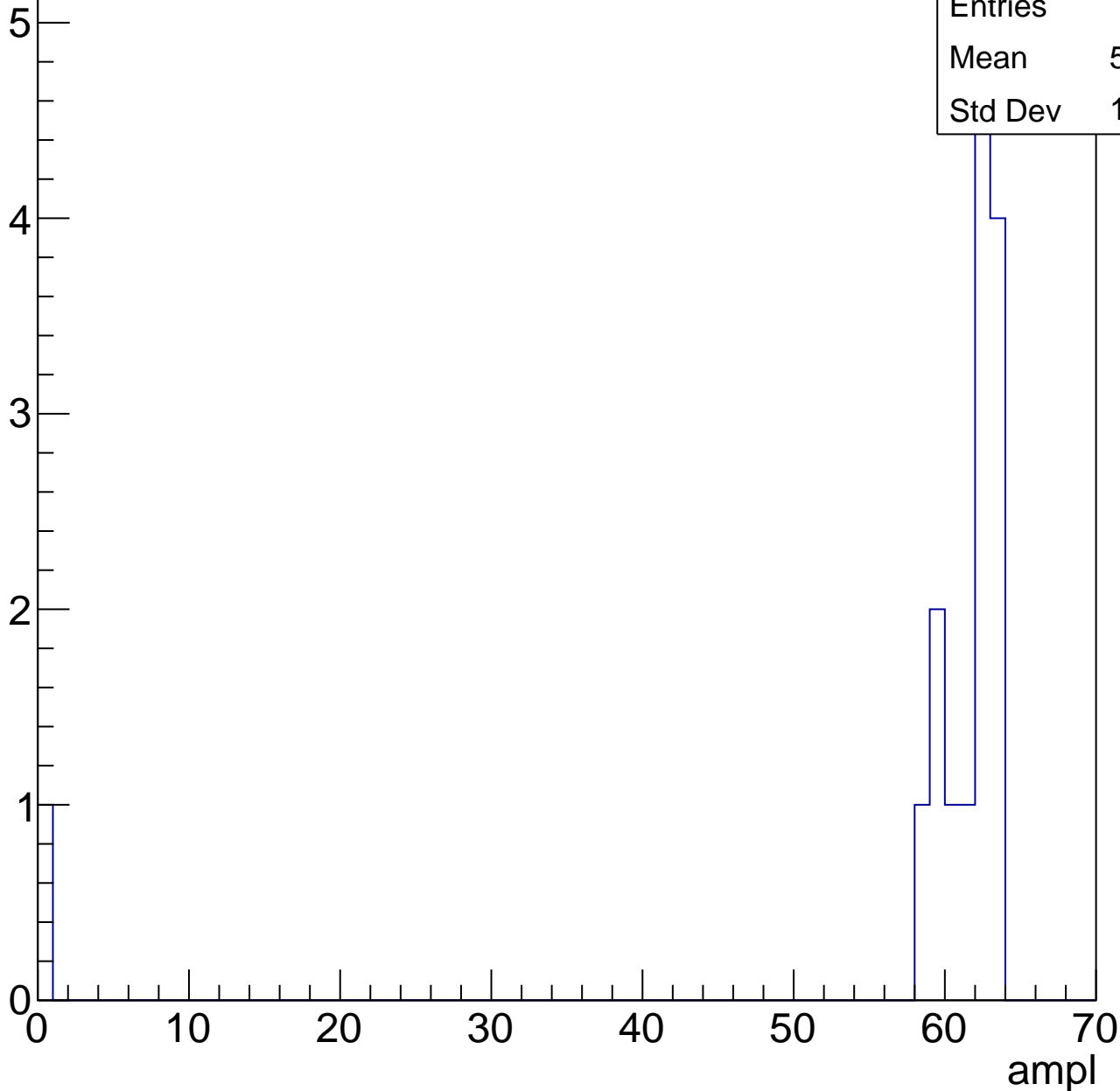


B1L102S, U2-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.27
Std Dev	15.39



B1L102S, U2-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch76, adc0

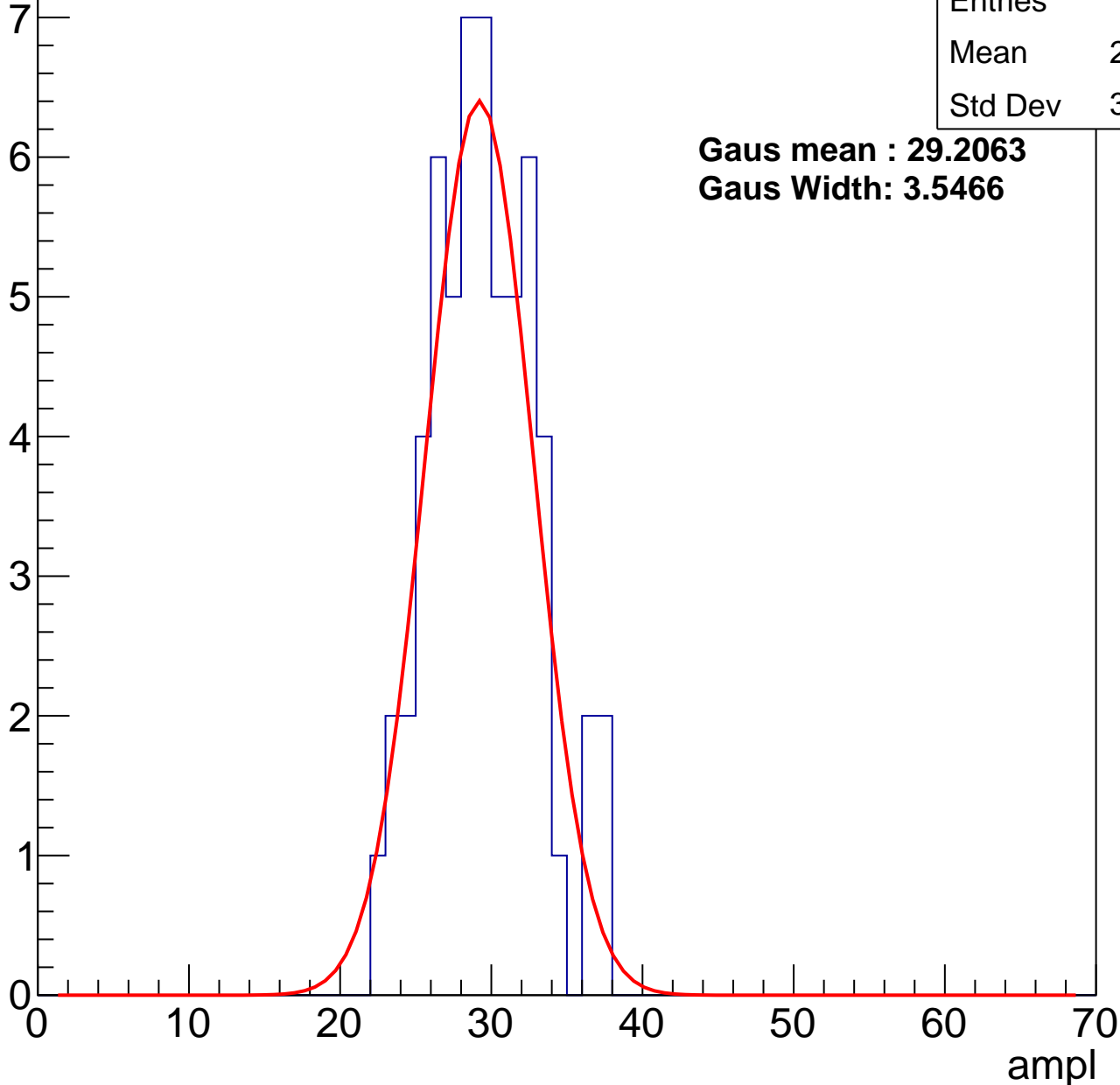
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.07
Std Dev	3.468

Gaus mean : 29.2063

Gaus Width: 3.5466



B1L102S, U2-ch76, adc1

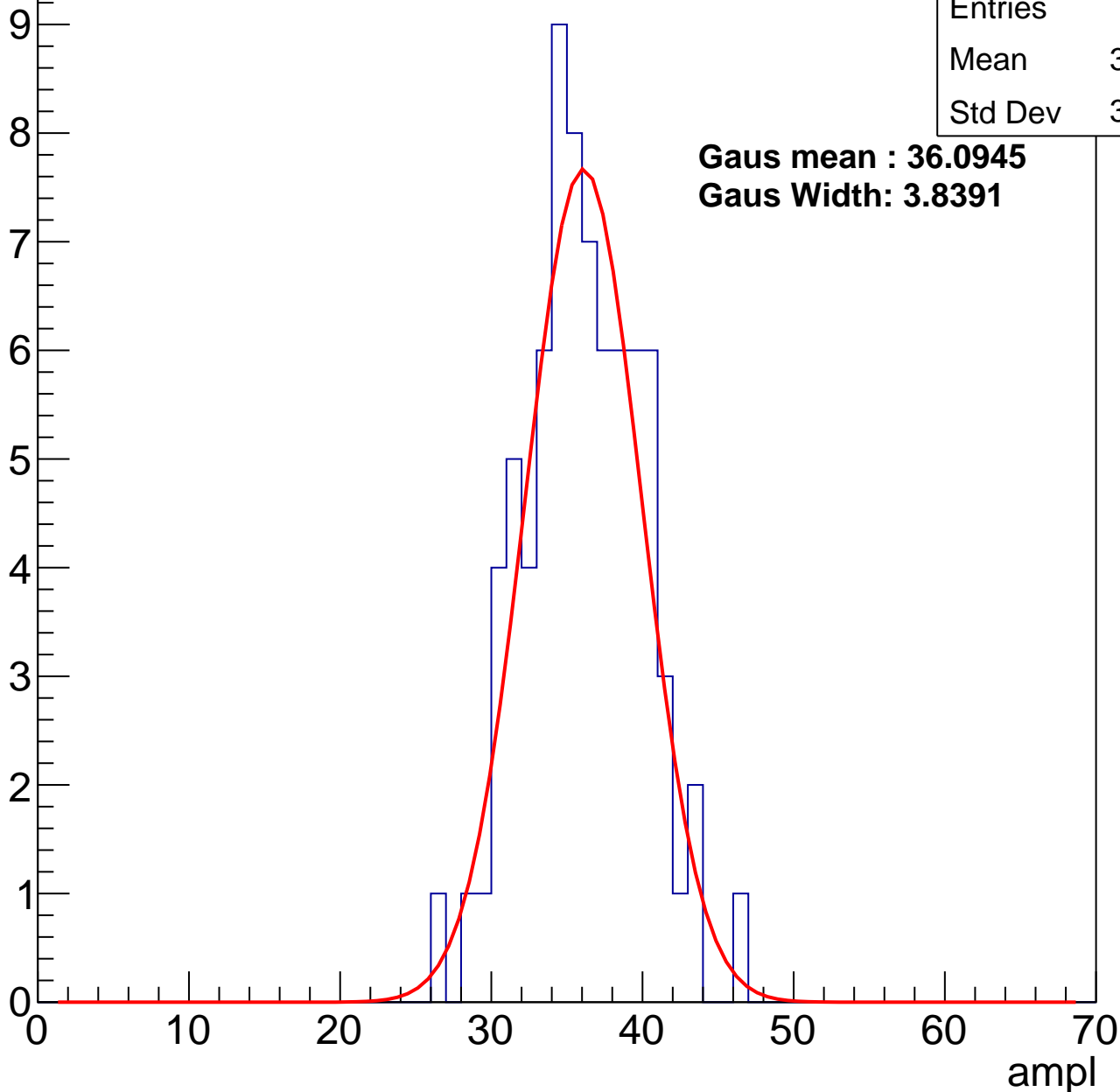
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.62
Std Dev	3.817

Gaus mean : 36.0945

Gaus Width: 3.8391



B1L102S, U2-ch76, adc2

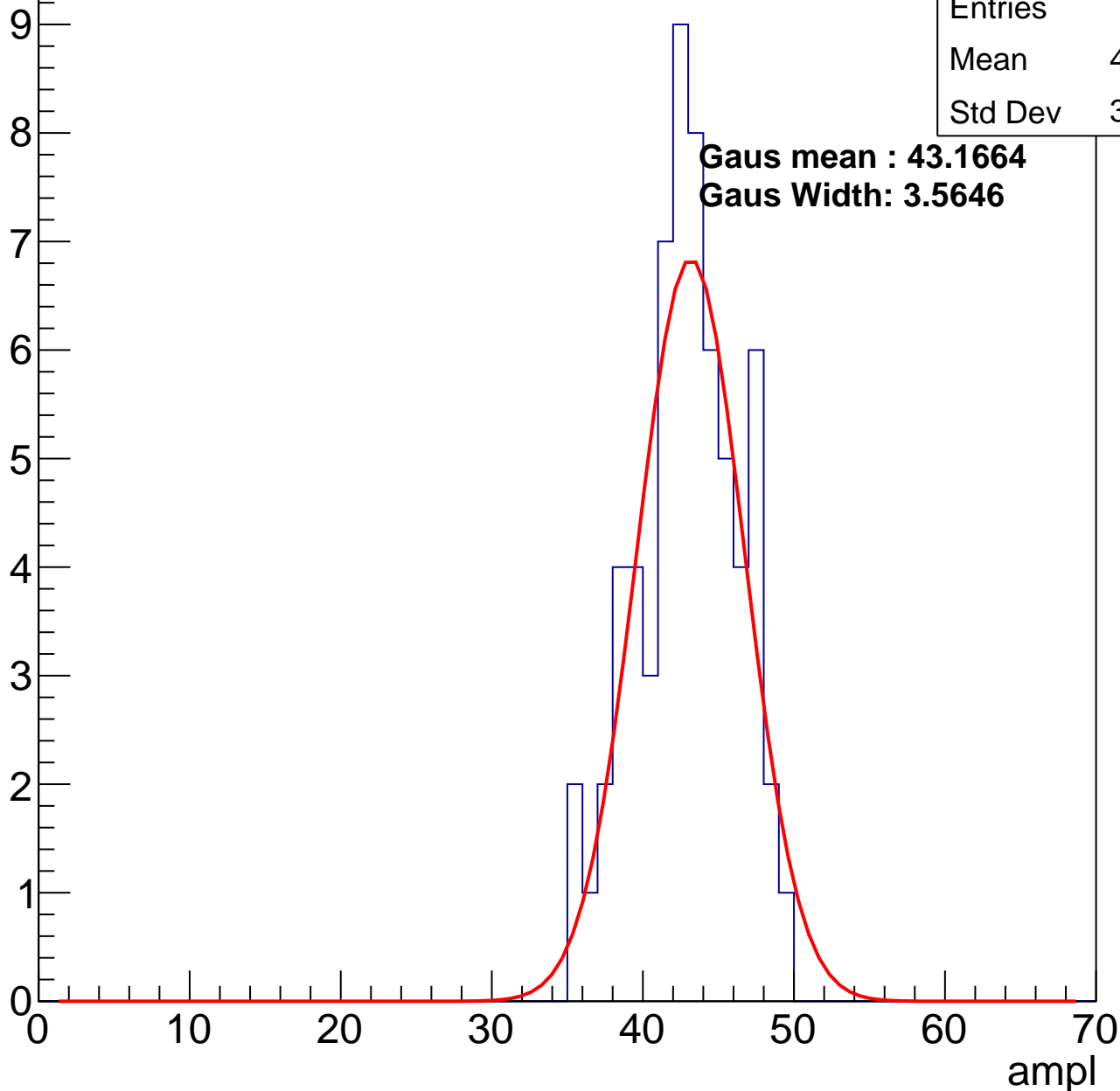
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.45
Std Dev	3.312

Gaus mean : 43.1664

Gaus Width: 3.5646

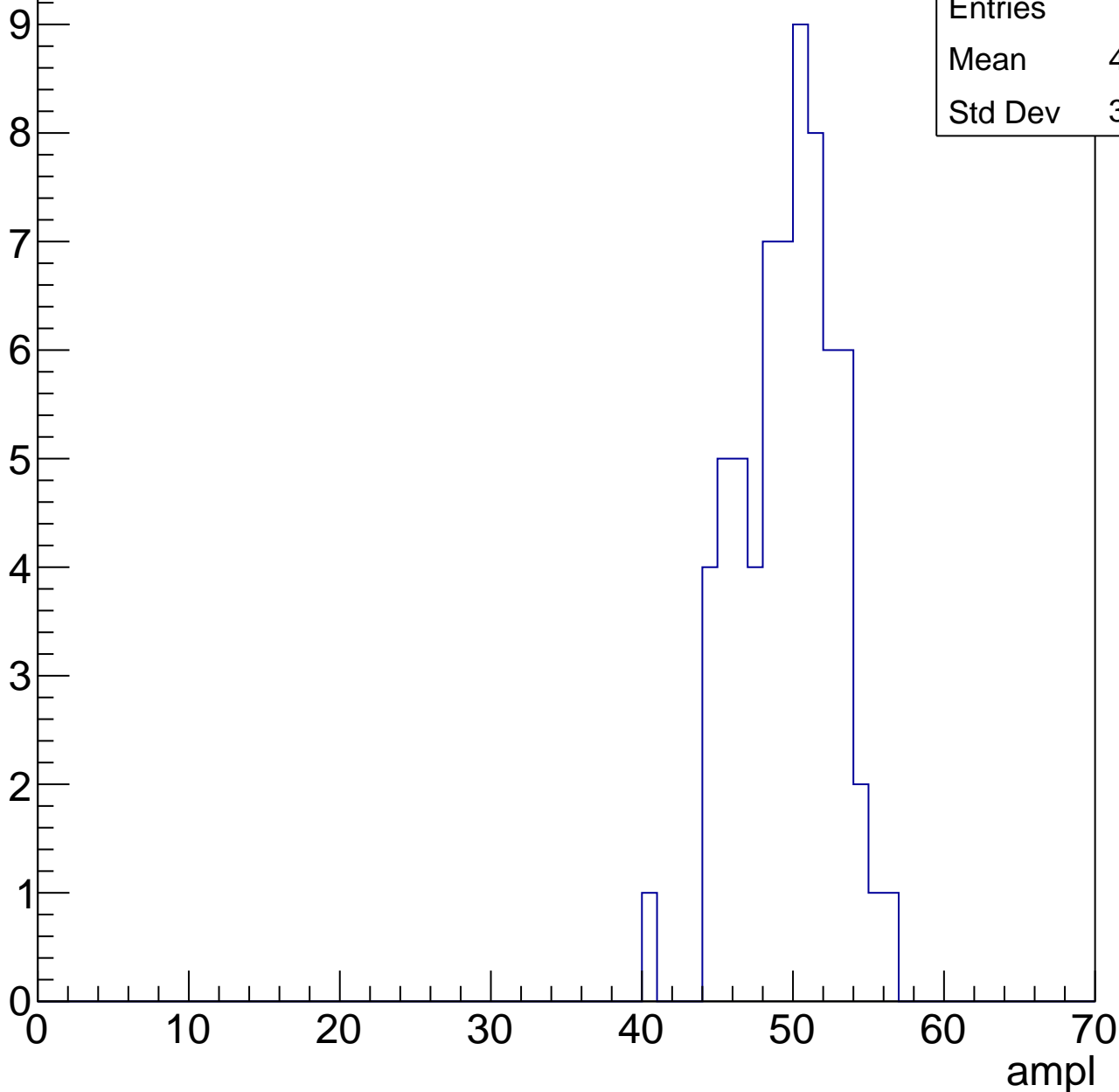


B1L102S, U2-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.17
Std Dev	3.146

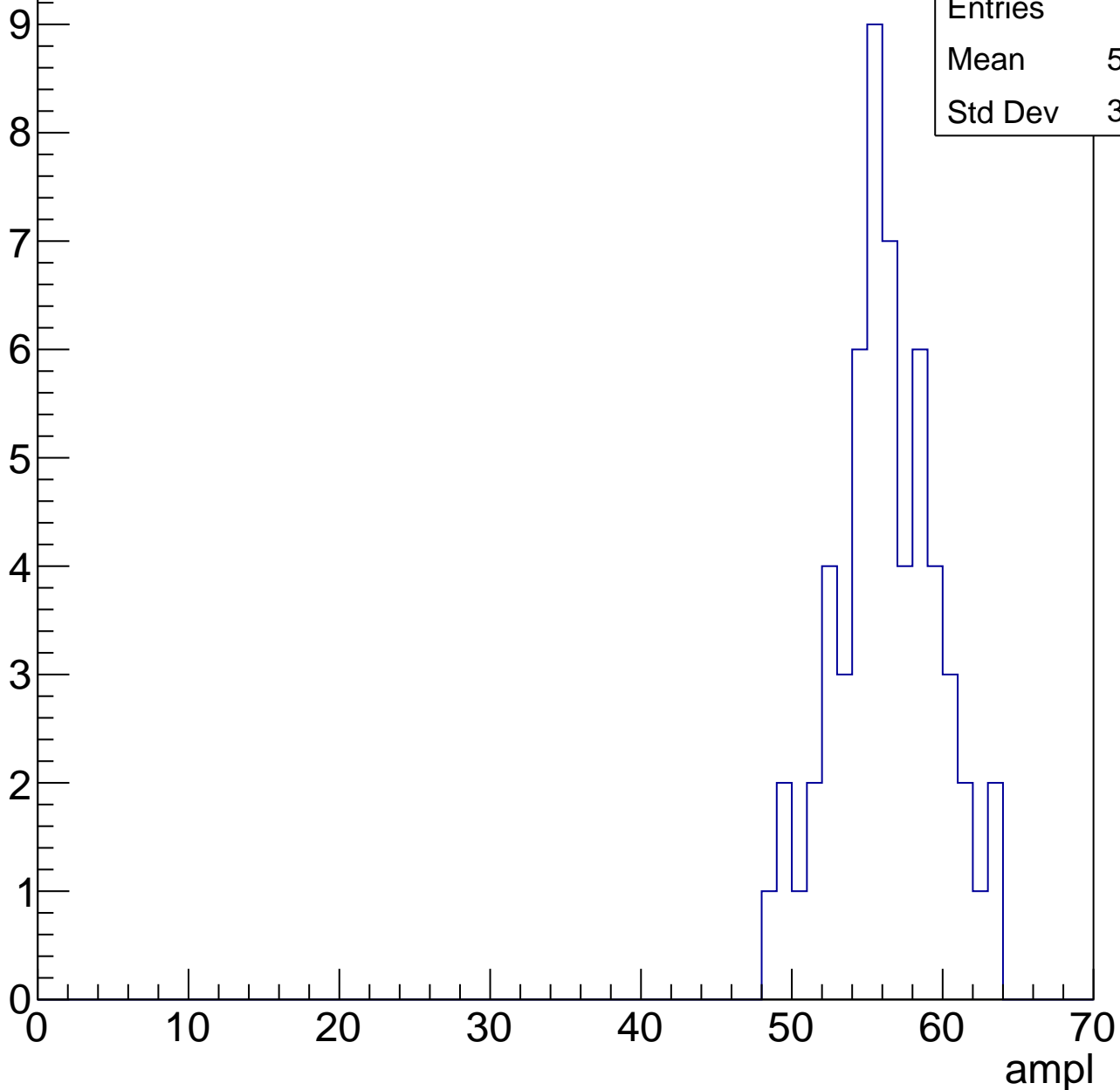


B1L102S, U2-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	55.75
Std Dev	3.415

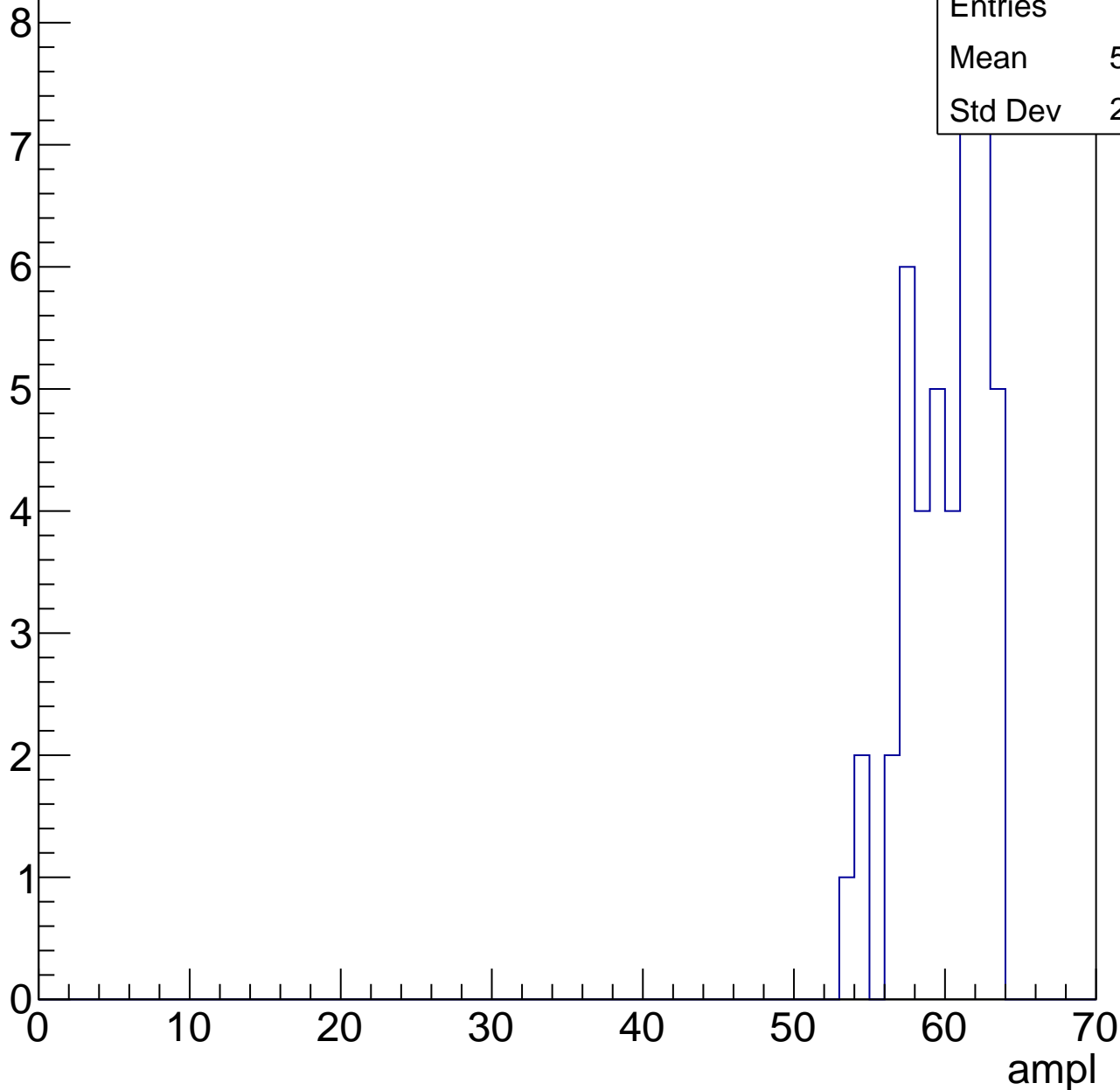


B1L102S, U2-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.58
Std Dev	2.603



B1L102S, U2-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.44
Std Dev	19.31

ampl

0 10 20 30 40 50 60 70

B1L102S, U2-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10
Std Dev	10

B1L102S, U2-ch77, adc0

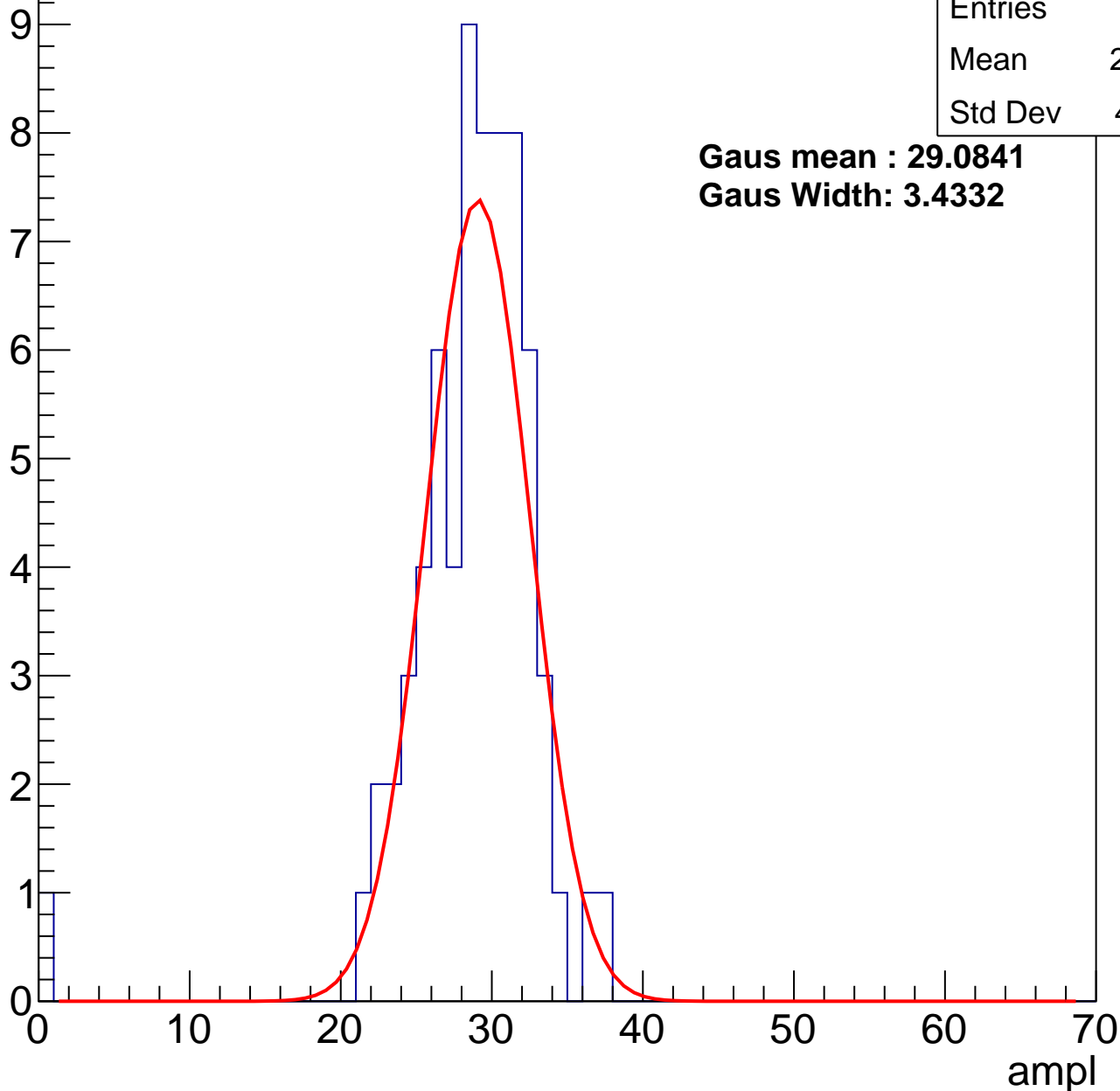
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.19
Std Dev	4.741

Gaus mean : 29.0841

Gaus Width: 3.4332



B1L102S, U2-ch77, adc1

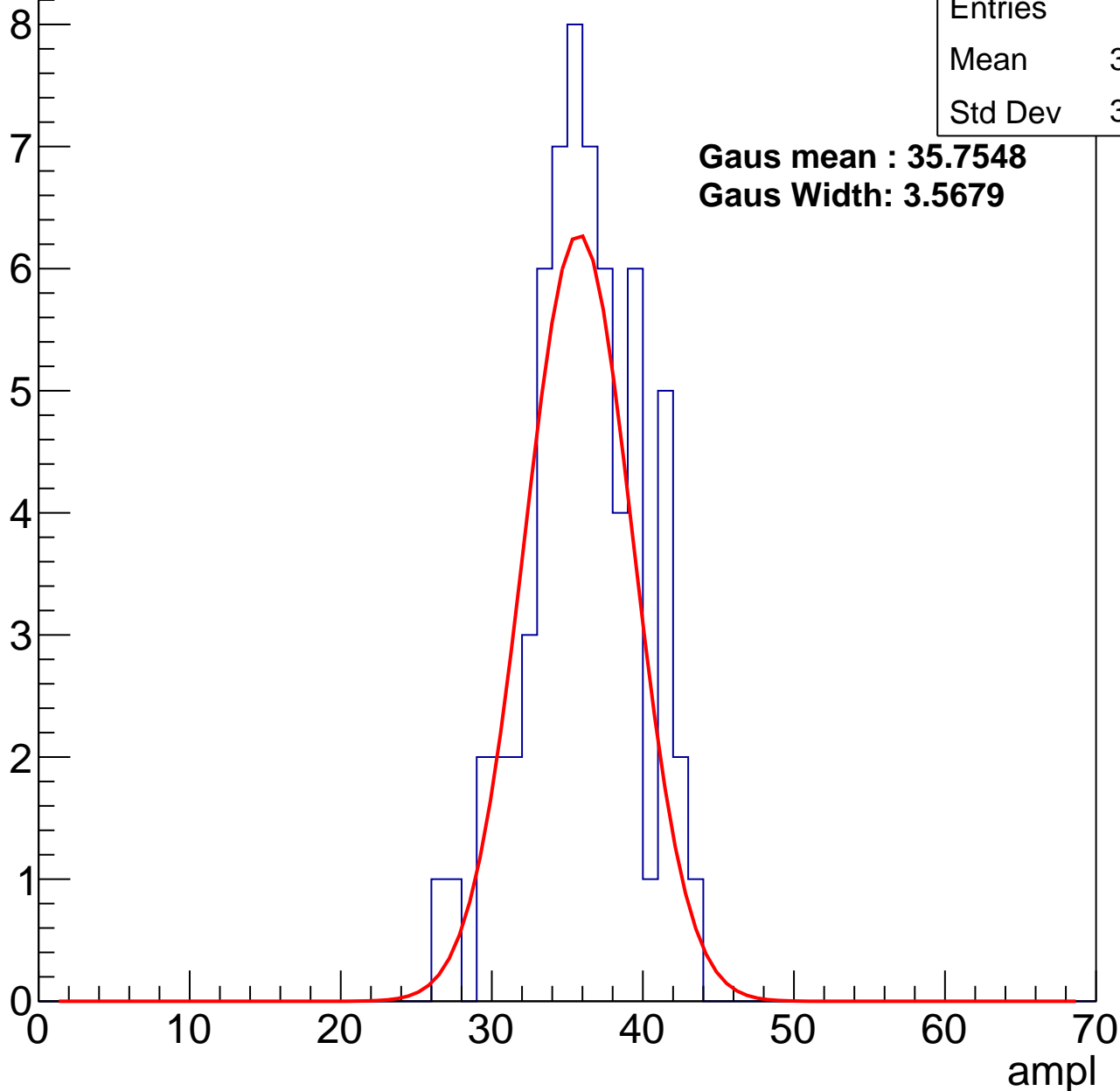
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.58
Std Dev	3.678

Gaus mean : 35.7548

Gaus Width: 3.5679



B1L102S, U2-ch77, adc2

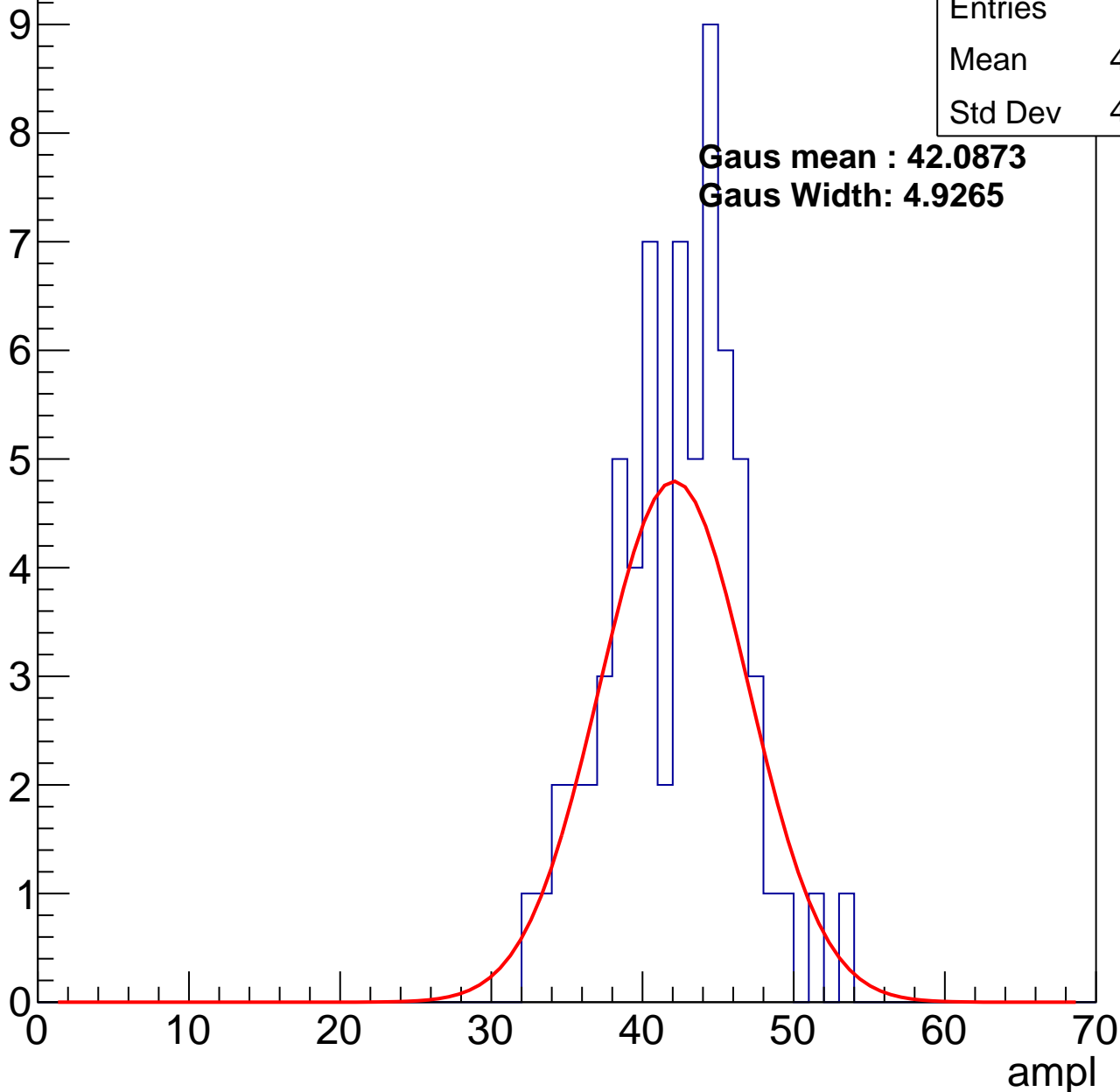
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.78
Std Dev	4.259

Gaus mean : 42.0873

Gaus Width: 4.9265

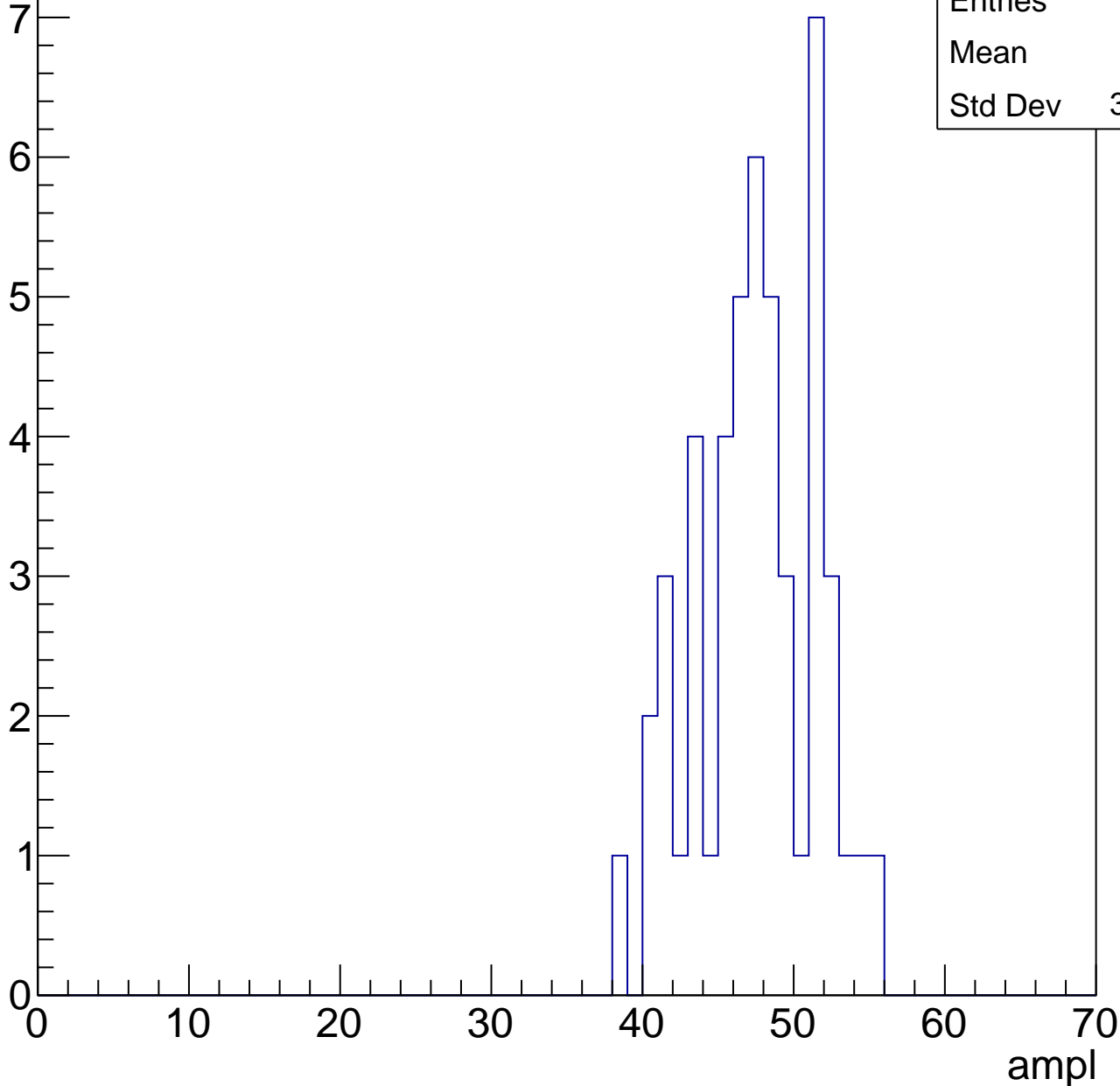


B1L102S, U2-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

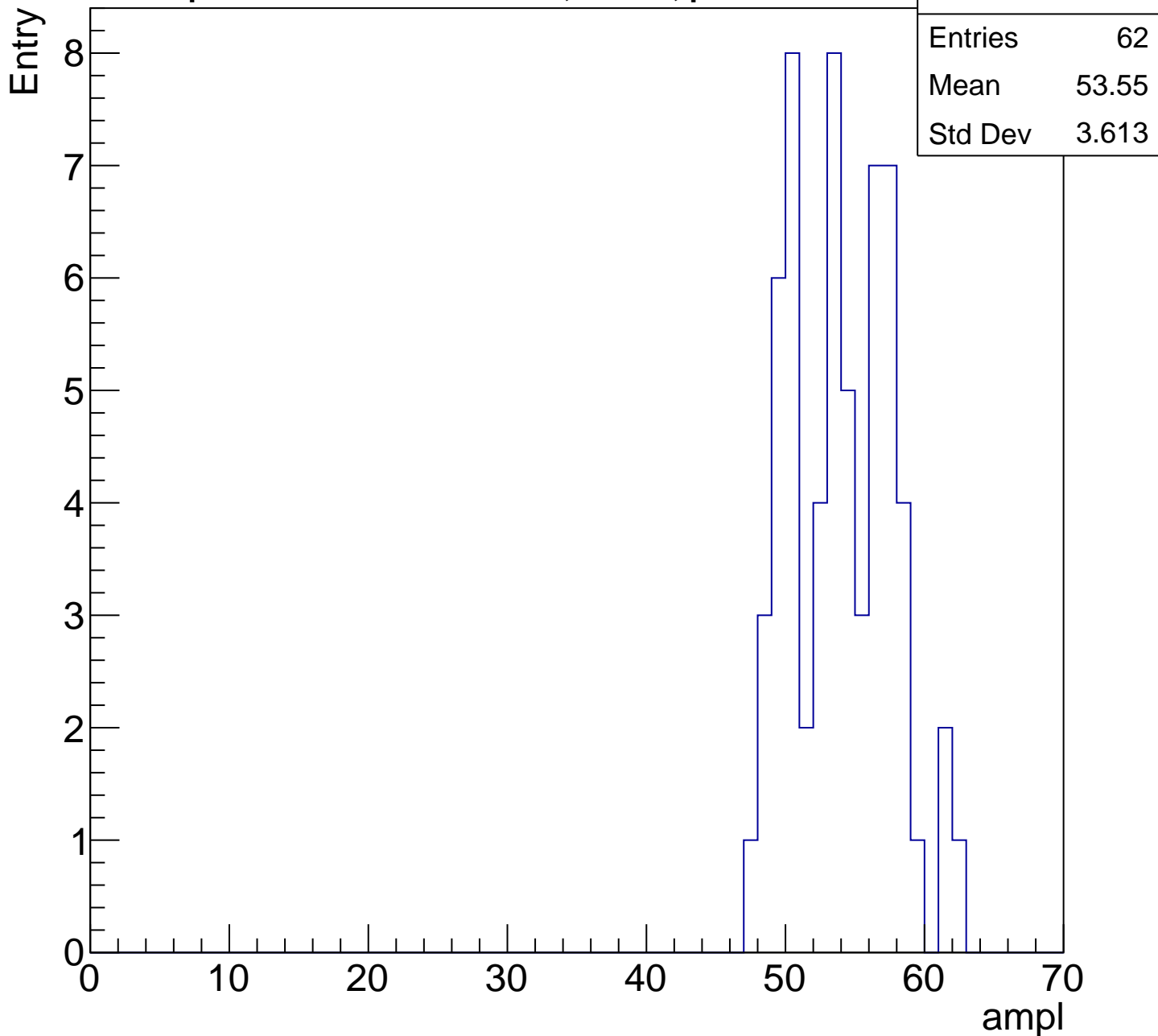
Entry

Entries	49
Mean	47
Std Dev	3.959



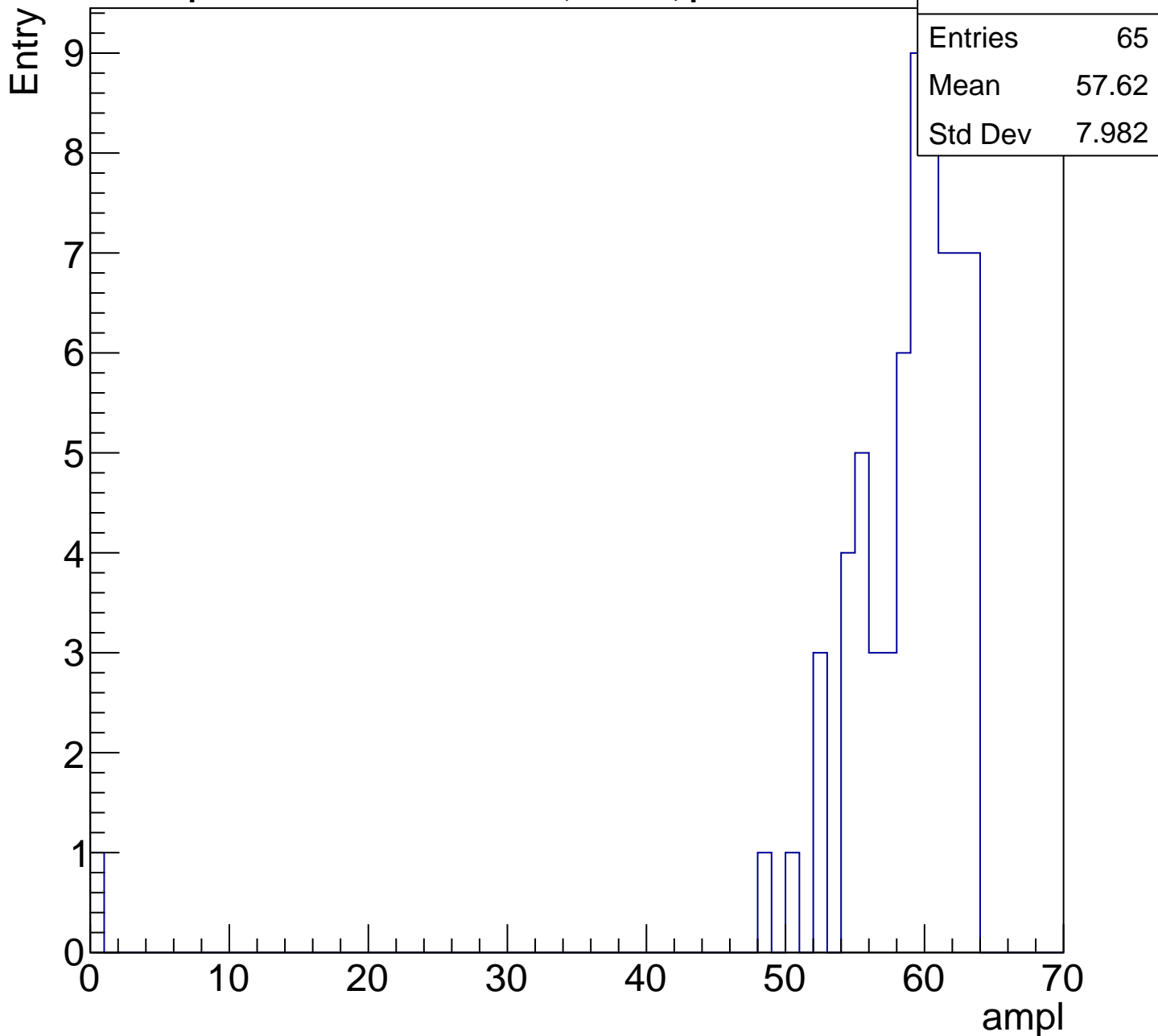
B1L102S, U2-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch77, adc6

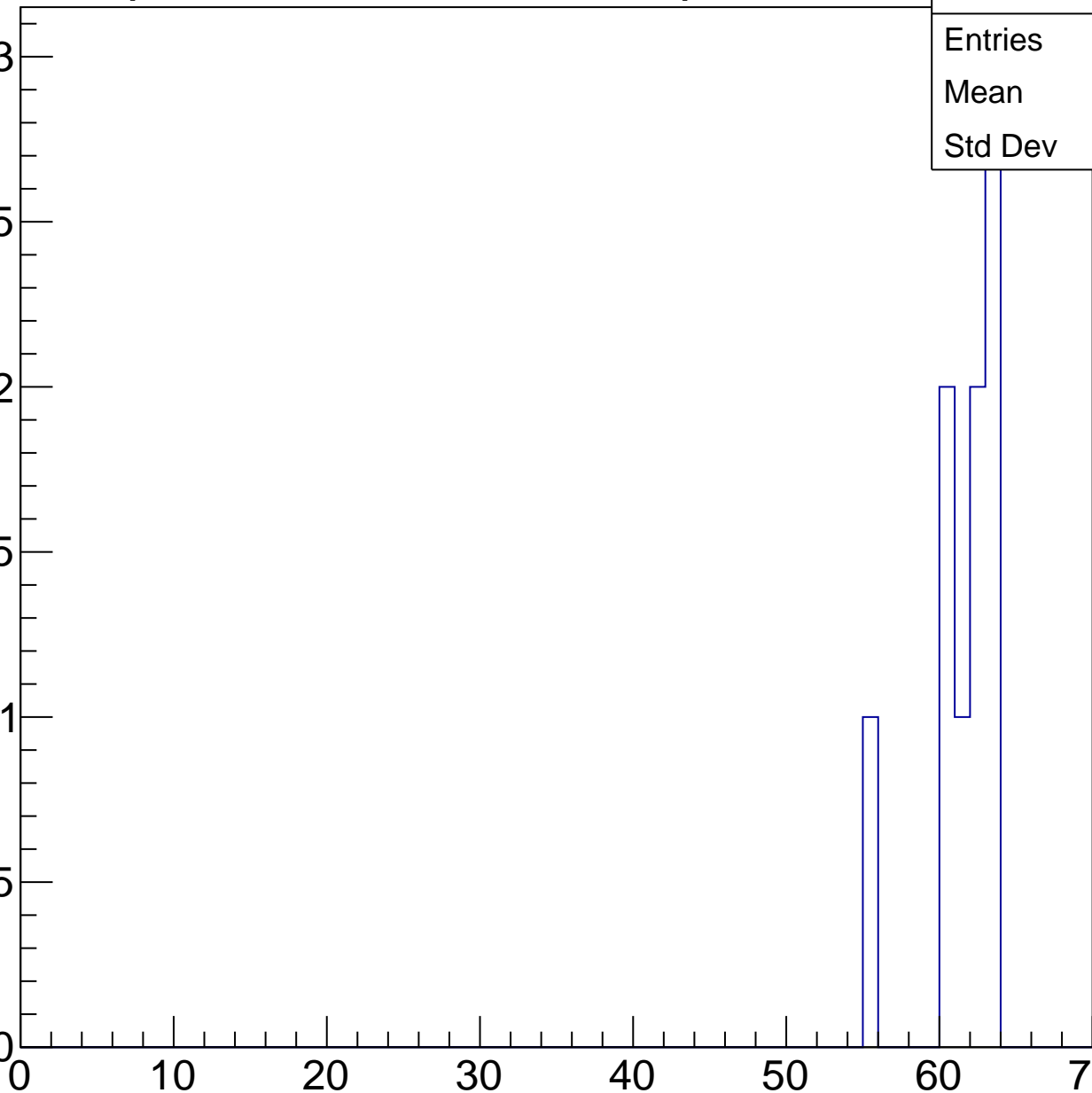
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61
Std Dev	2.404

ampl



B1L102S, U2-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U2-ch78, adc0

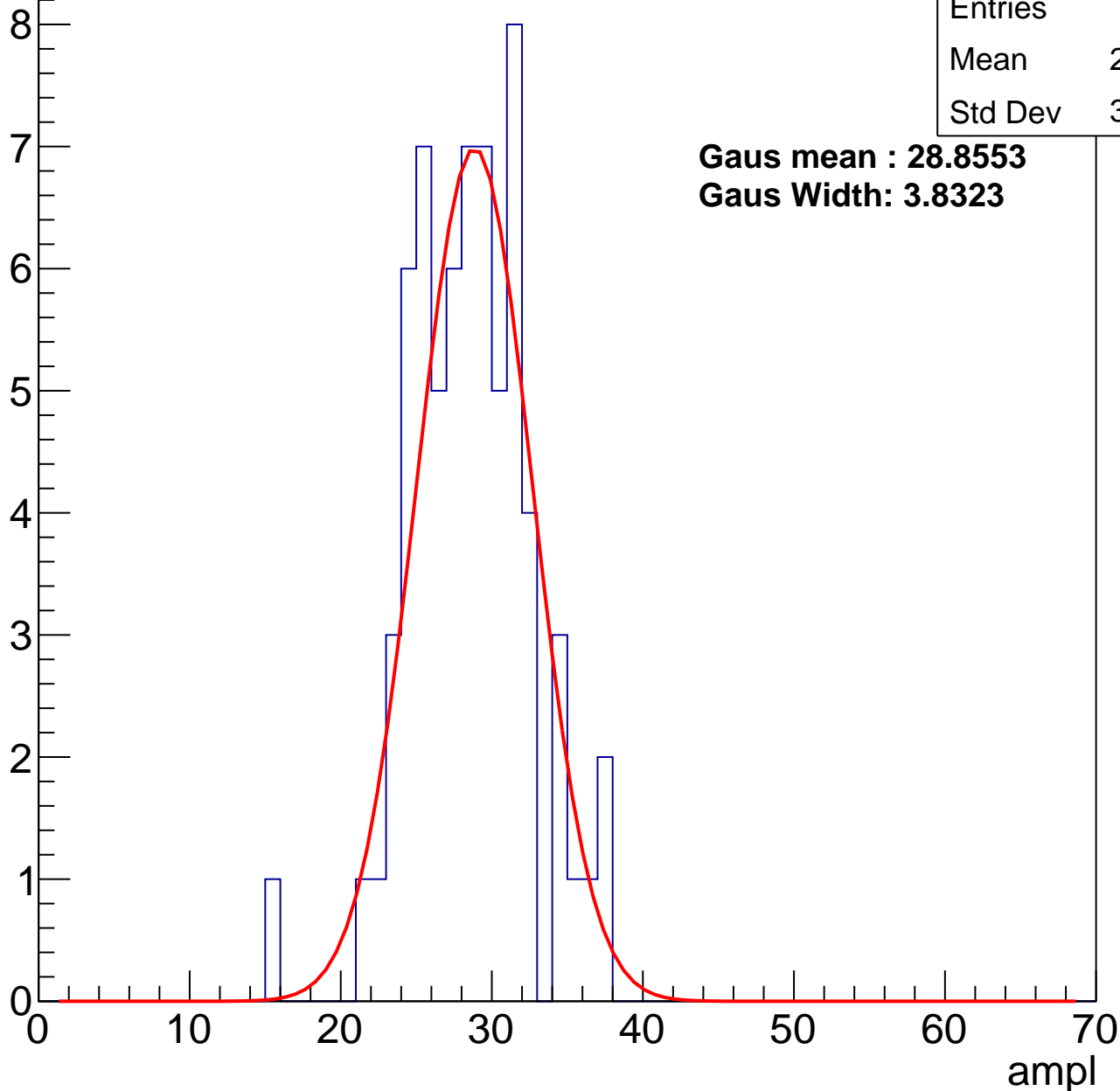
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.09
Std Dev	3.947

Gaus mean : 28.8553

Gaus Width: 3.8323



B1L102S, U2-ch78, adc1

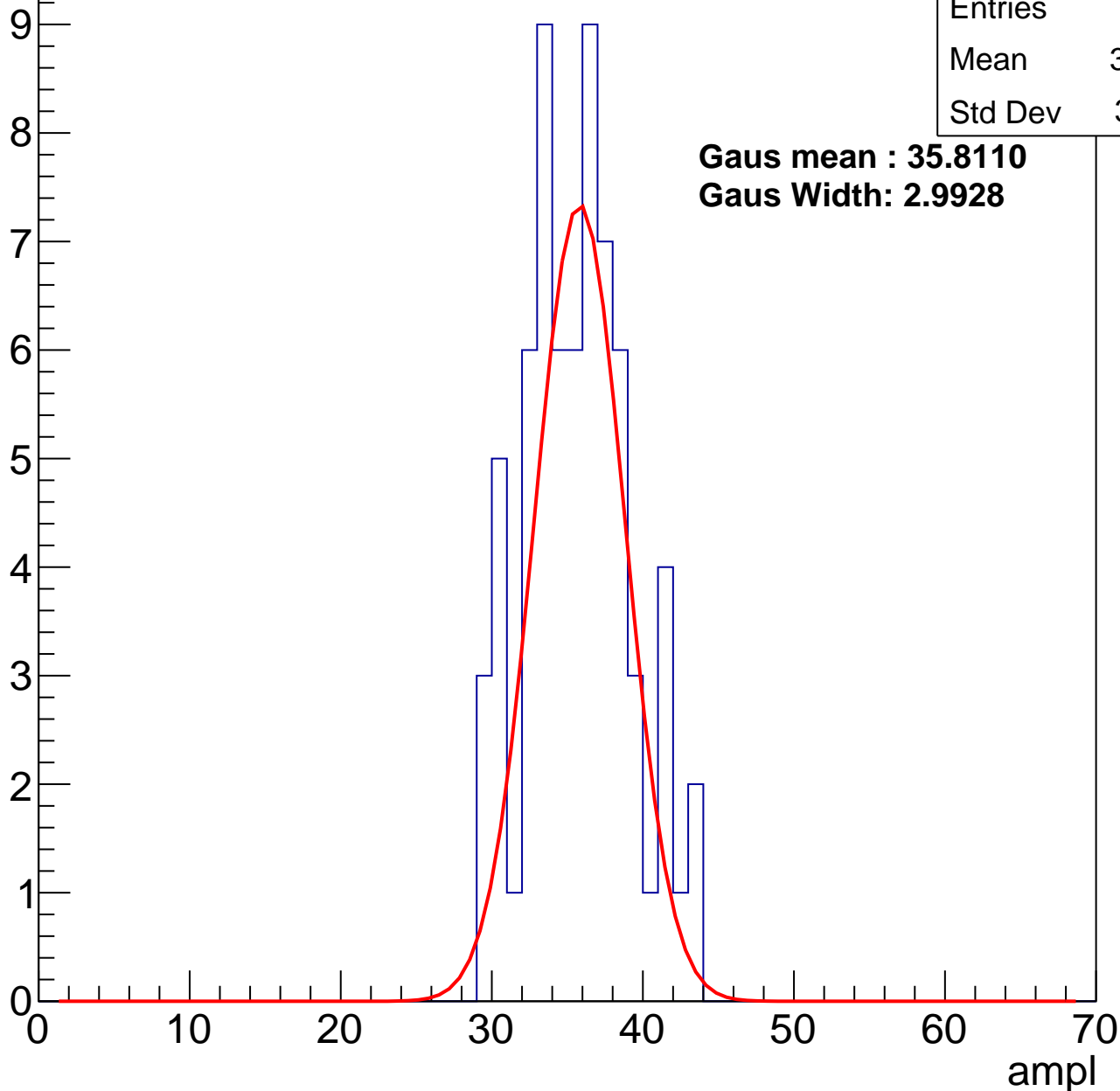
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.23
Std Dev	3.461

Gaus mean : 35.8110

Gaus Width: 2.9928



B1L102S, U2-ch78, adc2

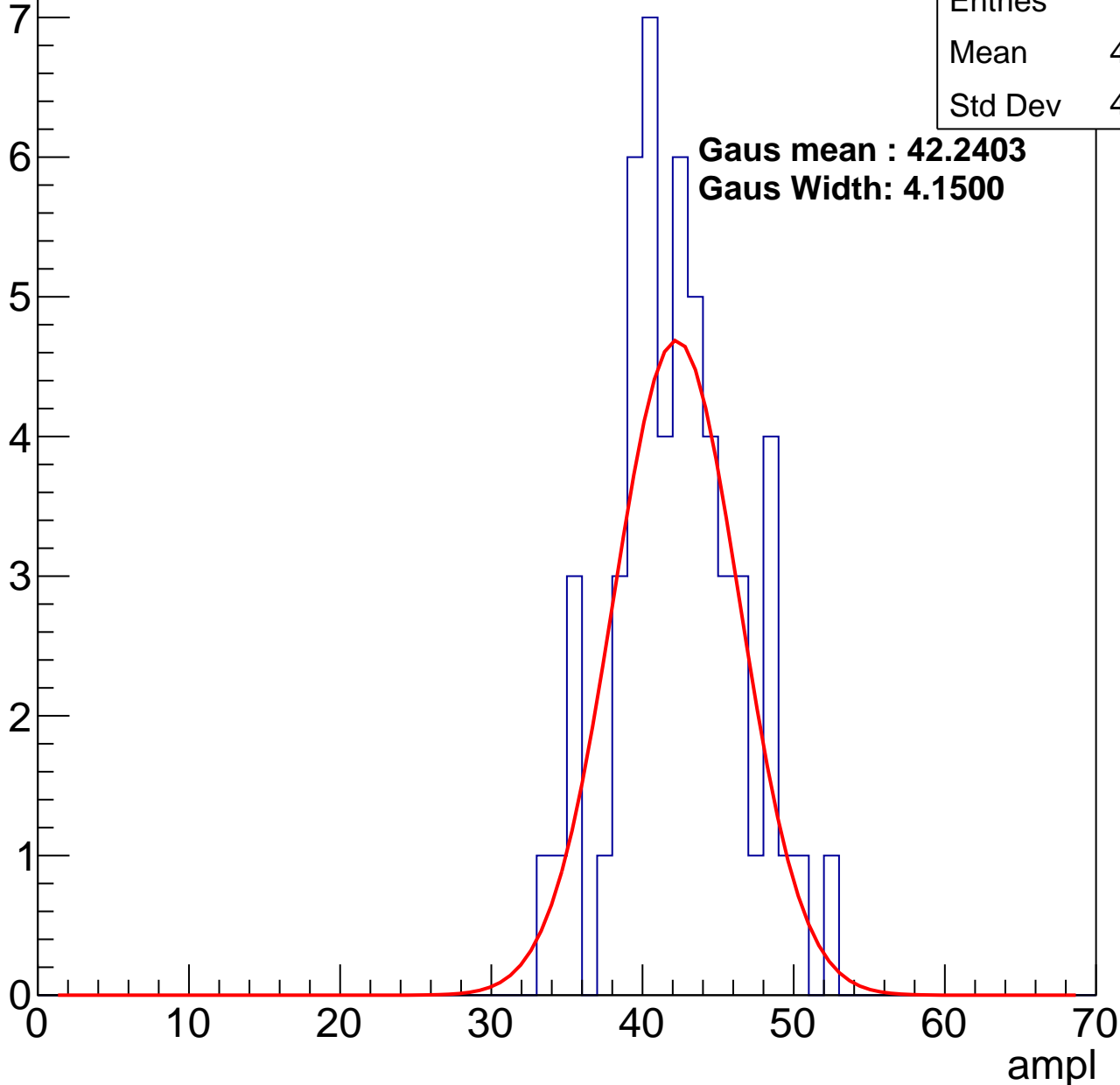
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.95
Std Dev	4.109

Gaus mean : 42.2403

Gaus Width: 4.1500

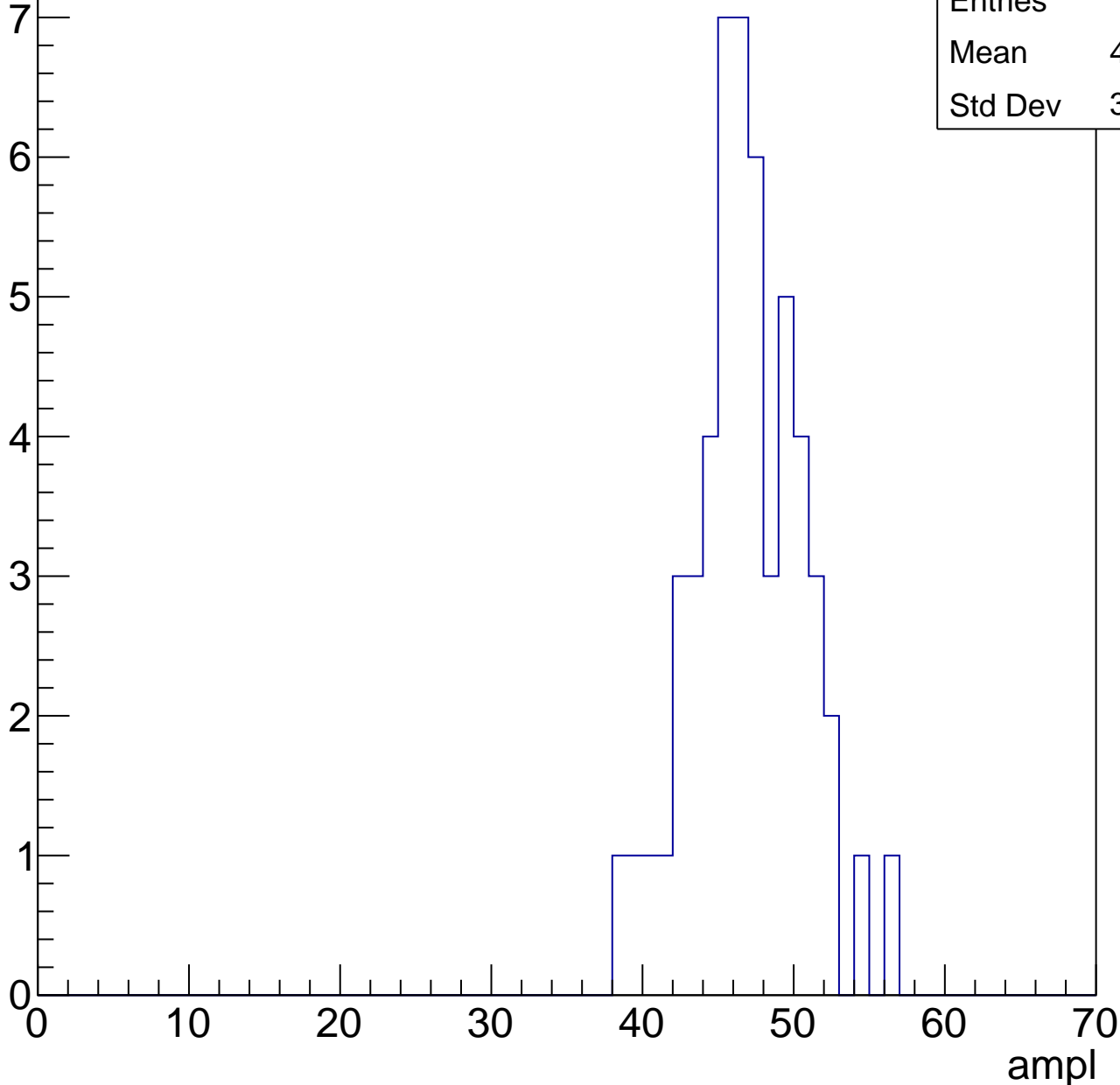


B1L102S, U2-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	46.49
Std Dev	3.627

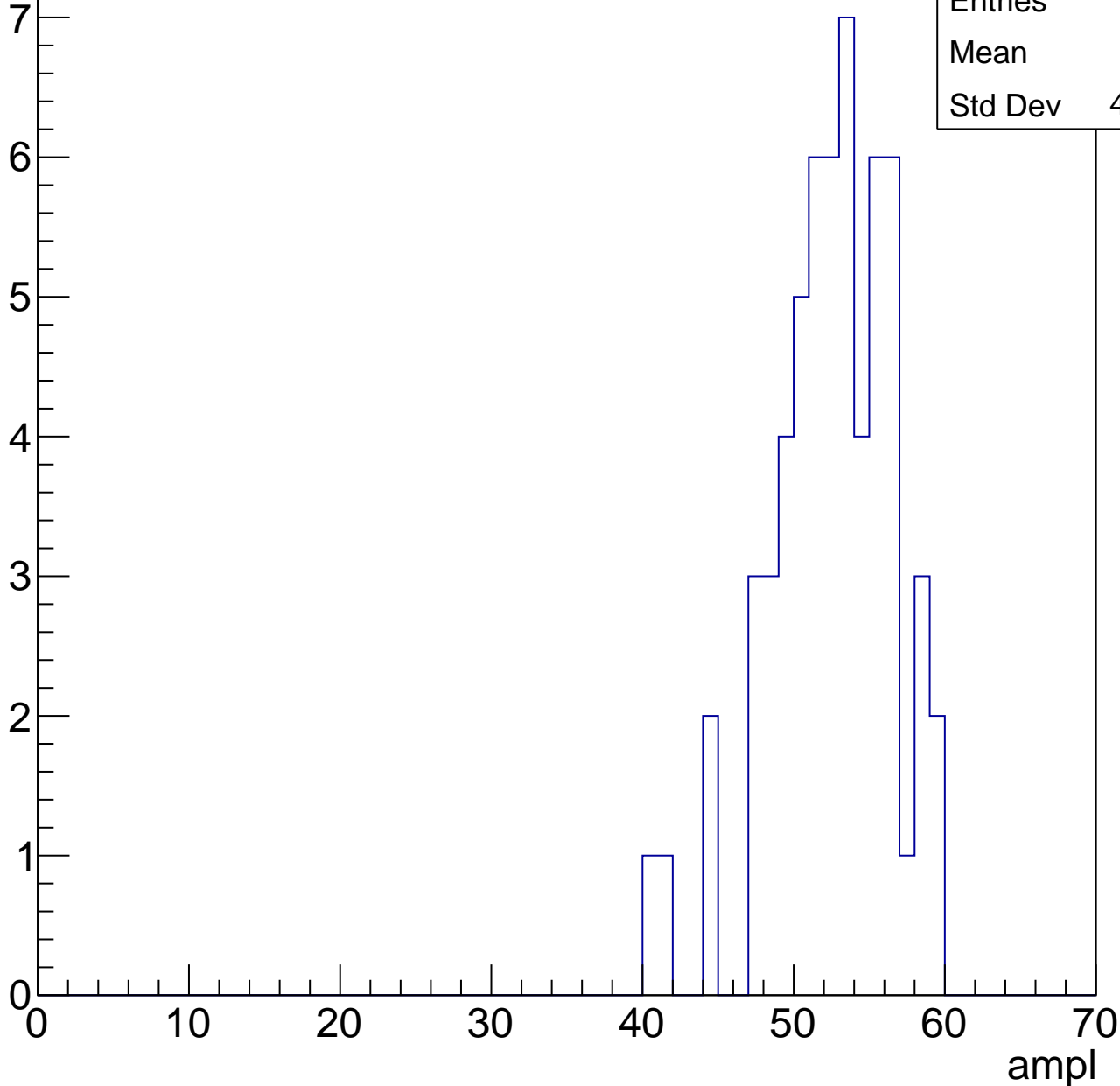


B1L102S, U2-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	52
Std Dev	4.046

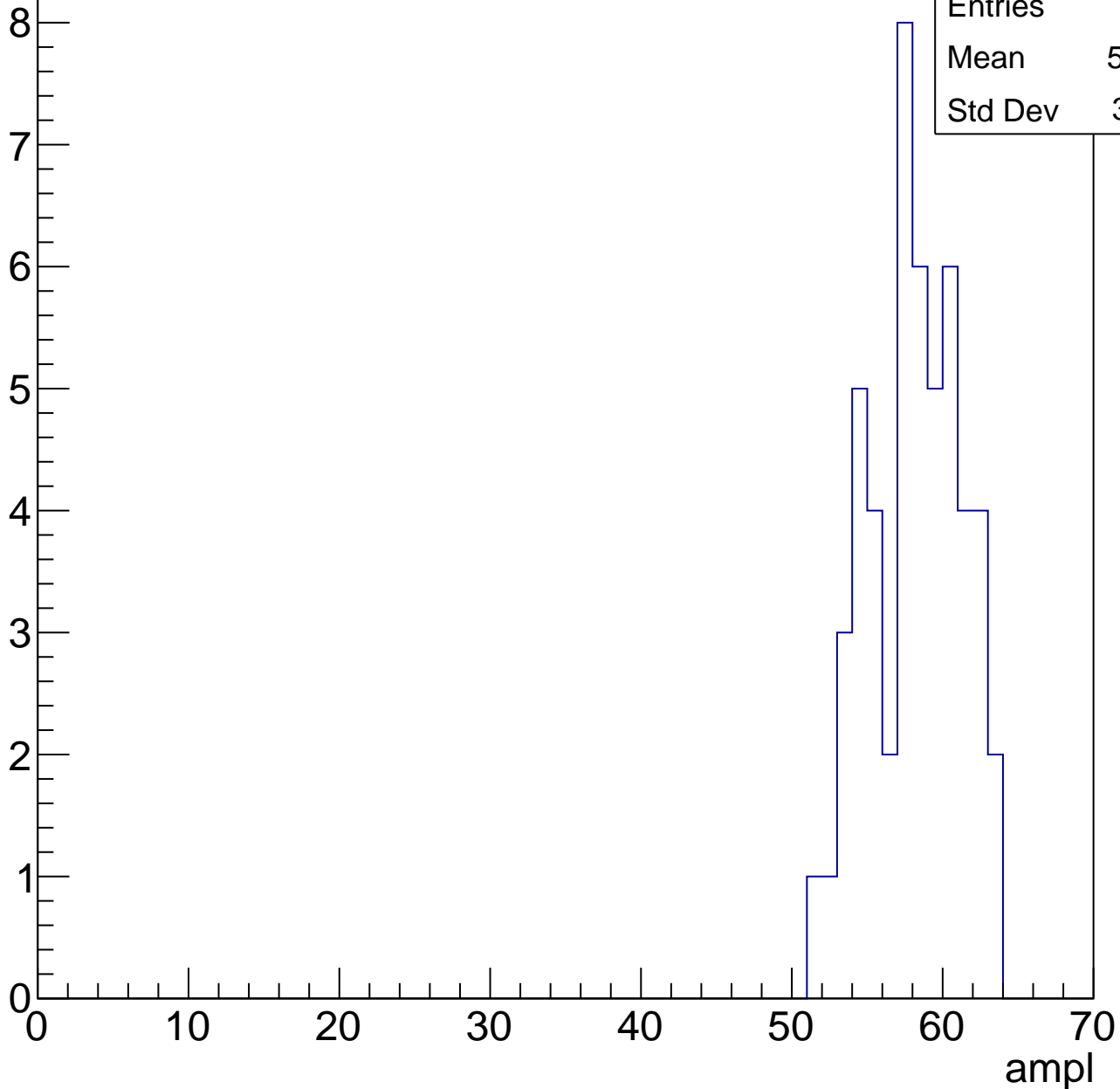


B1L102S, U2-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	57.67
Std Dev	3.021

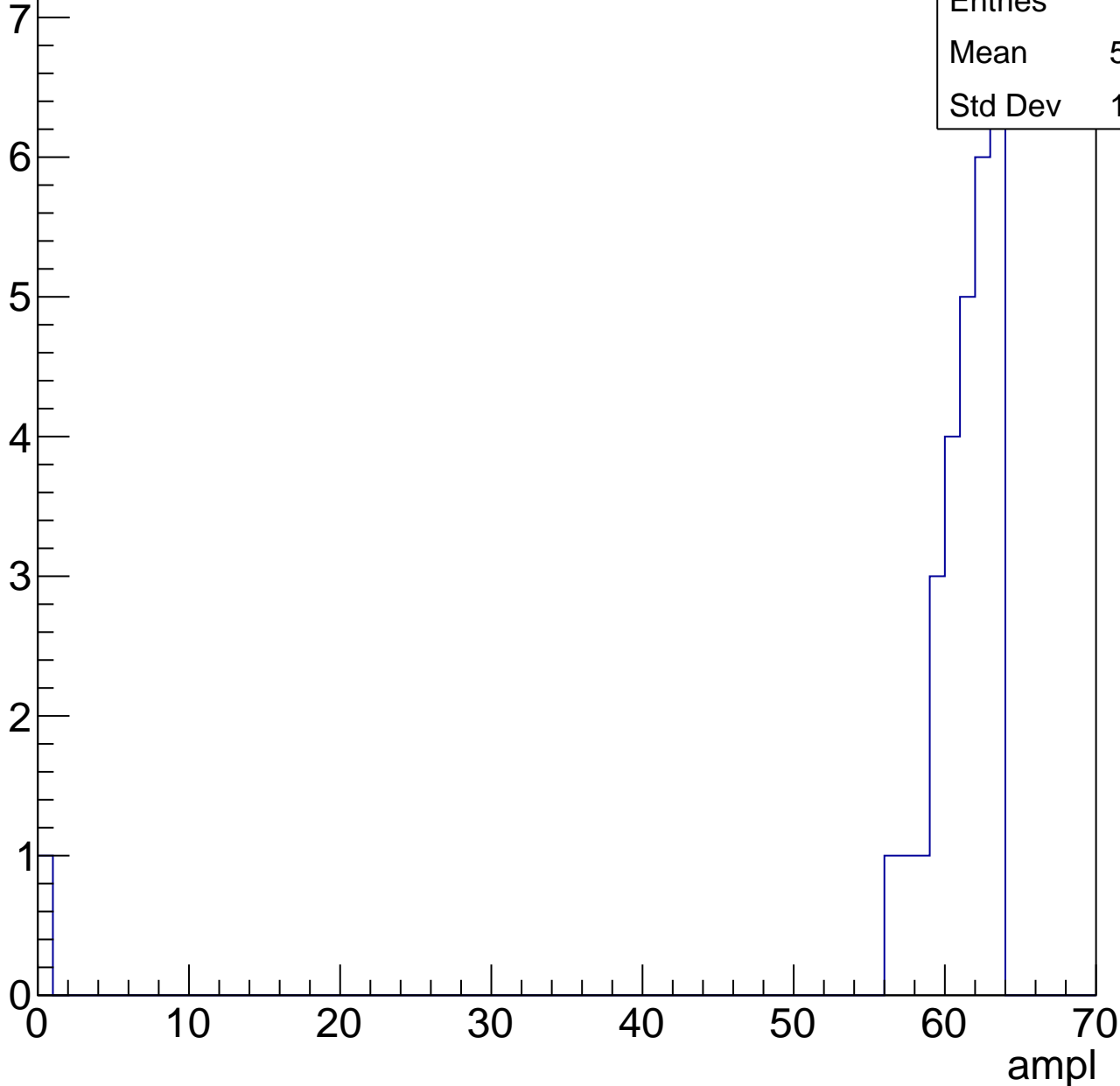


B1L102S, U2-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	58.83
Std Dev	11.27



B1L102S, U2-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

62

Std Dev

1

B1L102S, U2-ch79, adc0

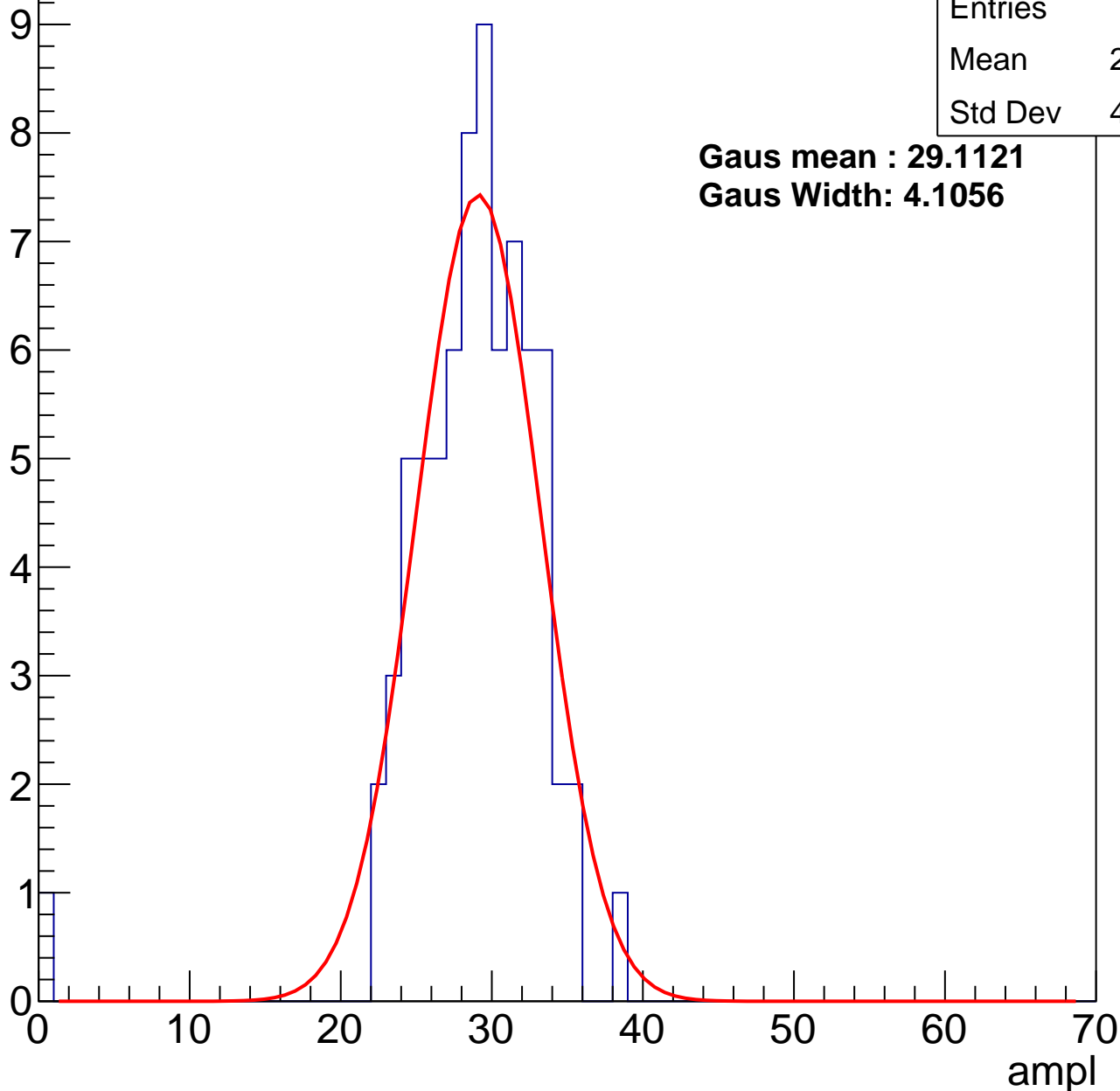
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.35
Std Dev	4.772

Gaus mean : 29.1121

Gaus Width: 4.1056



B1L102S, U2-ch79, adc1

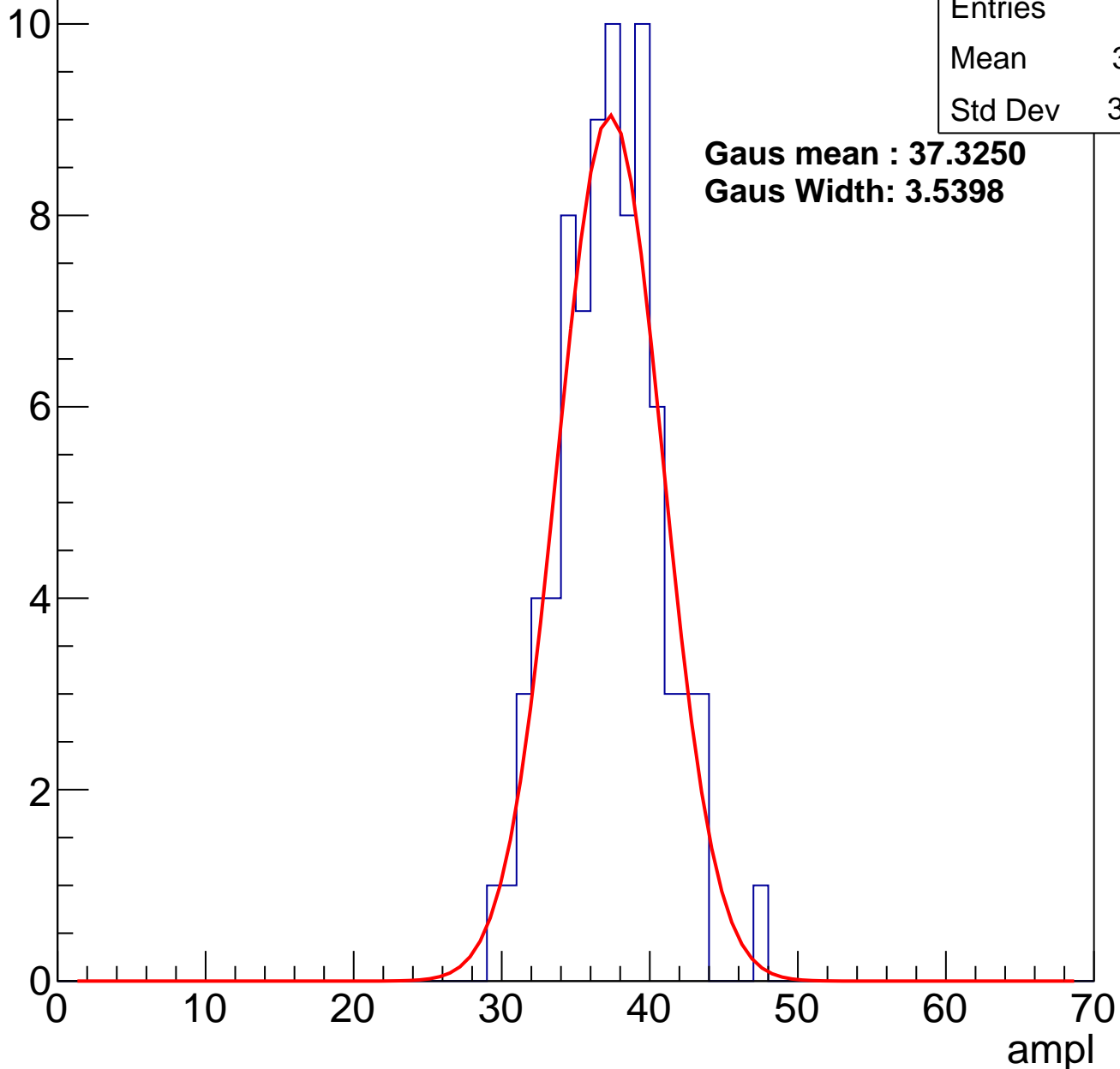
calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	36.81
Std Dev	3.378

Gaus mean : 37.3250

Gaus Width: 3.5398

Entry



B1L102S, U2-ch79, adc2

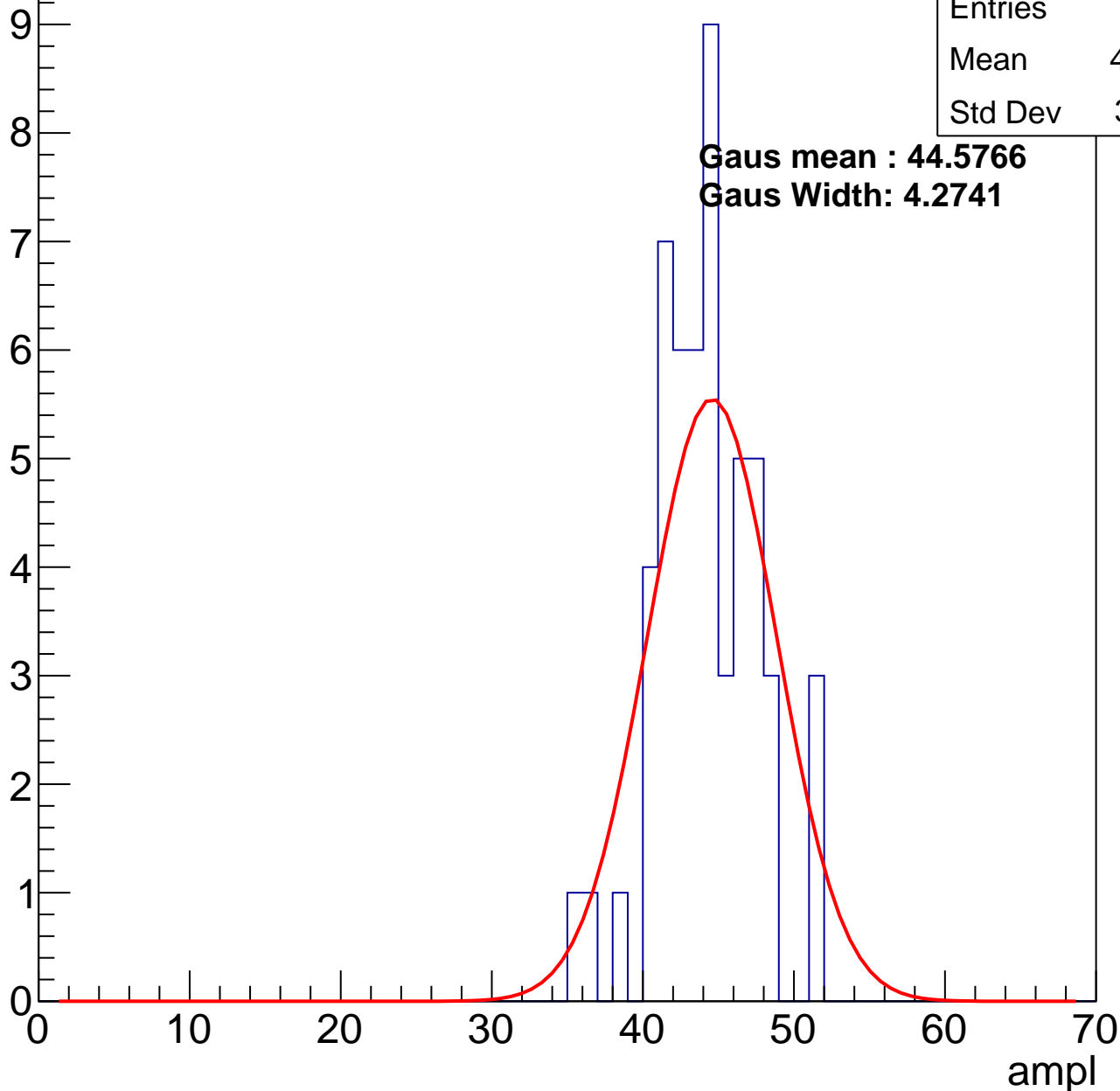
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	43.69
Std Dev	3.321

Gaus mean : 44.5766

Gaus Width: 4.2741

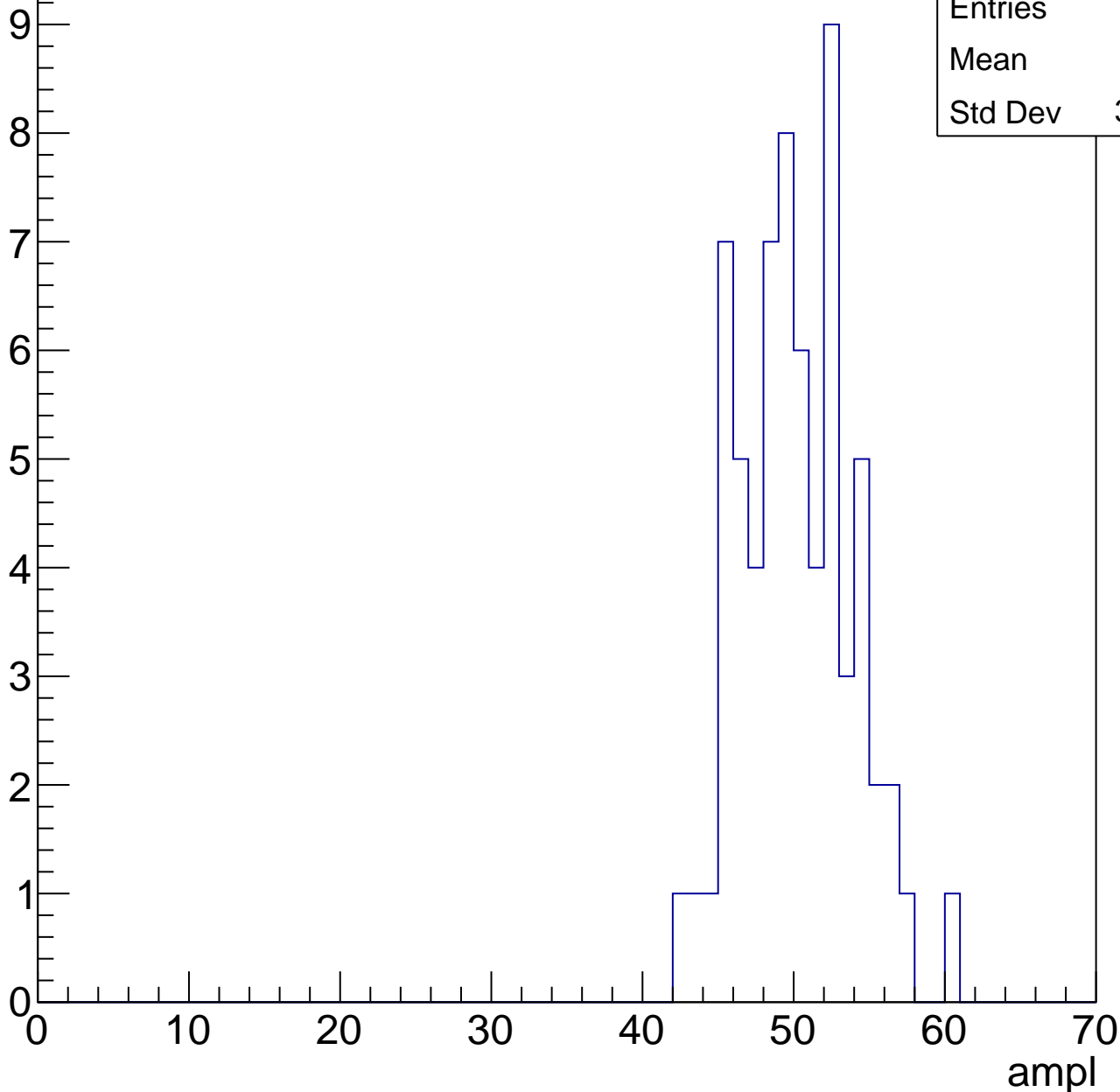


B1L102S, U2-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.7
Std Dev	3.641

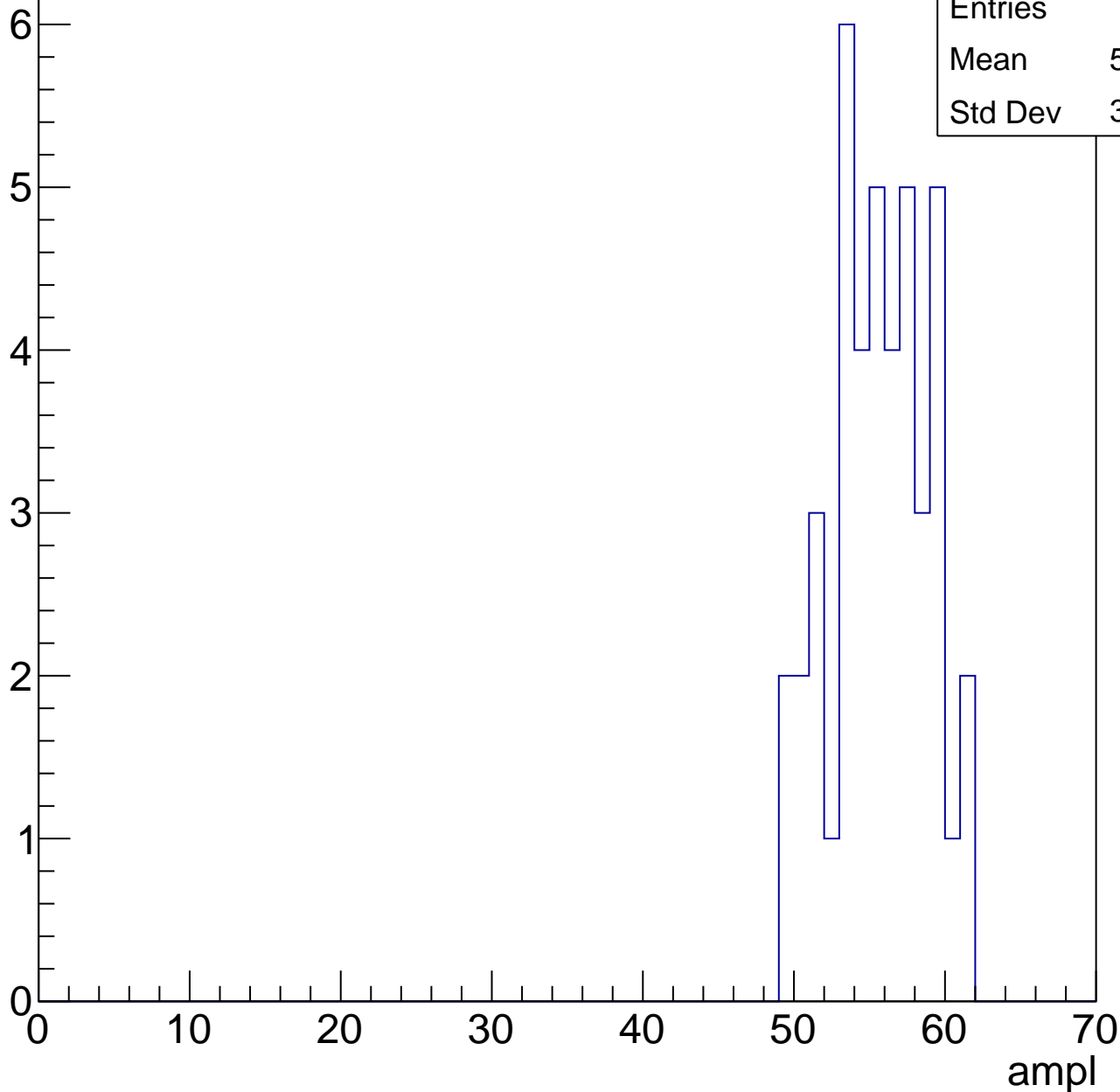


B1L102S, U2-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.16
Std Dev	3.176



B1L102S, U2-ch79, adc5

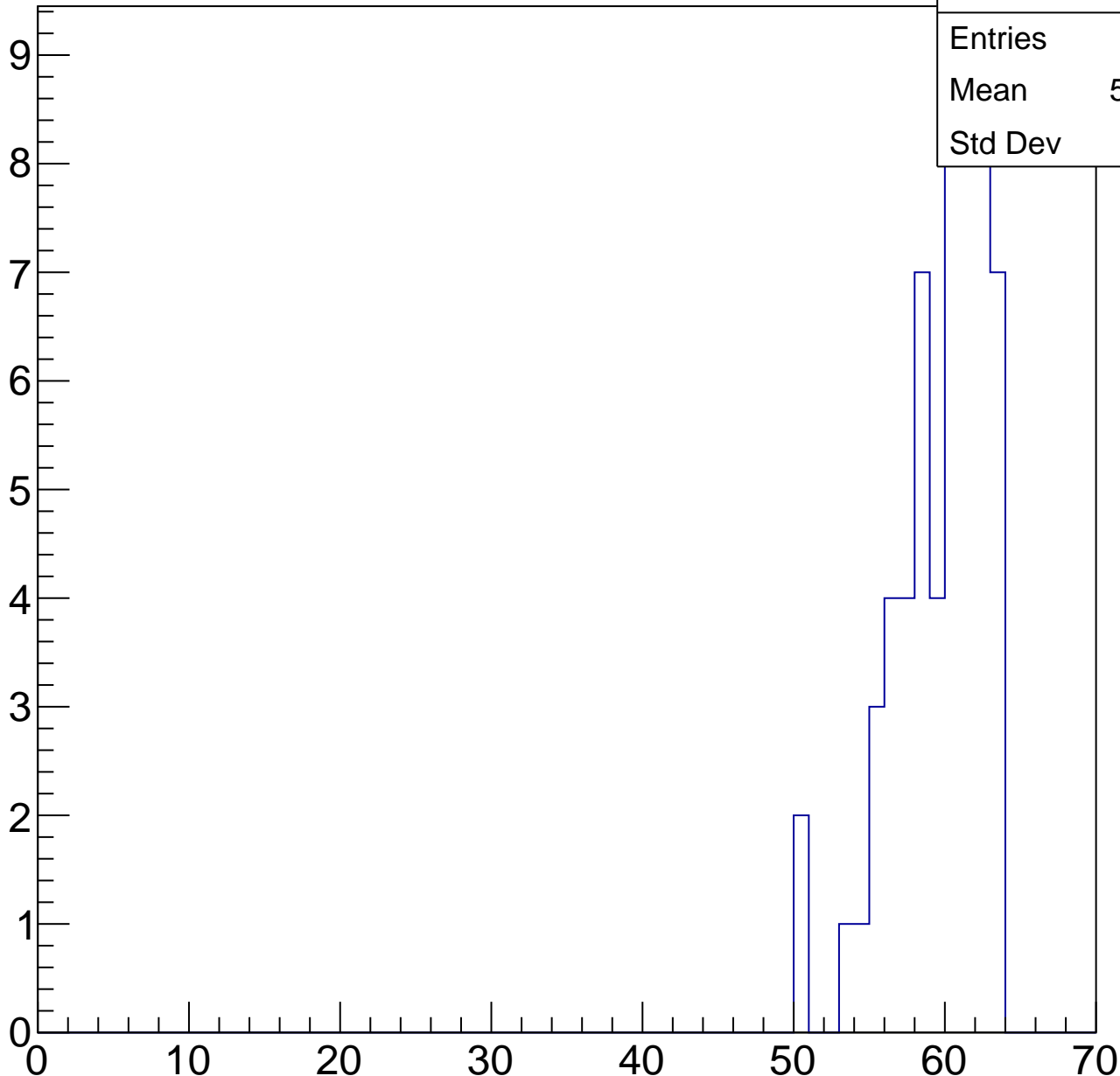
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	59.19
Std Dev	3.11

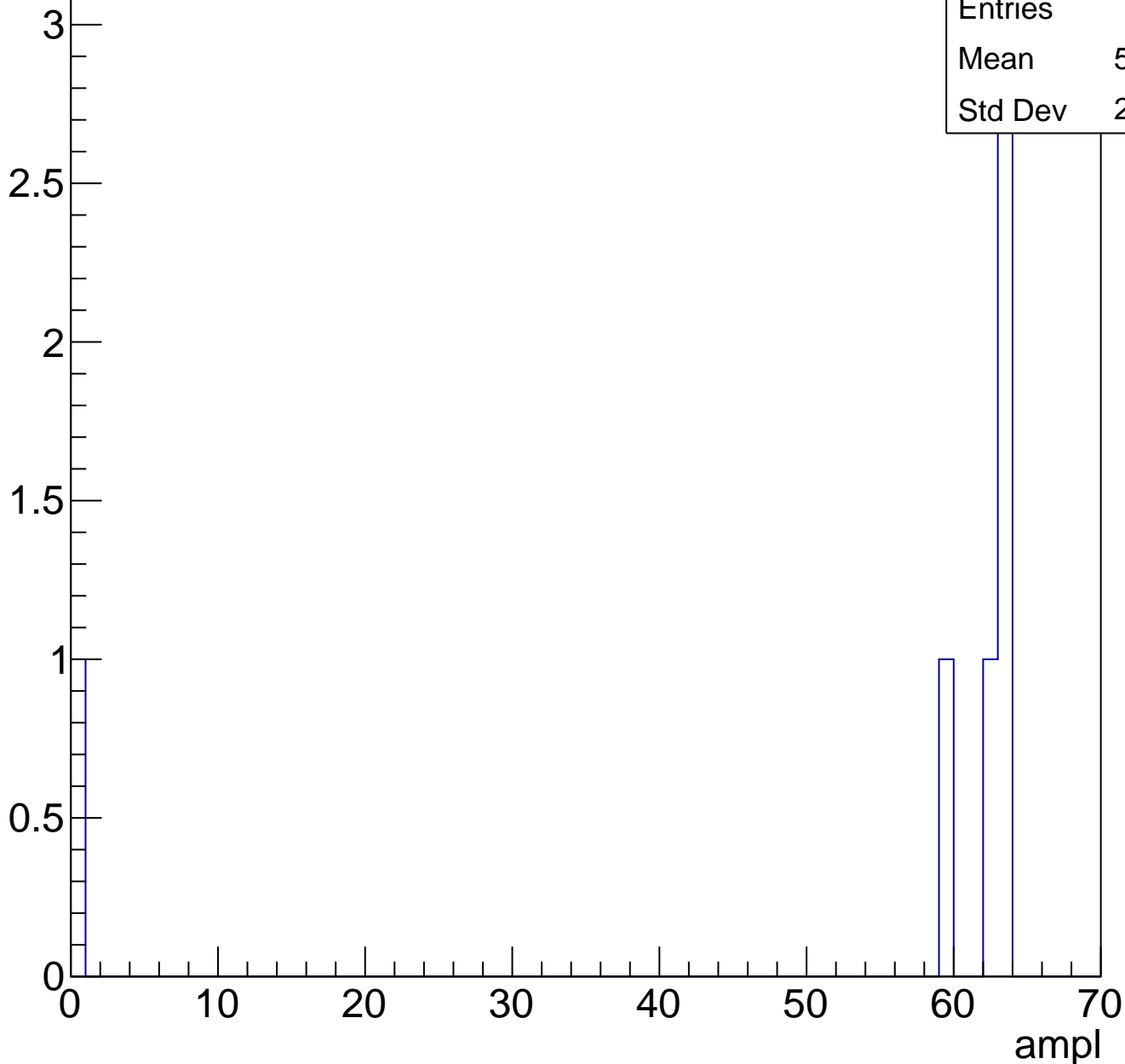
ampl



B1L102S, U2-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch80, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	27.88
Std Dev	4.092

Gaus mean : 28.4673

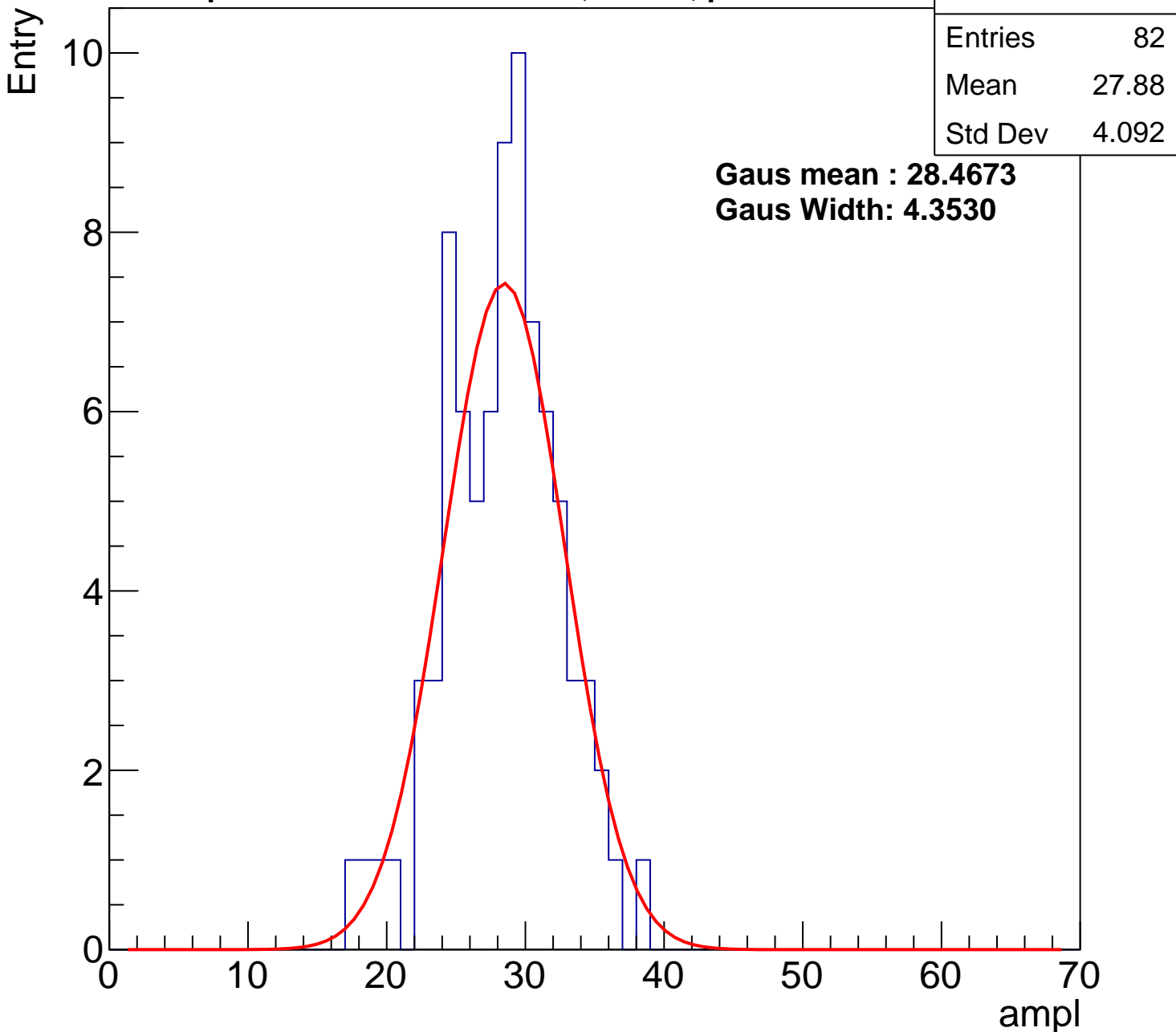
Gaus Width: 4.3530

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch80, adc1

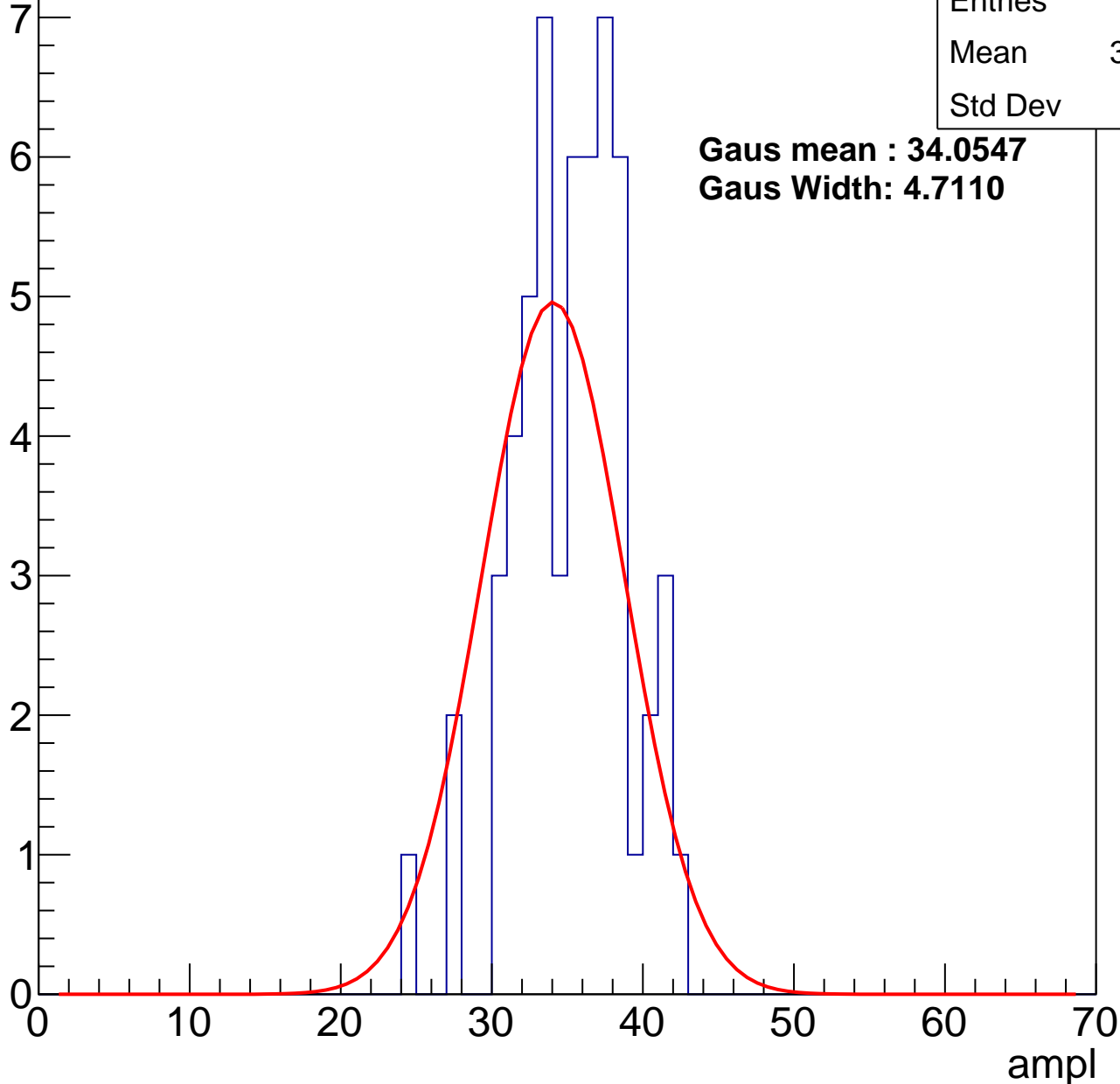
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.77
Std Dev	3.68

Gaus mean : 34.0547

Gaus Width: 4.7110



B1L102S, U2-ch80, adc2

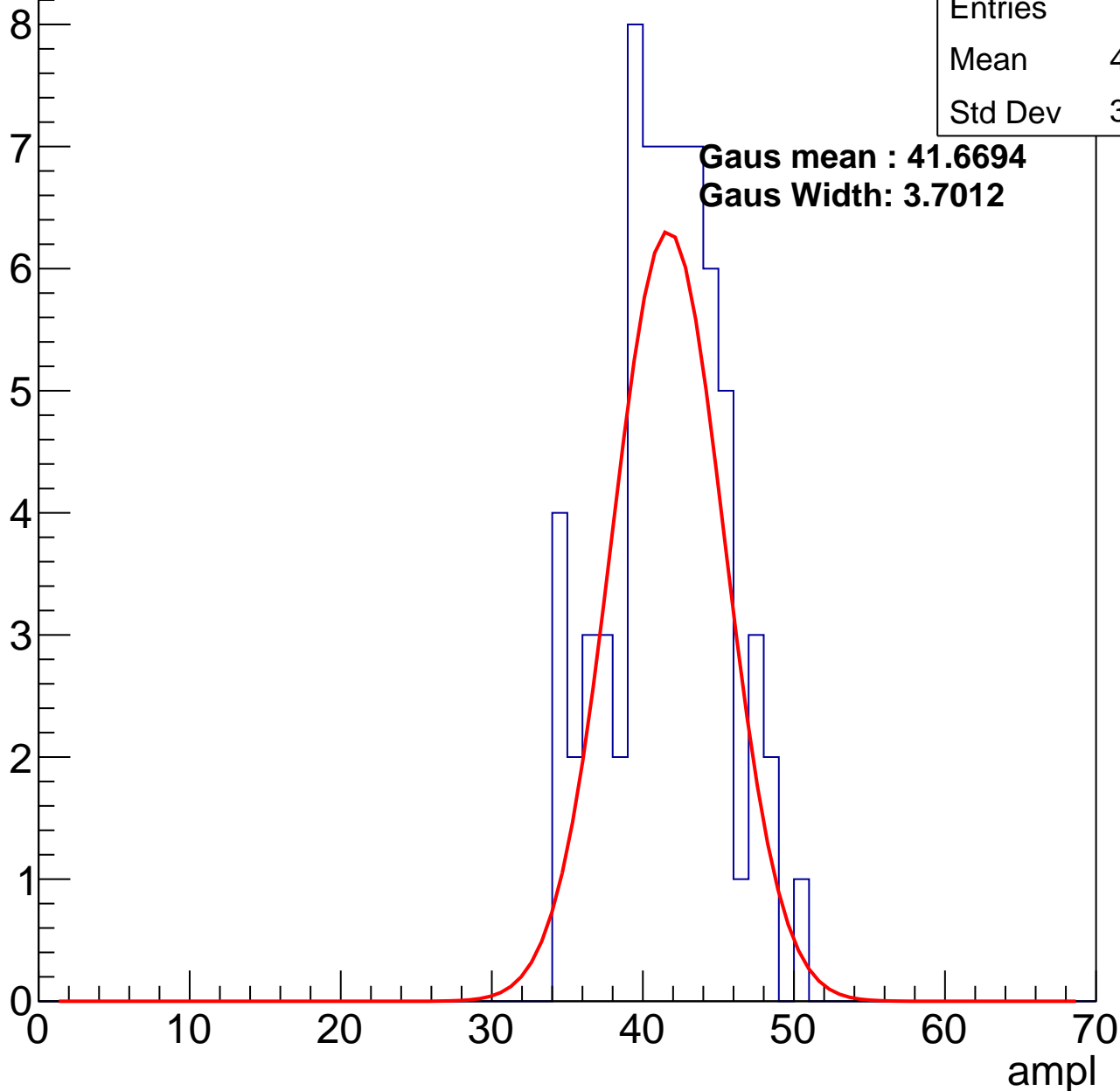
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.13
Std Dev	3.694

Gaus mean : 41.6694

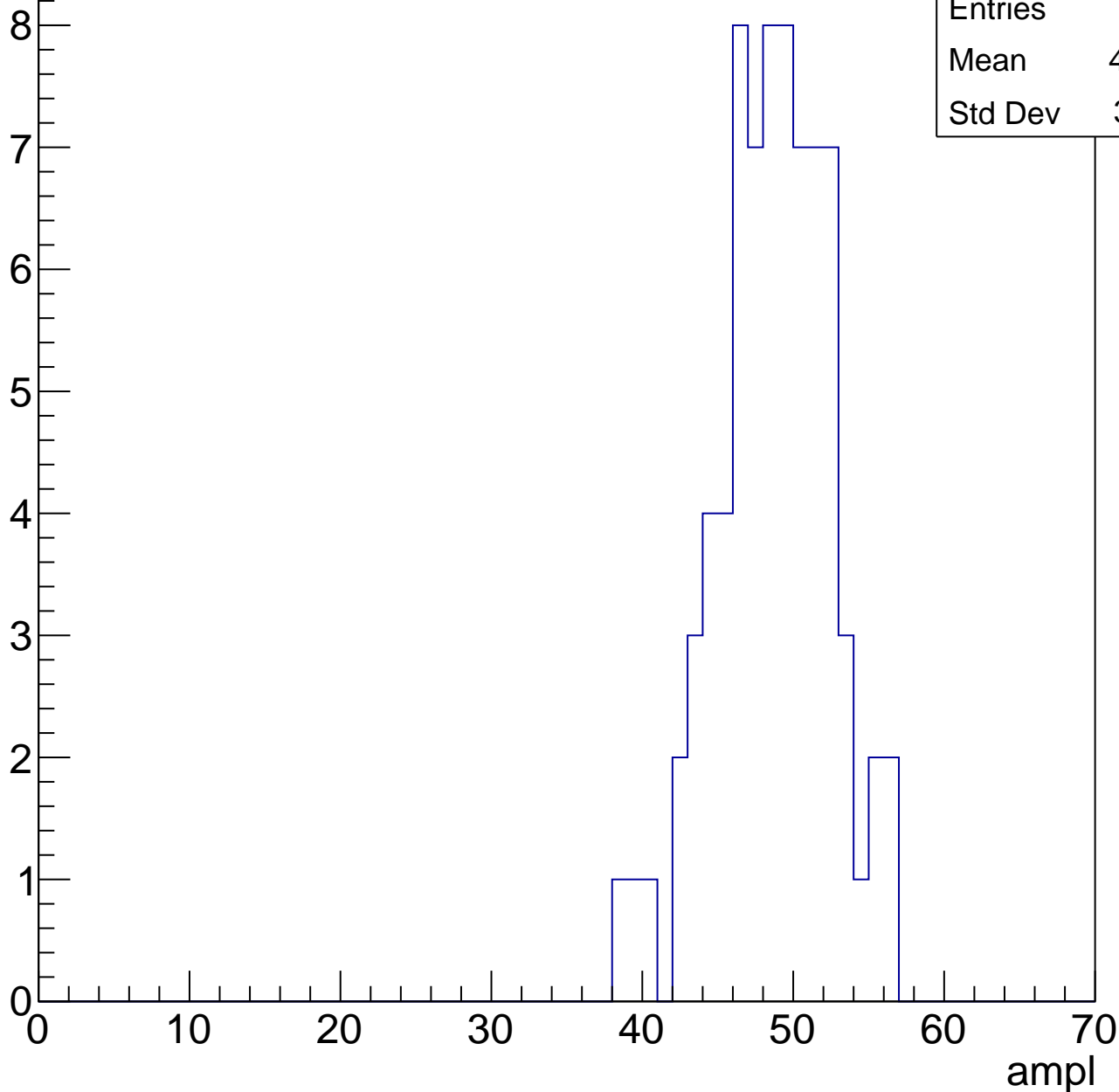
Gaus Width: 3.7012



B1L102S, U2-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



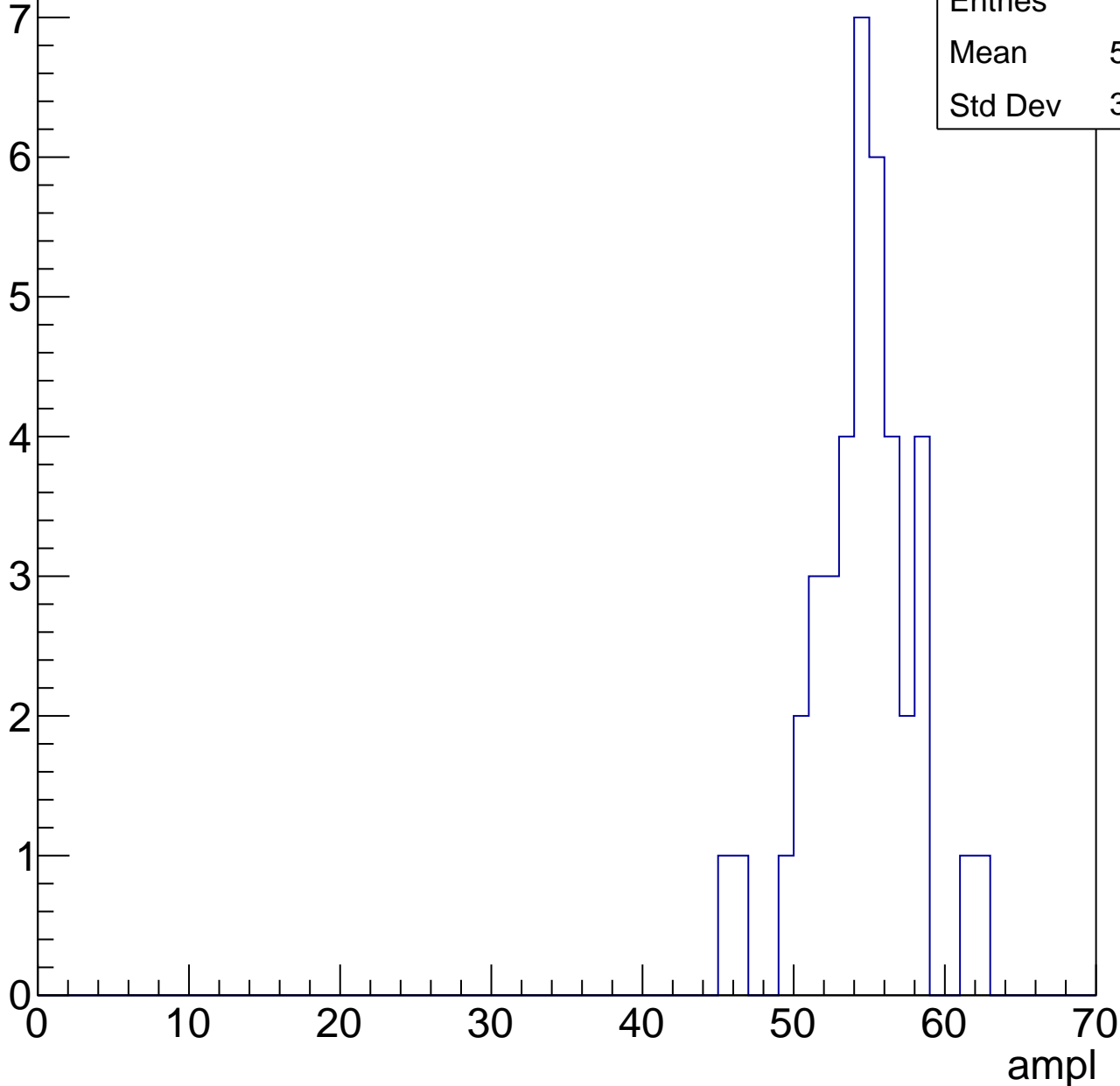
Entries	76
Mean	48.22
Std Dev	3.761

B1L102S, U2-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	54.05
Std Dev	3.398

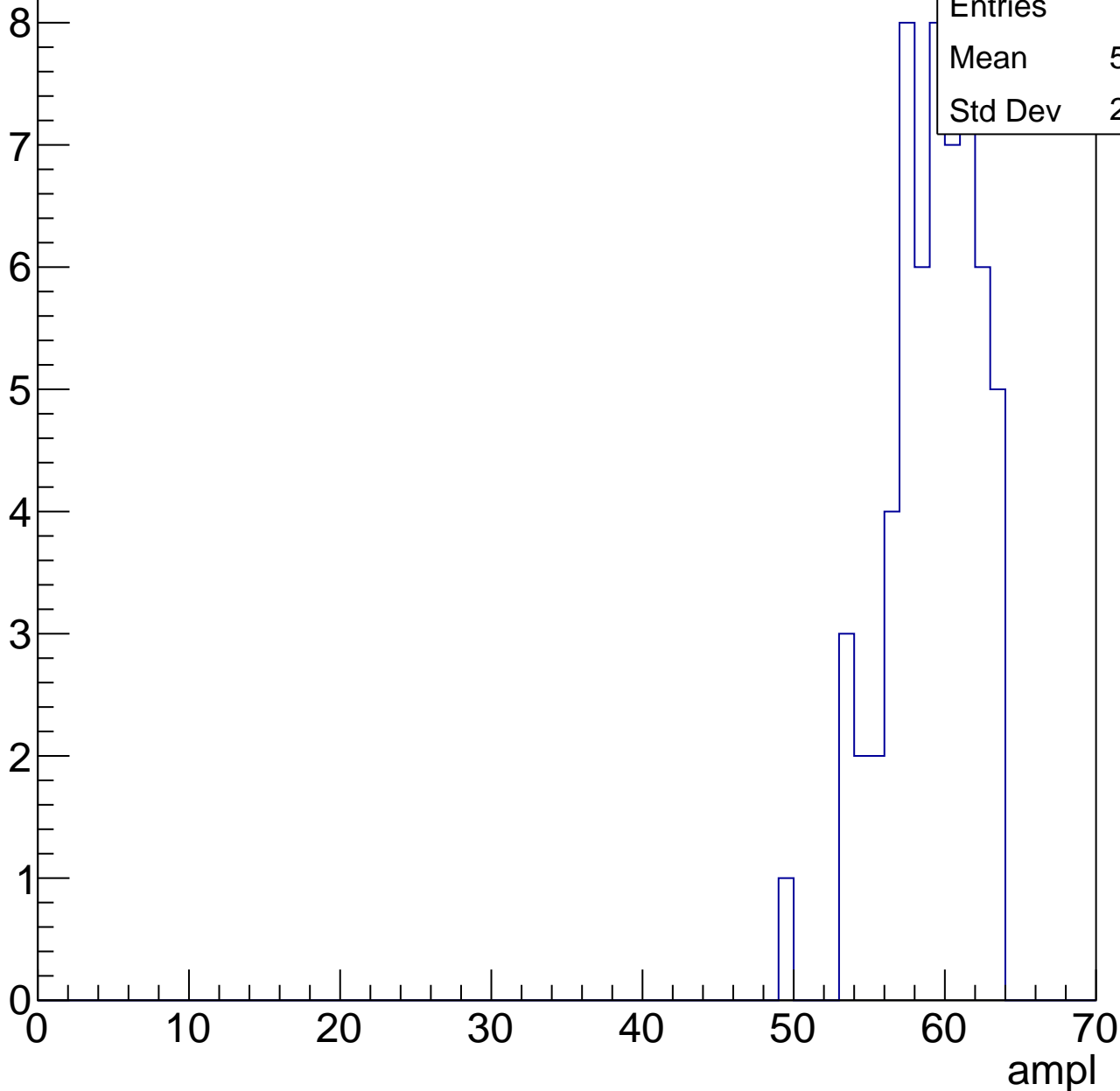


B1L102S, U2-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.68
Std Dev	2.975

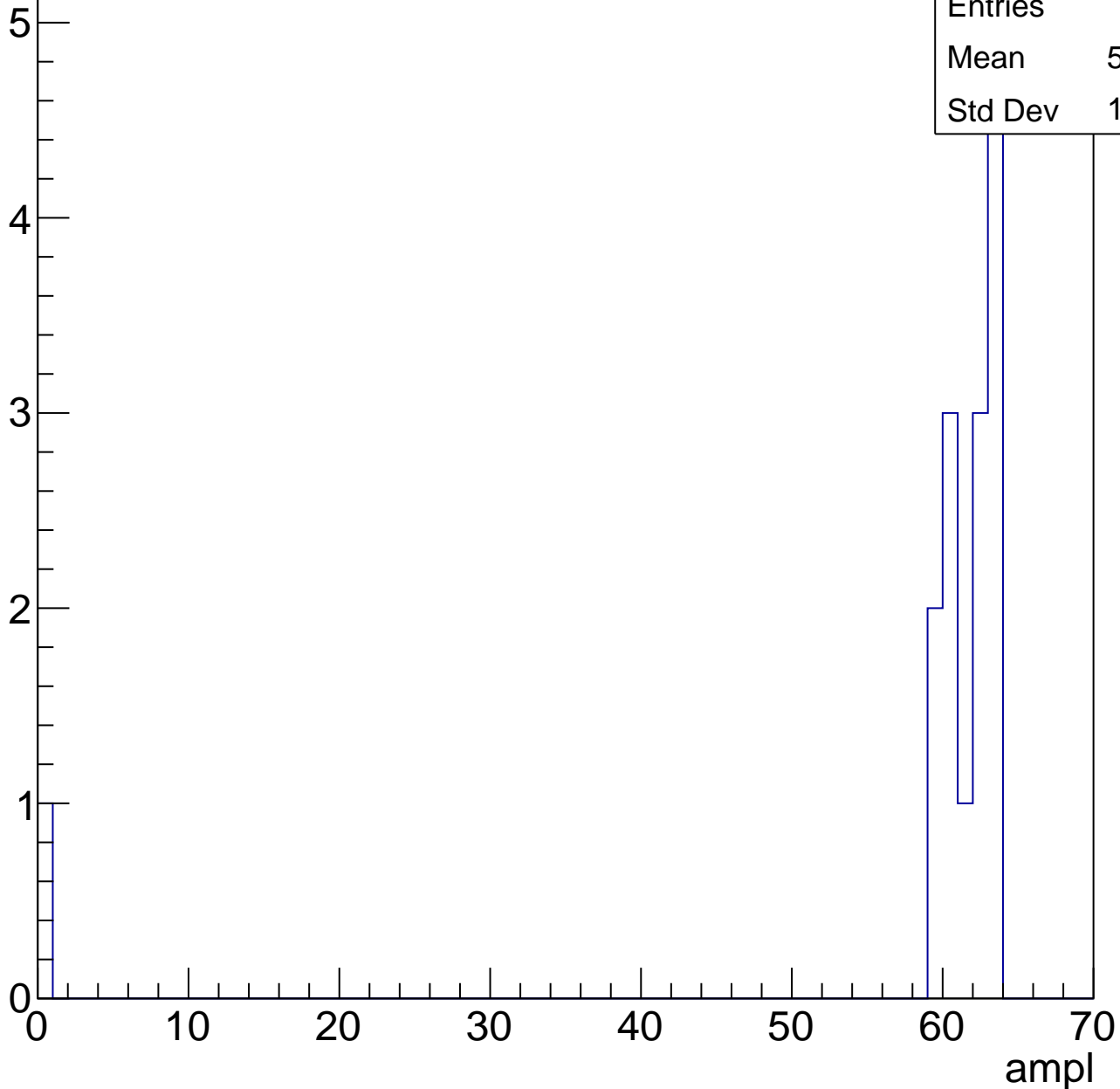


B1L102S, U2-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.33
Std Dev	15.39



B1L102S, U2-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch81, adc0

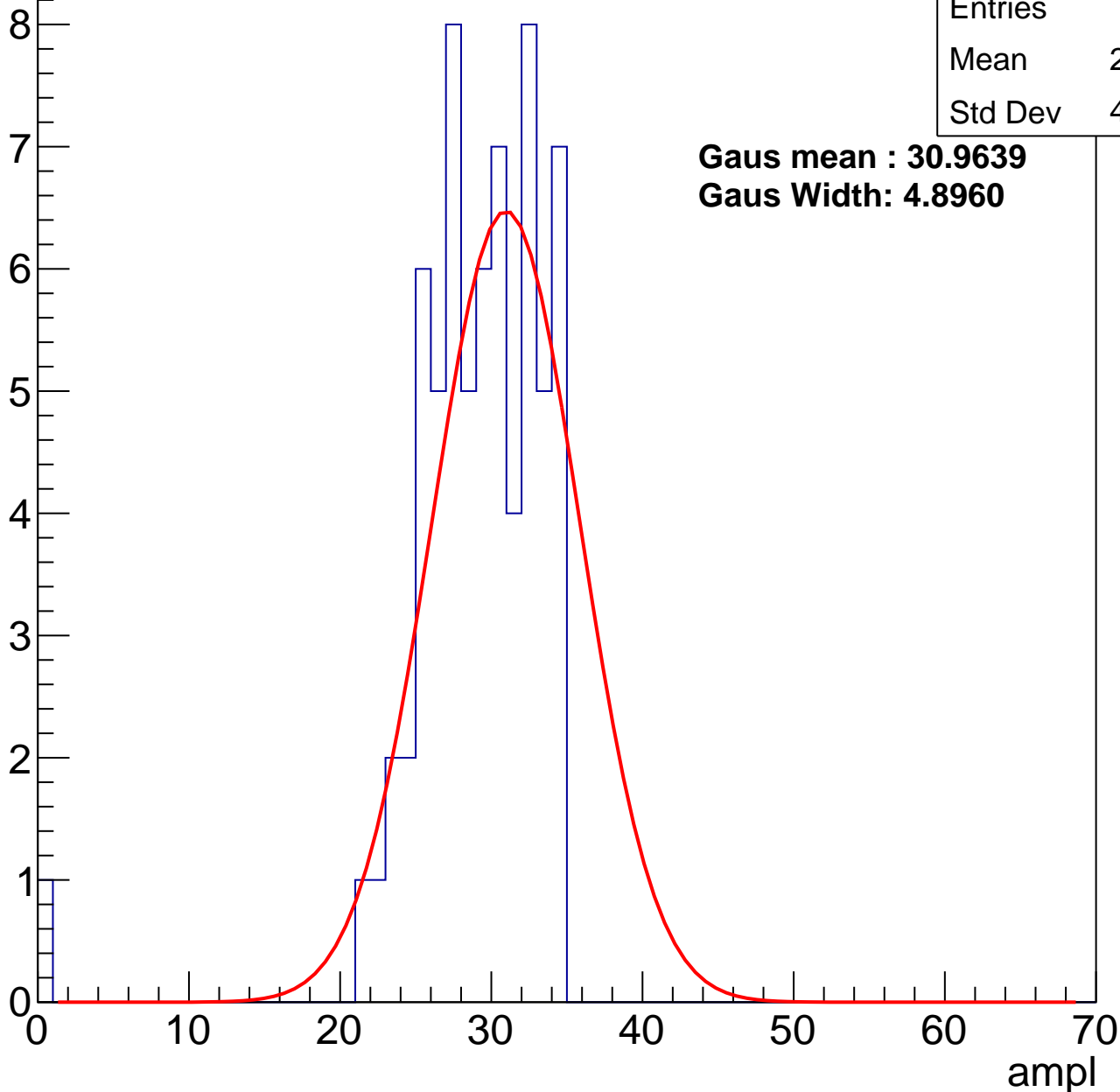
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.53
Std Dev	4.834

Gaus mean : 30.9639

Gaus Width: 4.8960



B1L102S, U2-ch81, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	35.59
Std Dev	3.993

Gaus mean : 37.2560

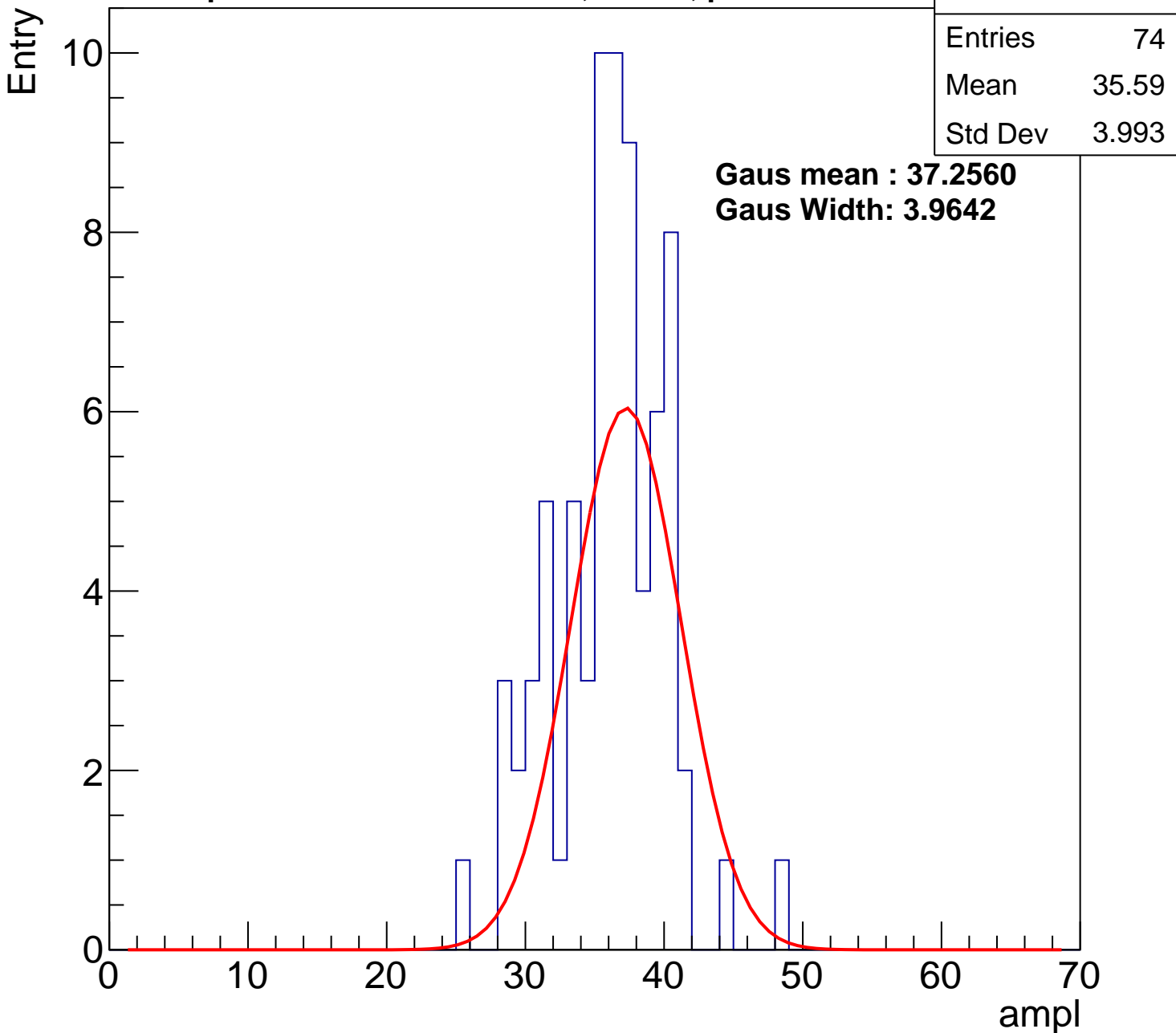
Gaus Width: 3.9642

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch81, adc2

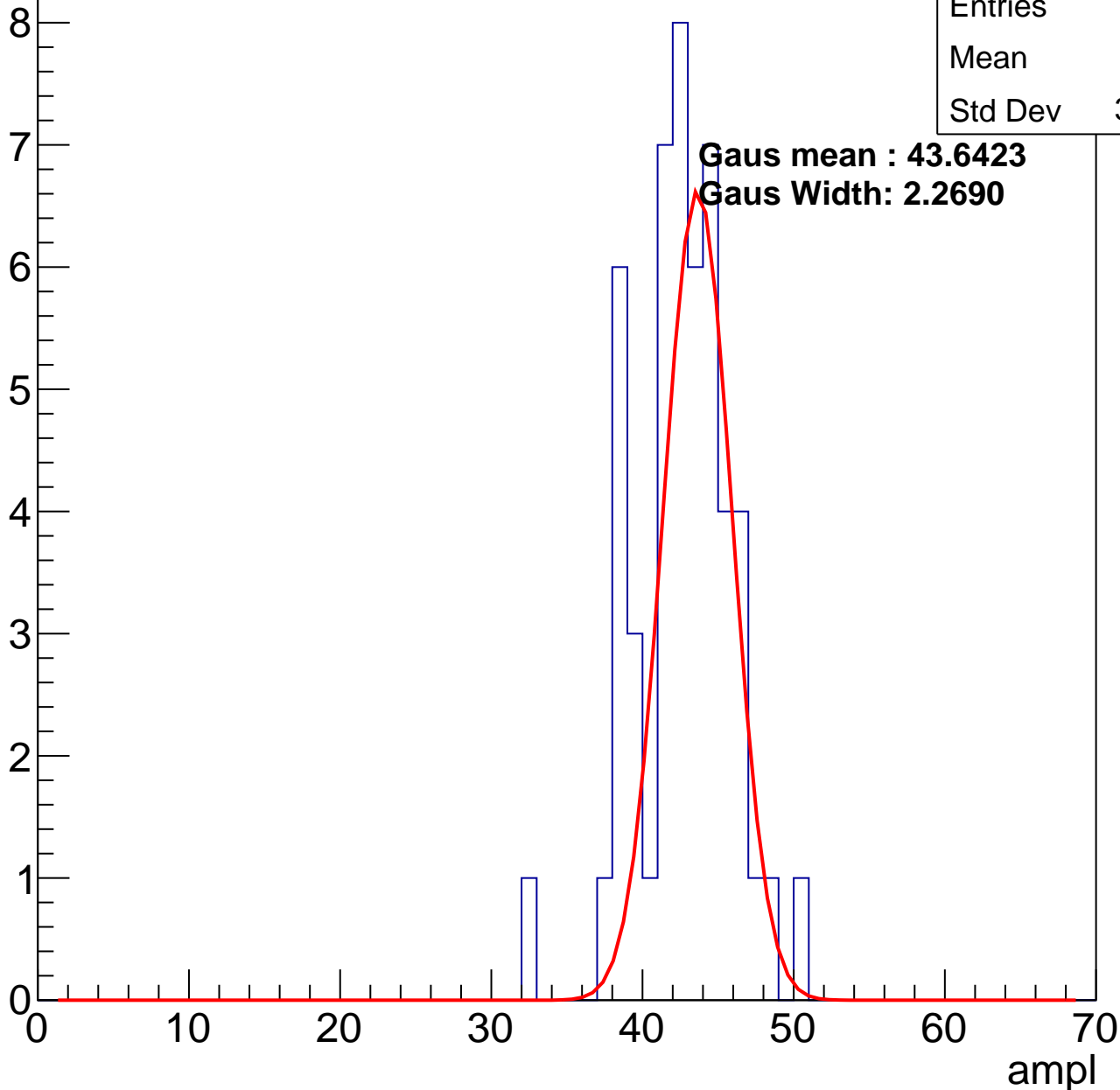
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.2
Std Dev	3.181

Gaus mean : 43.6423

Gaus Width: 2.2690

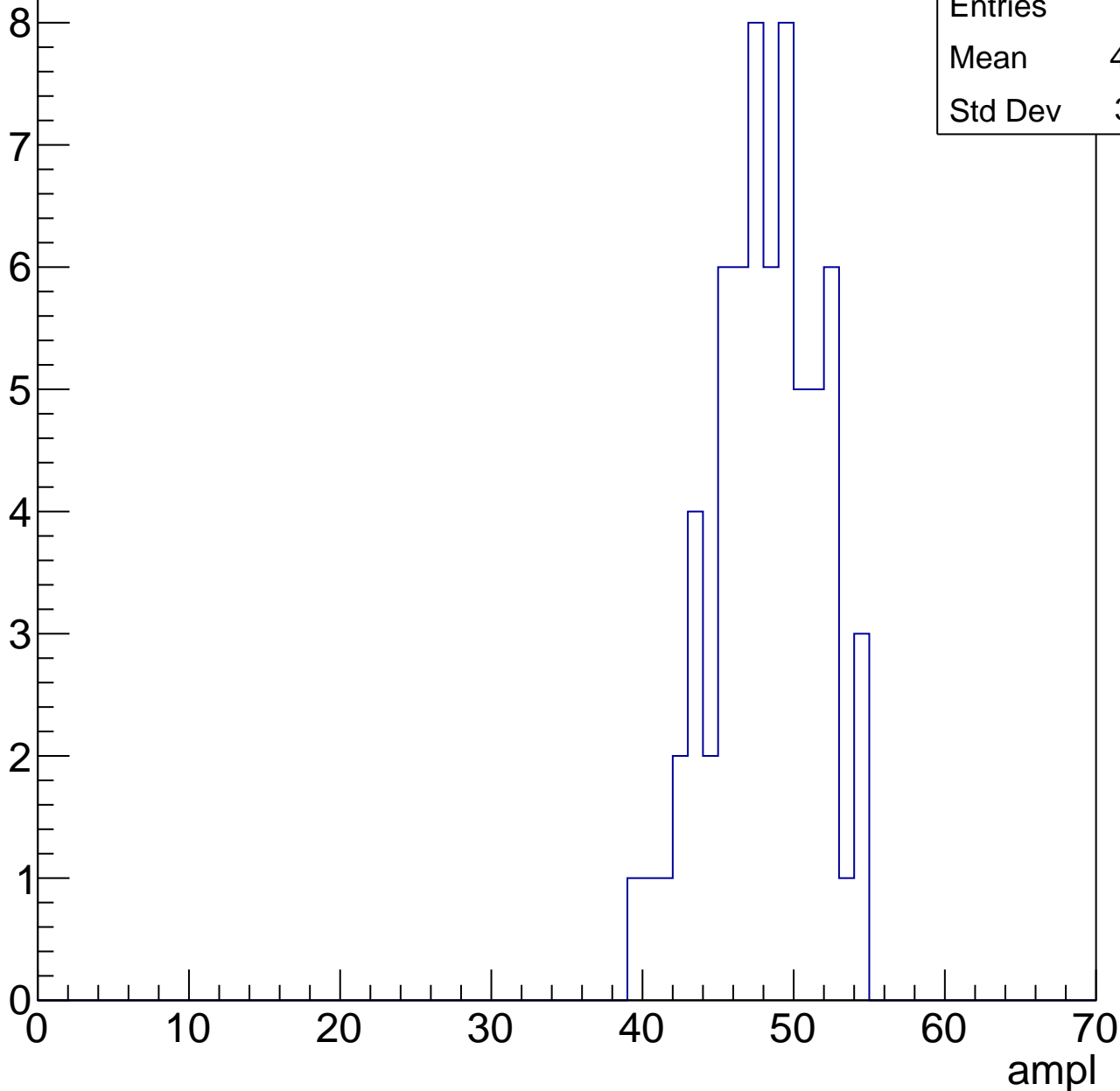


B1L102S, U2-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.66
Std Dev	3.461



B1L102S, U2-ch81, adc4

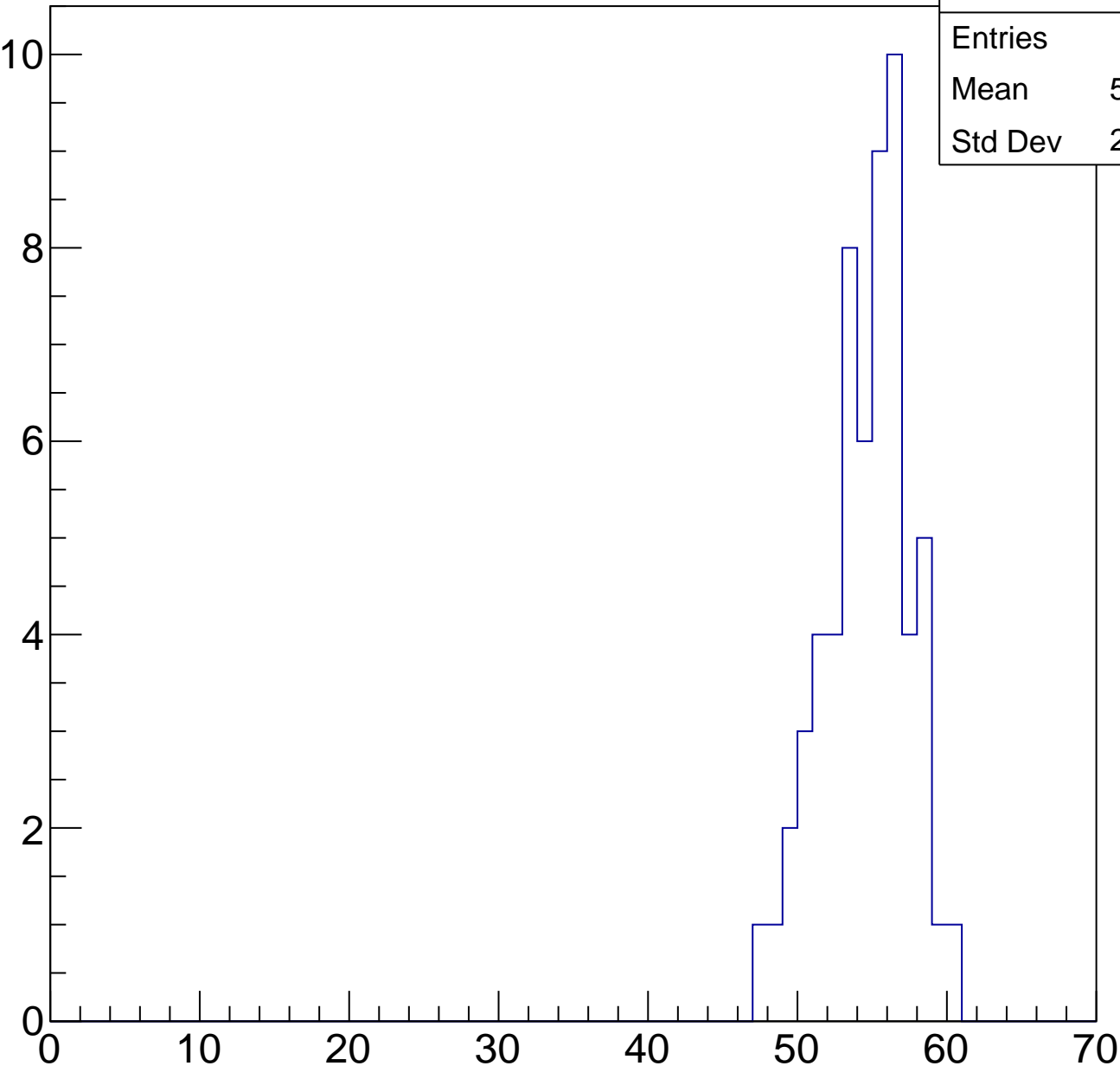
calib_packv5_042523_0143.root, FC#11, port A2

Entry

10
8
6
4
2
0

Entries	59
Mean	54.15
Std Dev	2.815

ampl

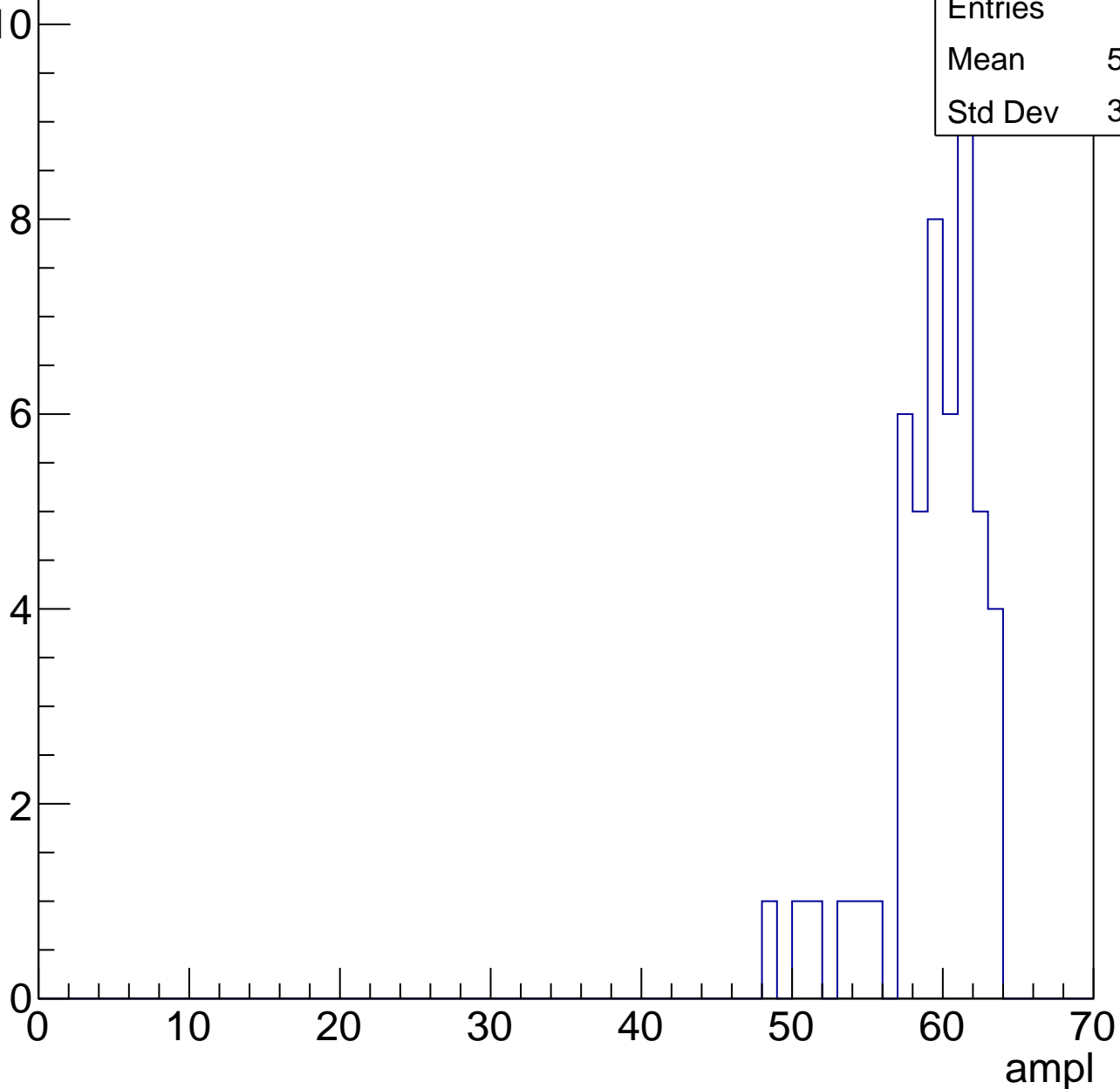


B1L102S, U2-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.94
Std Dev	3.246

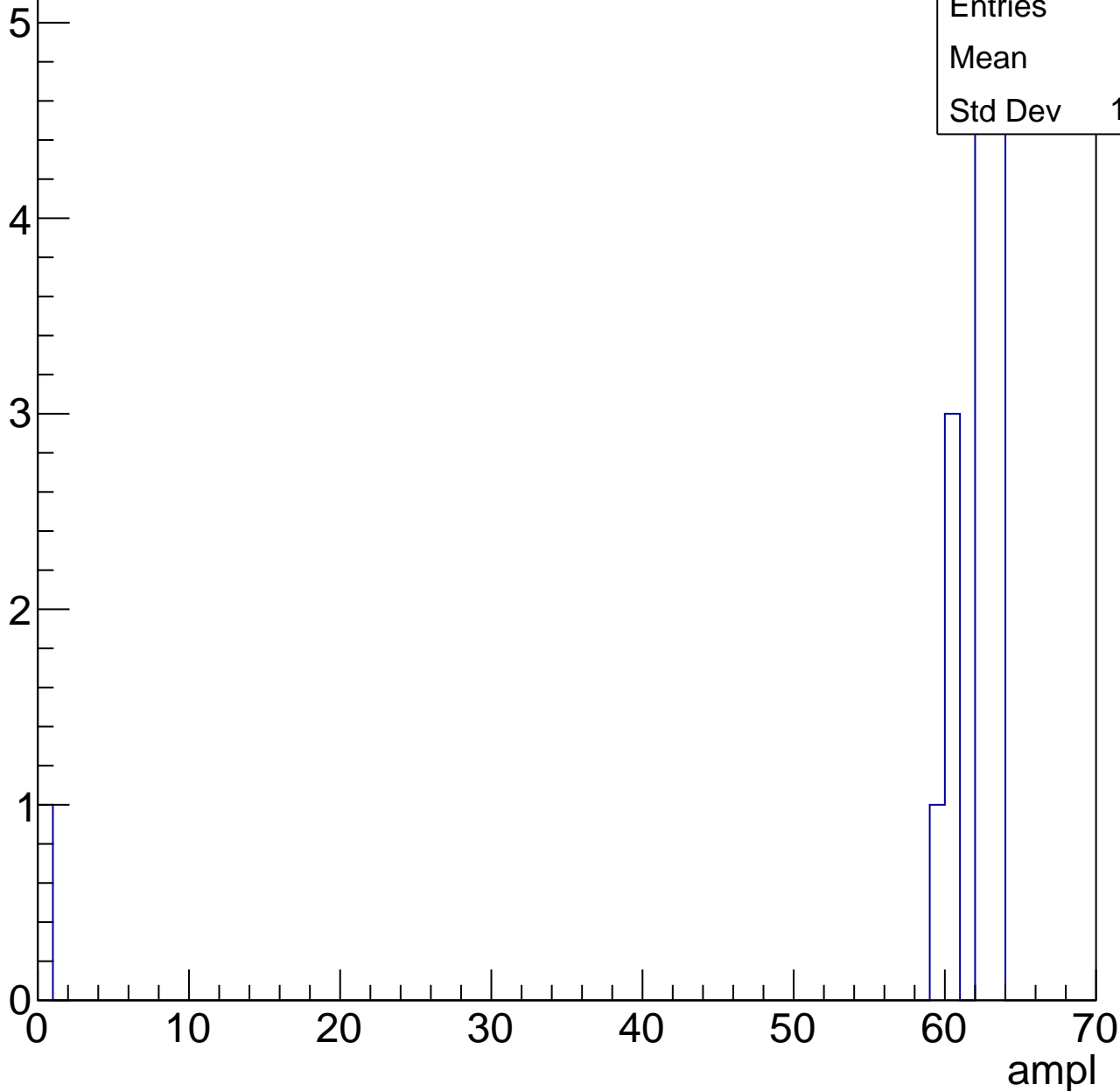


B1L102S, U2-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.6
Std Dev	15.45



B1L102S, U2-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch82, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	26.91
Std Dev	4.067

Gaus mean : 27.8804

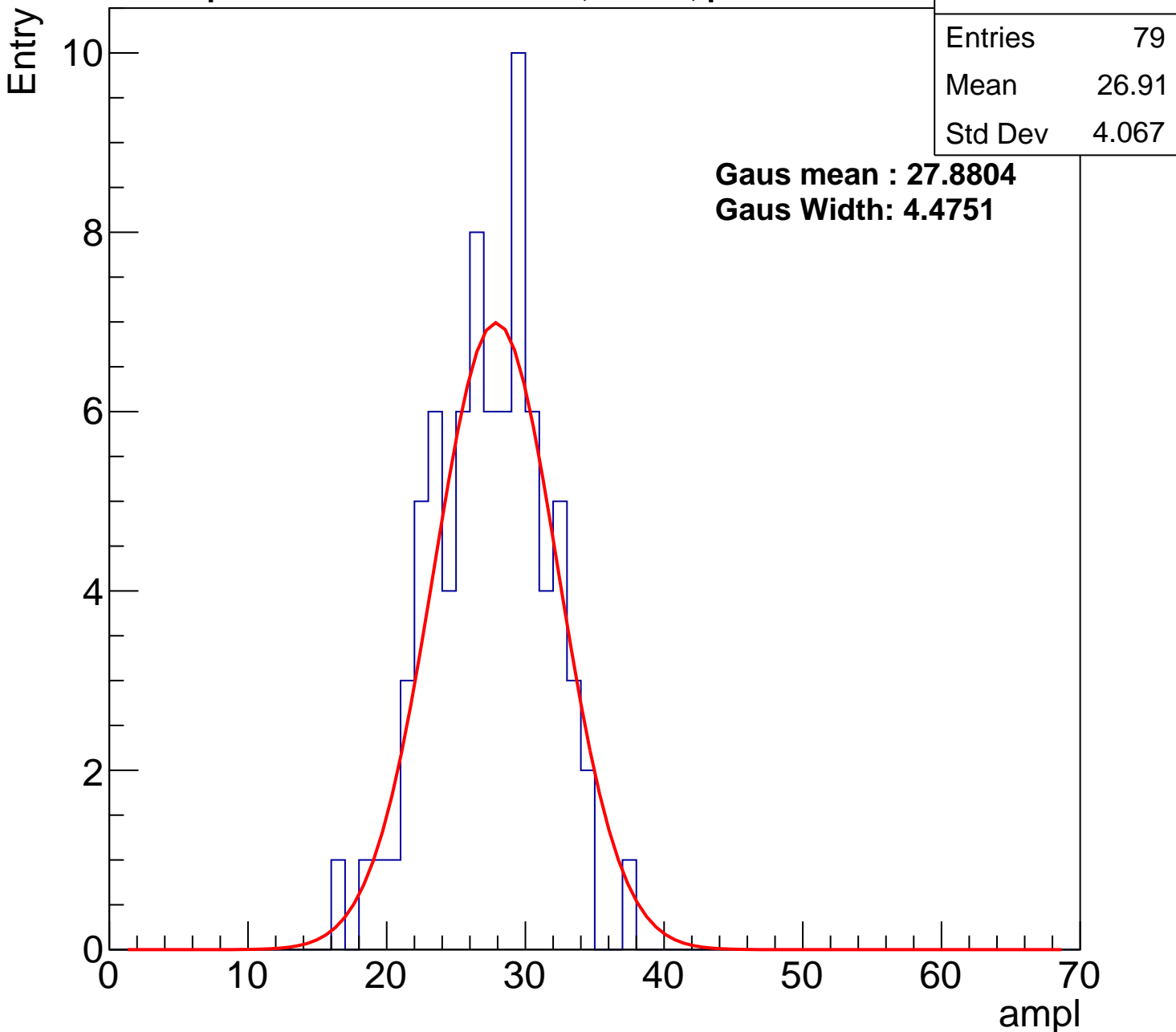
Gaus Width: 4.4751

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch82, adc1

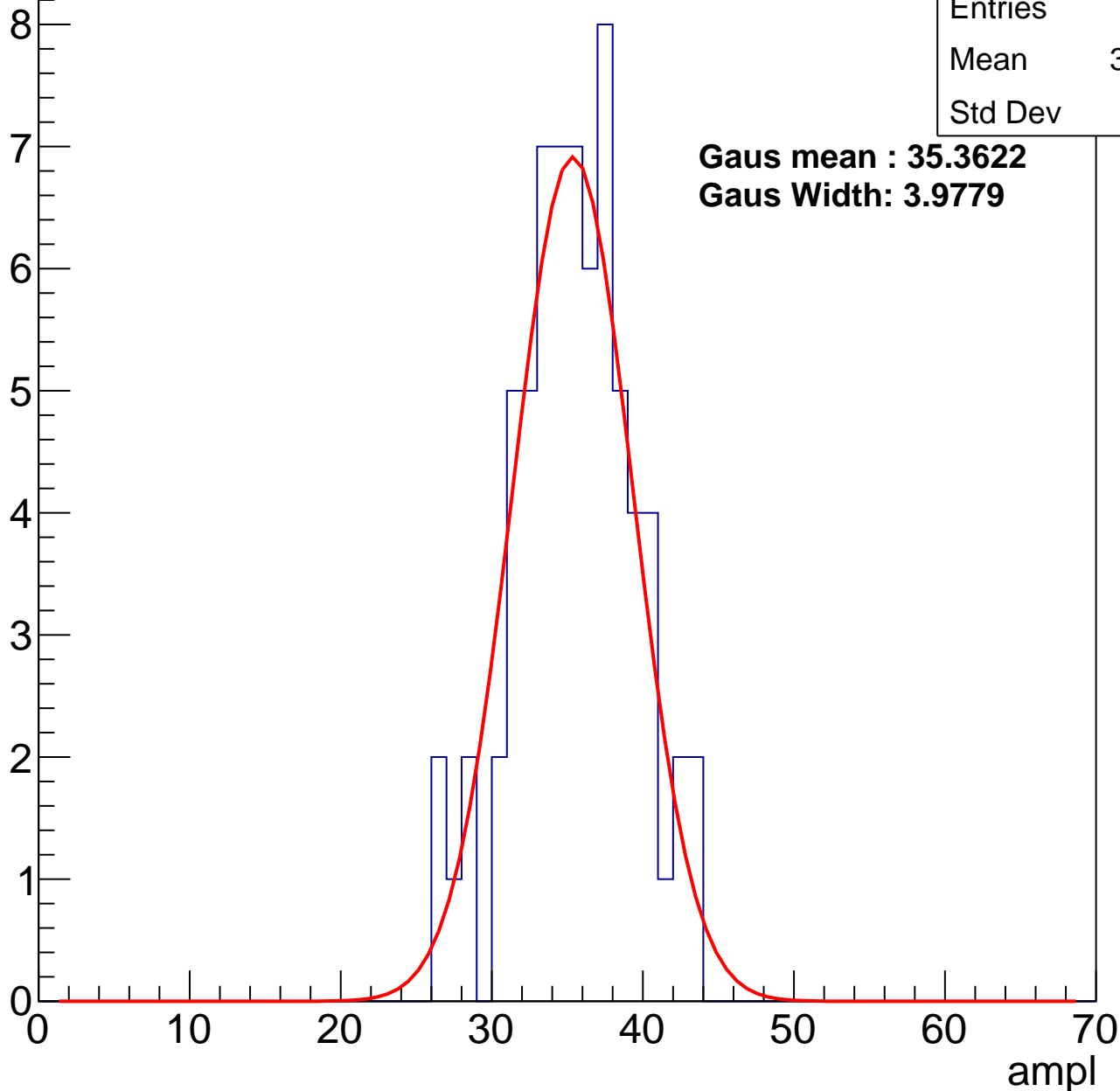
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.04
Std Dev	3.86

Gaus mean : 35.3622

Gaus Width: 3.9779



B1L102S, U2-ch82, adc2

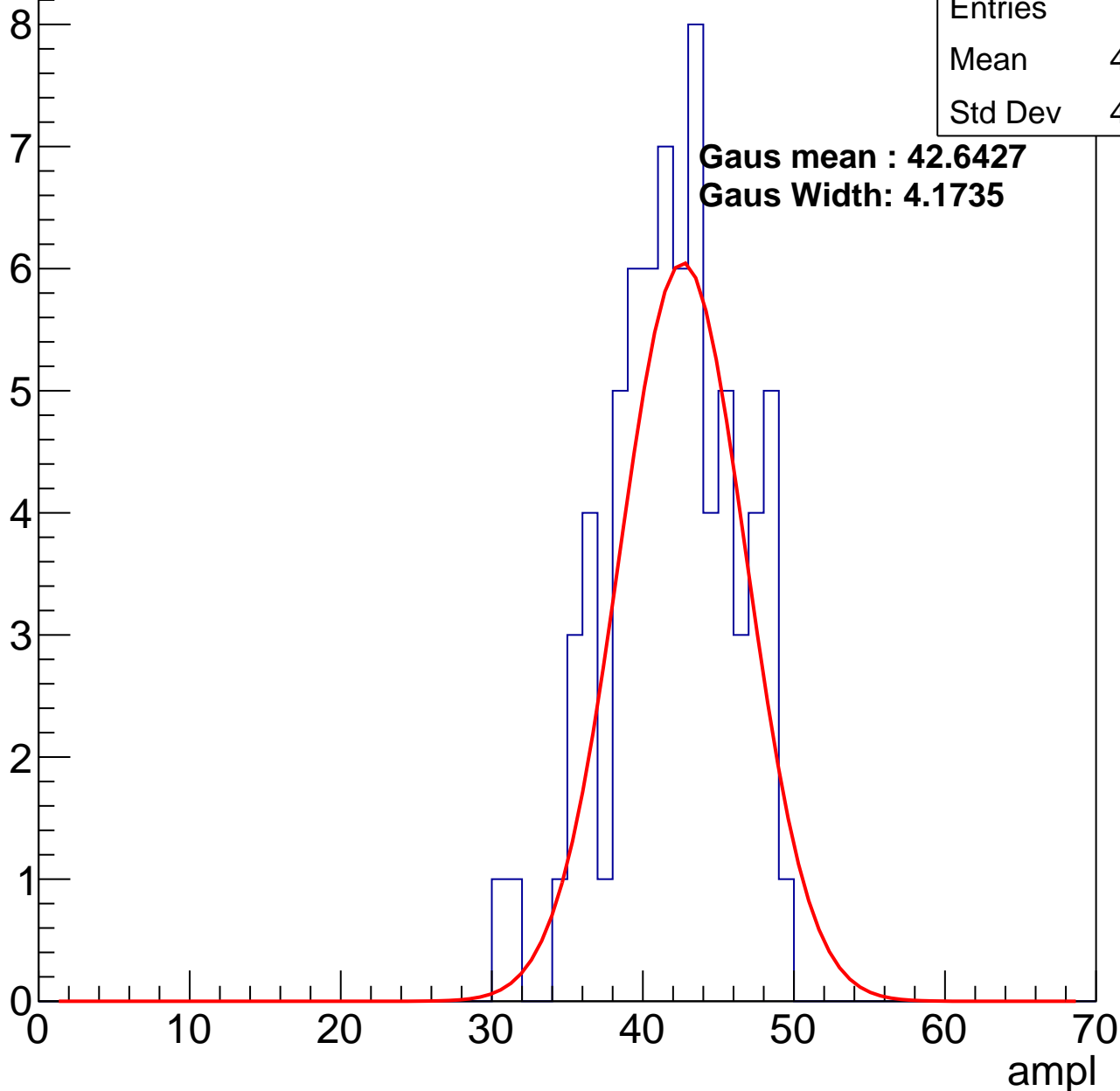
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.46
Std Dev	4.172

Gaus mean : 42.6427

Gaus Width: 4.1735

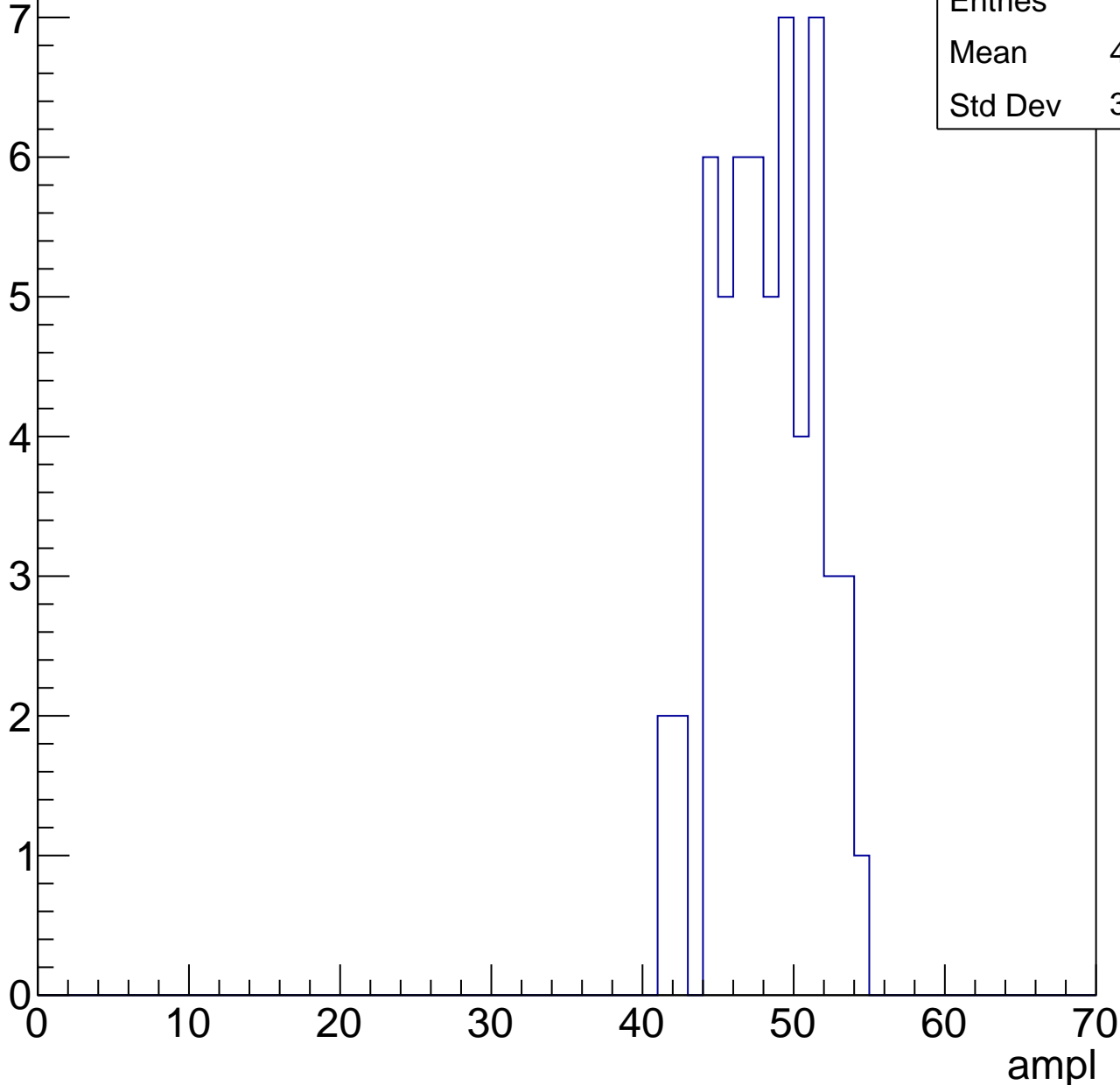


B1L102S, U2-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.75
Std Dev	3.197

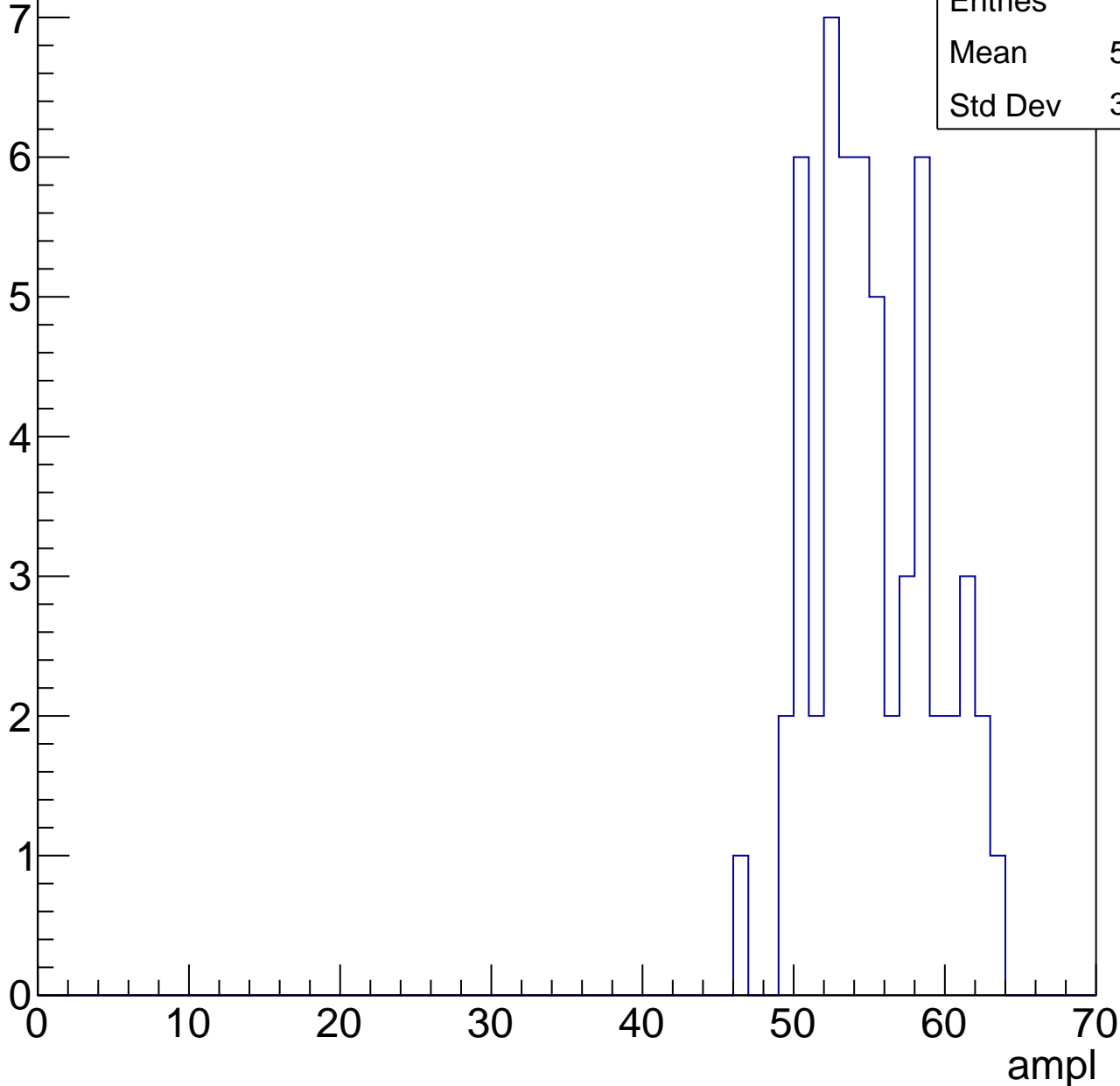


B1L102S, U2-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

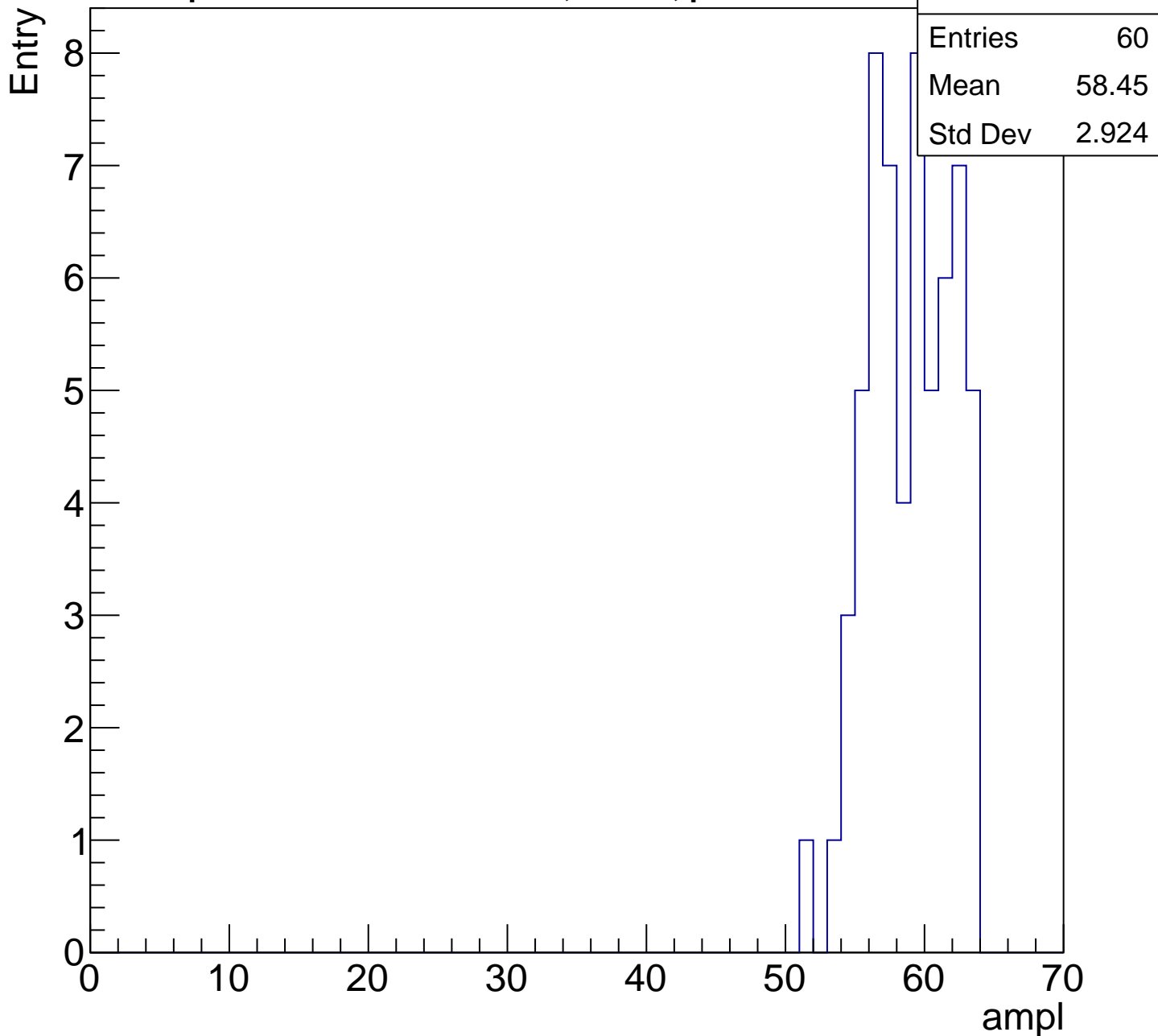
Entry

Entries	56
Mean	54.75
Std Dev	3.883



B1L102S, U2-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch82, adc6

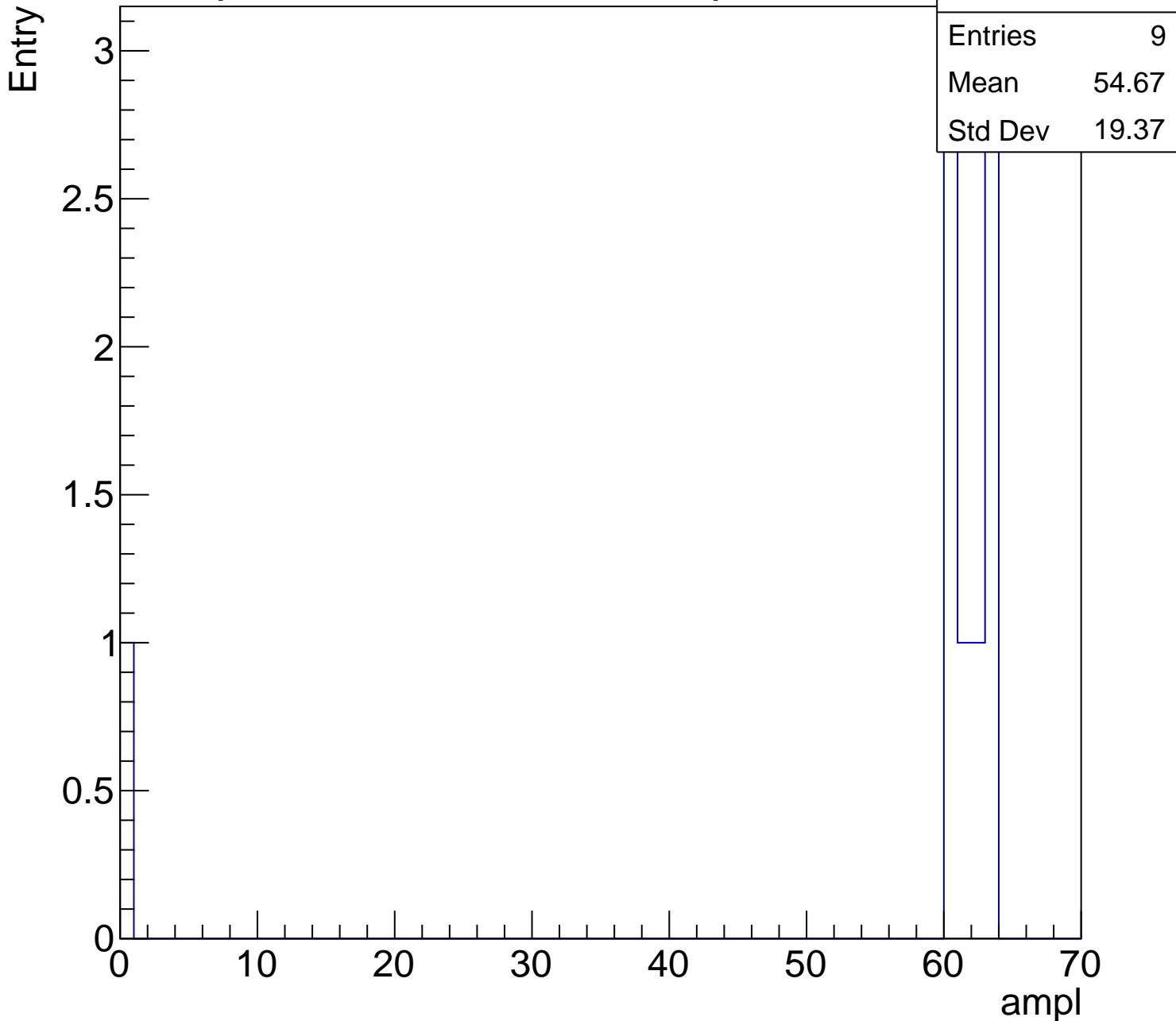
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.67
Std Dev	19.37

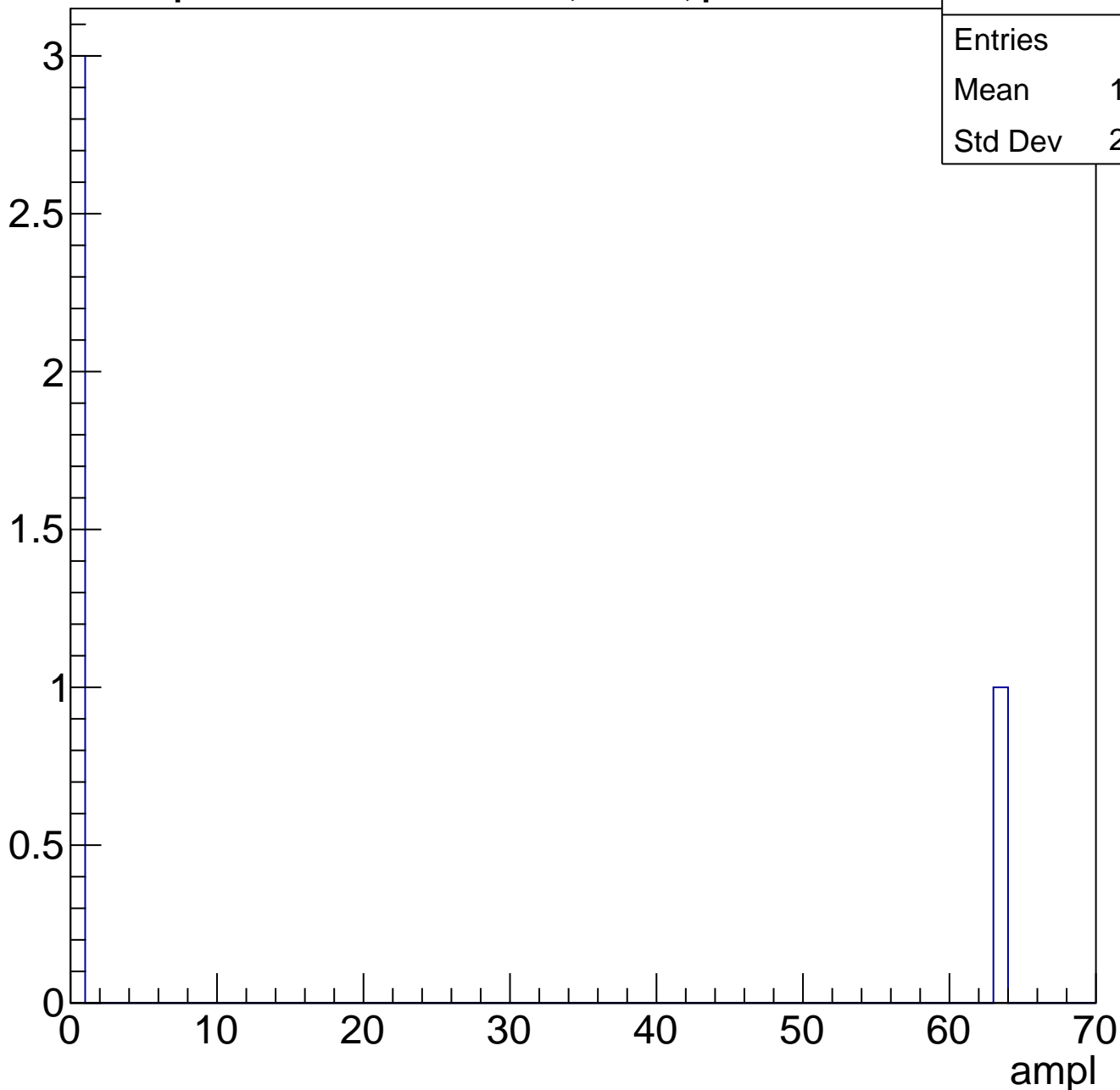
ampl



B1L102S, U2-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch83, adc0

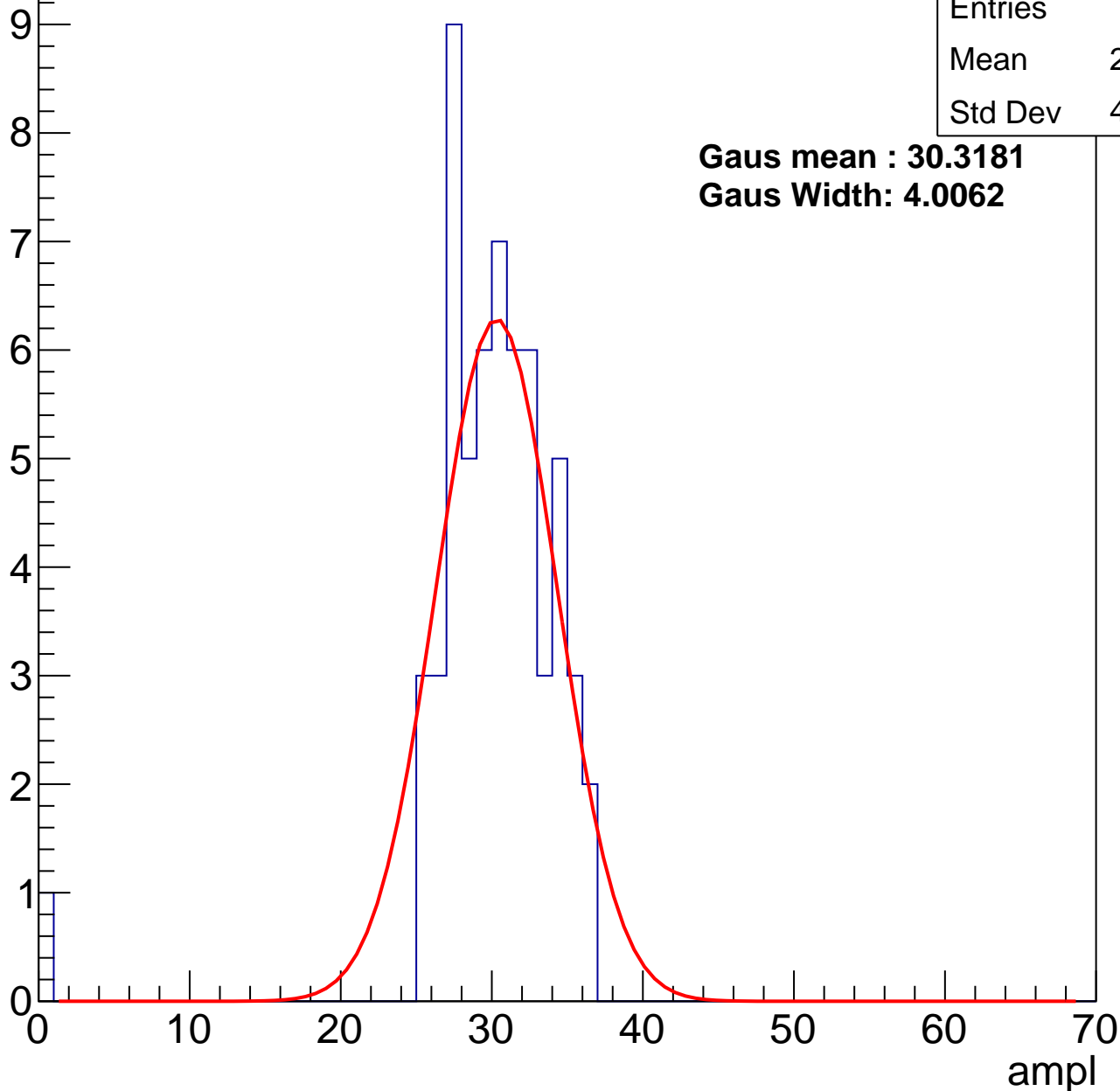
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.56
Std Dev	4.876

Gaus mean : 30.3181

Gaus Width: 4.0062



B1L102S, U2-ch83, adc1

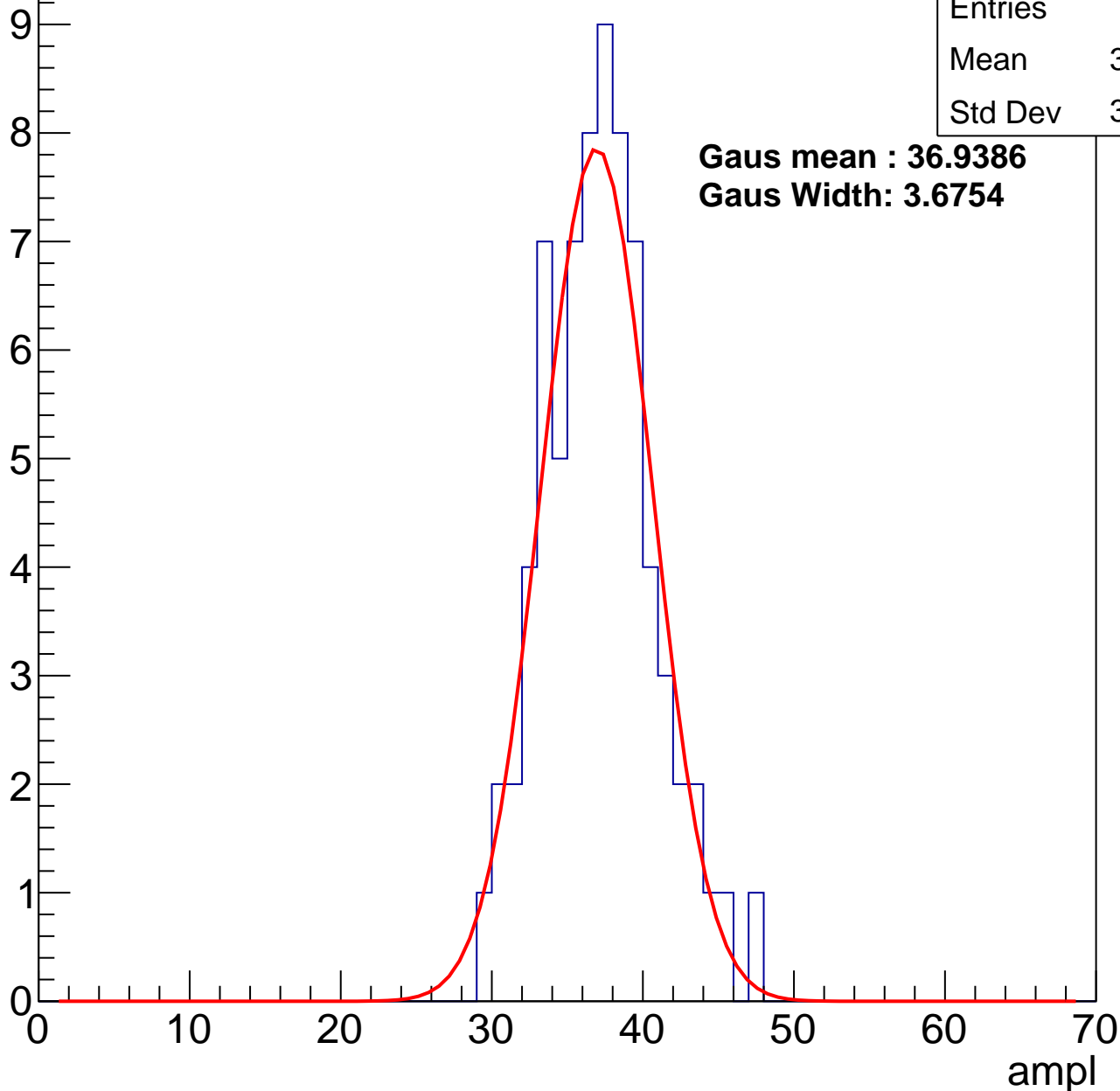
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.65
Std Dev	3.626

Gaus mean : 36.9386

Gaus Width: 3.6754



B1L102S, U2-ch83, adc2

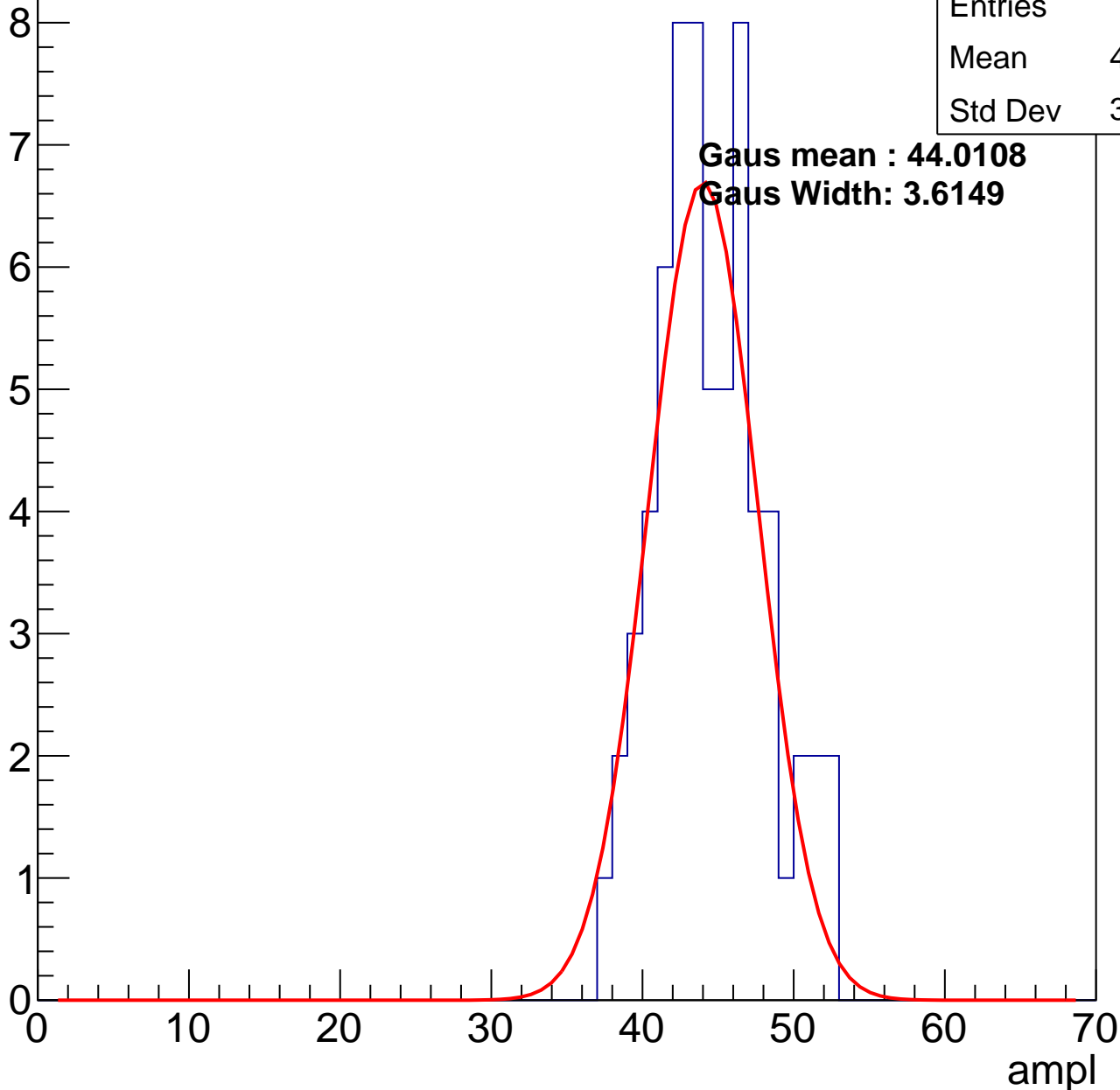
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	44.06
Std Dev	3.538

Gaus mean : 44.0108

Gaus Width: 3.6149

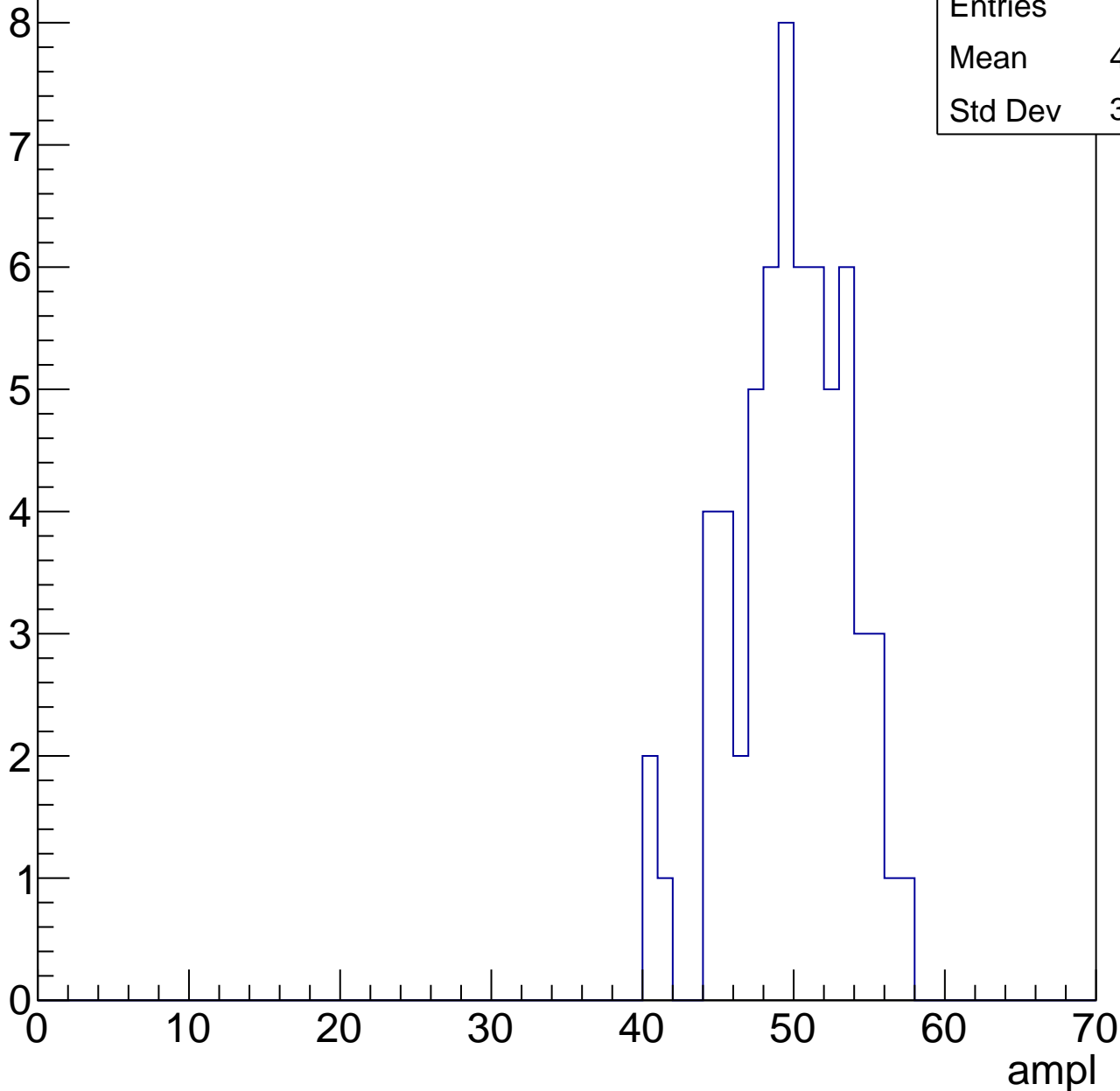


B1L102S, U2-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.33
Std Dev	3.767

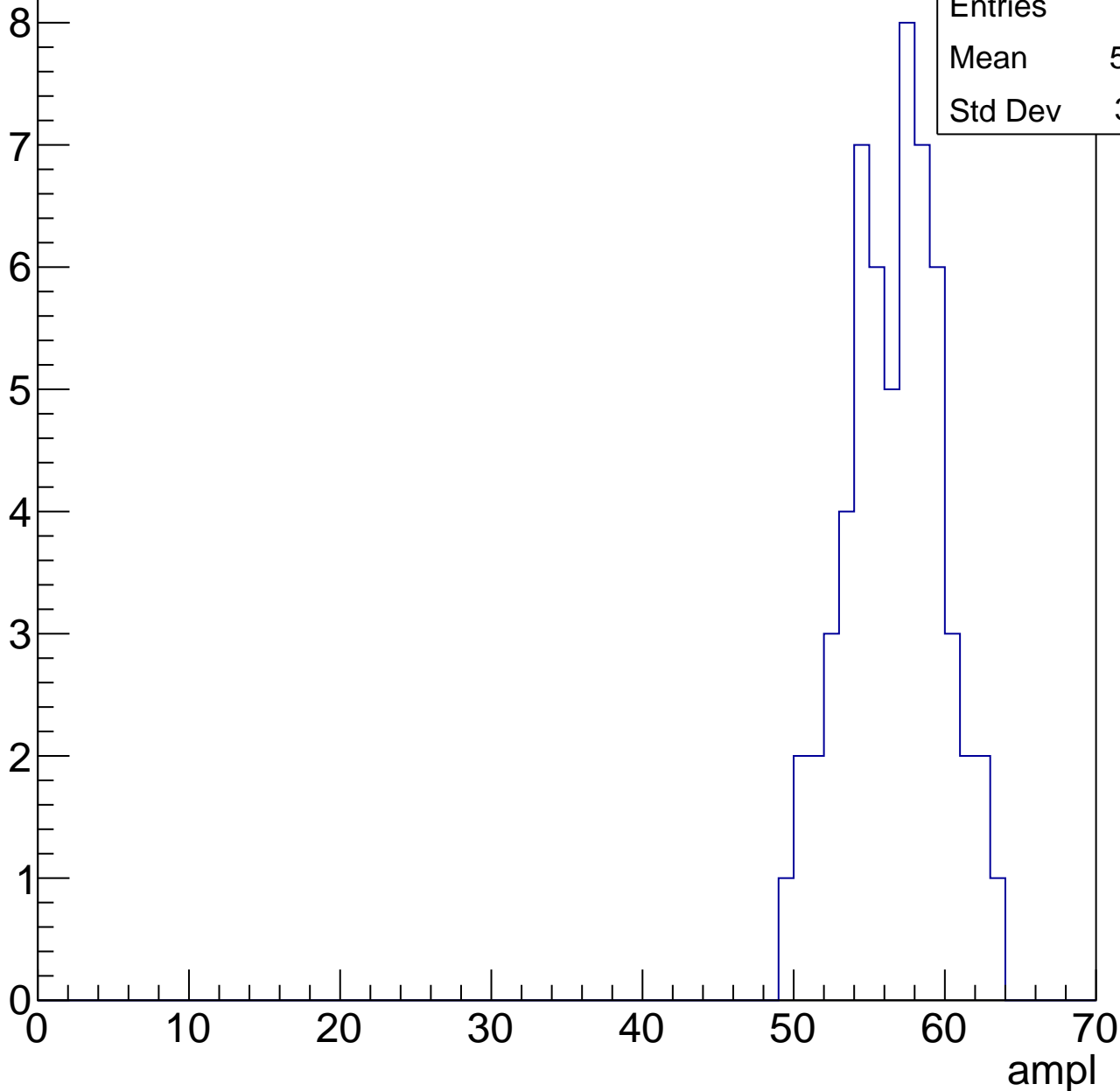


B1L102S, U2-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.14
Std Dev	3.181

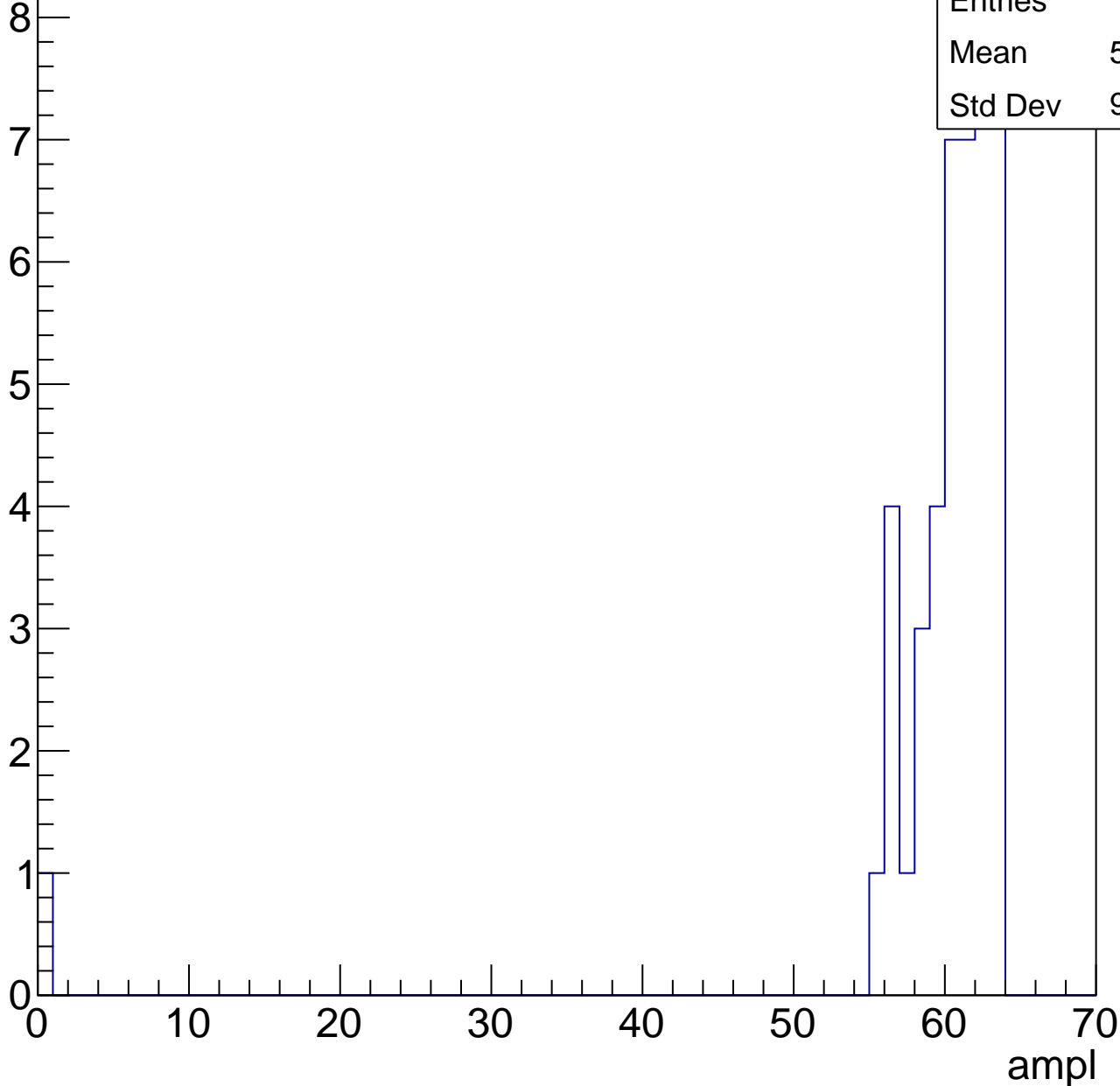


B1L102S, U2-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

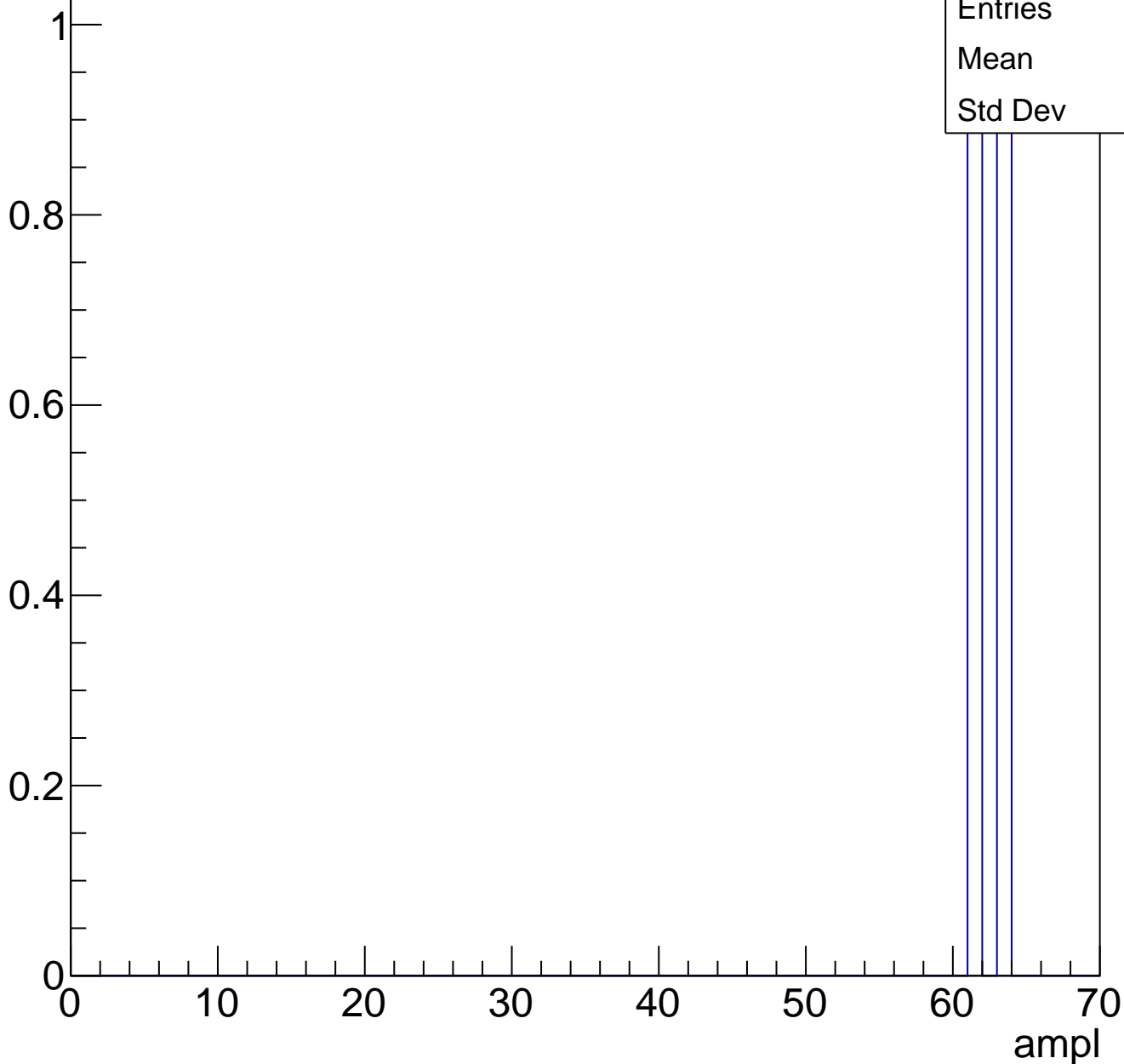
Entries	44
Mean	58.93
Std Dev	9.262



B1L102S, U2-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch84, adc0

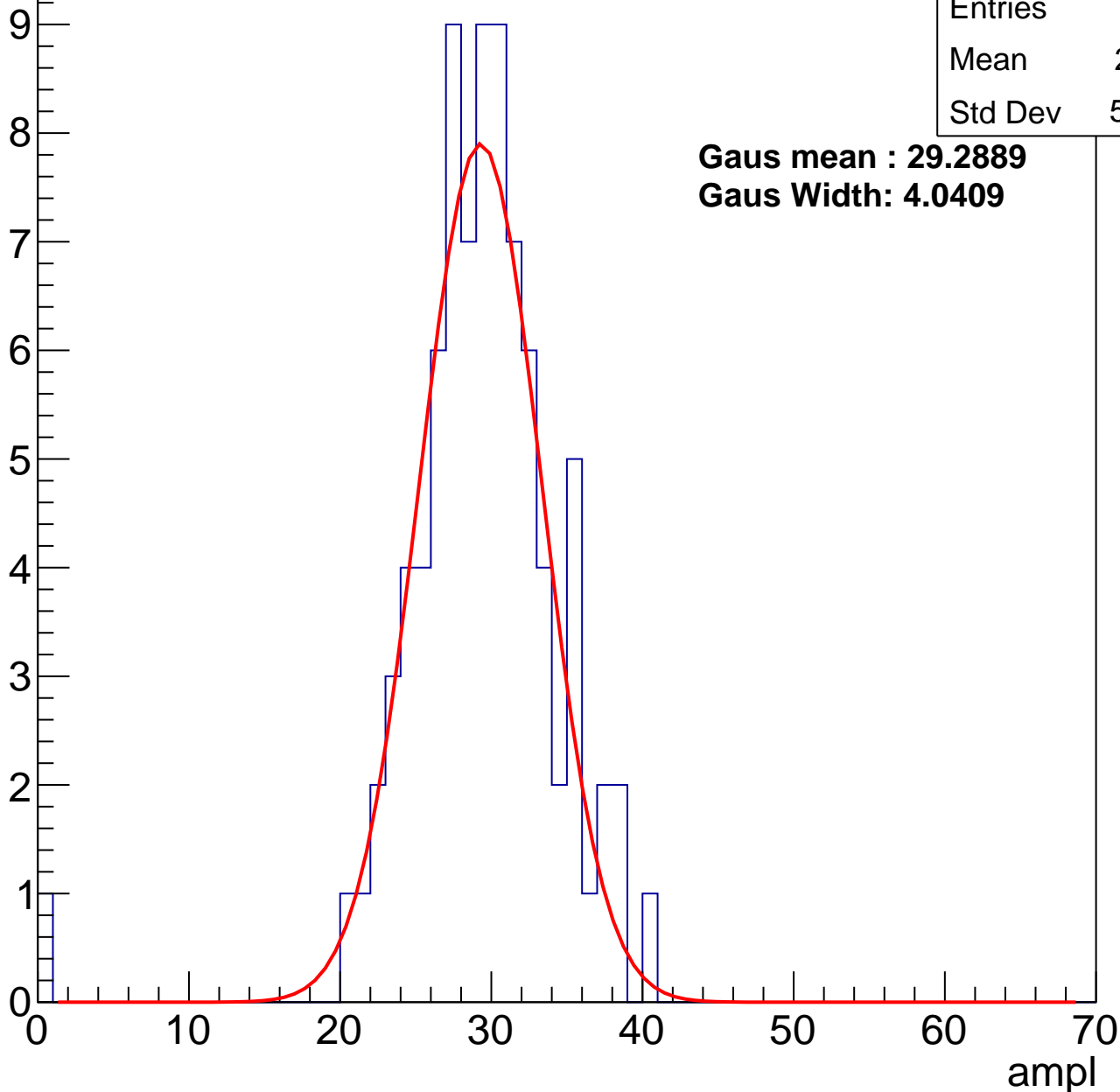
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	28.91
Std Dev	5.168

Gaus mean : 29.2889

Gaus Width: 4.0409



B1L102S, U2-ch84, adc1

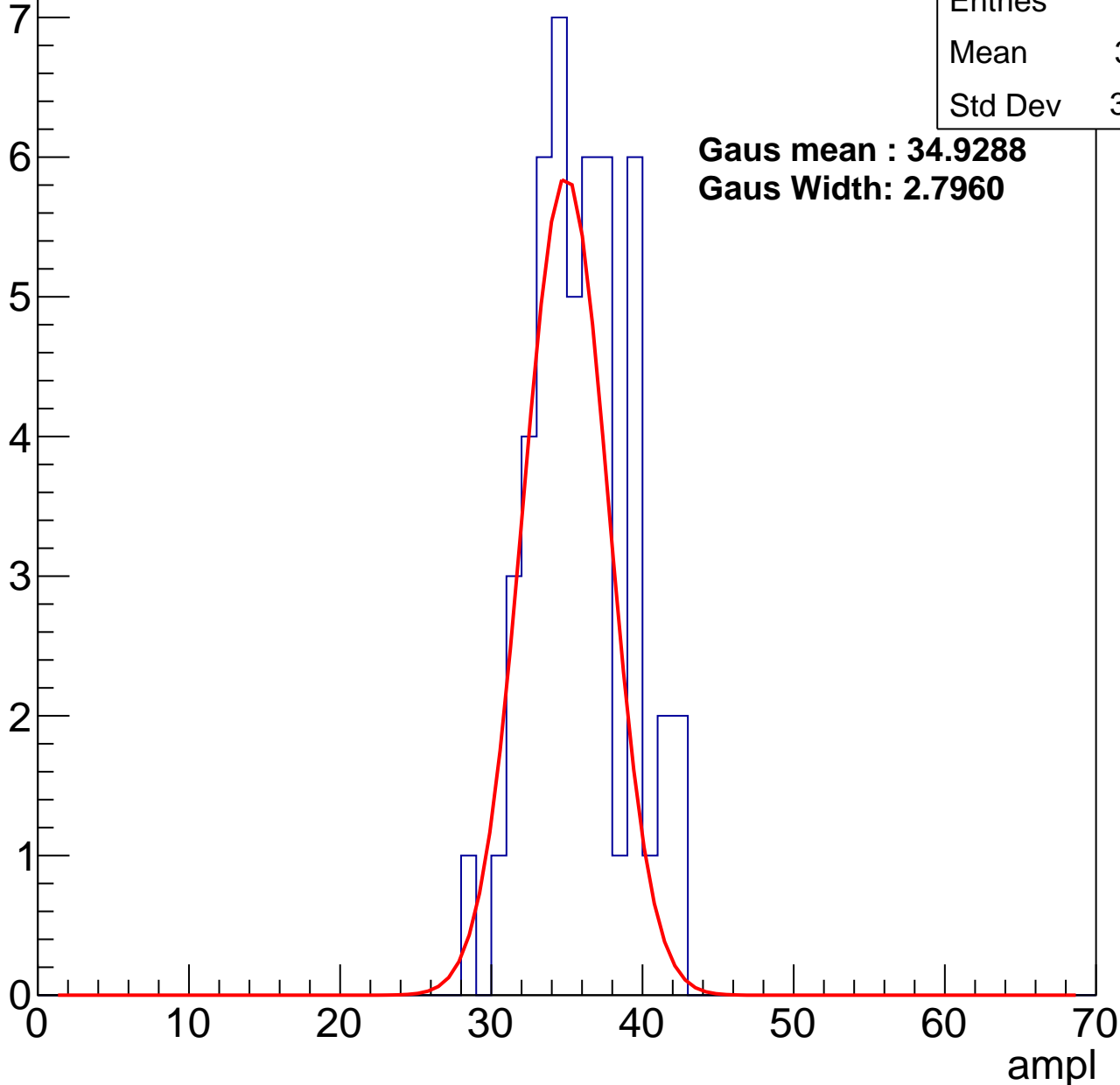
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	35.41
Std Dev	3.163

Gaus mean : 34.9288

Gaus Width: 2.7960



B1L102S, U2-ch84, adc2

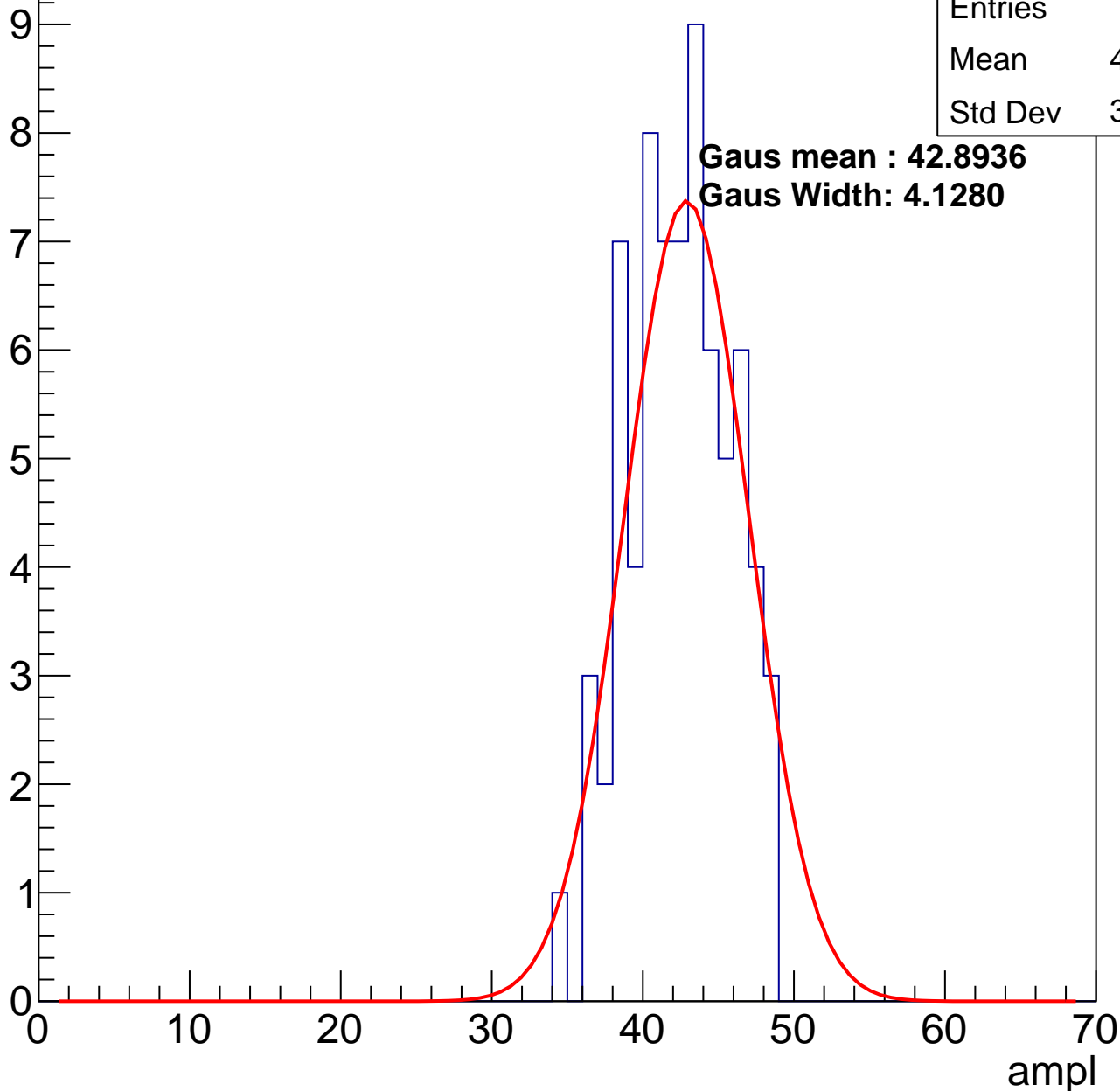
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.99
Std Dev	3.315

Gaus mean : 42.8936

Gaus Width: 4.1280

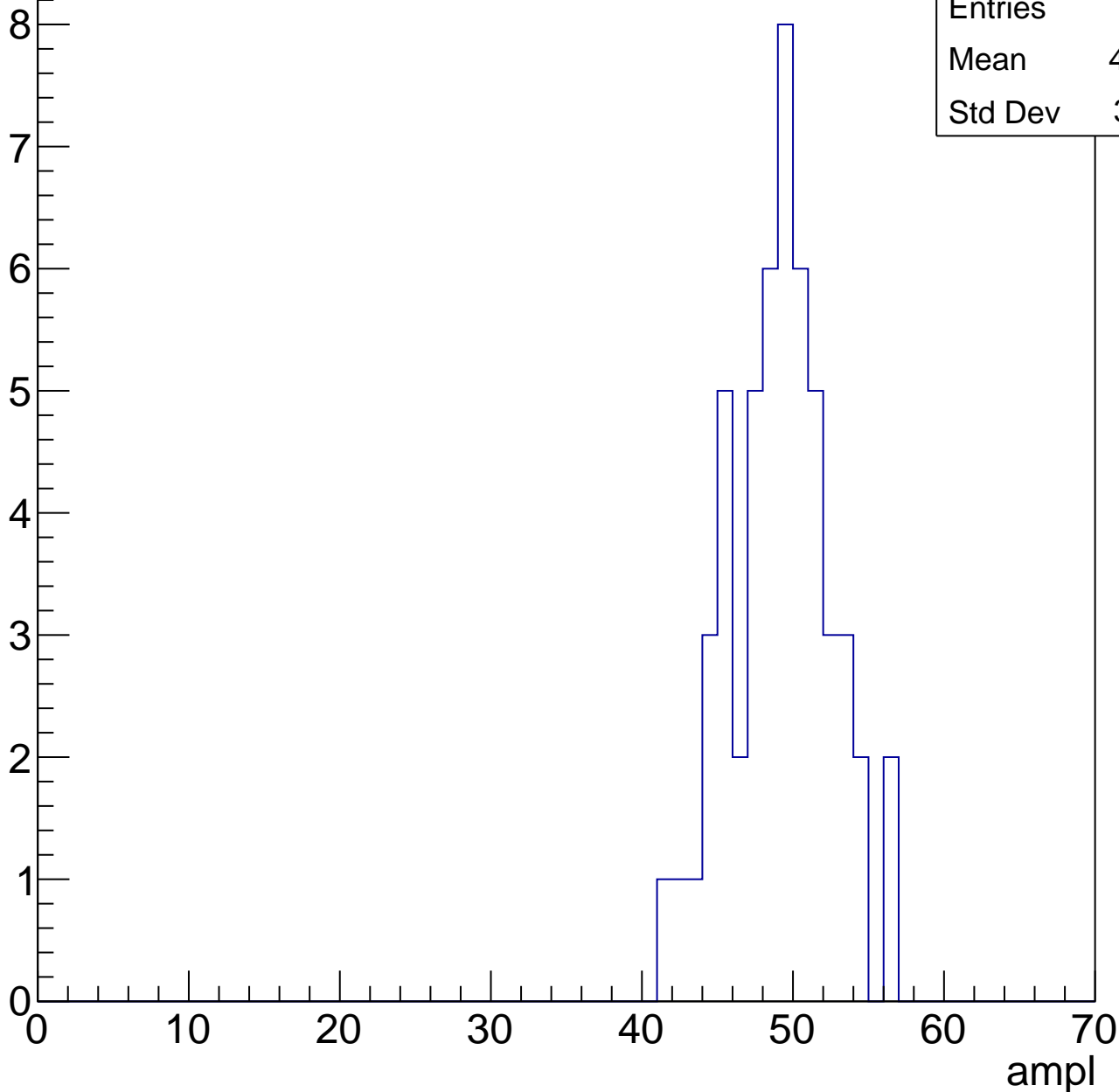


B1L102S, U2-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

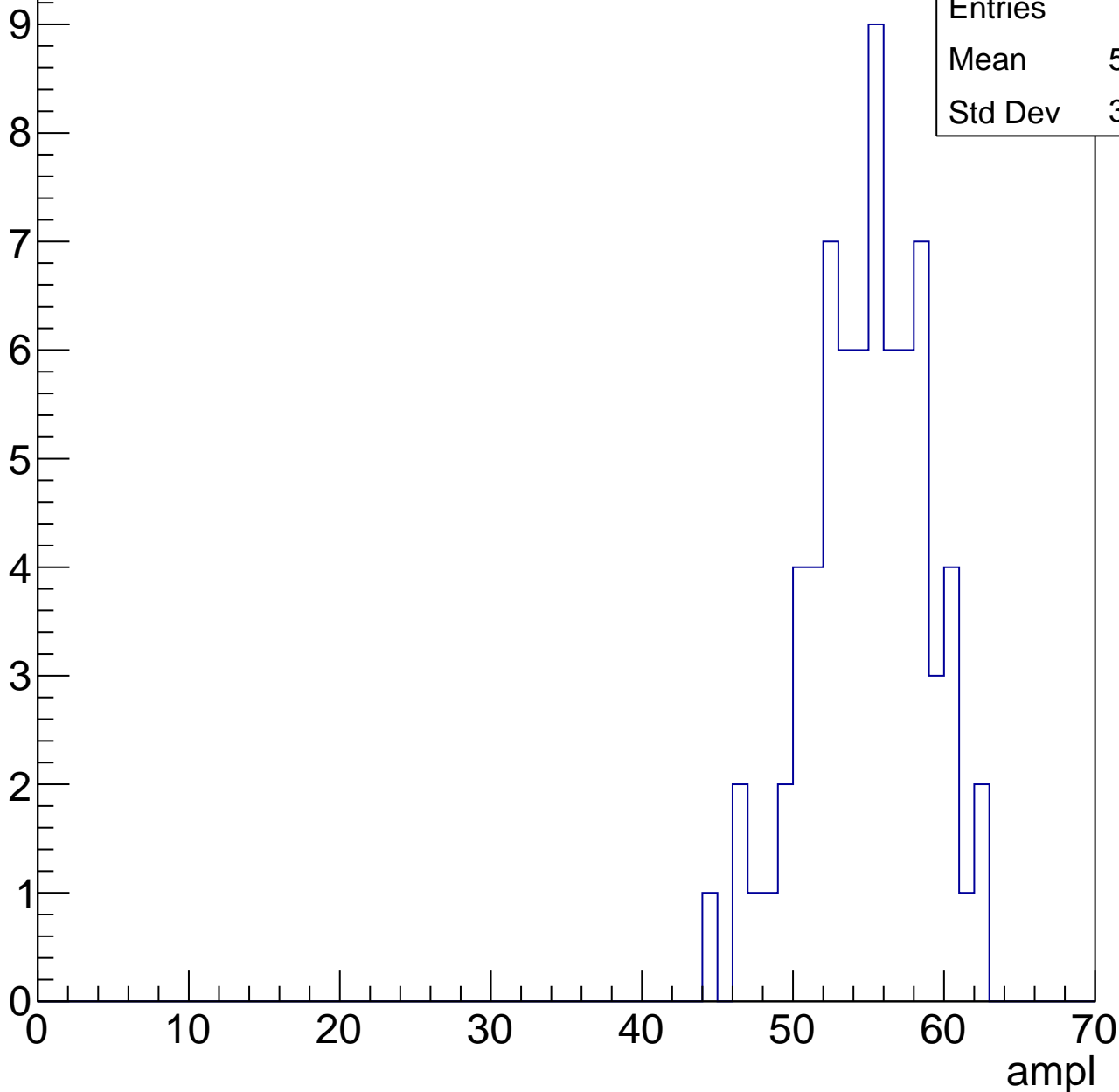
Entries	53
Mean	48.68
Std Dev	3.341



B1L102S, U2-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

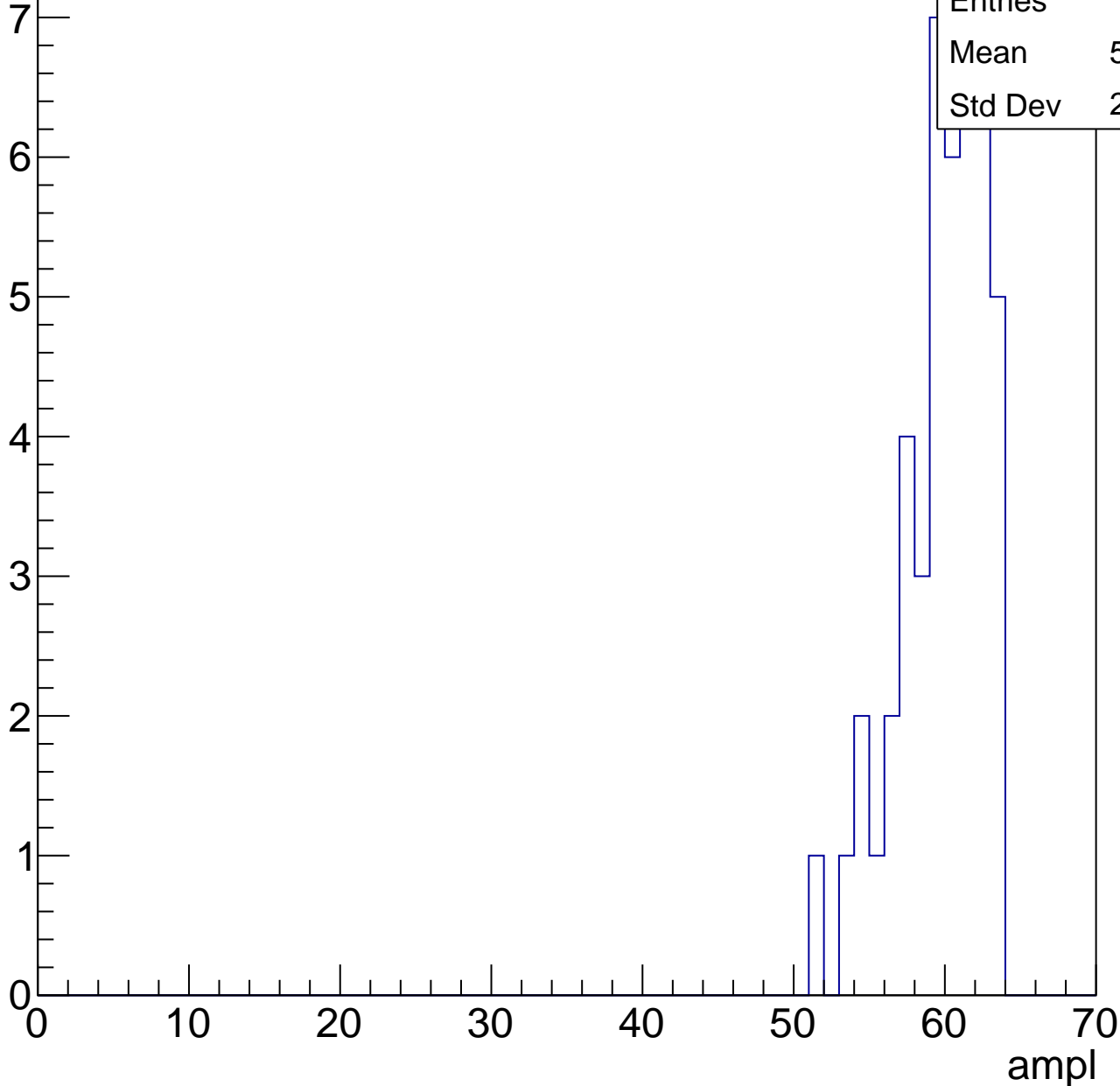


B1L102S, U2-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	59.35
Std Dev	2.853

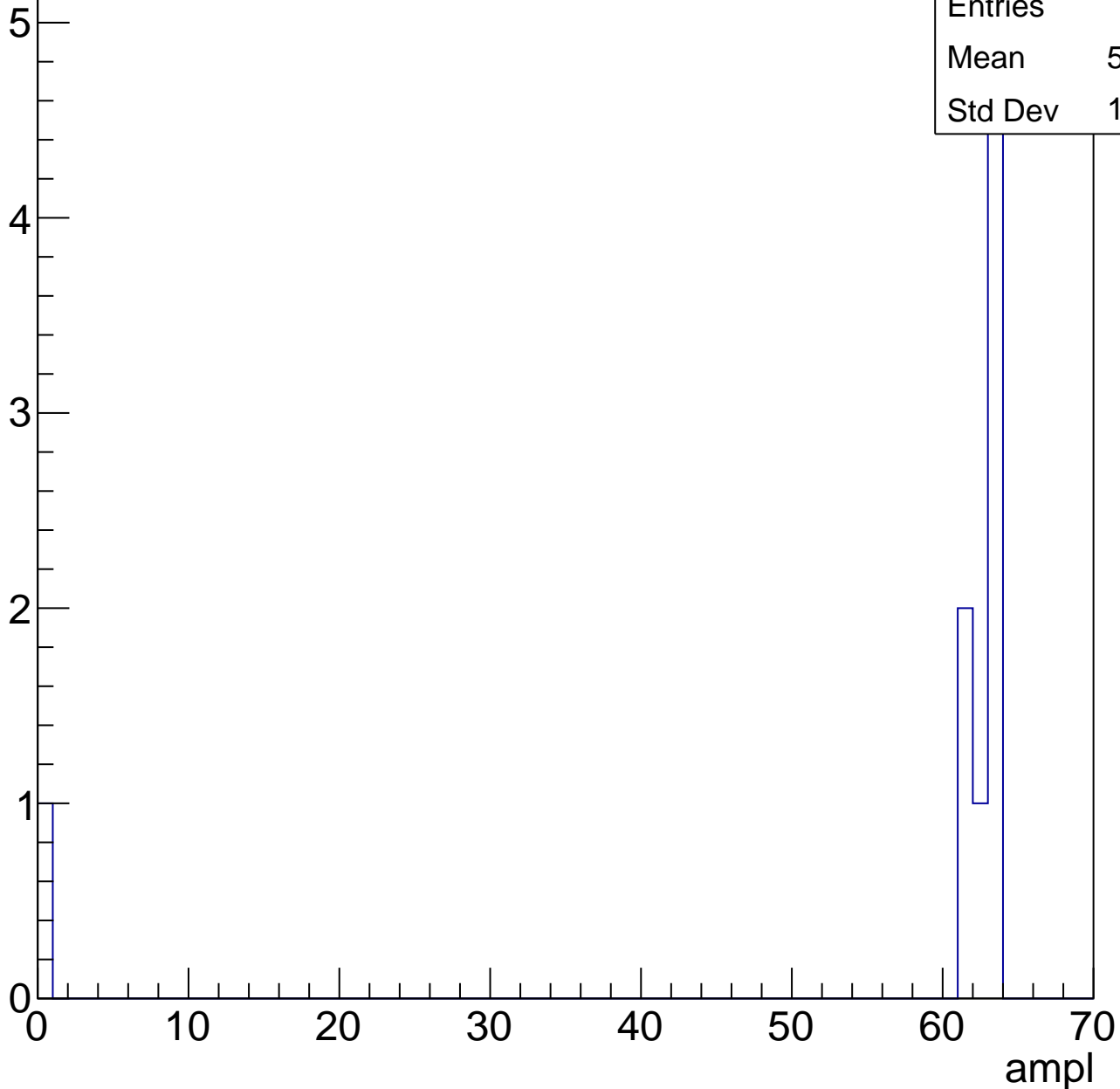


B1L102S, U2-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

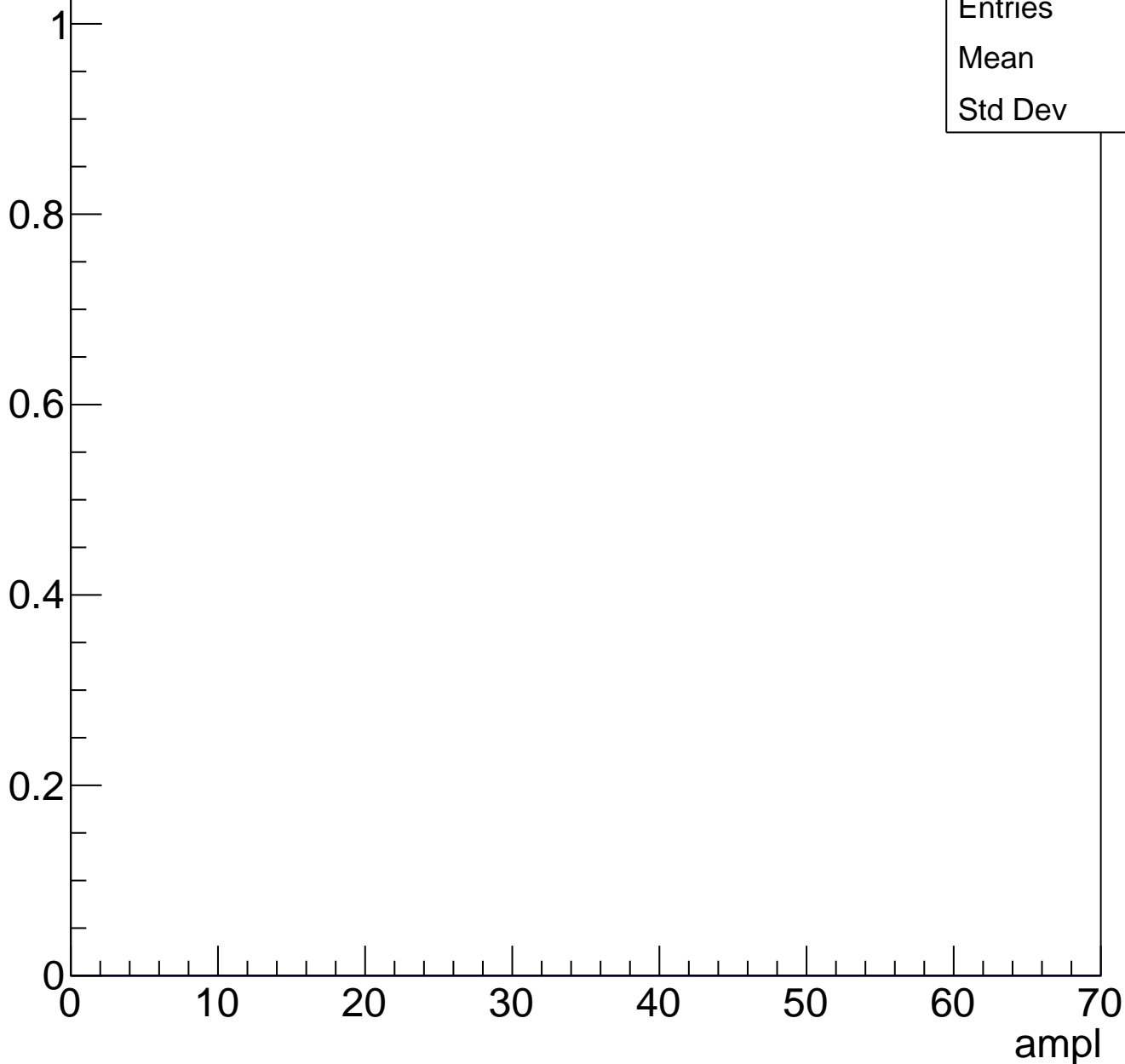
Entries	9
Mean	55.44
Std Dev	19.62



B1L102S, U2-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch85, adc0

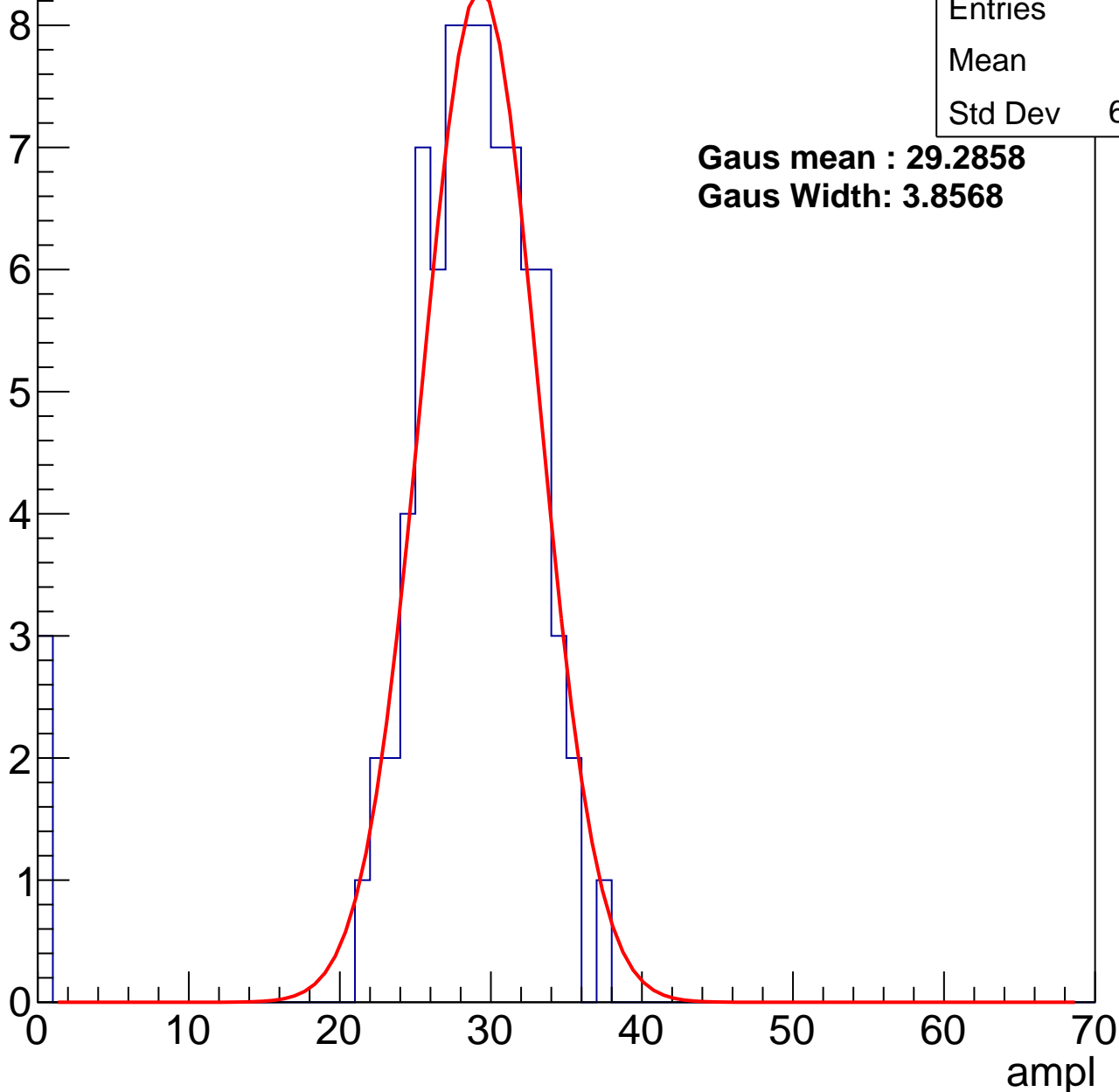
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.6
Std Dev	6.384

Gaus mean : 29.2858

Gaus Width: 3.8568



B1L102S, U2-ch85, adc1

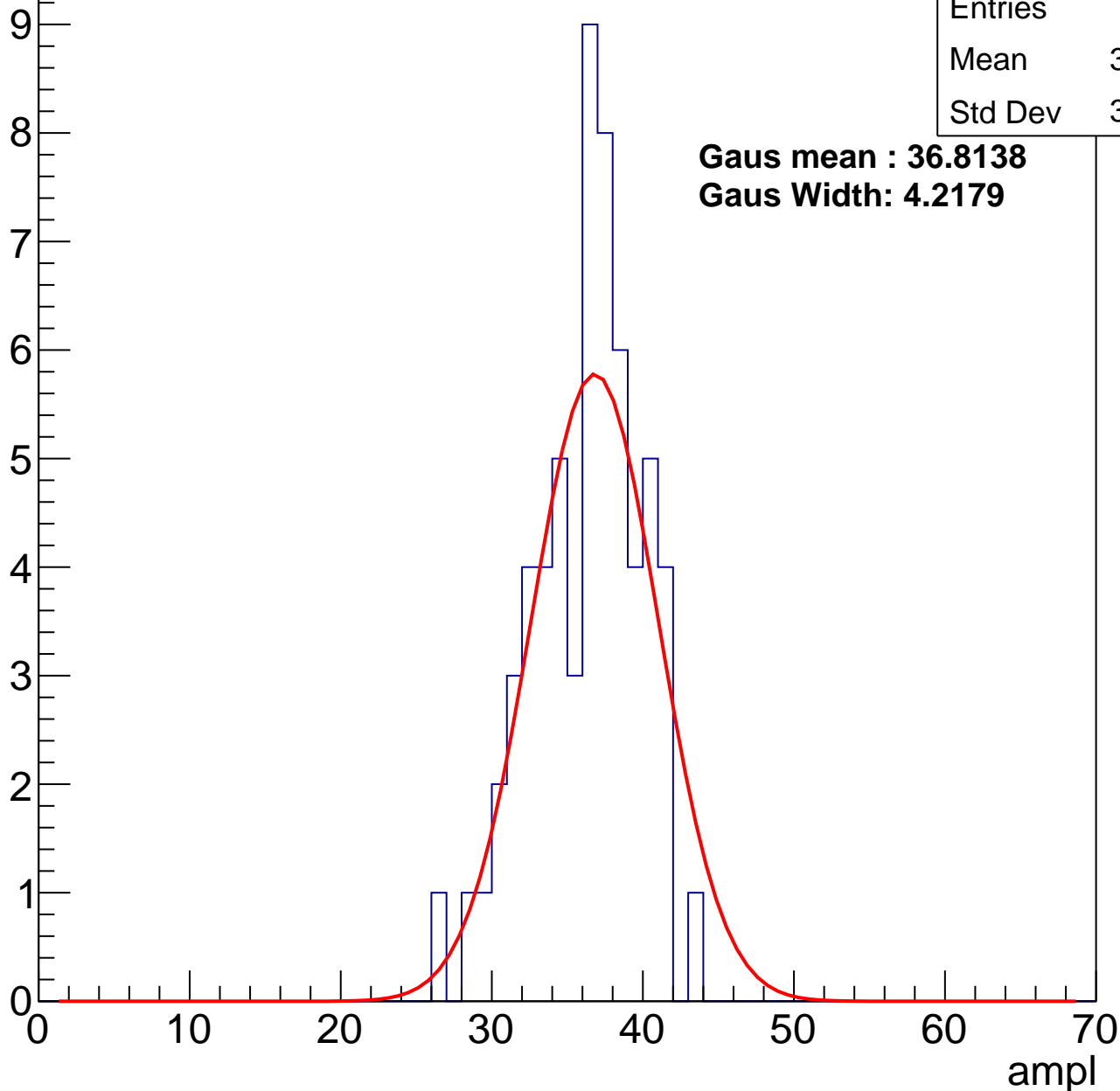
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.77
Std Dev	3.569

Gaus mean : 36.8138

Gaus Width: 4.2179



B1L102S, U2-ch85, adc2

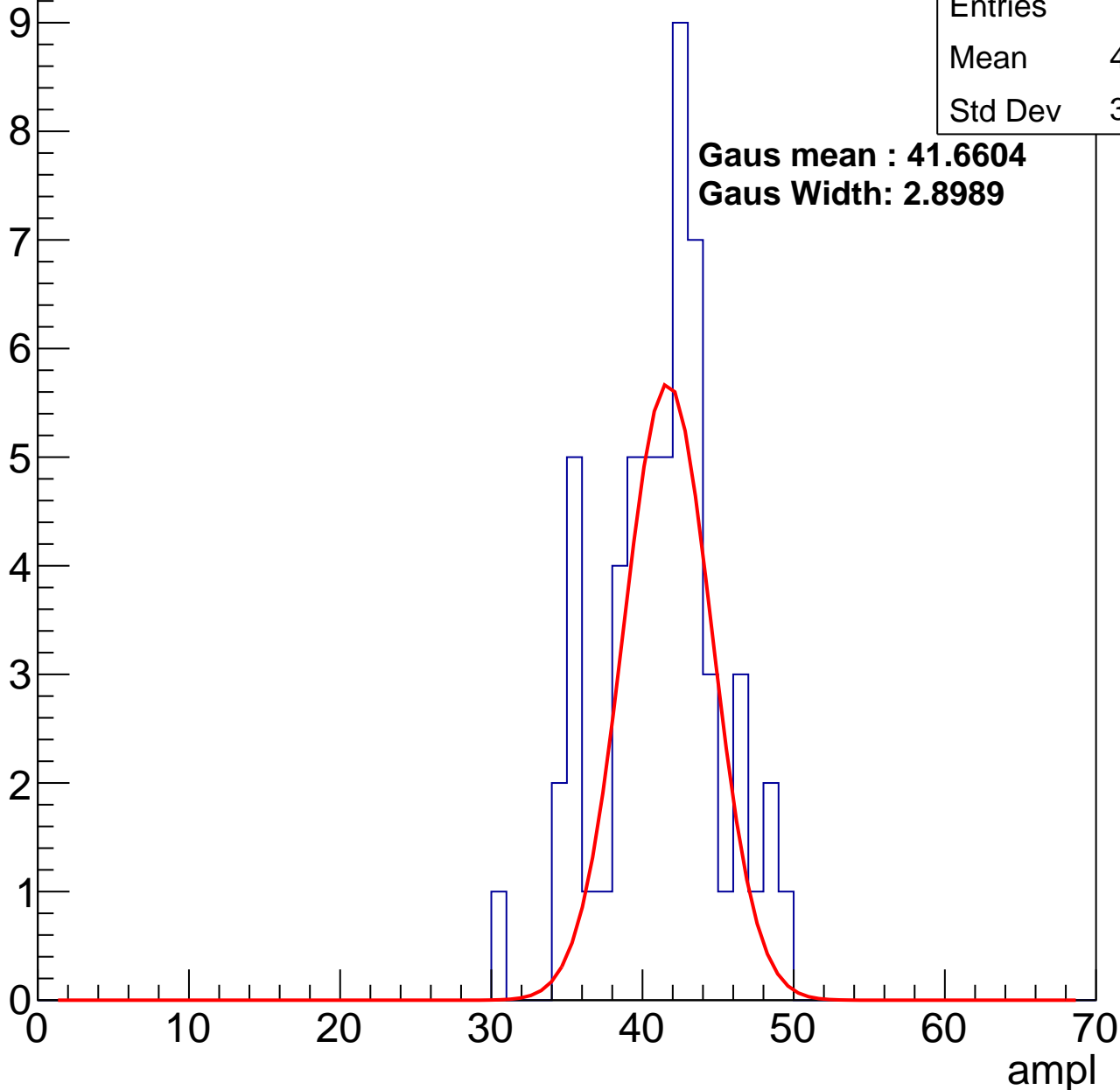
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	40.79
Std Dev	3.899

Gaus mean : 41.6604

Gaus Width: 2.8989

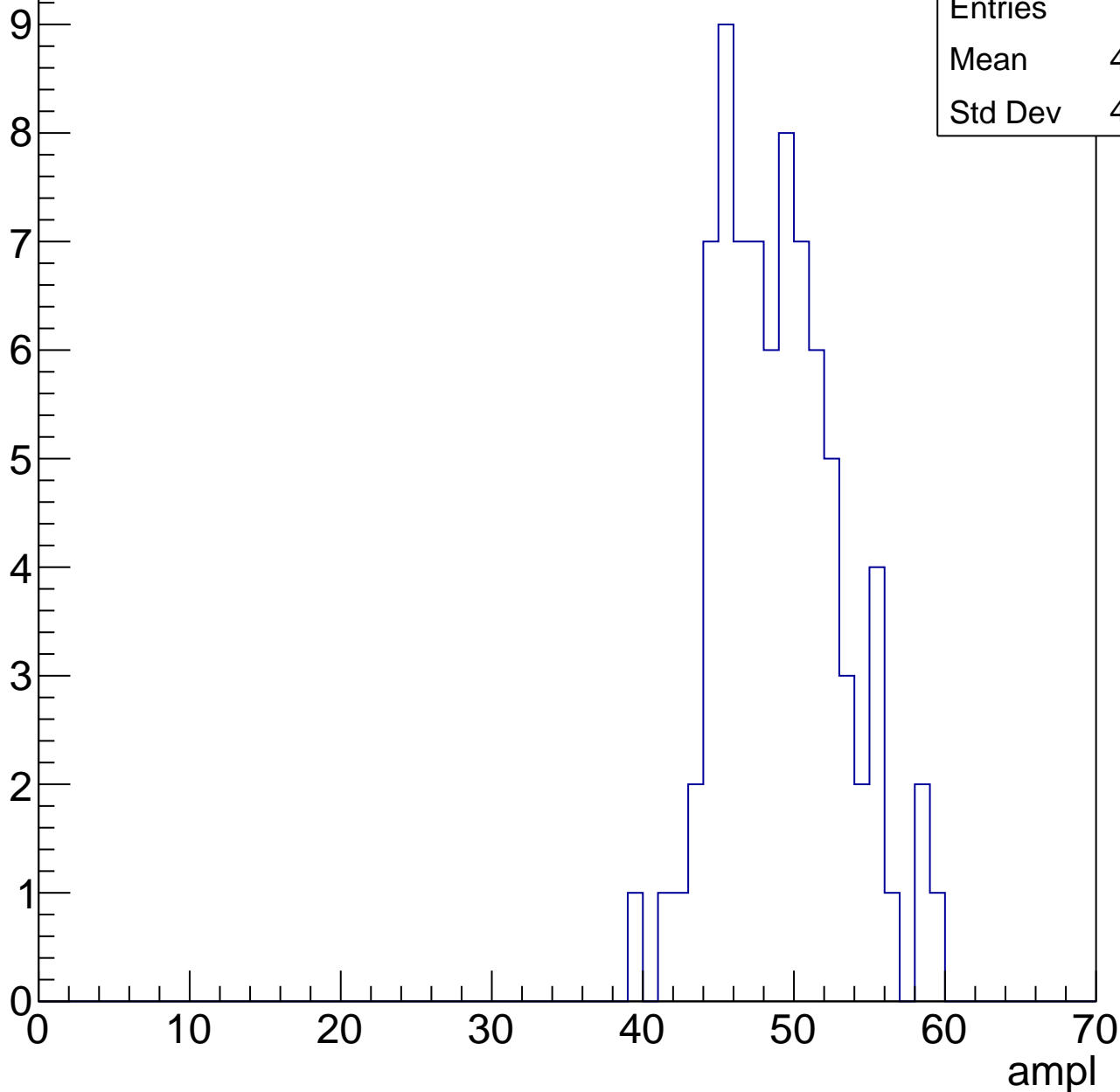


B1L102S, U2-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

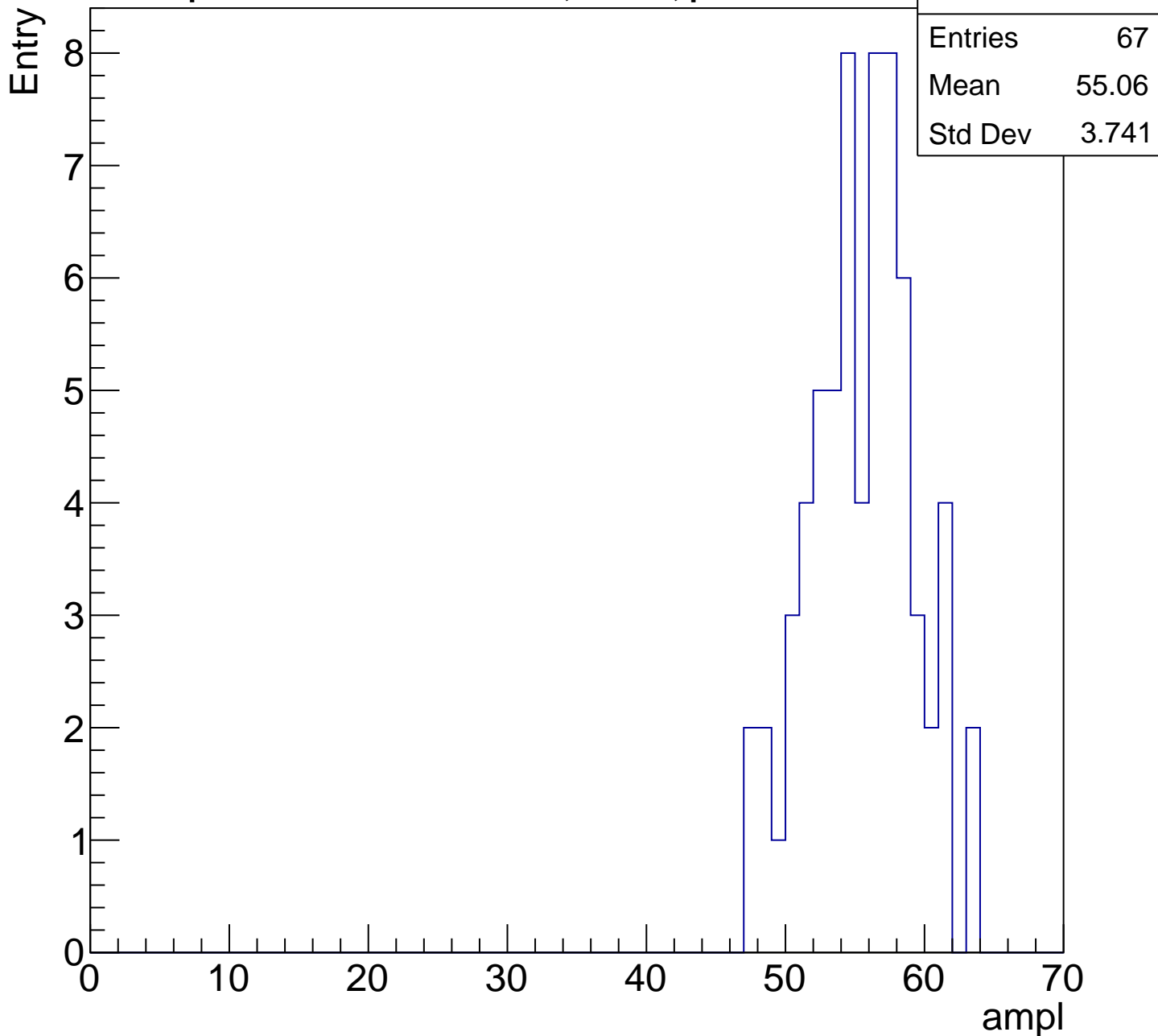
Entry

Entries	80
Mean	48.58
Std Dev	4.055



B1L102S, U2-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

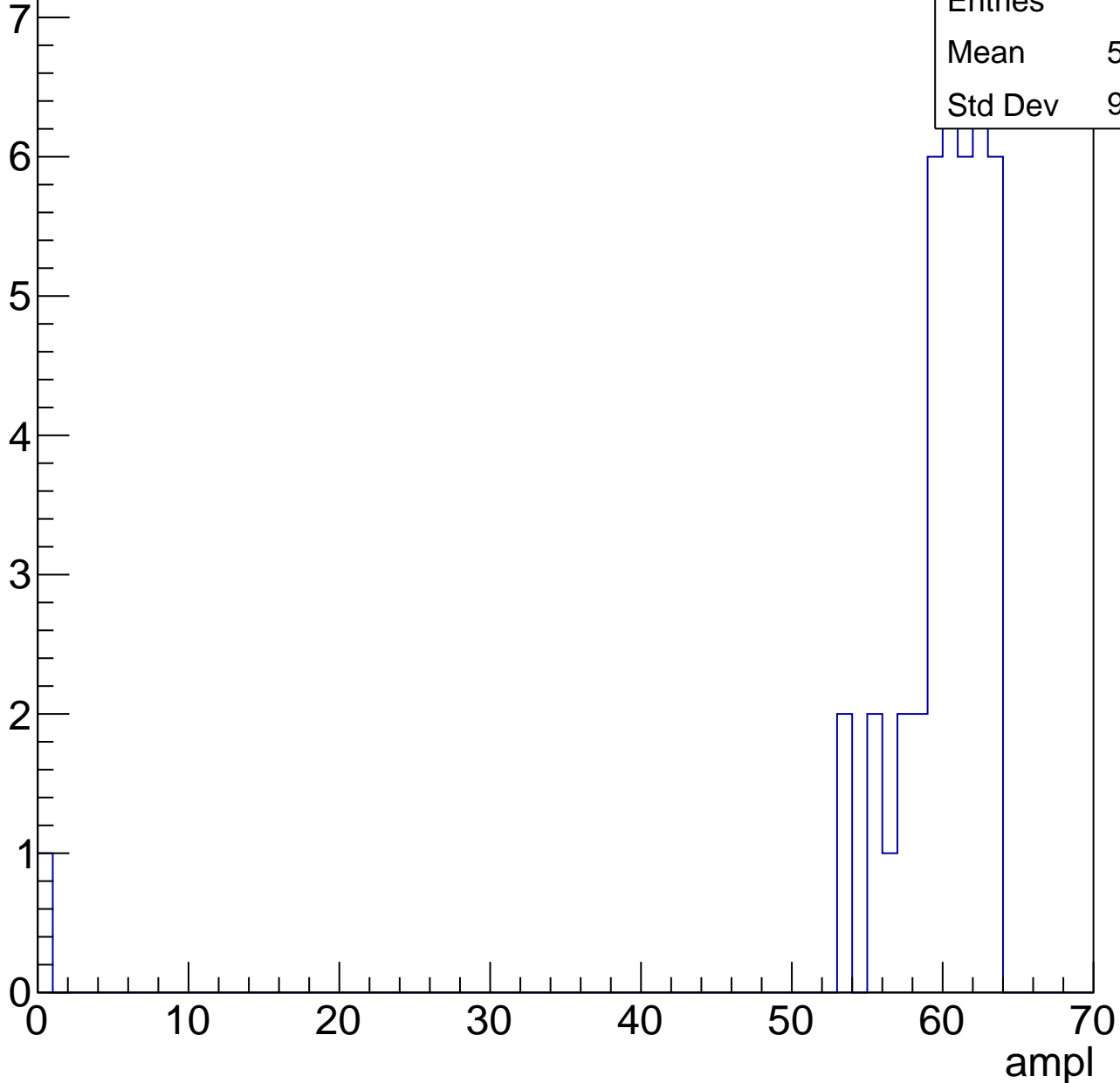


B1L102S, U2-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

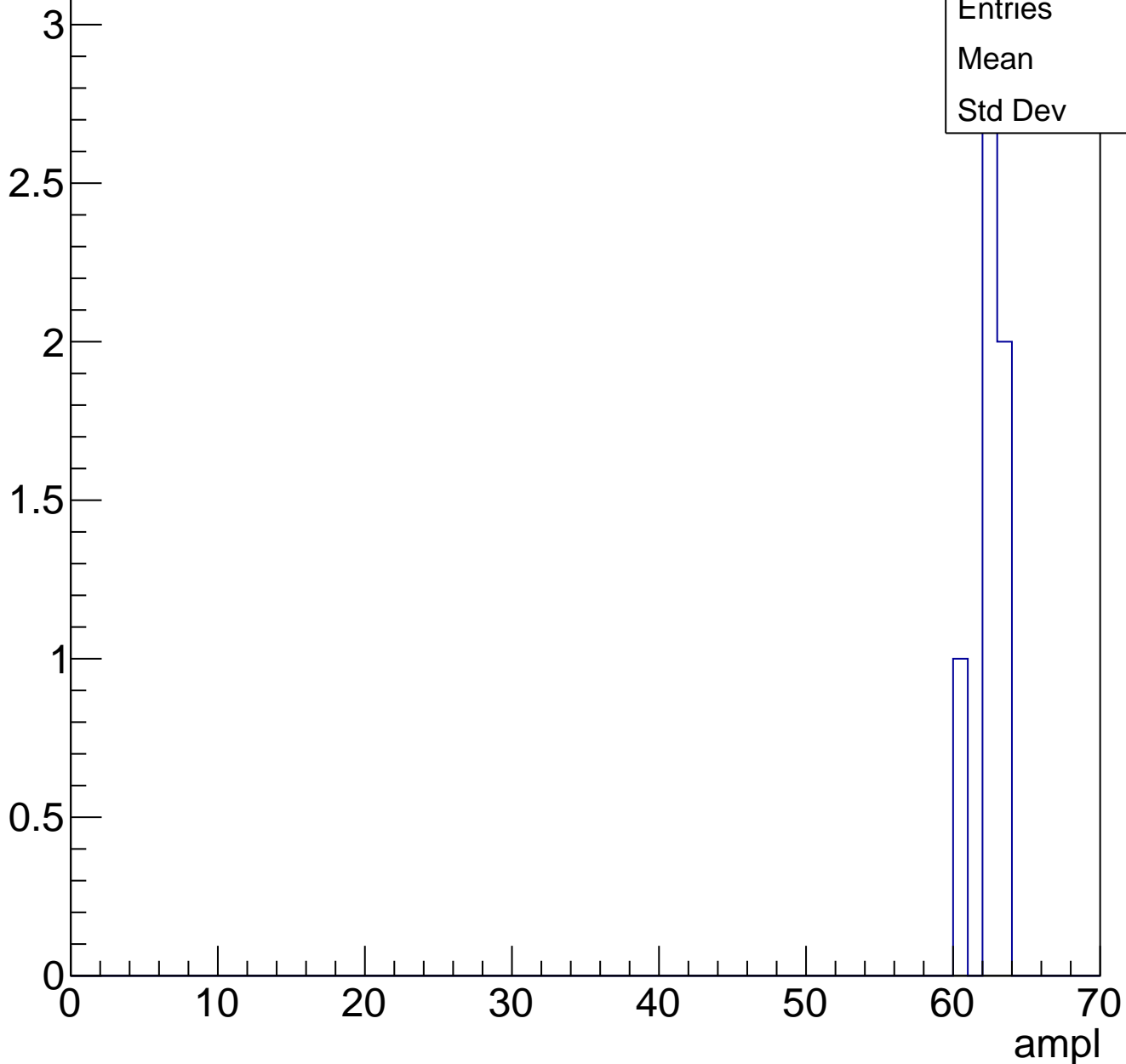
Entries	42
Mean	58.43
Std Dev	9.487



B1L102S, U2-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch86, adc0

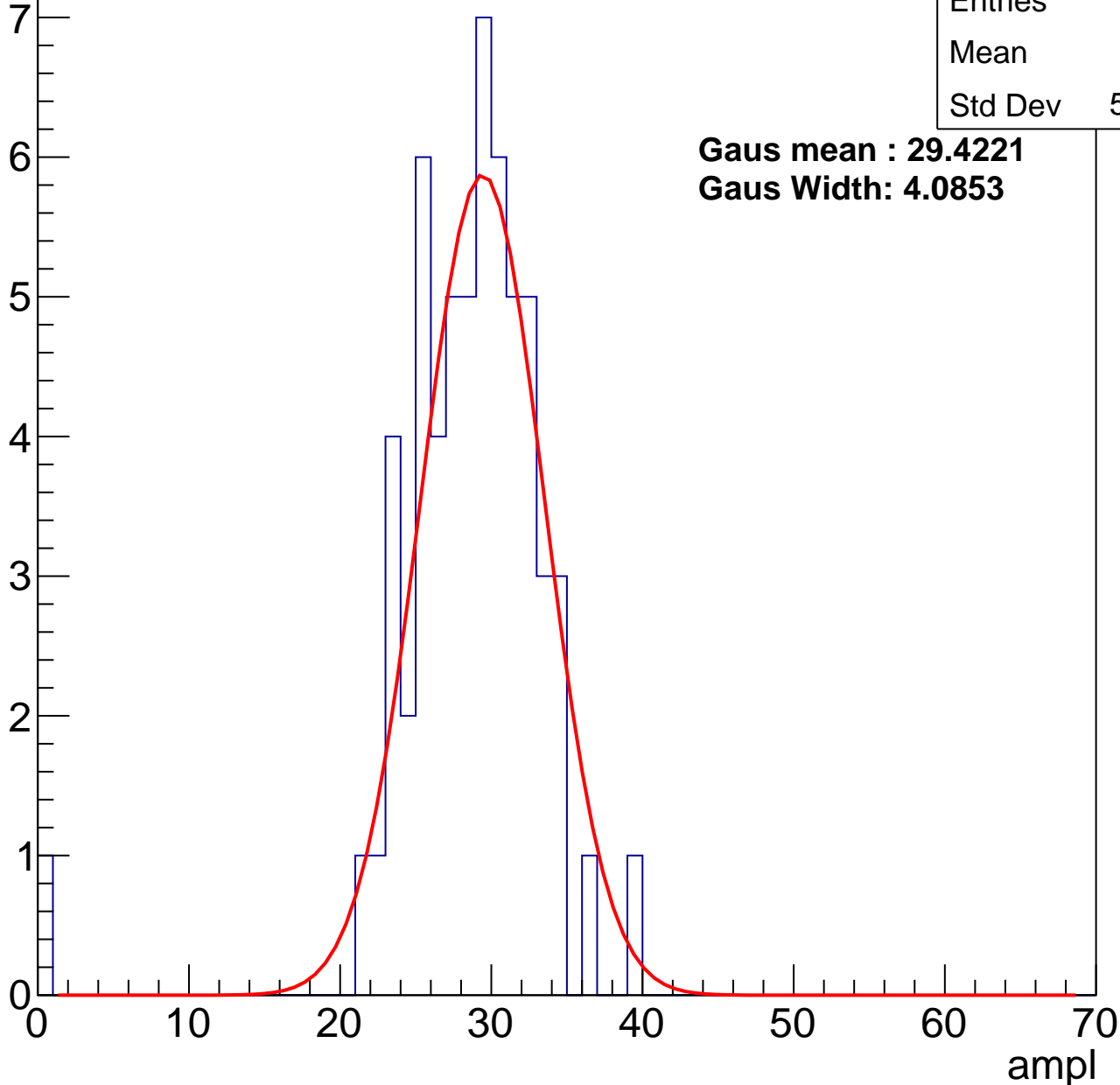
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.1
Std Dev	5.163

Gaus mean : 29.4221

Gaus Width: 4.0853



B1L102S, U2-ch86, adc1

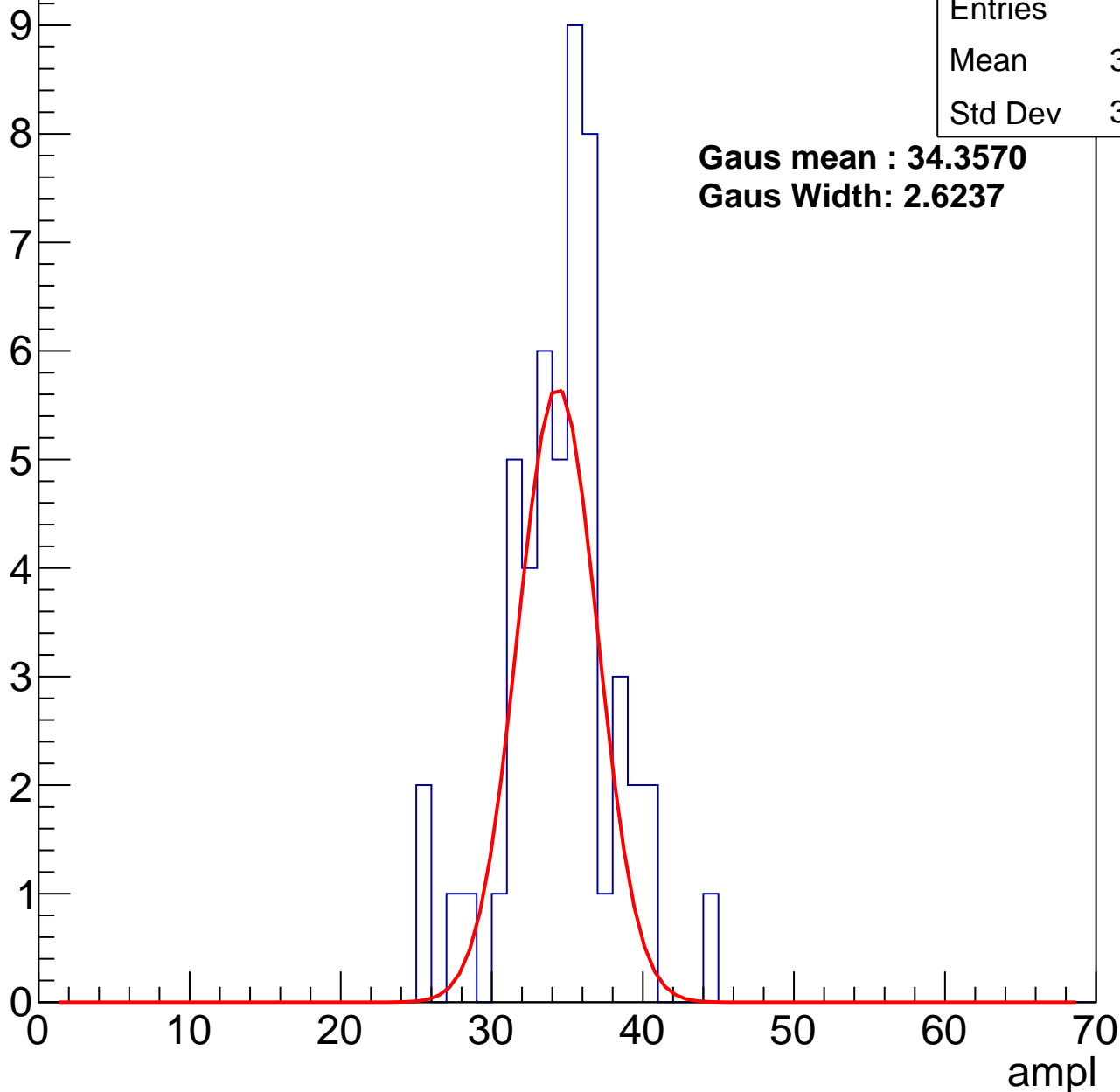
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	34.16
Std Dev	3.567

Gaus mean : 34.3570

Gaus Width: 2.6237



B1L102S, U2-ch86, adc2

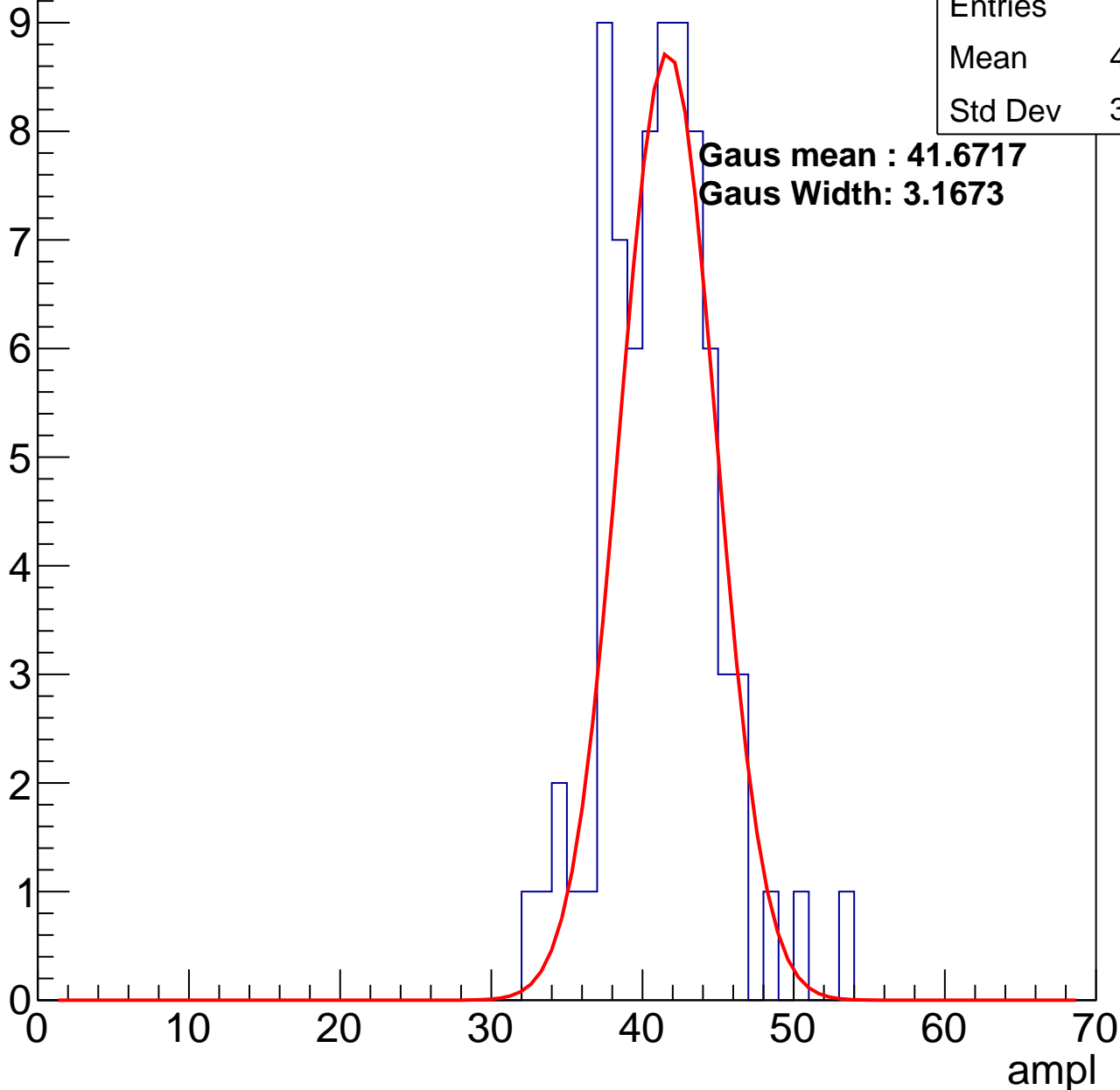
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	40.73
Std Dev	3.656

Gaus mean : 41.6717

Gaus Width: 3.1673

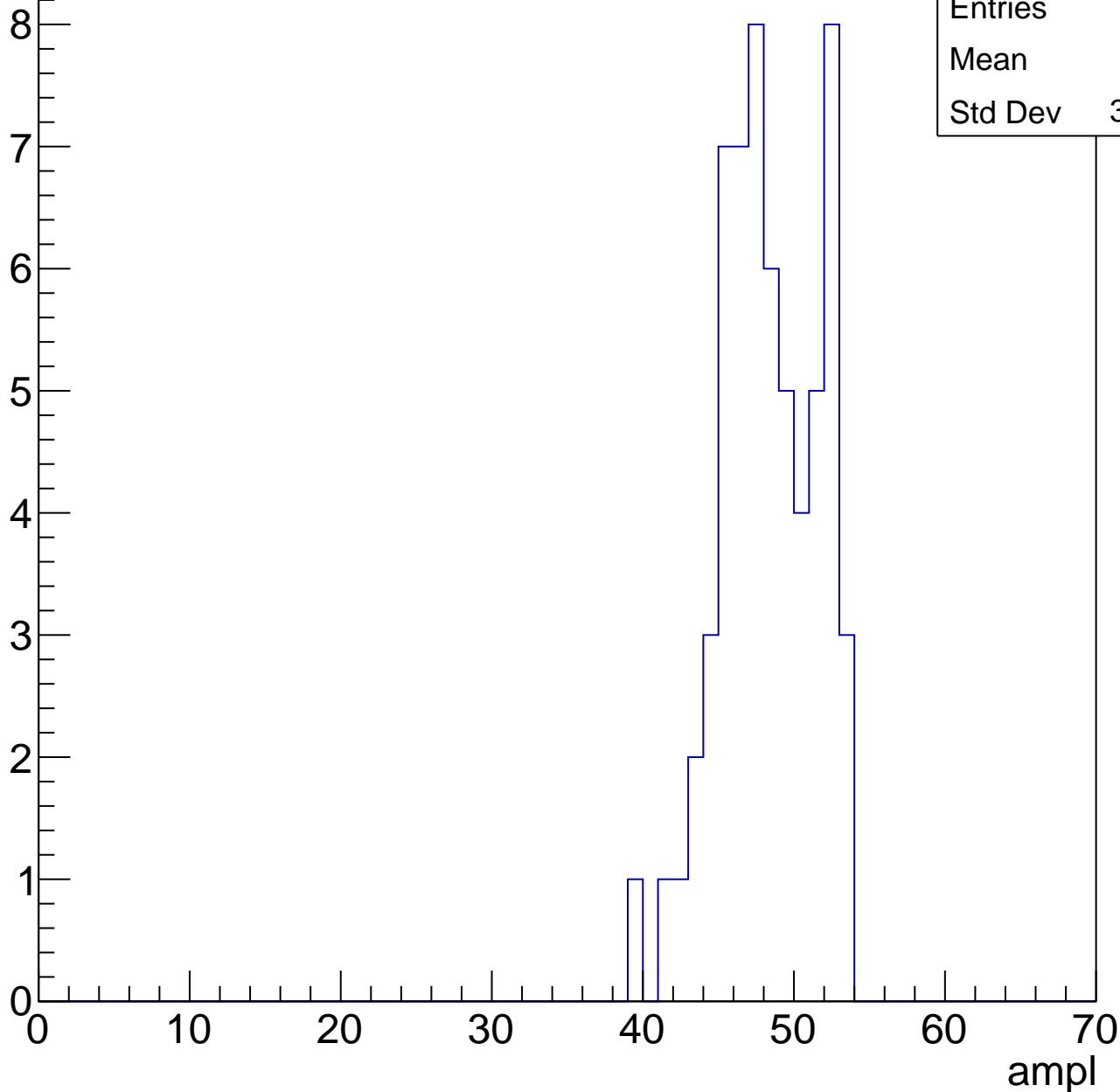


B1L102S, U2-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	47.8
Std Dev	3.213

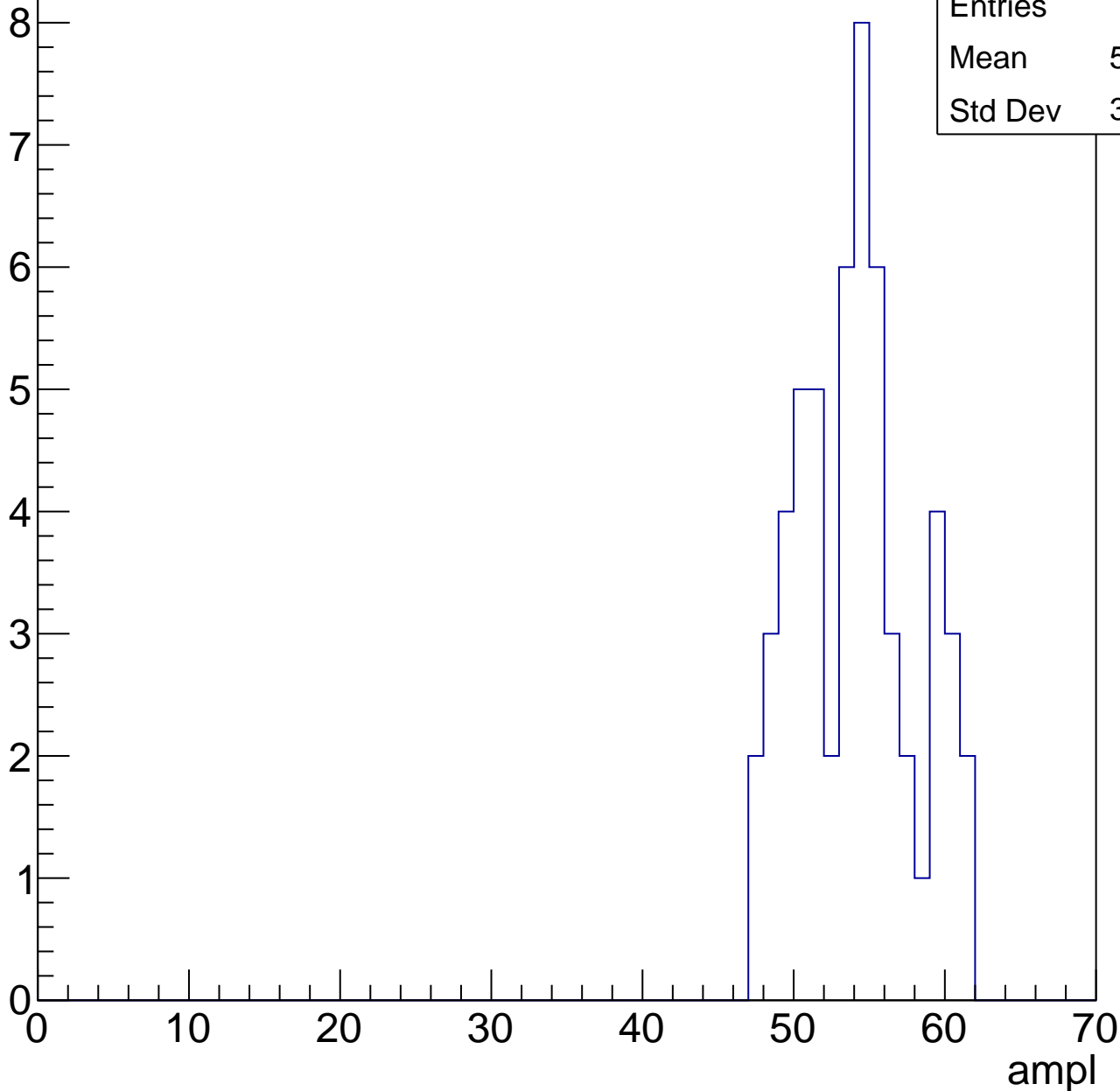


B1L102S, U2-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.59
Std Dev	3.764

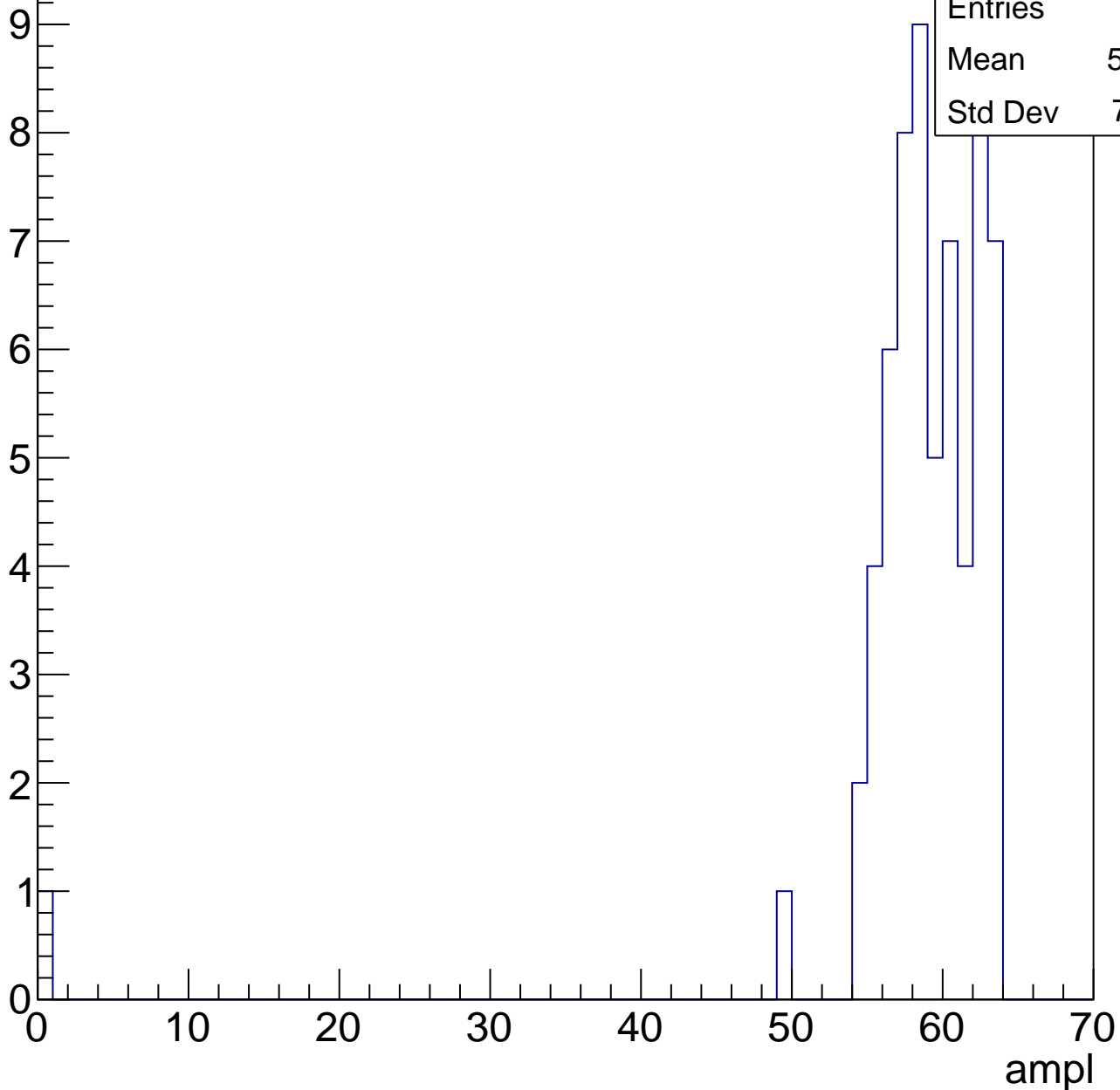


B1L102S, U2-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

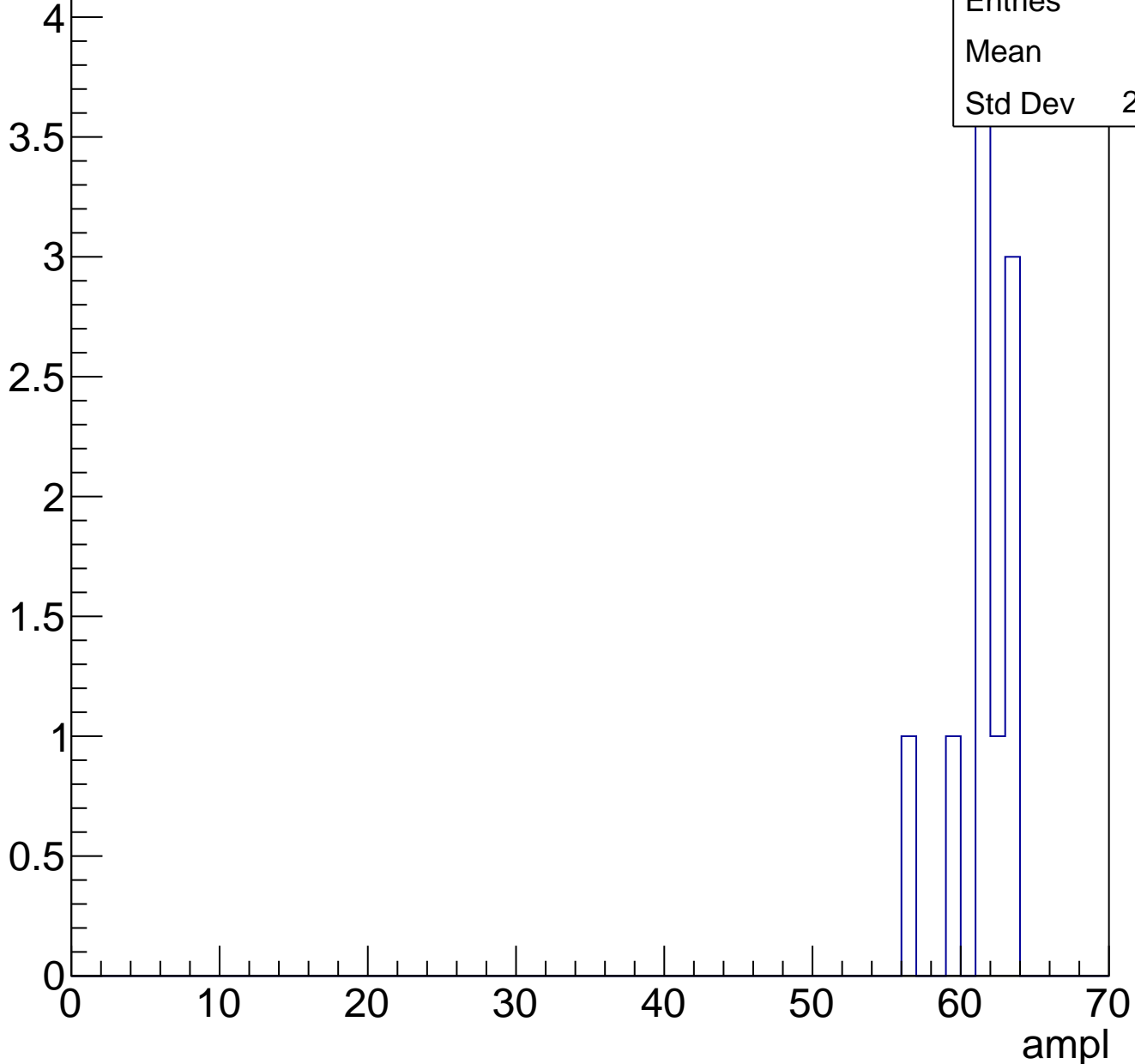
Entries	63
Mean	57.92
Std Dev	7.901



B1L102S, U2-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch87, adc0

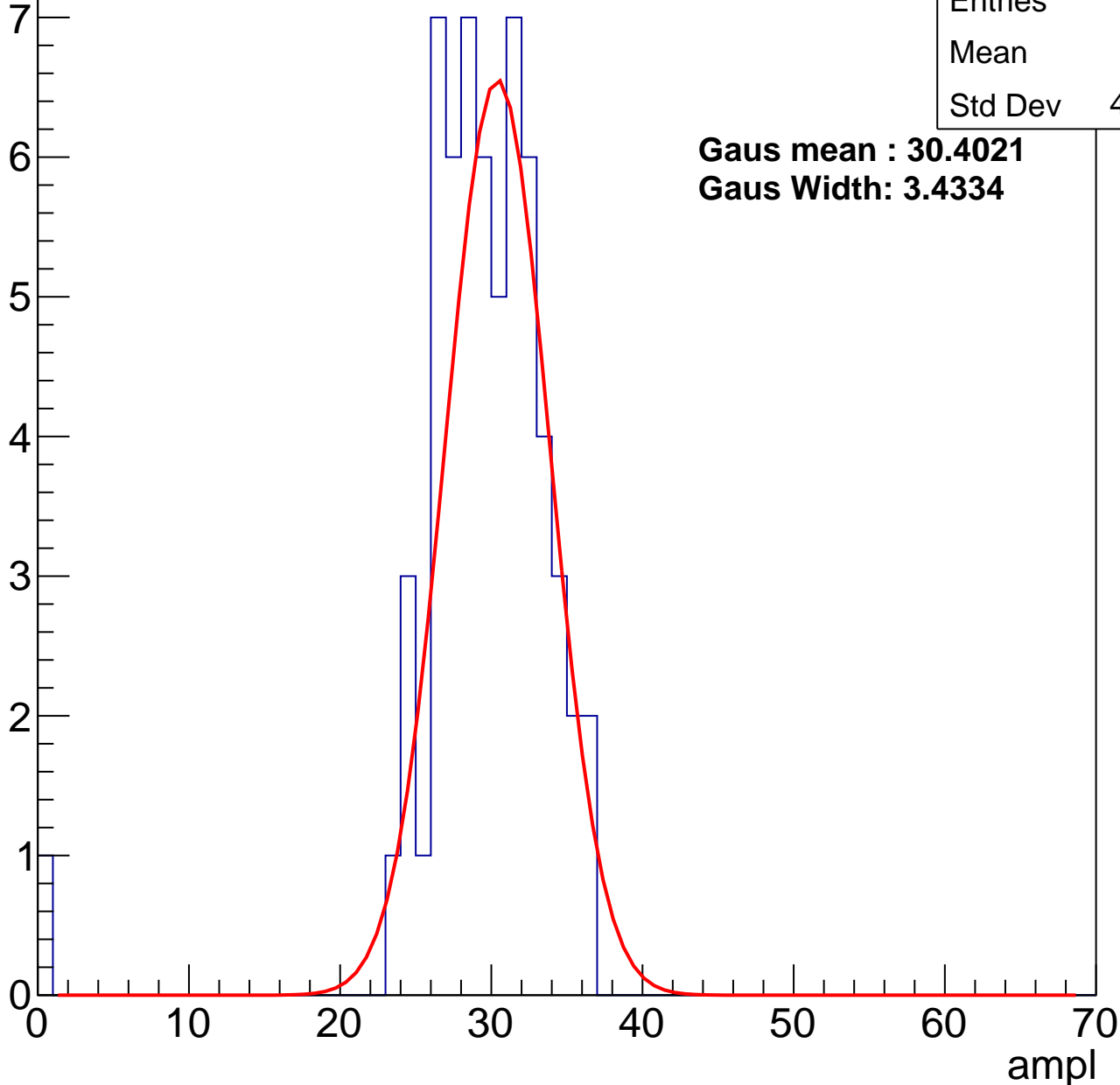
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	29
Std Dev	4.889

Gaus mean : 30.4021

Gaus Width: 3.4334



B1L102S, U2-ch87, adc1

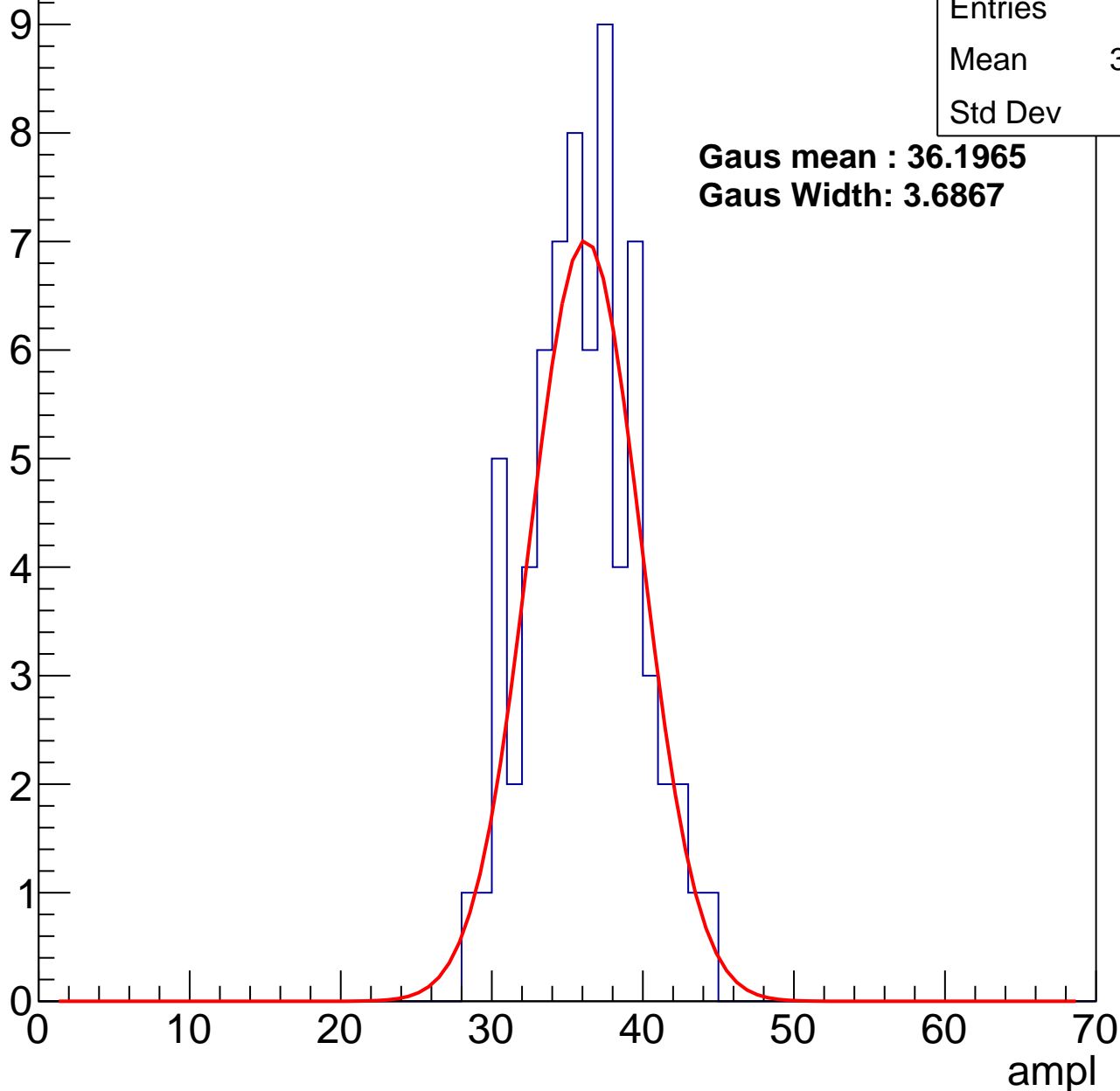
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.65
Std Dev	3.53

Gaus mean : 36.1965

Gaus Width: 3.6867



B1L102S, U2-ch87, adc2

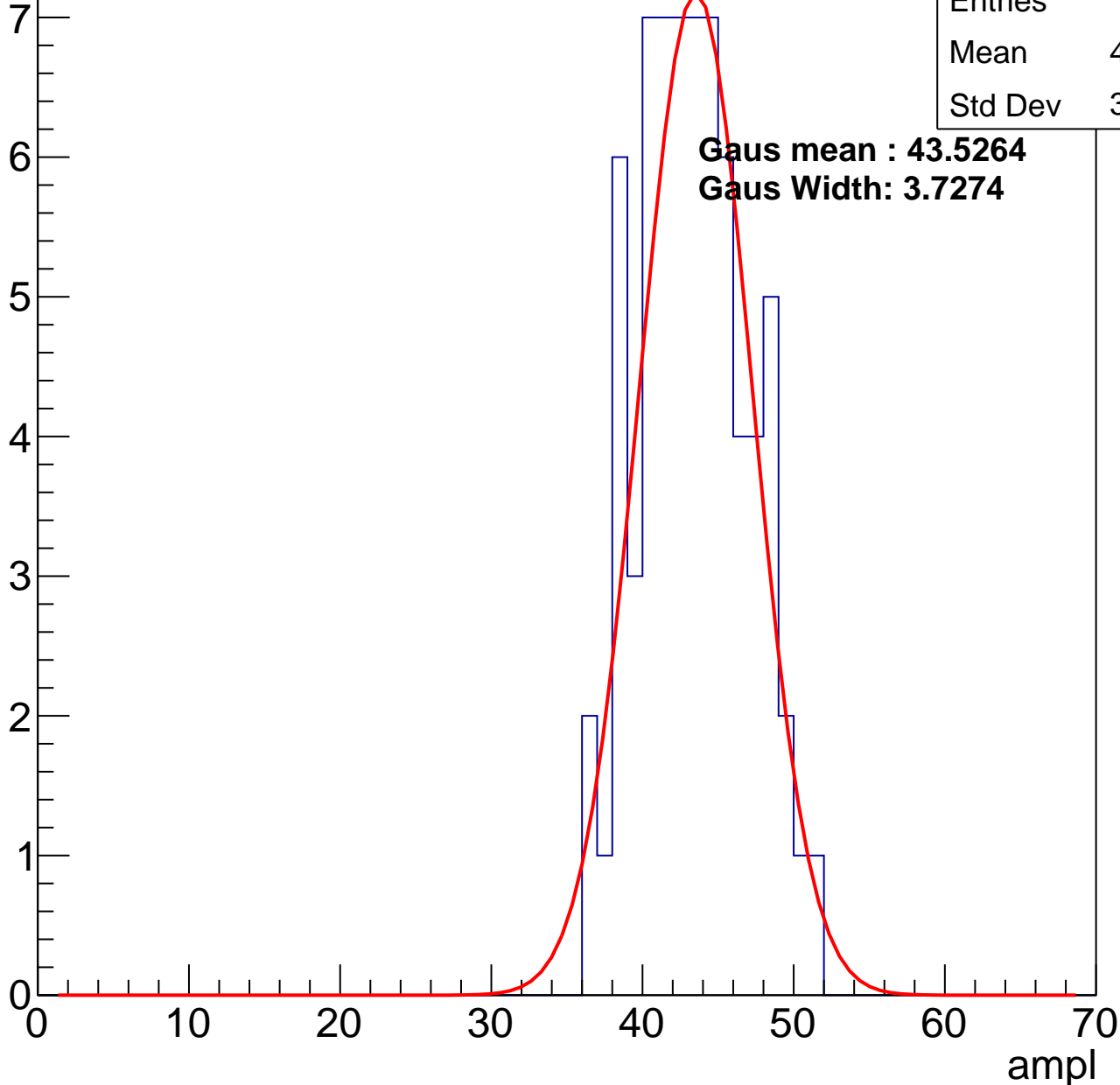
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.93
Std Dev	3.527

Gaus mean : 43.5264

Gaus Width: 3.7274



B1L102S, U2-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	49.35
Std Dev	3.73

Entry

10

8

6

4

2

0

0

10

20

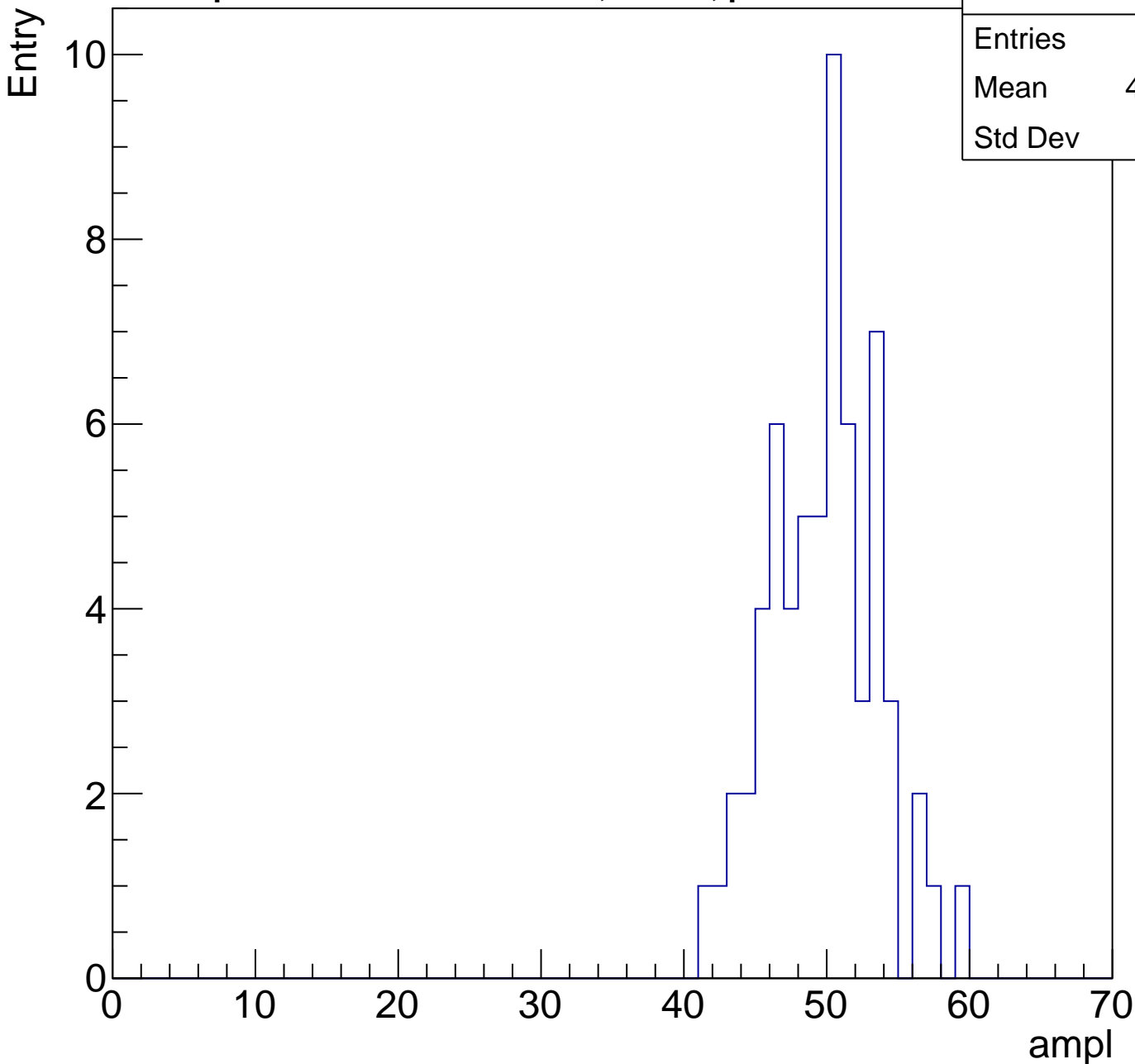
30

40

50

60

ampl

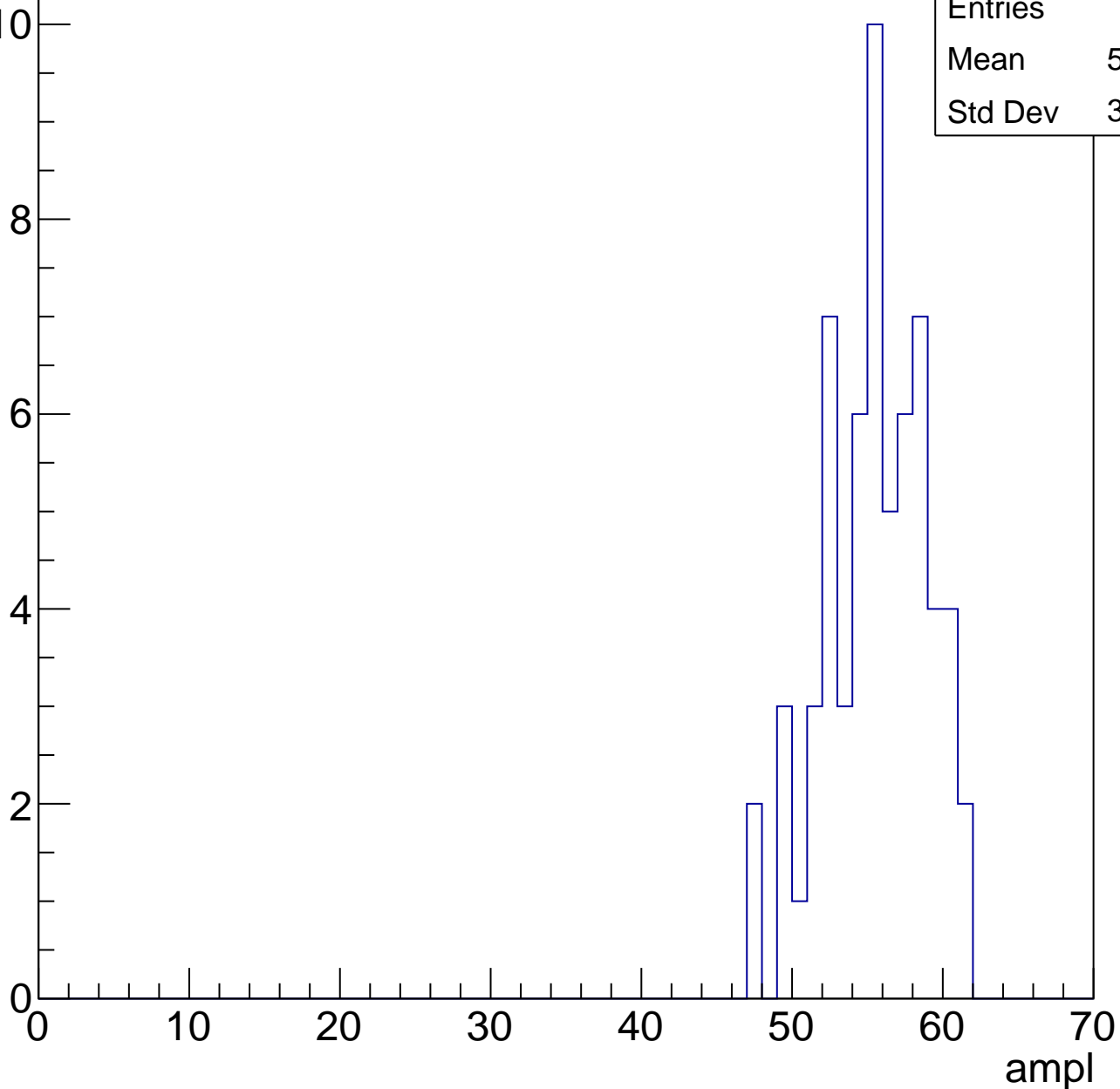


B1L102S, U2-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.03
Std Dev	3.376

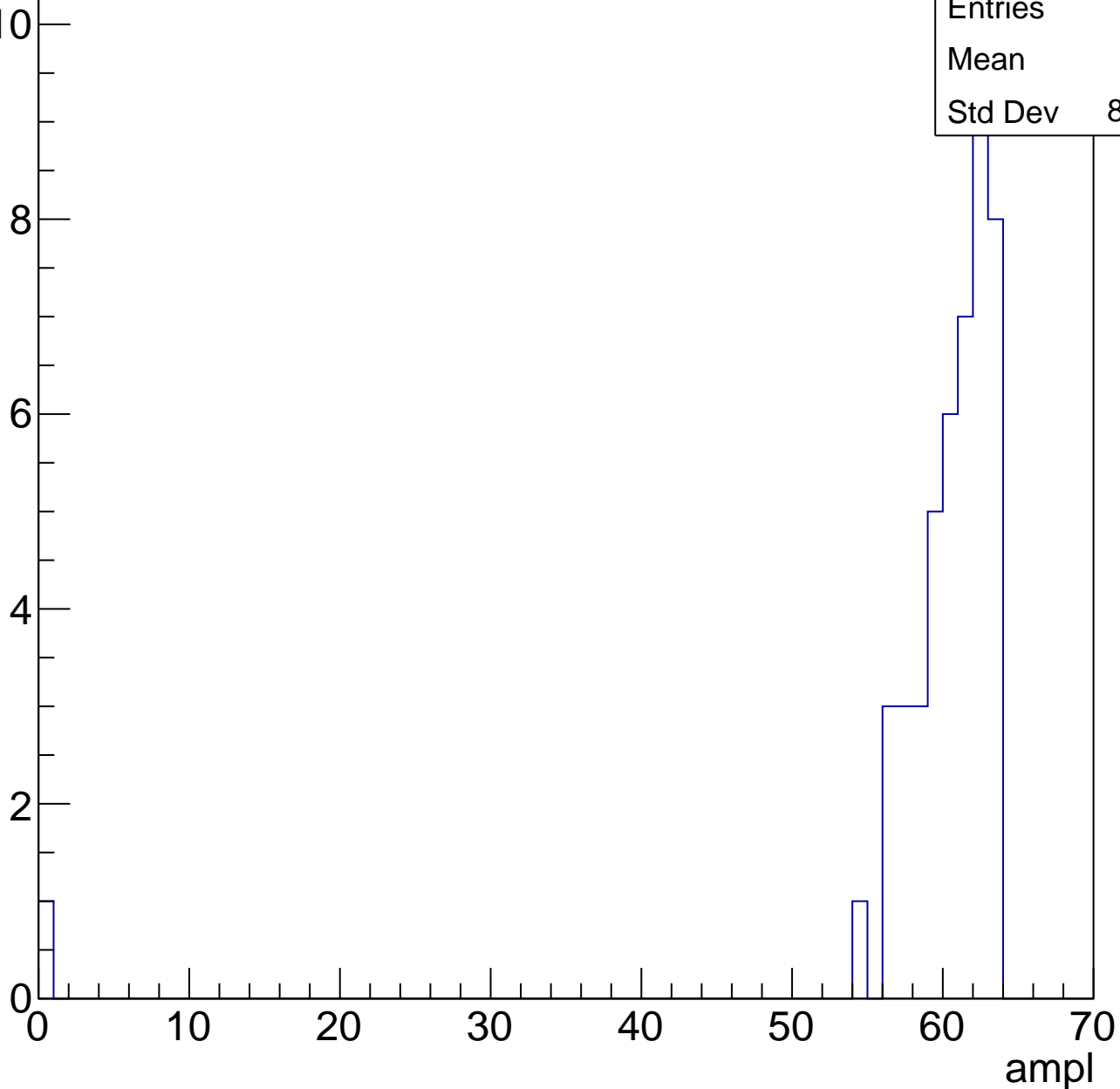


B1L102S, U2-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

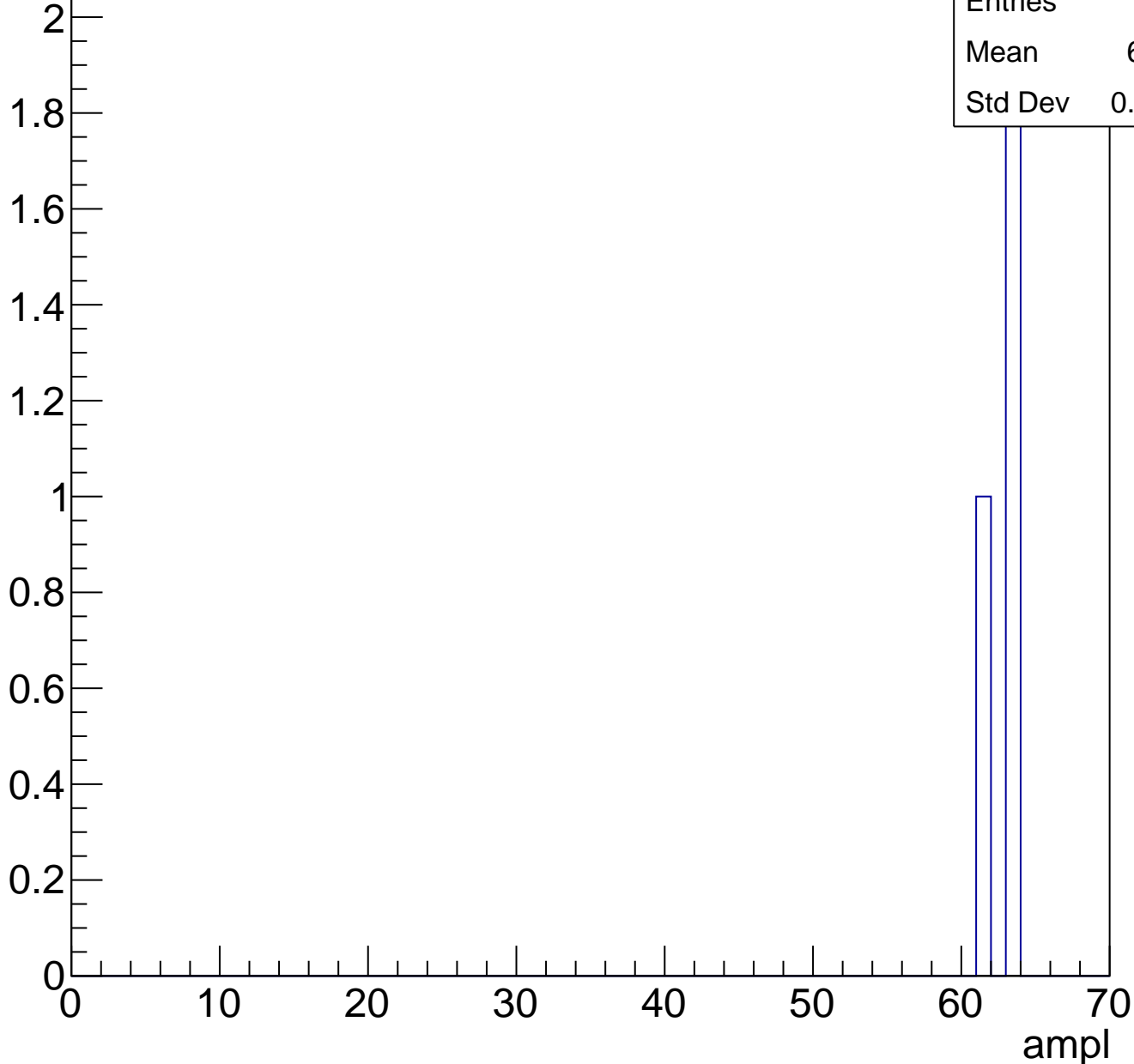
Entries	47
Mean	59
Std Dev	8.992



B1L102S, U2-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U2-ch88, adc0

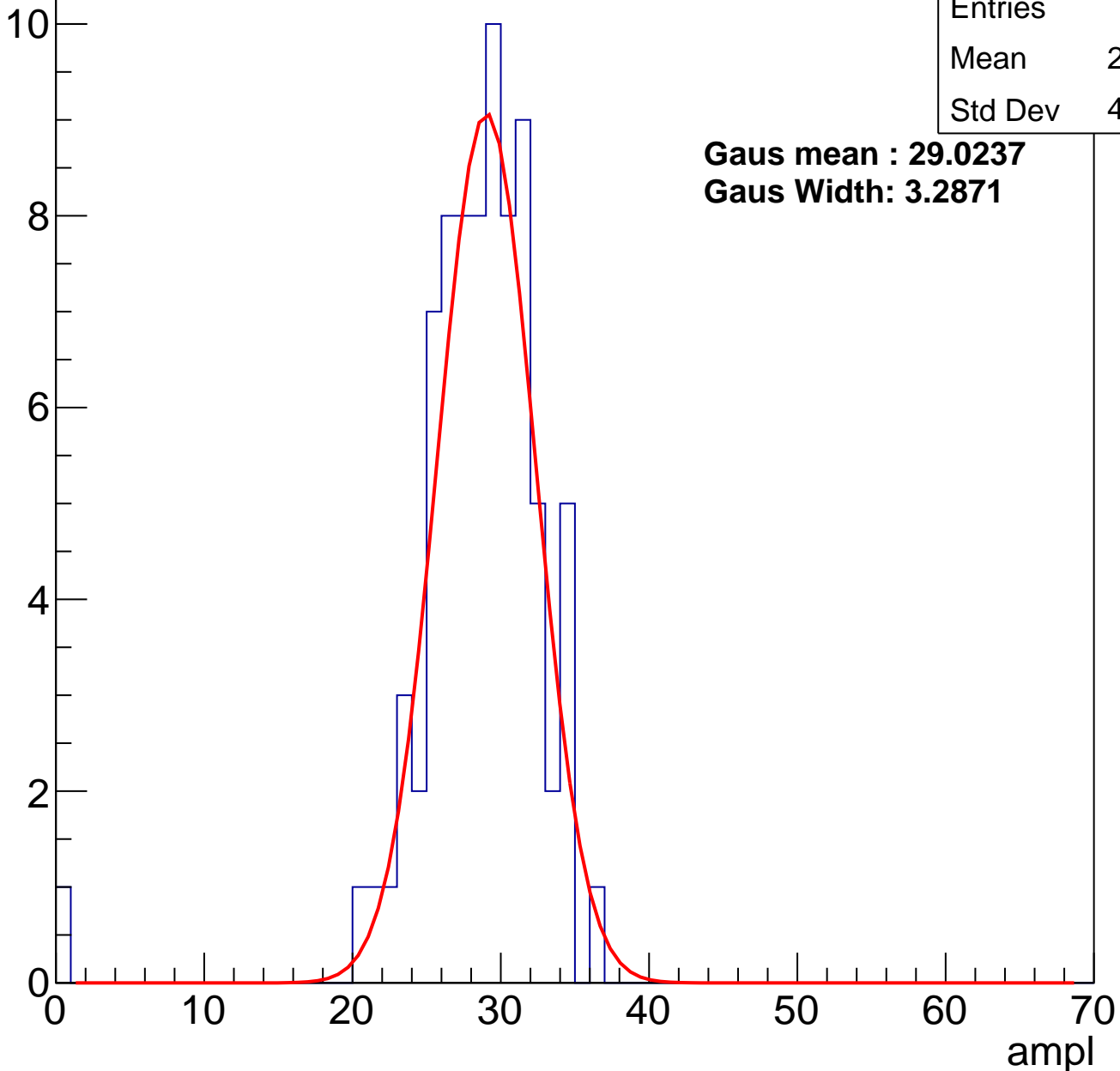
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	28.05
Std Dev	4.527

Gaus mean : 29.0237

Gaus Width: 3.2871

Entry



B1L102S, U2-ch88, adc1

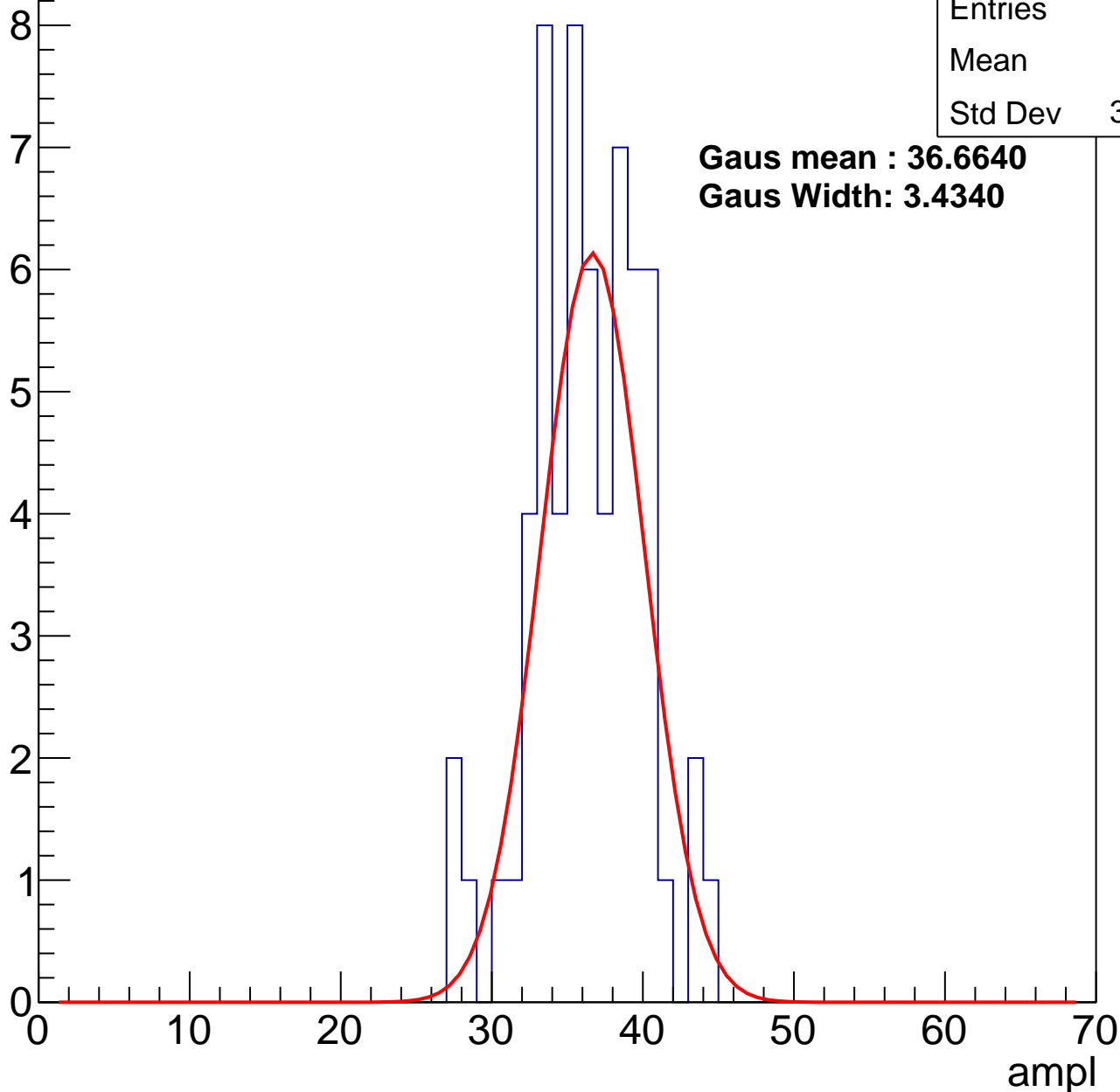
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.9
Std Dev	3.627

Gaus mean : 36.6640

Gaus Width: 3.4340



B1L102S, U2-ch88, adc2

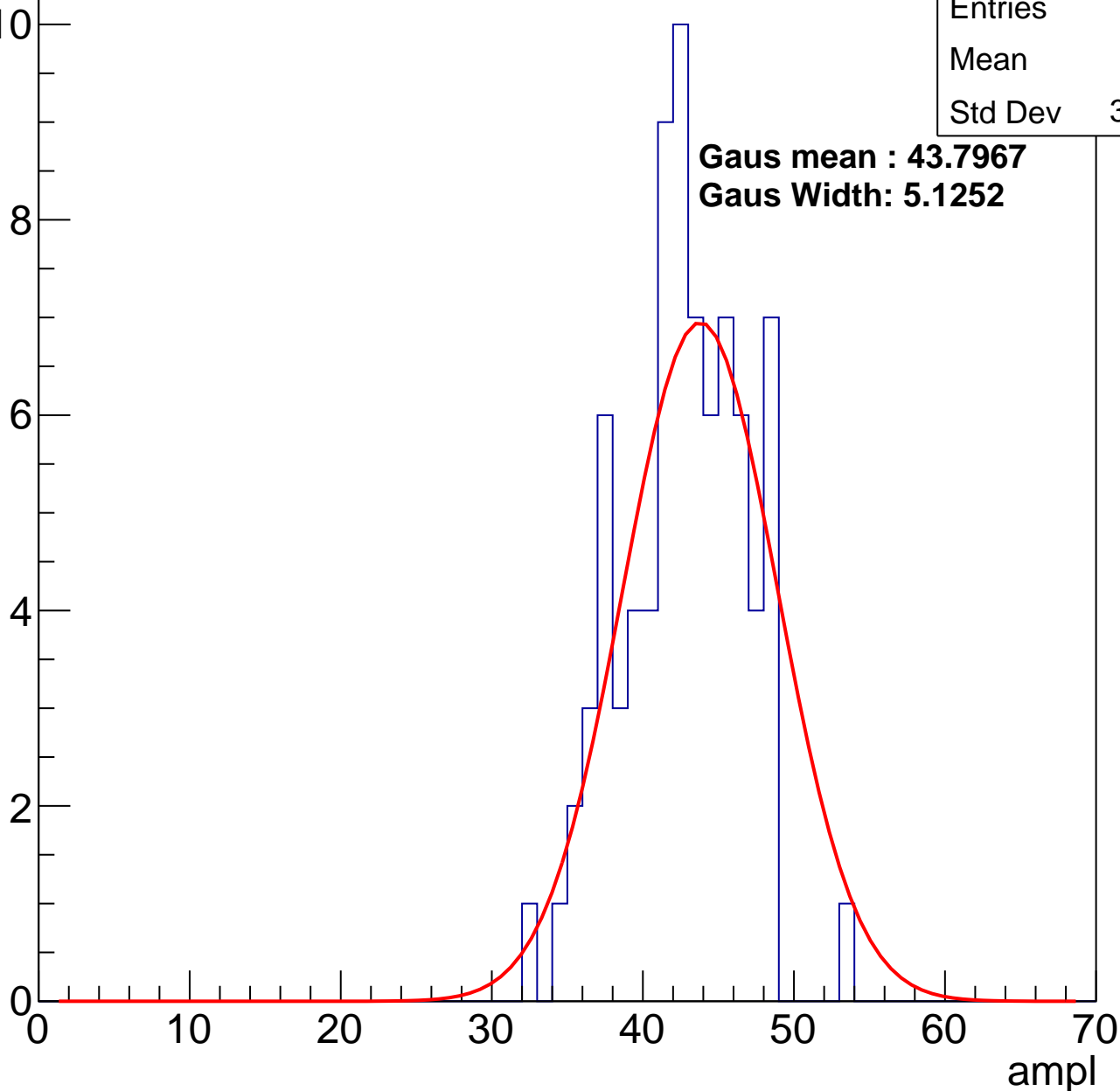
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.2
Std Dev	3.989

Gaus mean : 43.7967

Gaus Width: 5.1252

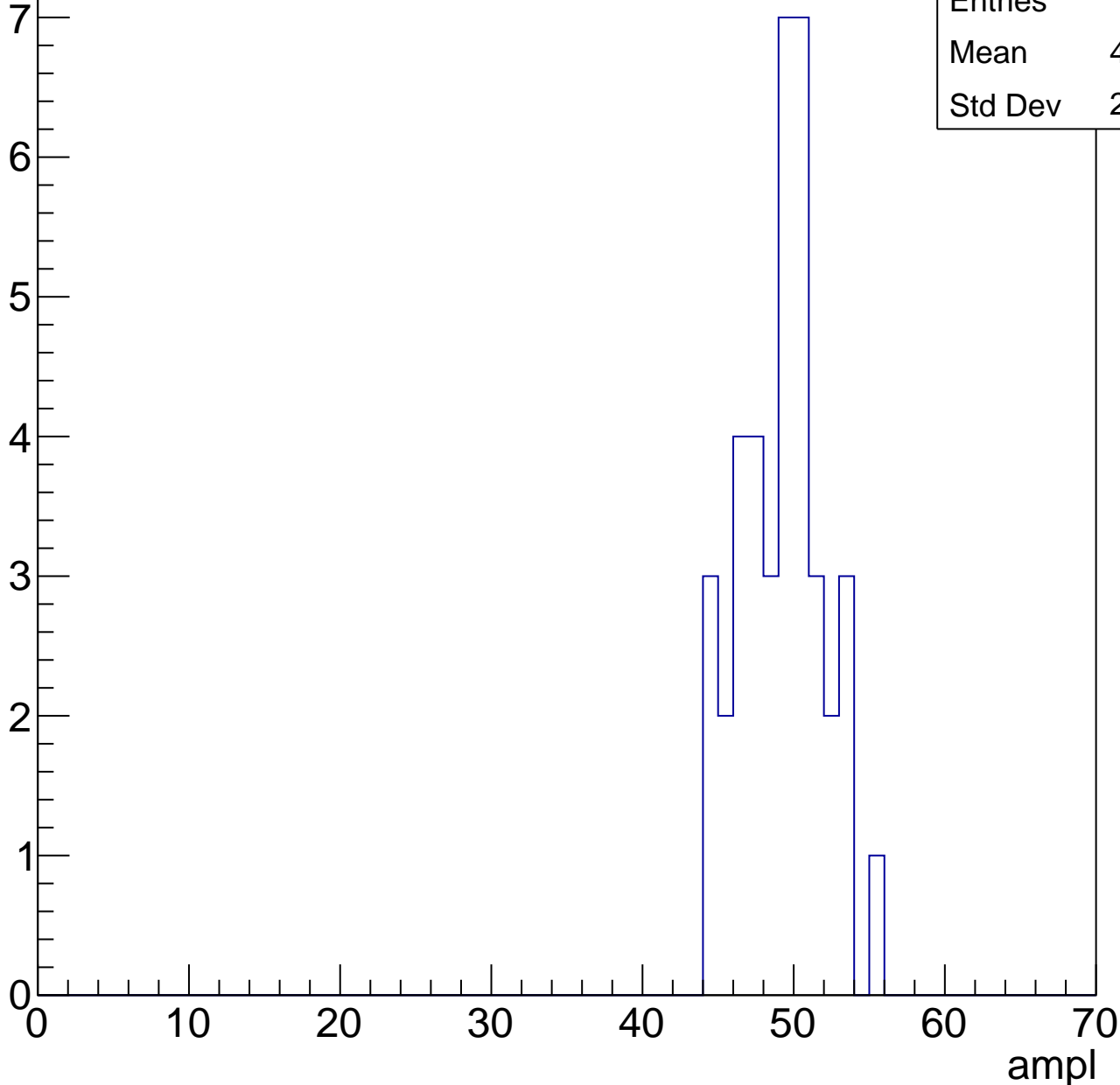


B1L102S, U2-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	48.77
Std Dev	2.684

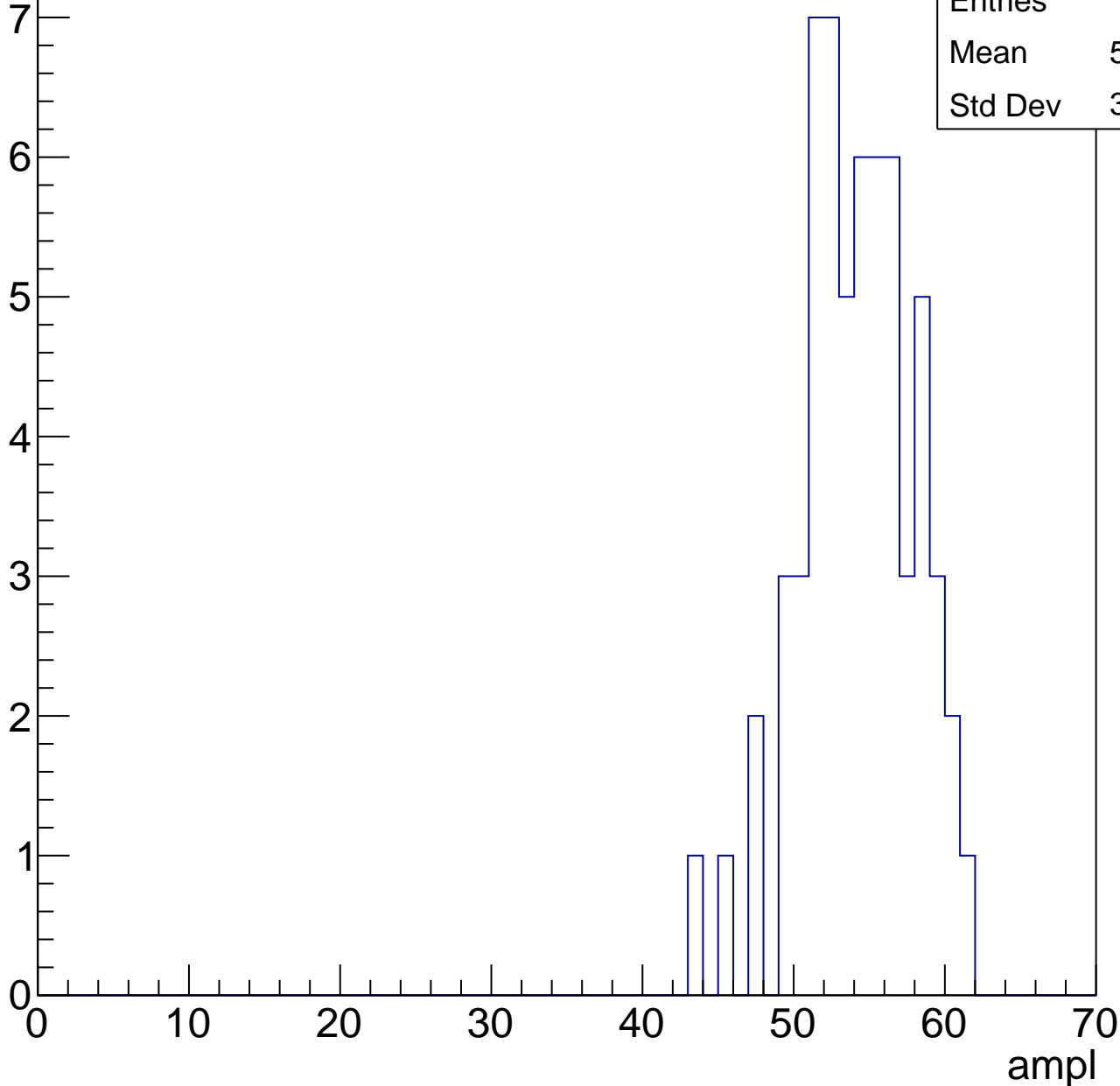


B1L102S, U2-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.67
Std Dev	3.723



B1L102S, U2-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

54

Mean

57.5

Std Dev

8.395

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

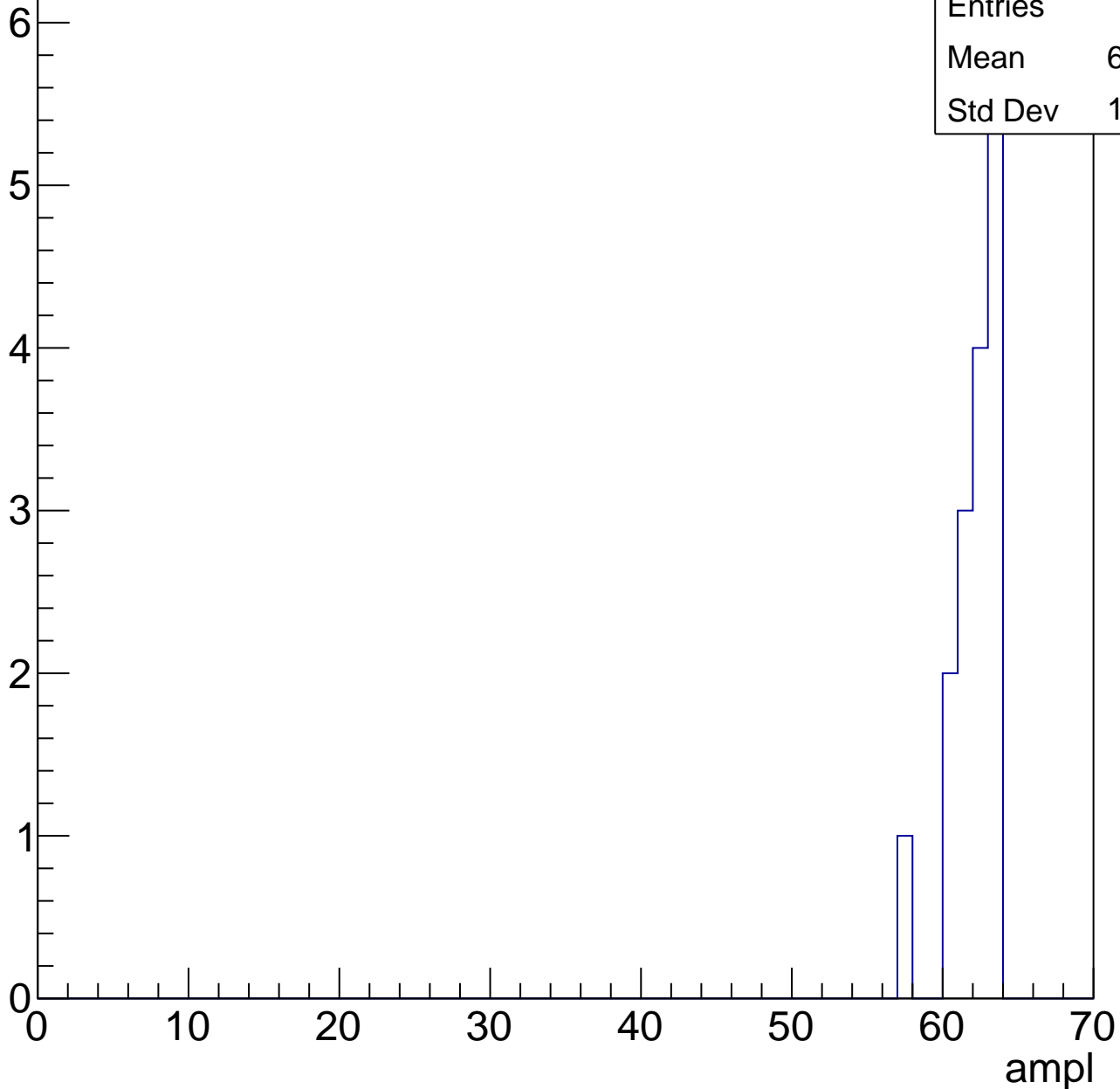
7

B1L102S, U2-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

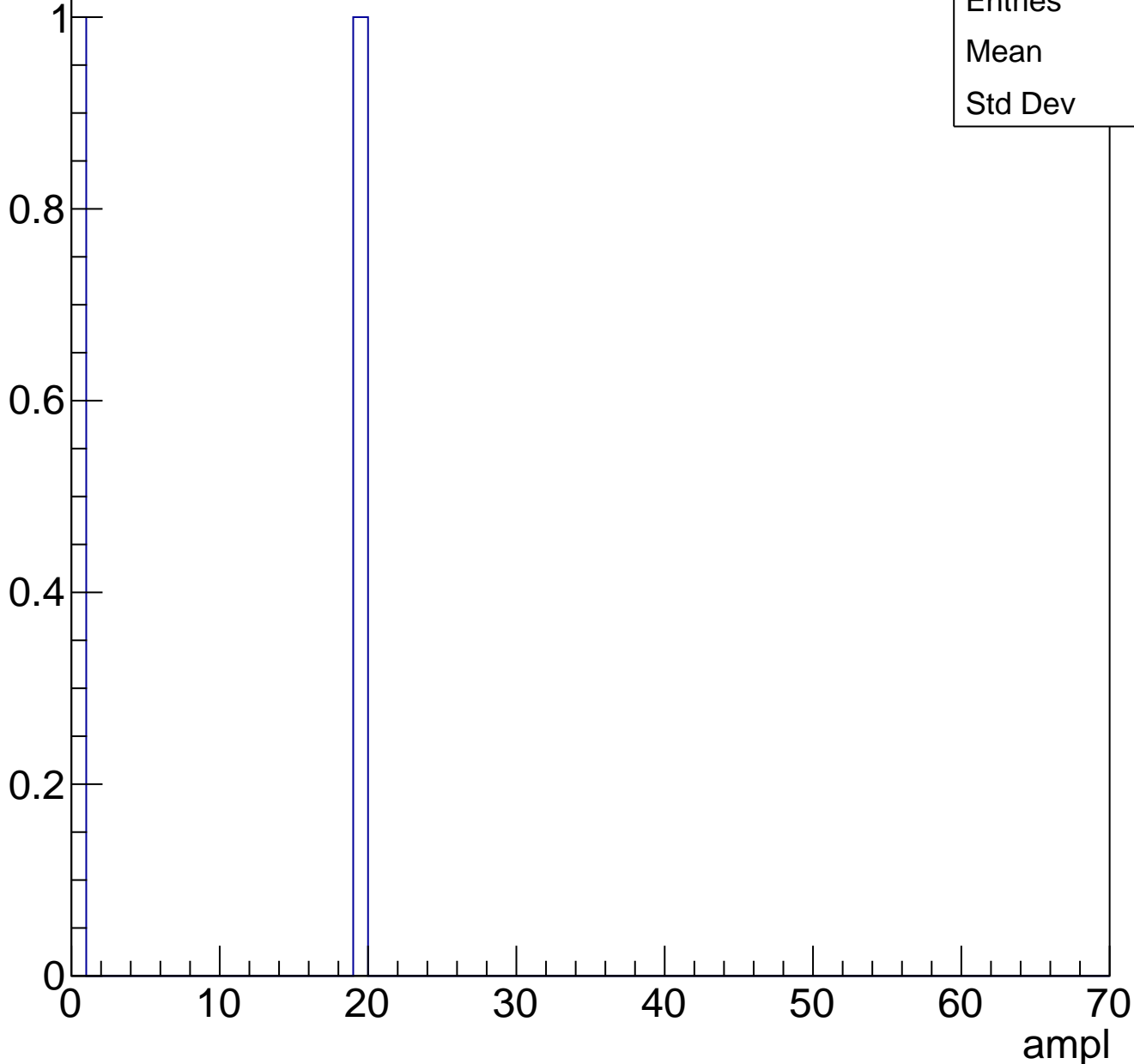
Entries	16
Mean	61.62
Std Dev	1.576



B1L102S, U2-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch89, adc0

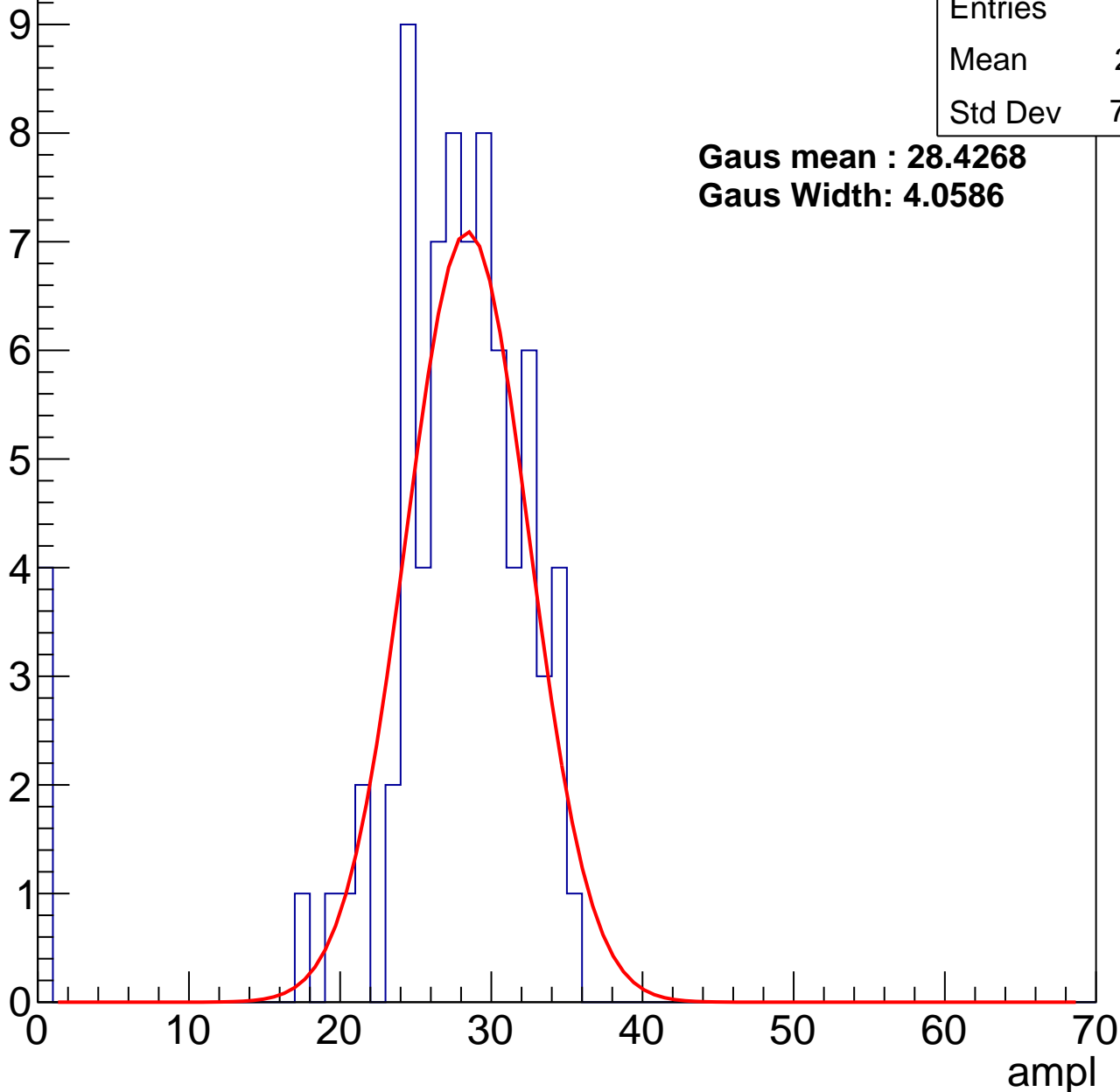
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	26.31
Std Dev	7.138

Gaus mean : 28.4268

Gaus Width: 4.0586



B1L102S, U2-ch89, adc1

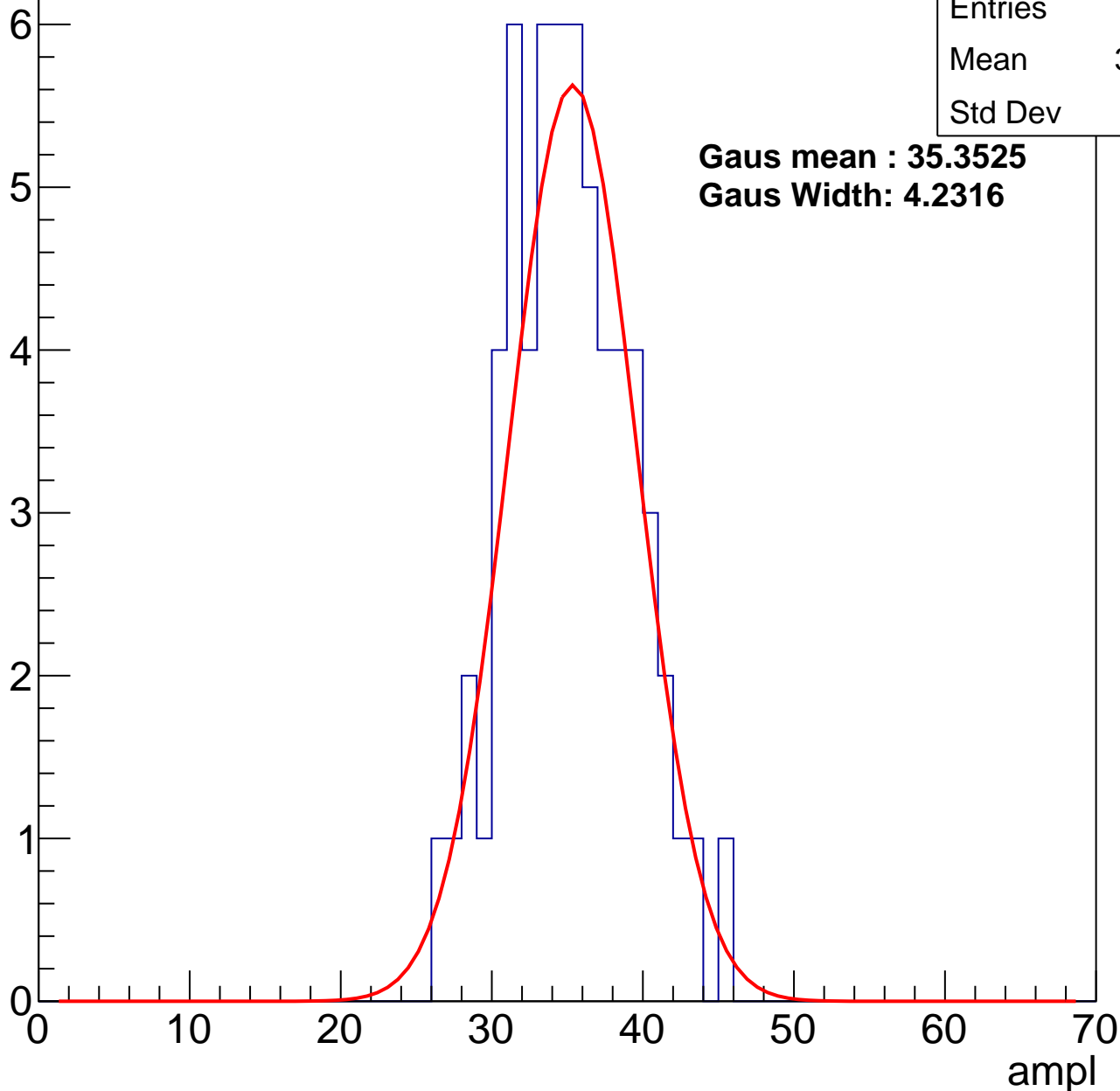
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.71
Std Dev	4.05

Gaus mean : 35.3525

Gaus Width: 4.2316



B1L102S, U2-ch89, adc2

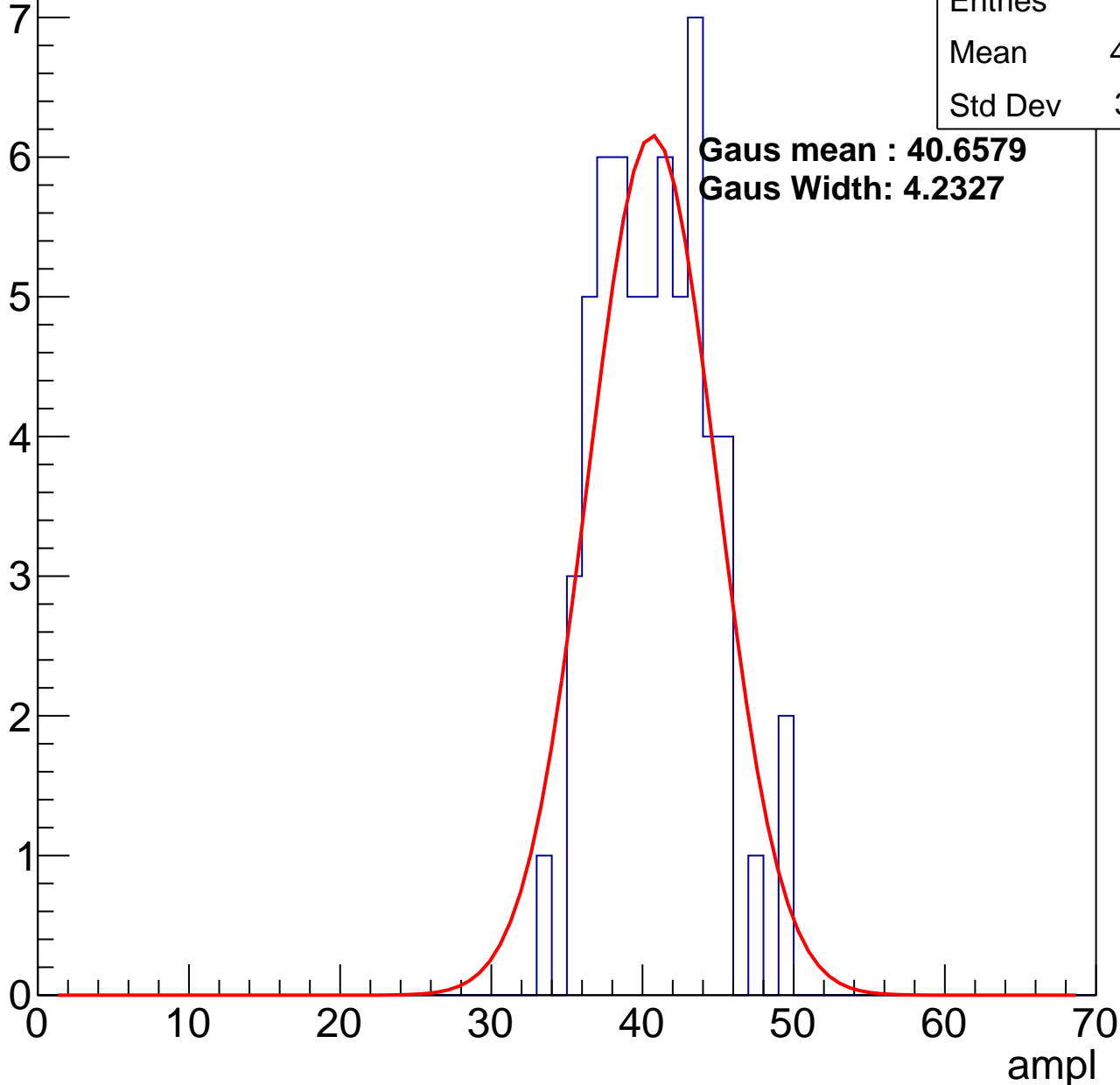
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.35
Std Dev	3.521

Gaus mean : 40.6579

Gaus Width: 4.2327

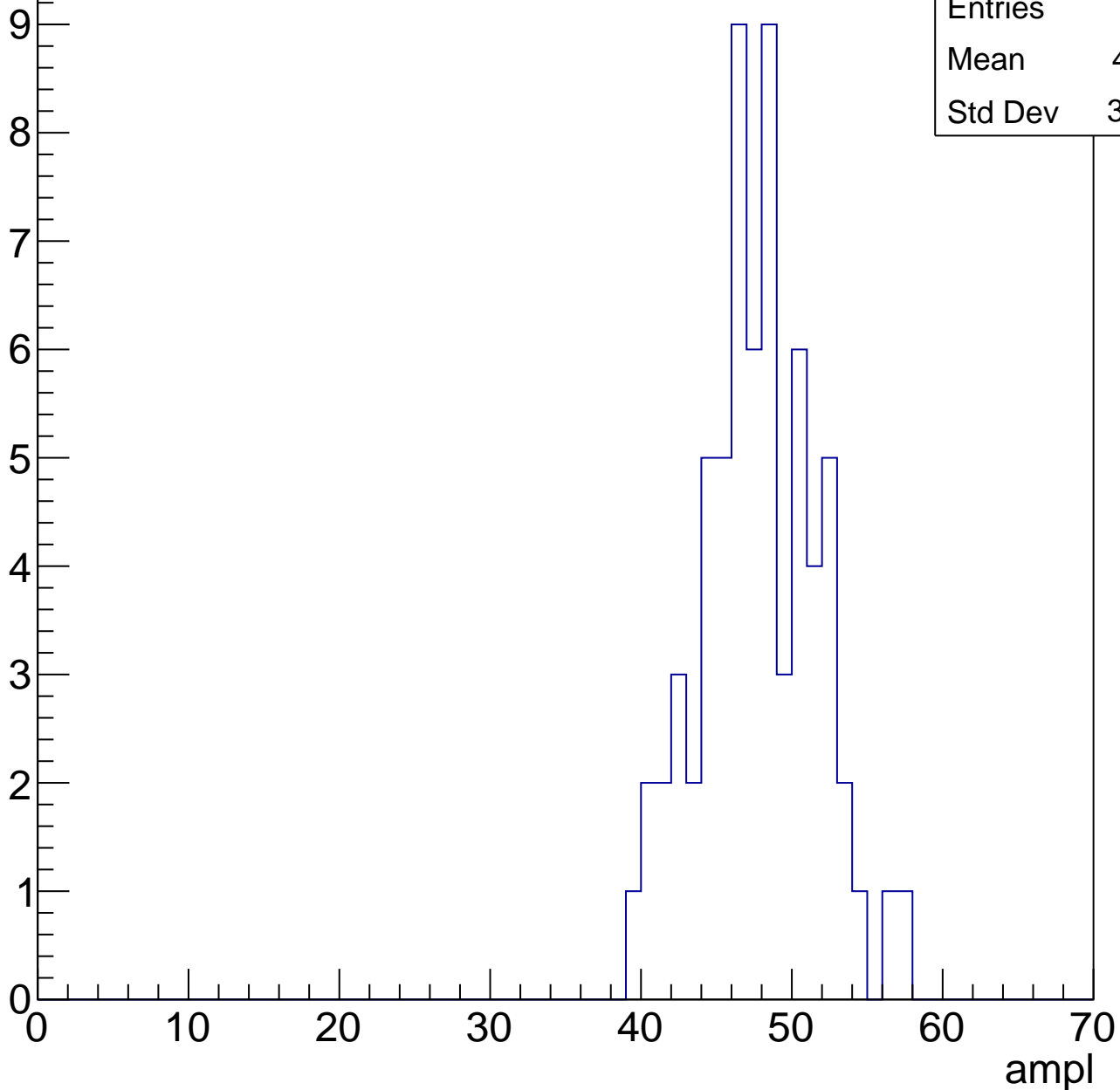


B1L102S, U2-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

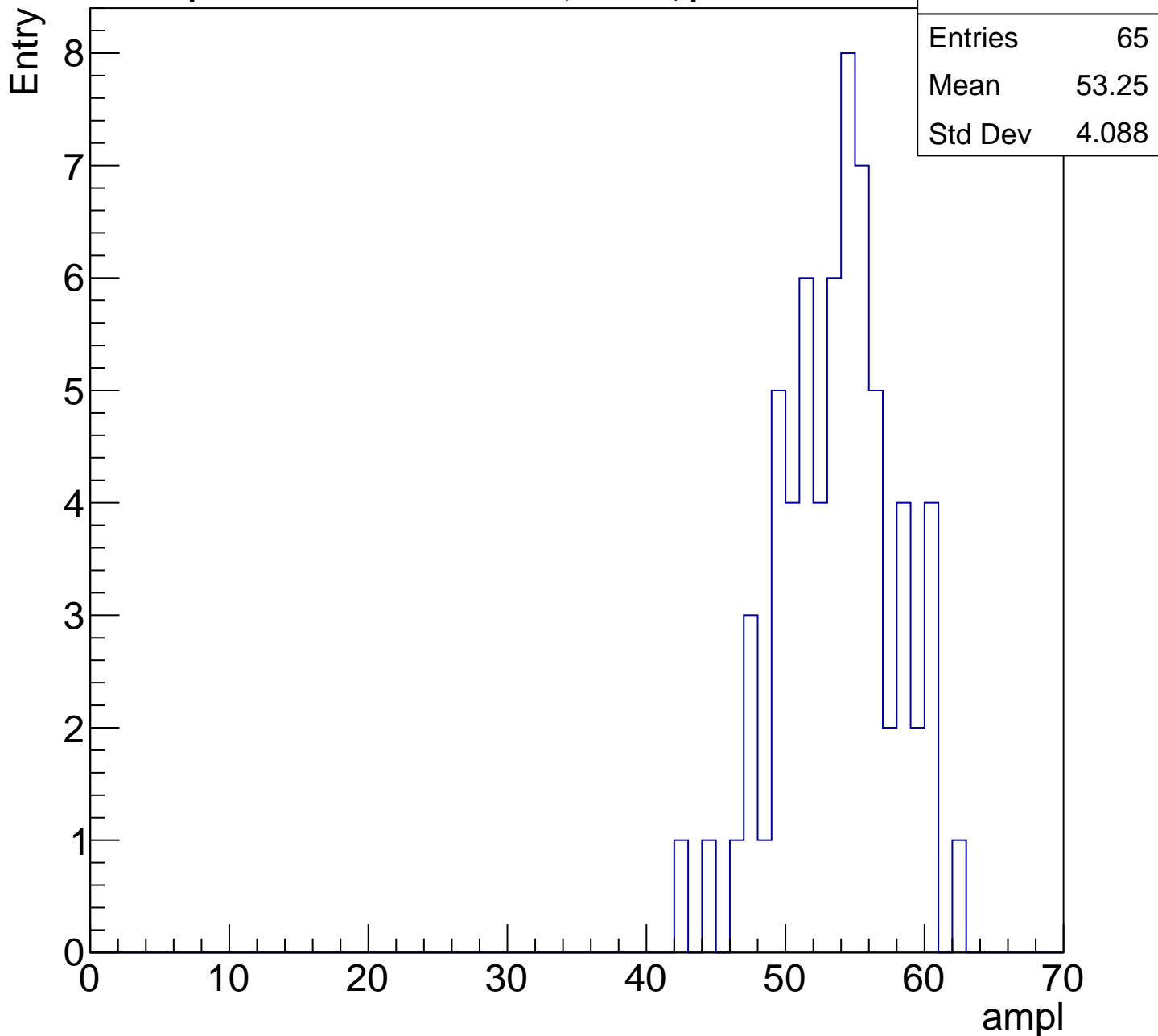
Entry

Entries	67
Mean	47.31
Std Dev	3.814



B1L102S, U2-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U2-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 54

Mean 57.76

Std Dev 8.43

ampl

0

10

20

30

40

50

60

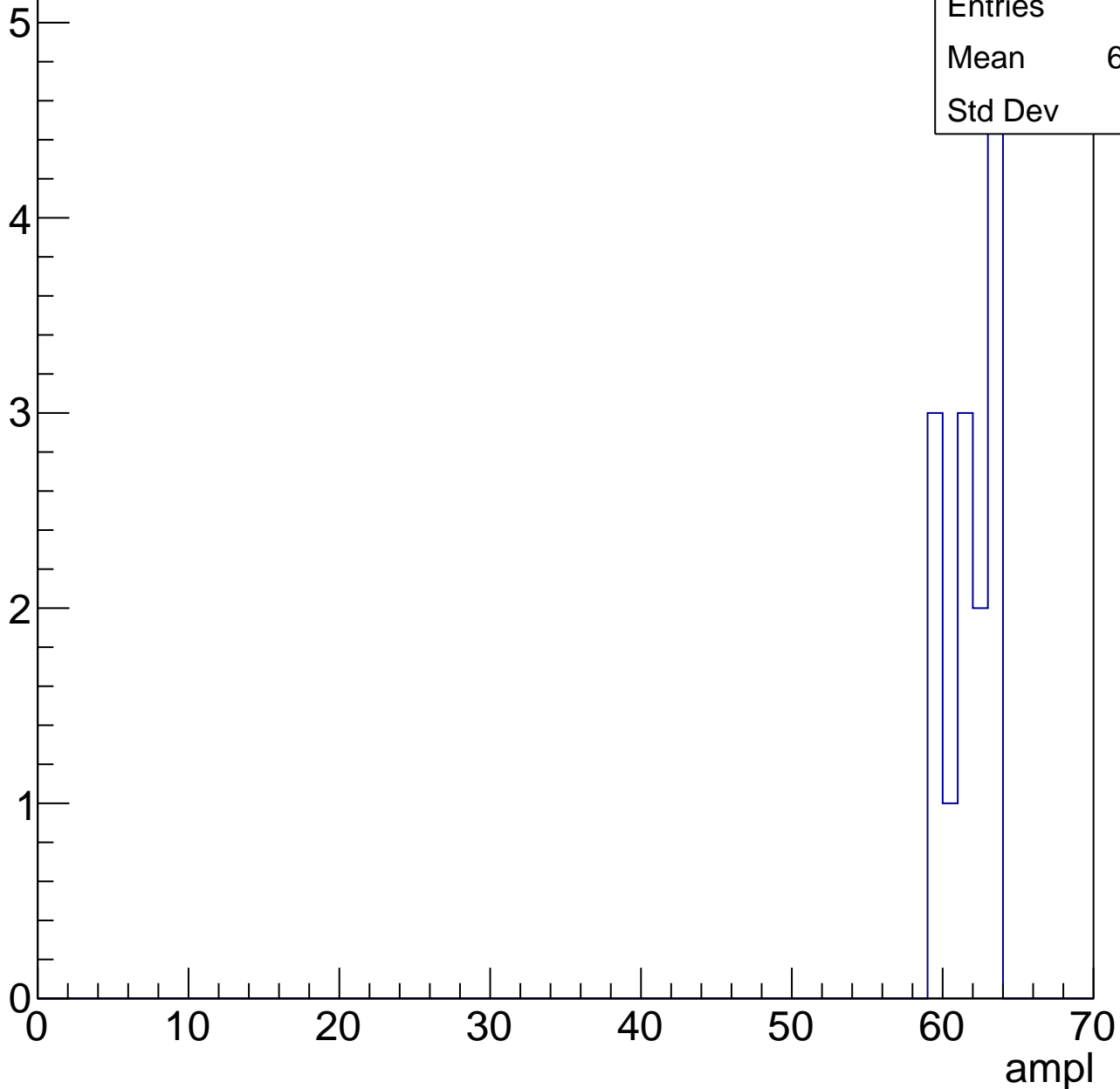
70

B1L102S, U2-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.36
Std Dev	1.54



B1L102S, U2-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch90, adc0

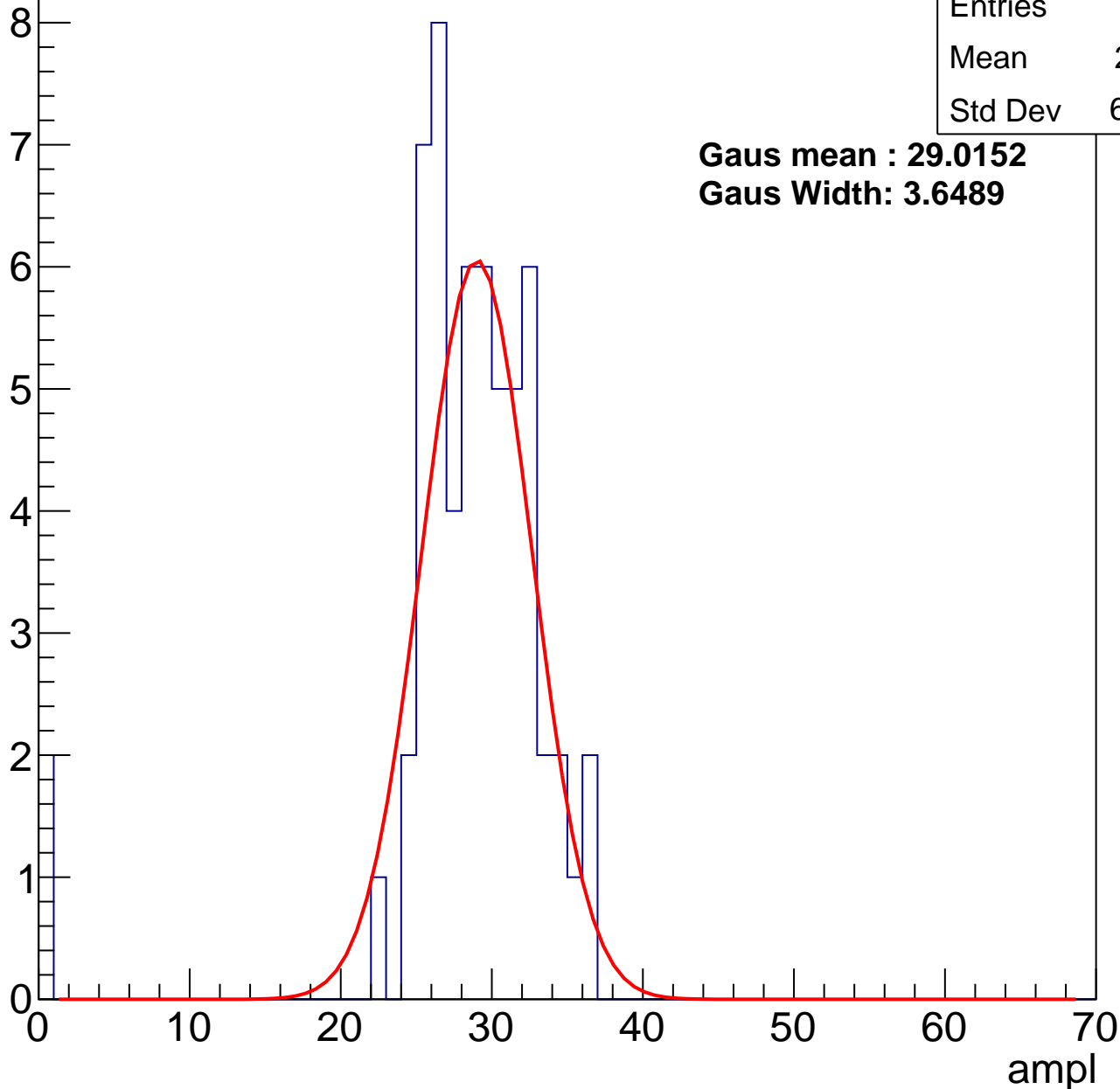
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.81
Std Dev	6.105

Gaus mean : 29.0152

Gaus Width: 3.6489



B1L102S, U2-ch90, adc1

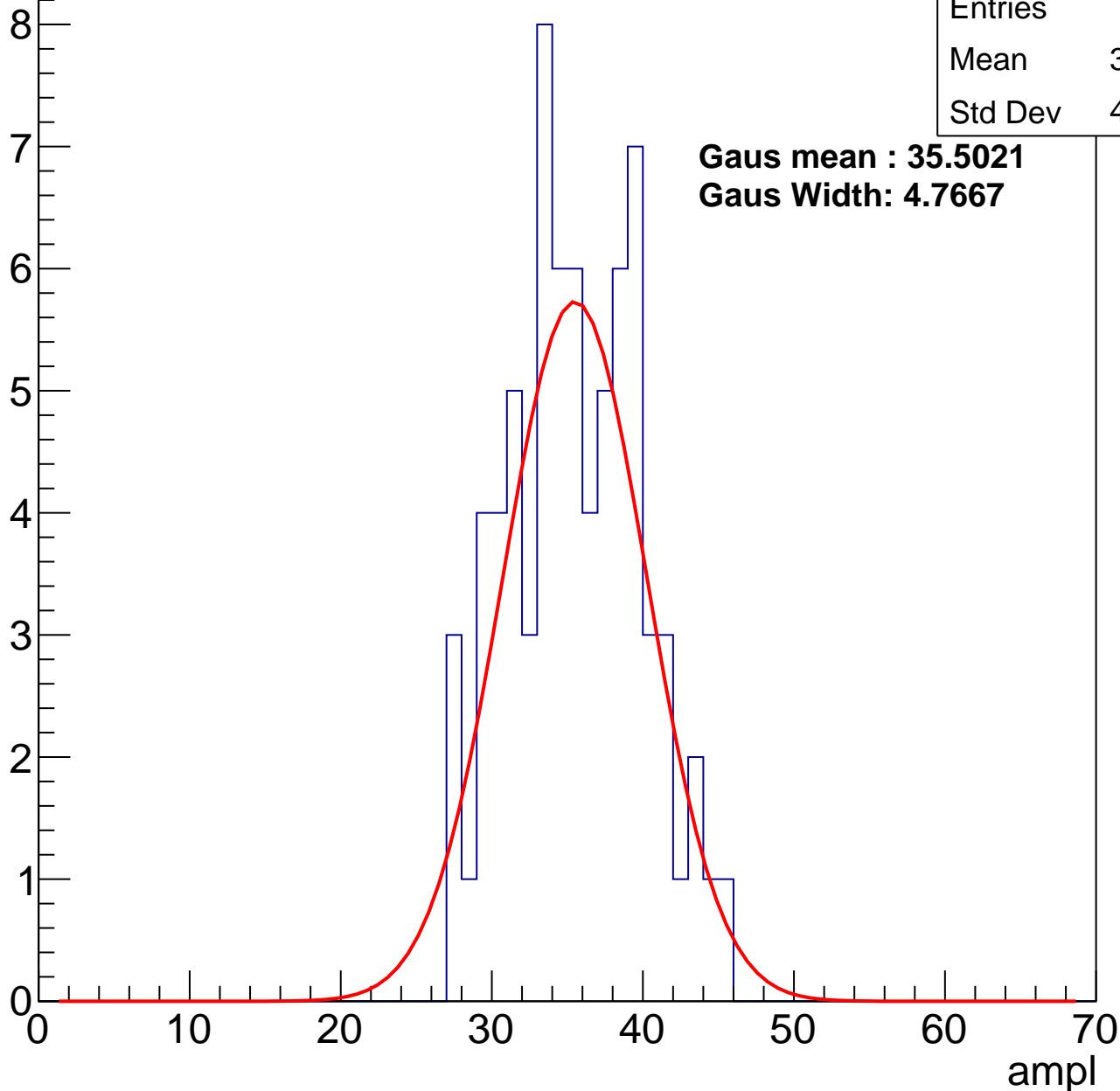
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.12
Std Dev	4.316

Gaus mean : 35.5021

Gaus Width: 4.7667



B1L102S, U2-ch90, adc2

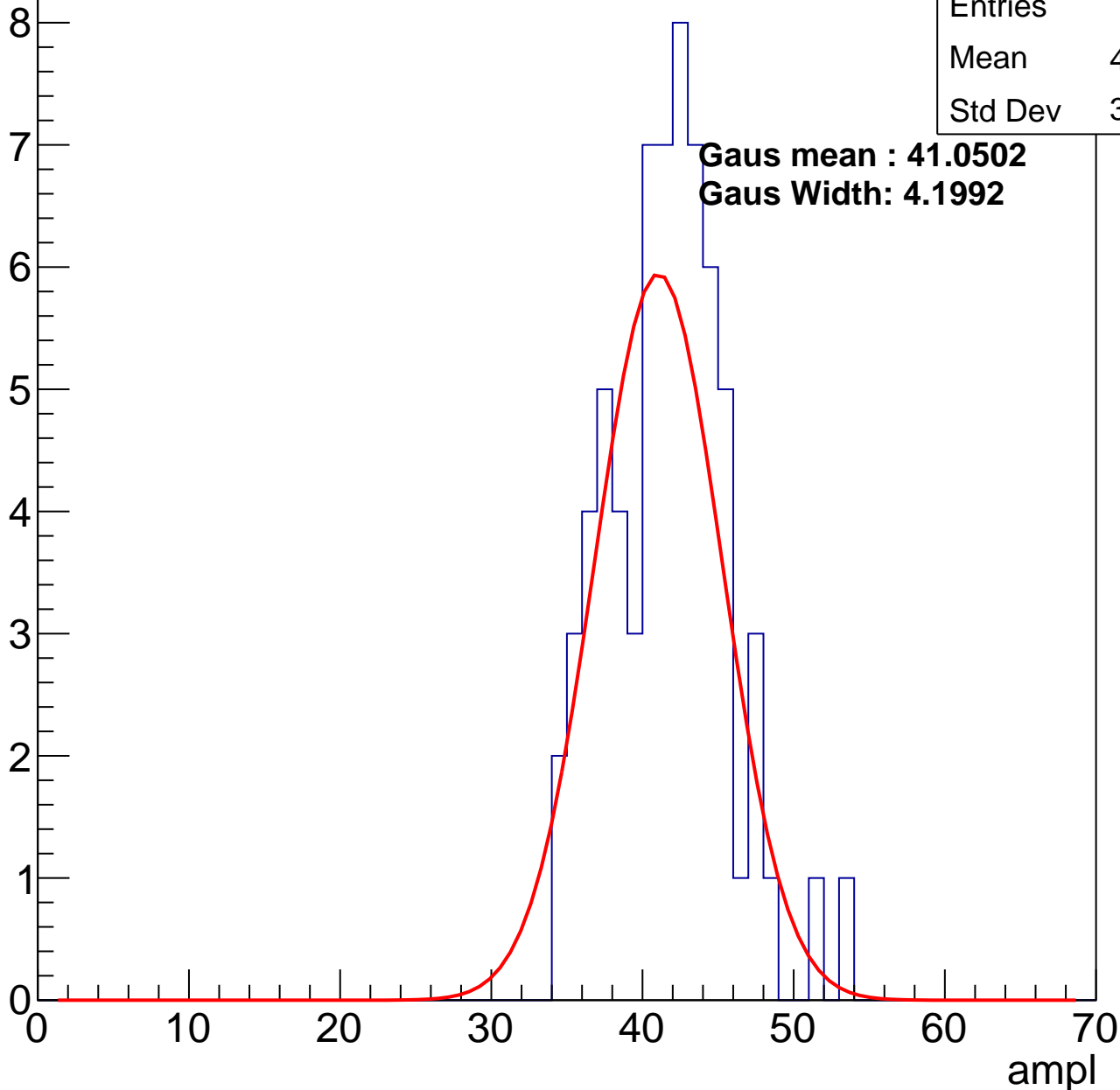
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.22
Std Dev	3.899

Gaus mean : 41.0502

Gaus Width: 4.1992

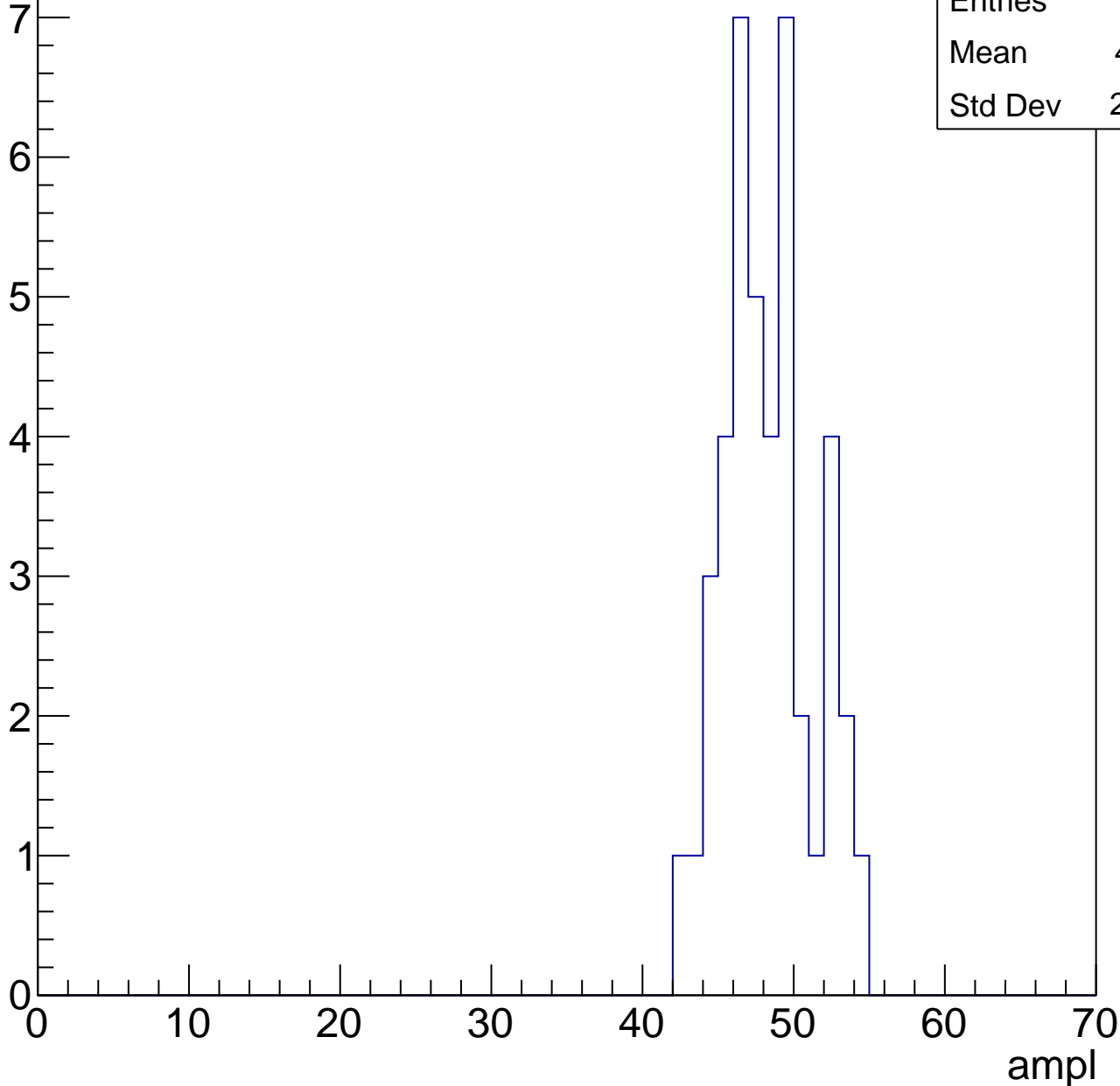


B1L102S, U2-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

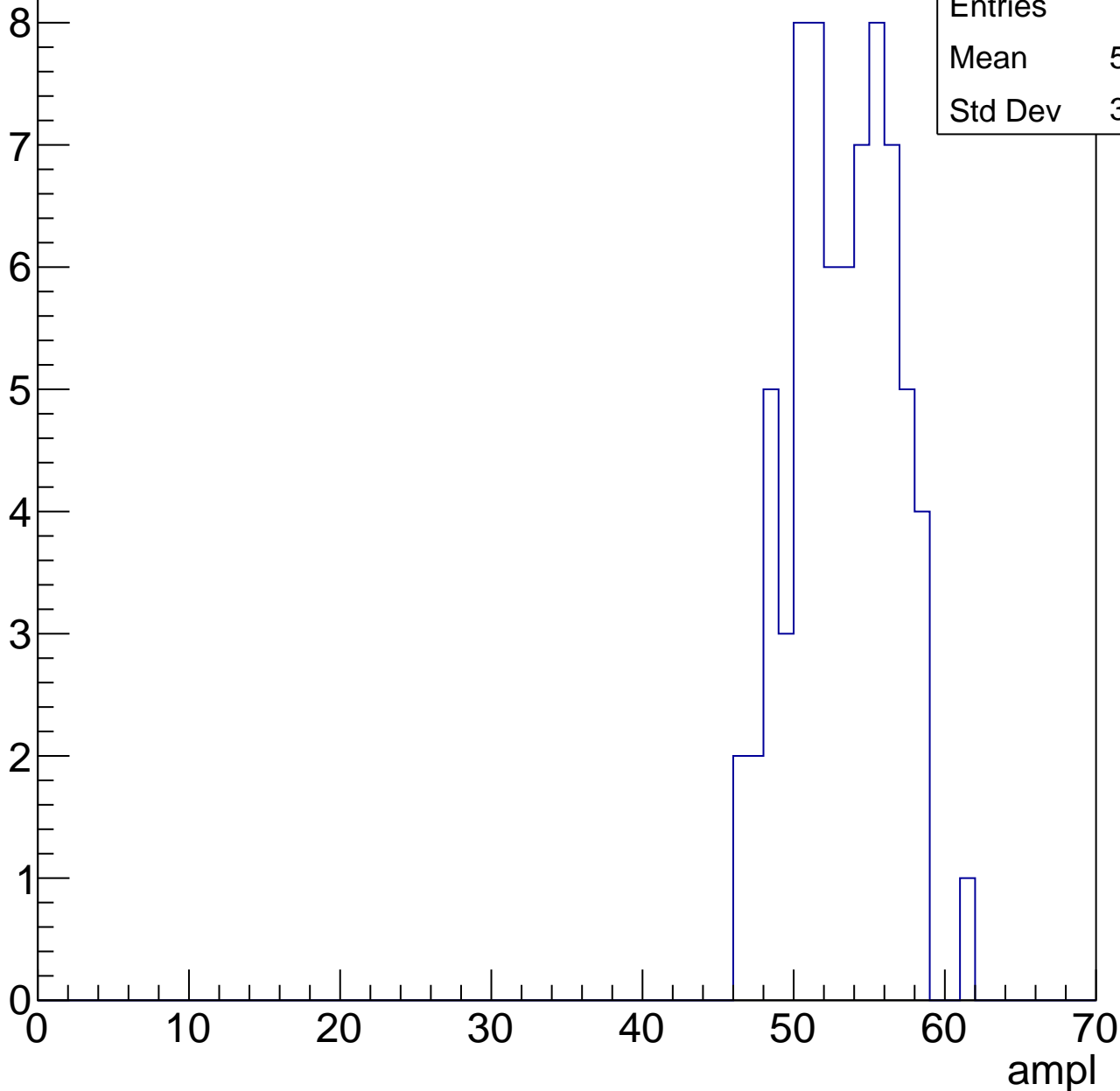
Entries	42
Mean	47.81
Std Dev	2.889



B1L102S, U2-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

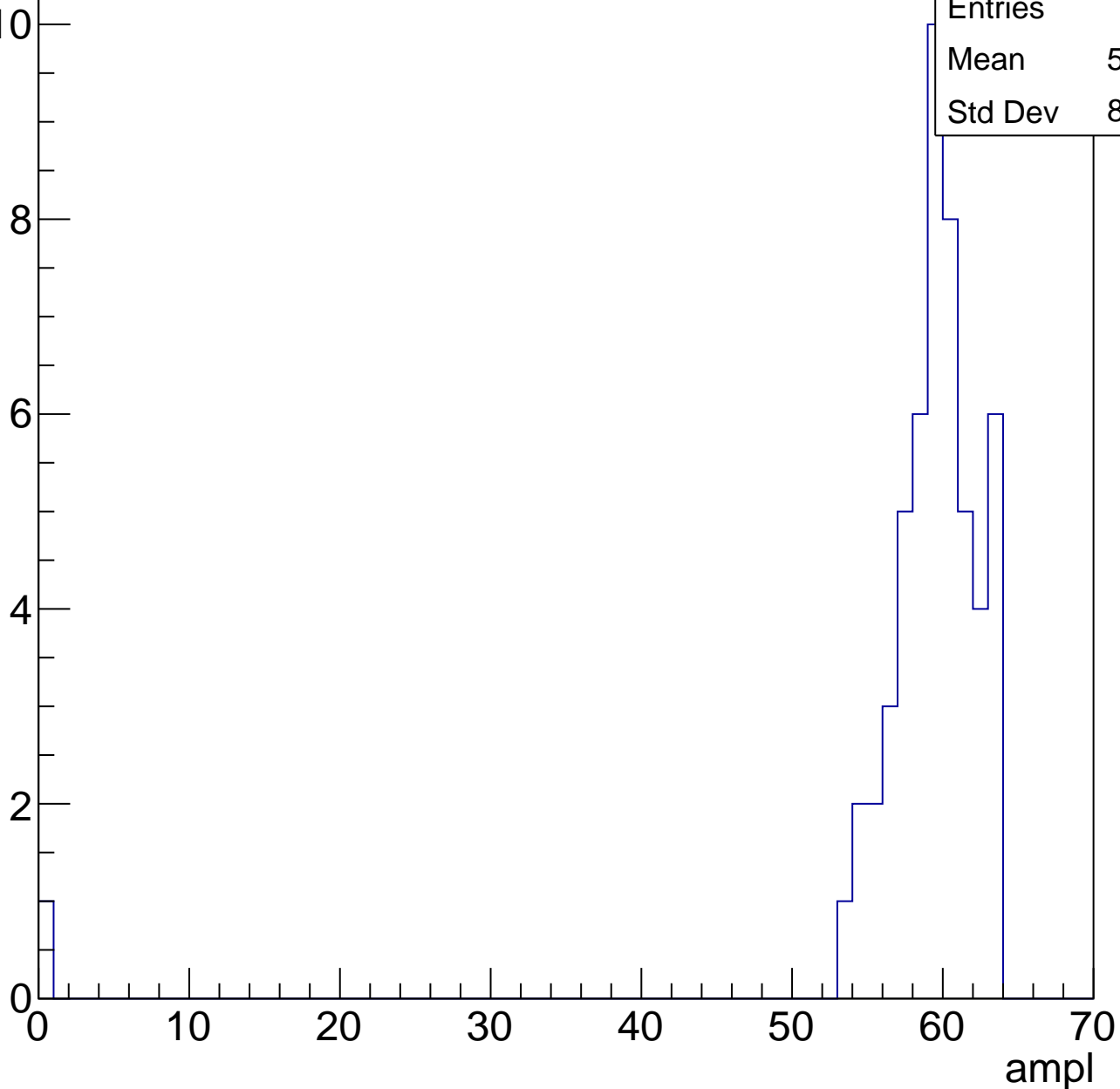


B1L102S, U2-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.98
Std Dev	8.419

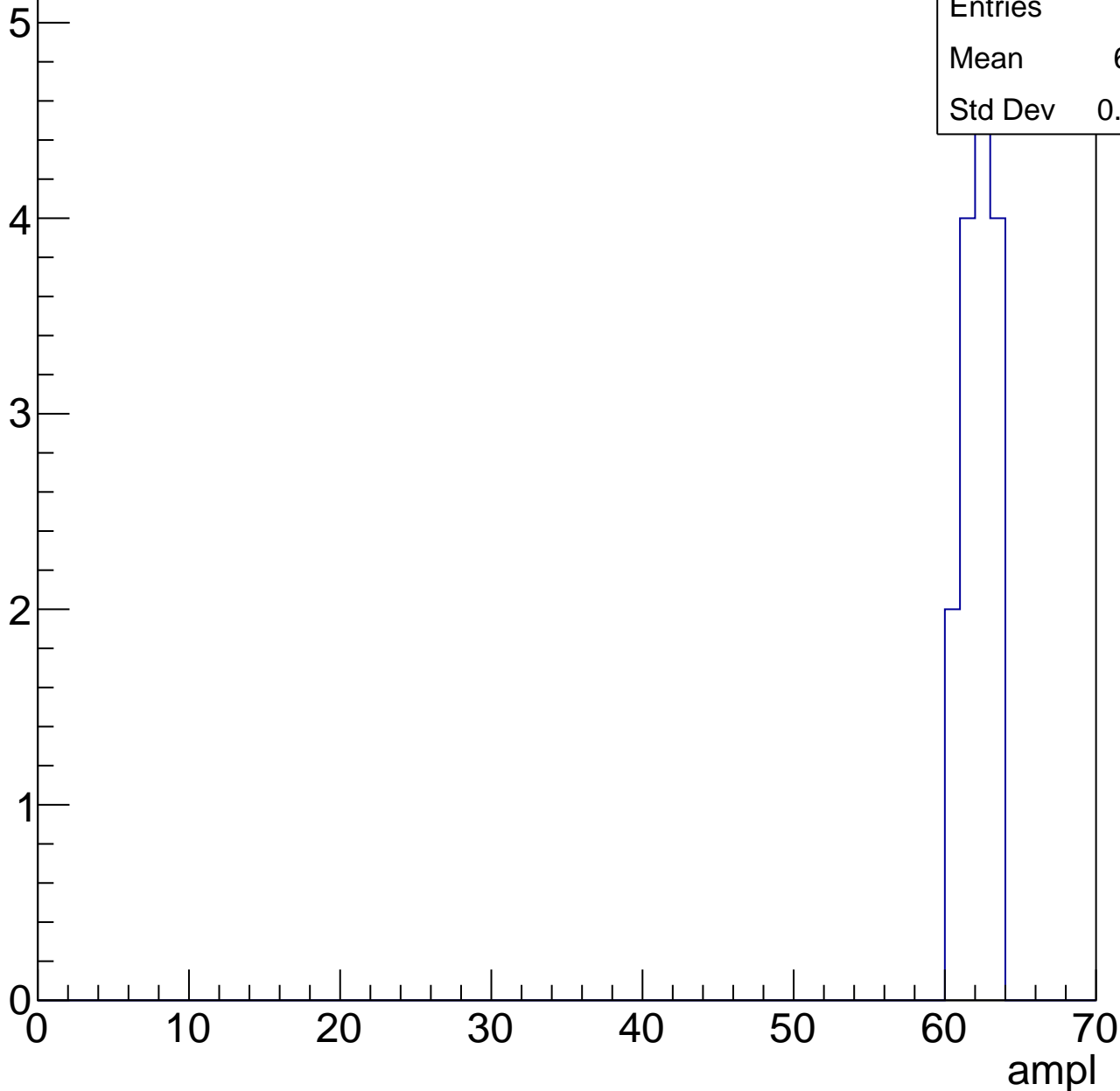


B1L102S, U2-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.73
Std Dev	0.9978



B1L102S, U2-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch91, adc0

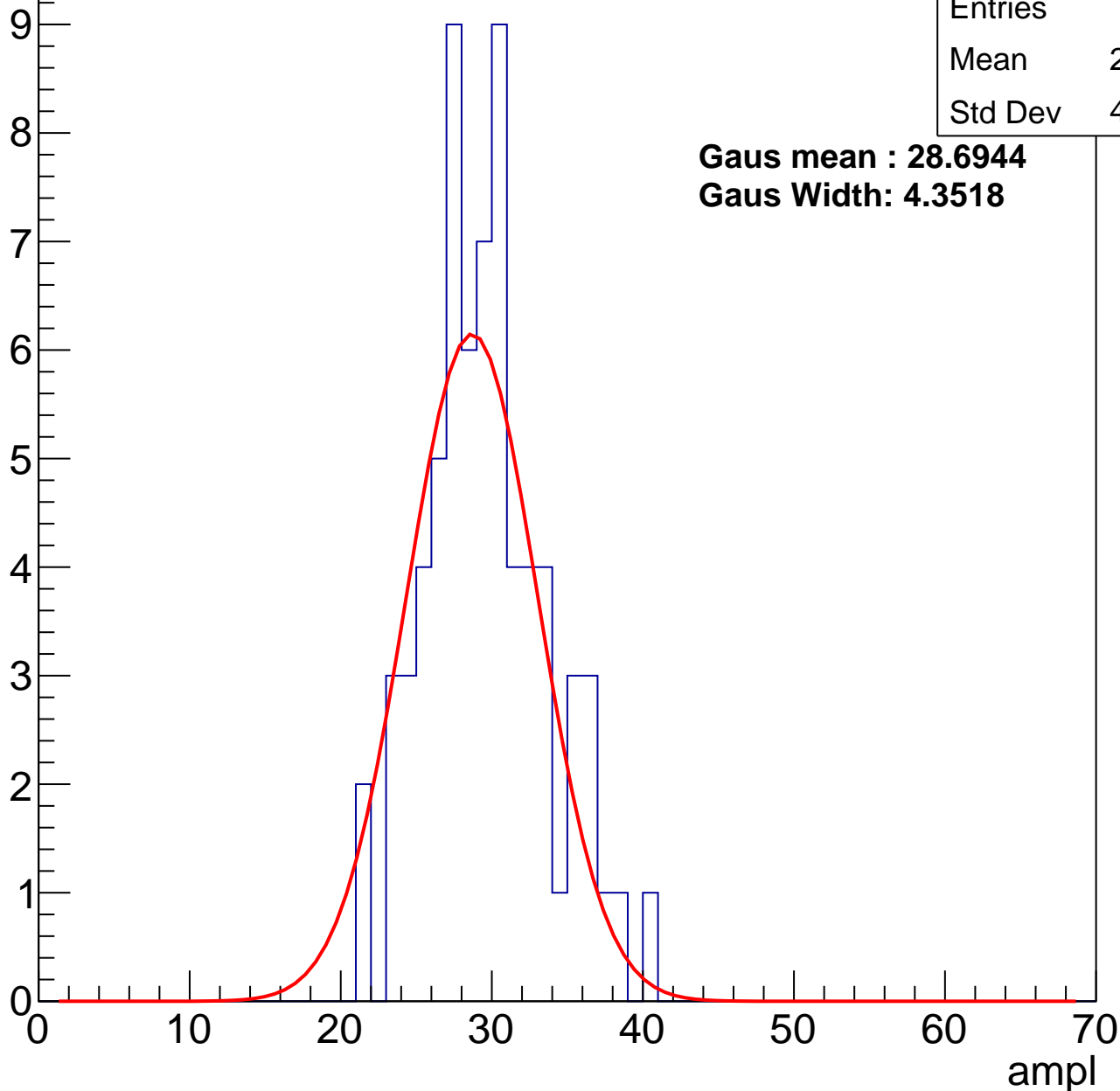
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	29.19
Std Dev	4.019

Gaus mean : 28.6944

Gaus Width: 4.3518



B1L102S, U2-ch91, adc1

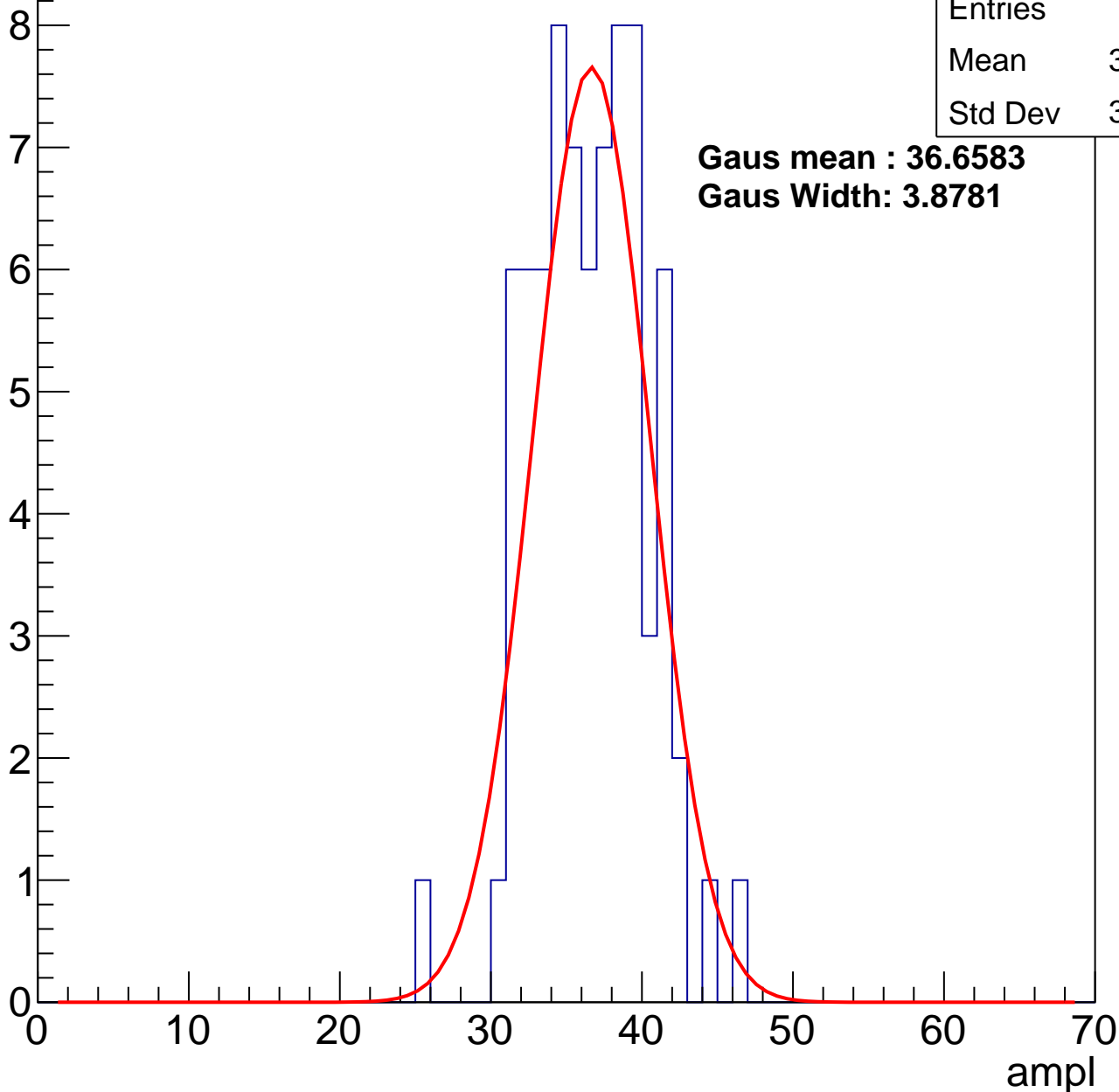
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.09
Std Dev	3.676

Gaus mean : 36.6583

Gaus Width: 3.8781



B1L102S, U2-ch91, adc2

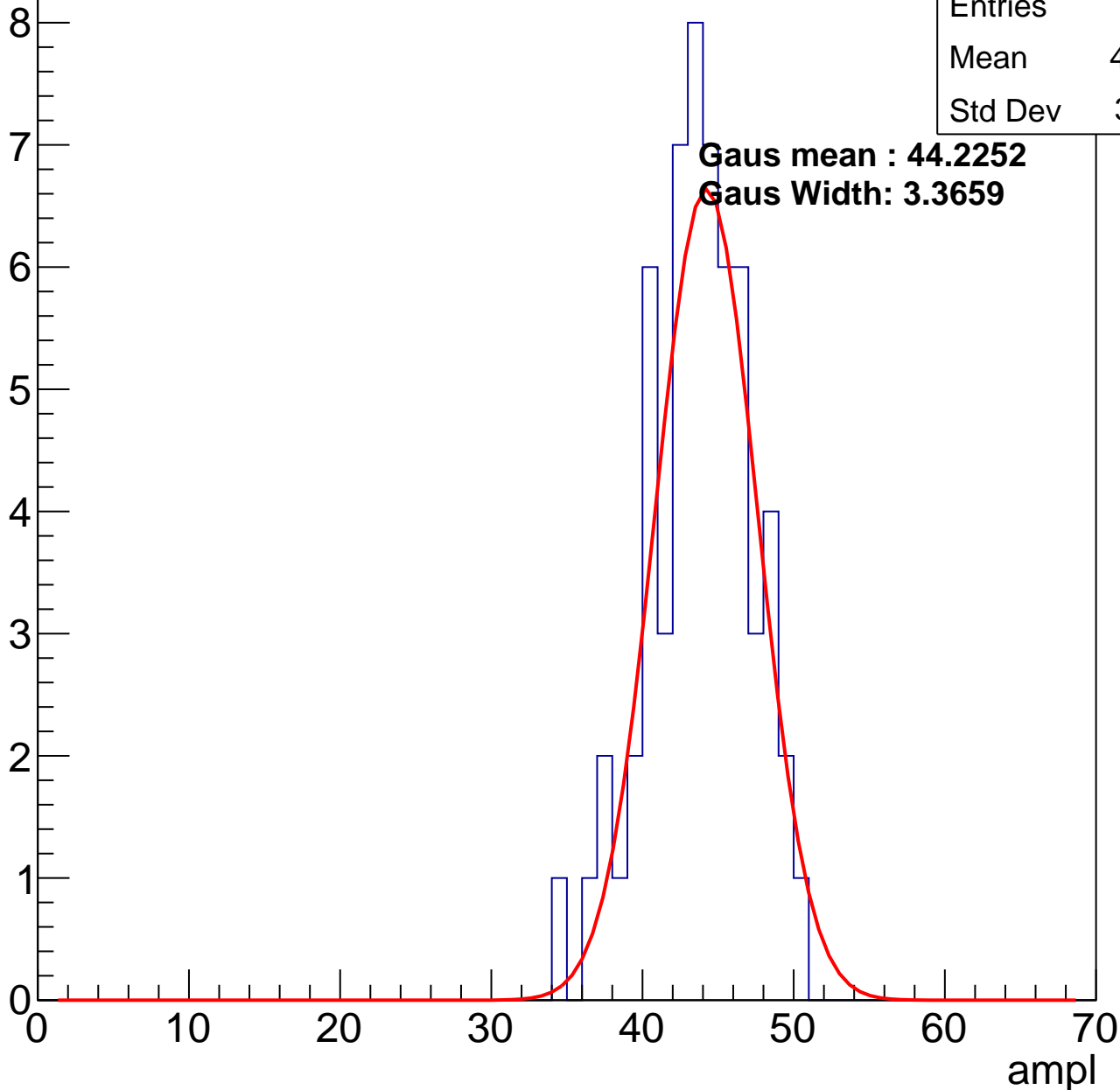
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	43.27
Std Dev	3.371

Gaus mean : 44.2252

Gaus Width: 3.3659

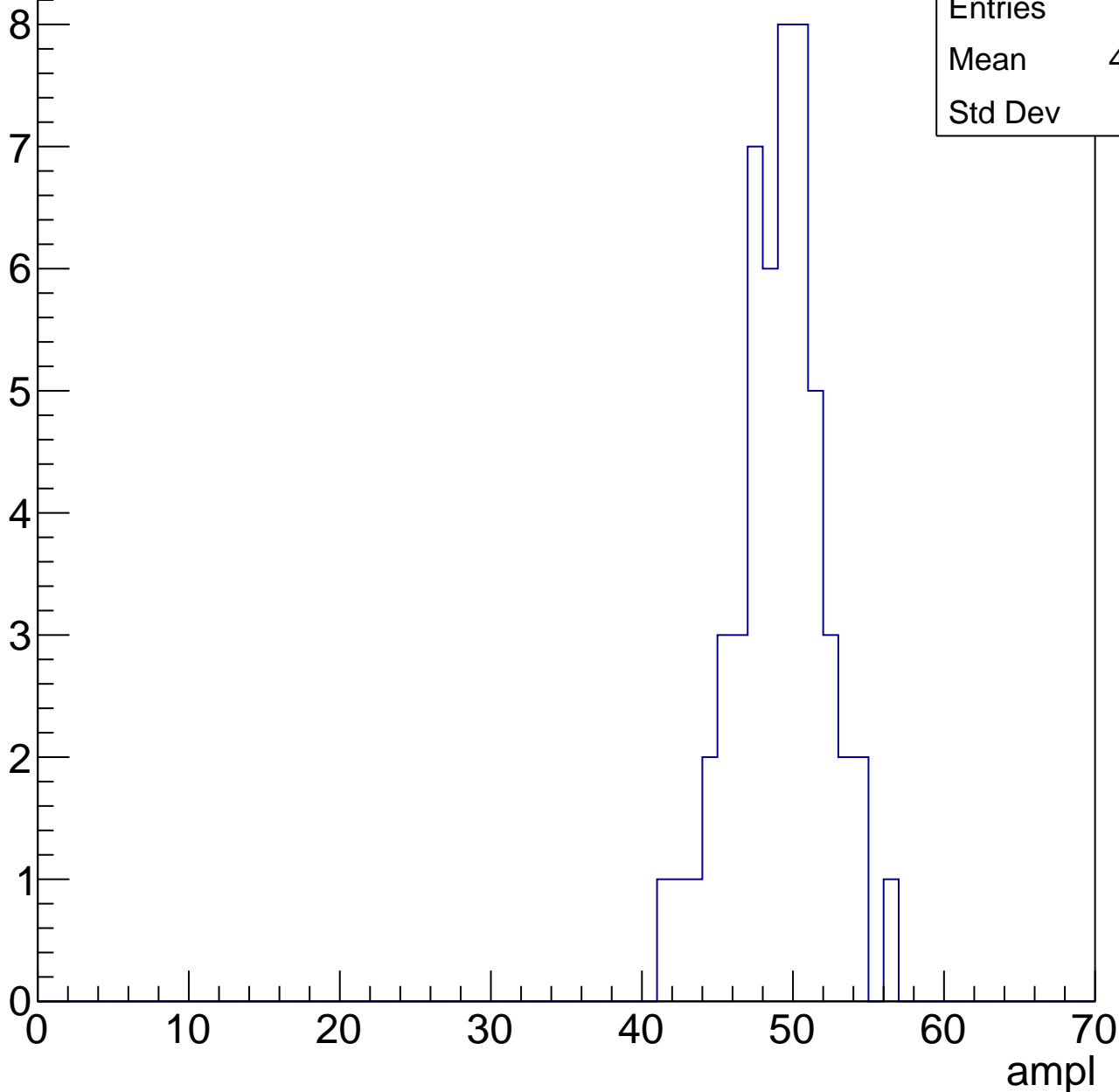


B1L102S, U2-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.62
Std Dev	3.03



B1L102S, U2-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

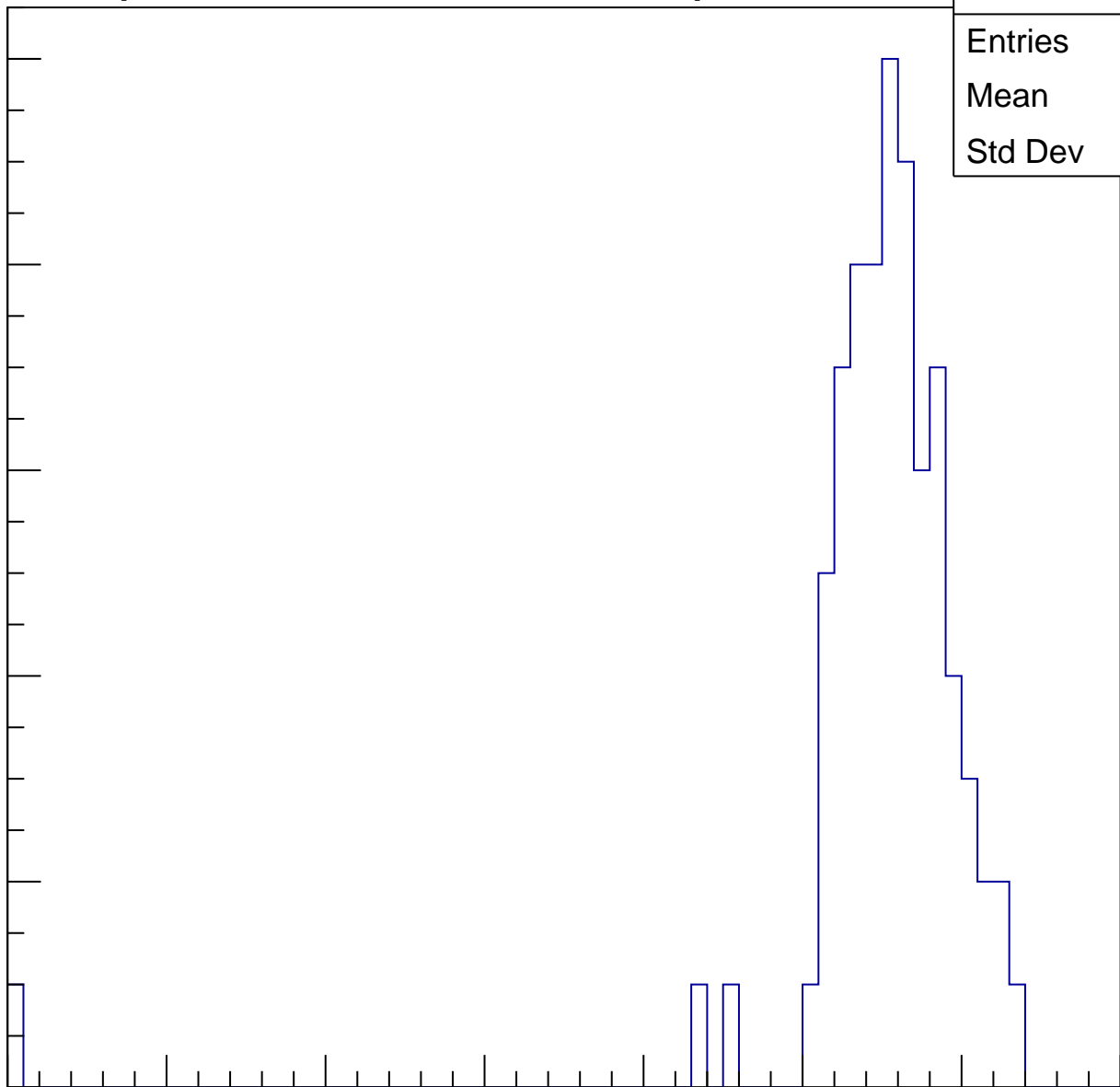
Entries	76
Mean	54.47
Std Dev	7.186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch91, adc5

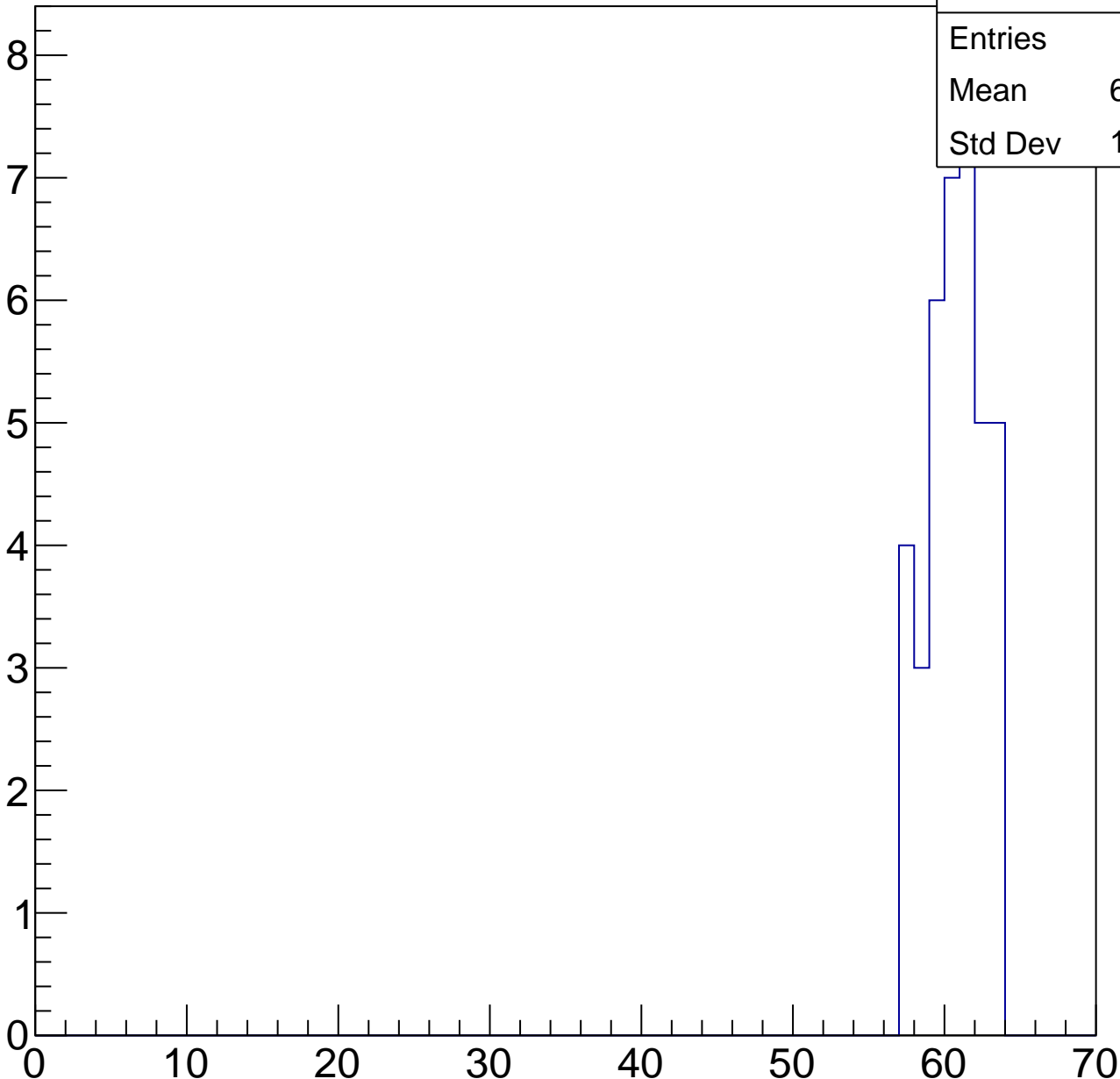
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.24
Std Dev	1.813

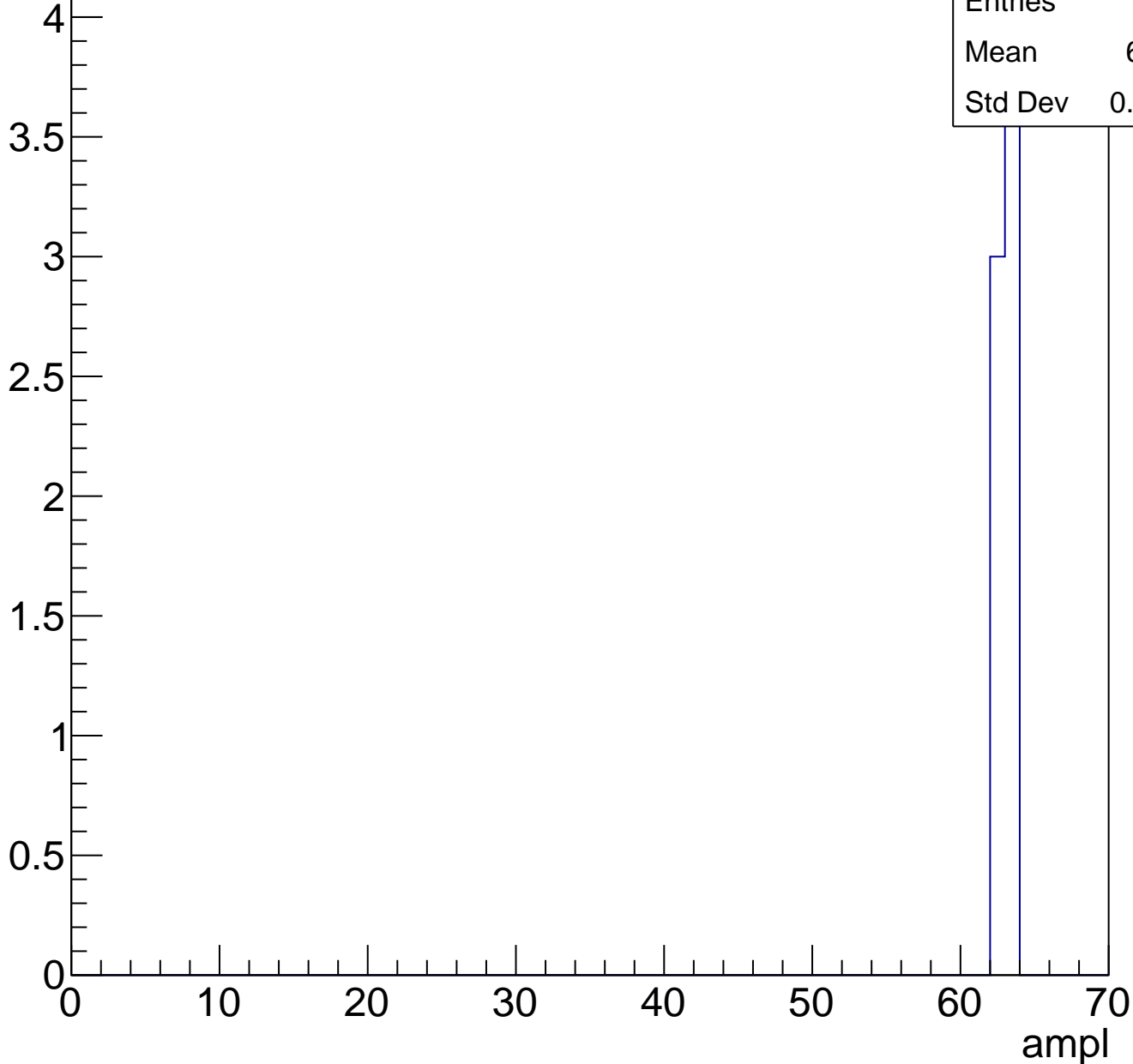
ampl



B1L102S, U2-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch92, adc0

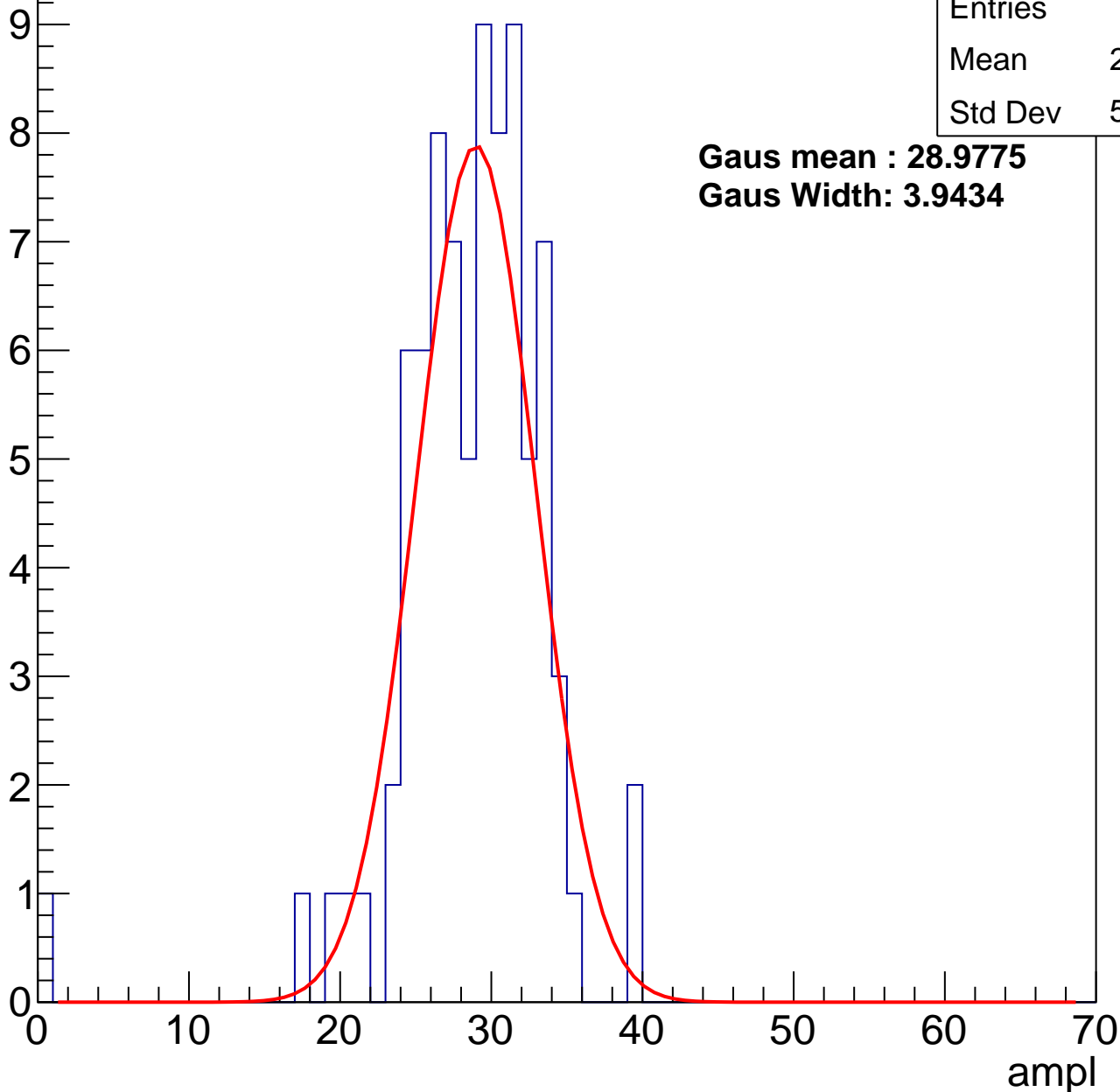
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.19
Std Dev	5.043

Gaus mean : 28.9775

Gaus Width: 3.9434



B1L102S, U2-ch92, adc1

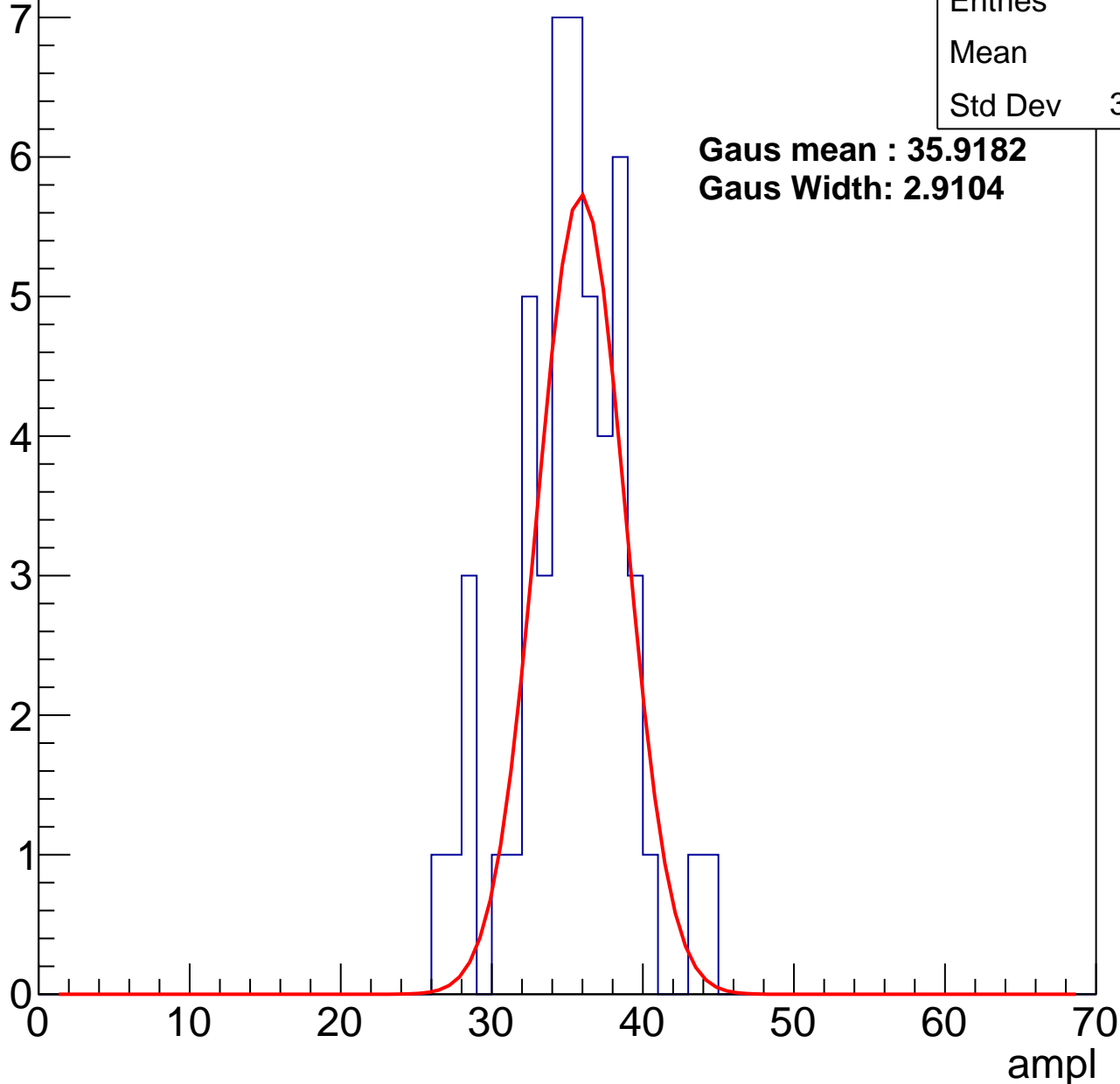
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	34.8
Std Dev	3.709

Gaus mean : 35.9182

Gaus Width: 2.9104



B1L102S, U2-ch92, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	41.55
Std Dev	4.169

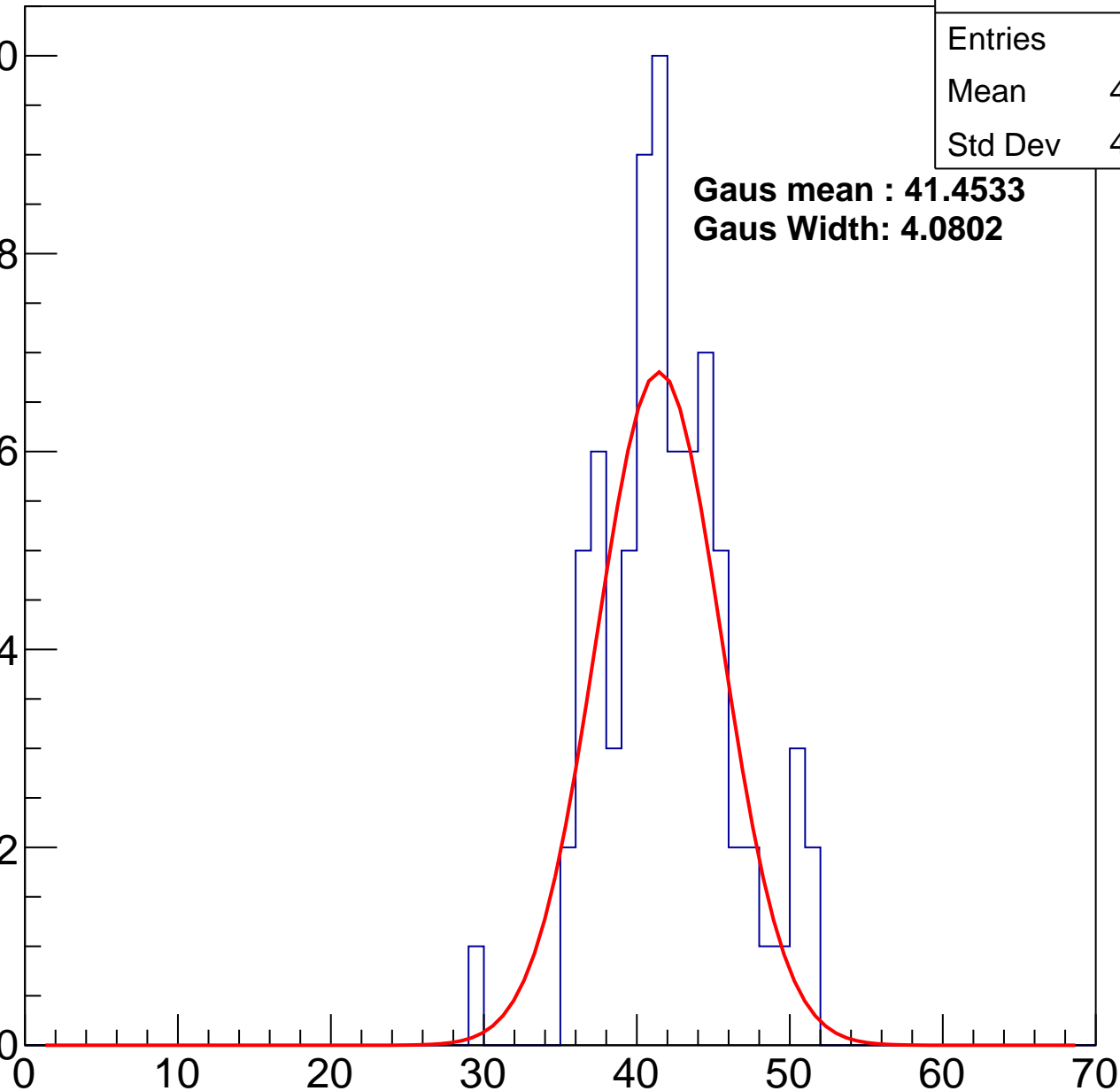
Gaus mean : 41.4533

Gaus Width: 4.0802

Entry

10
8
6
4
2
0

ampl

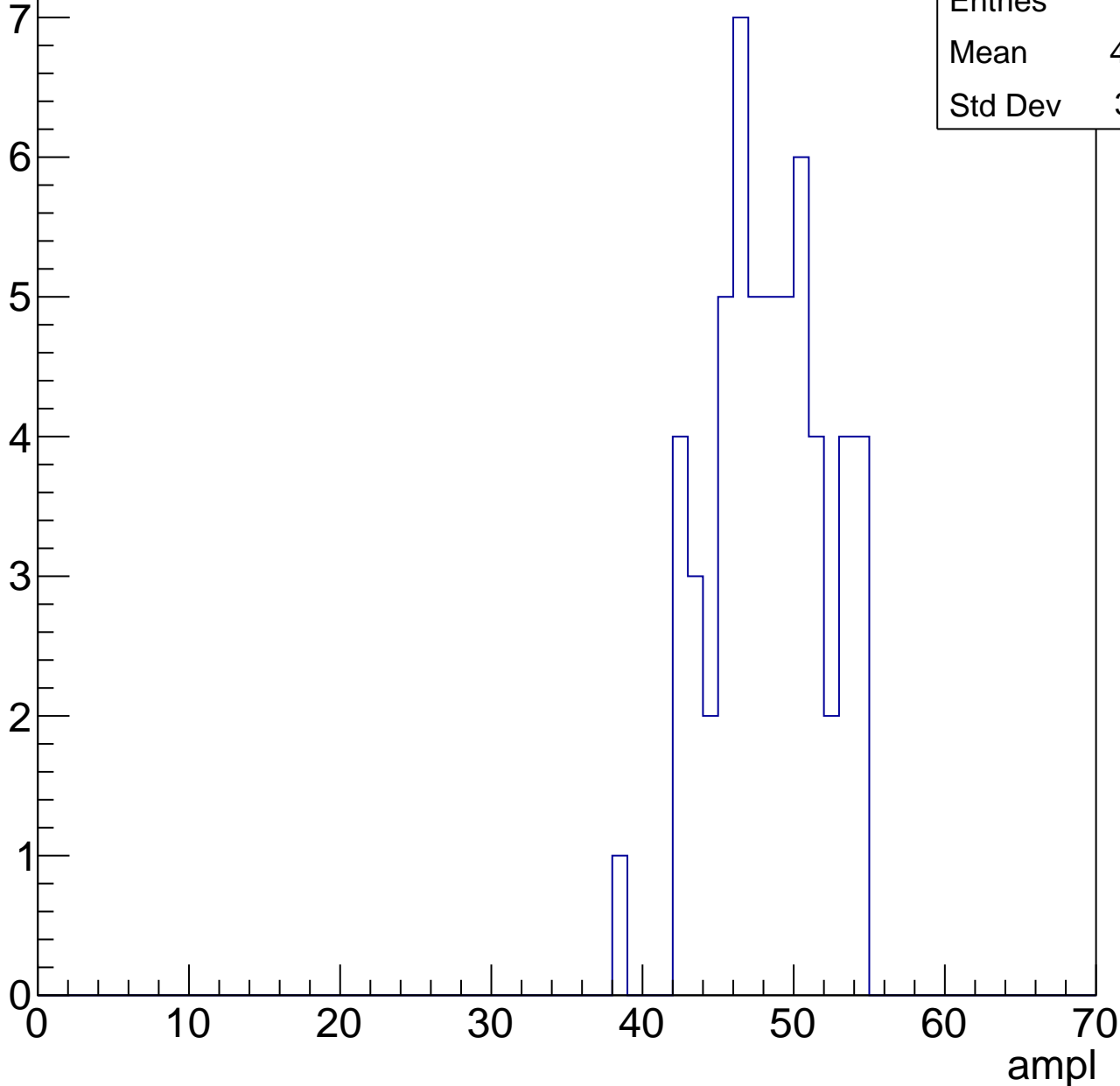


B1L102S, U2-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.82
Std Dev	3.671

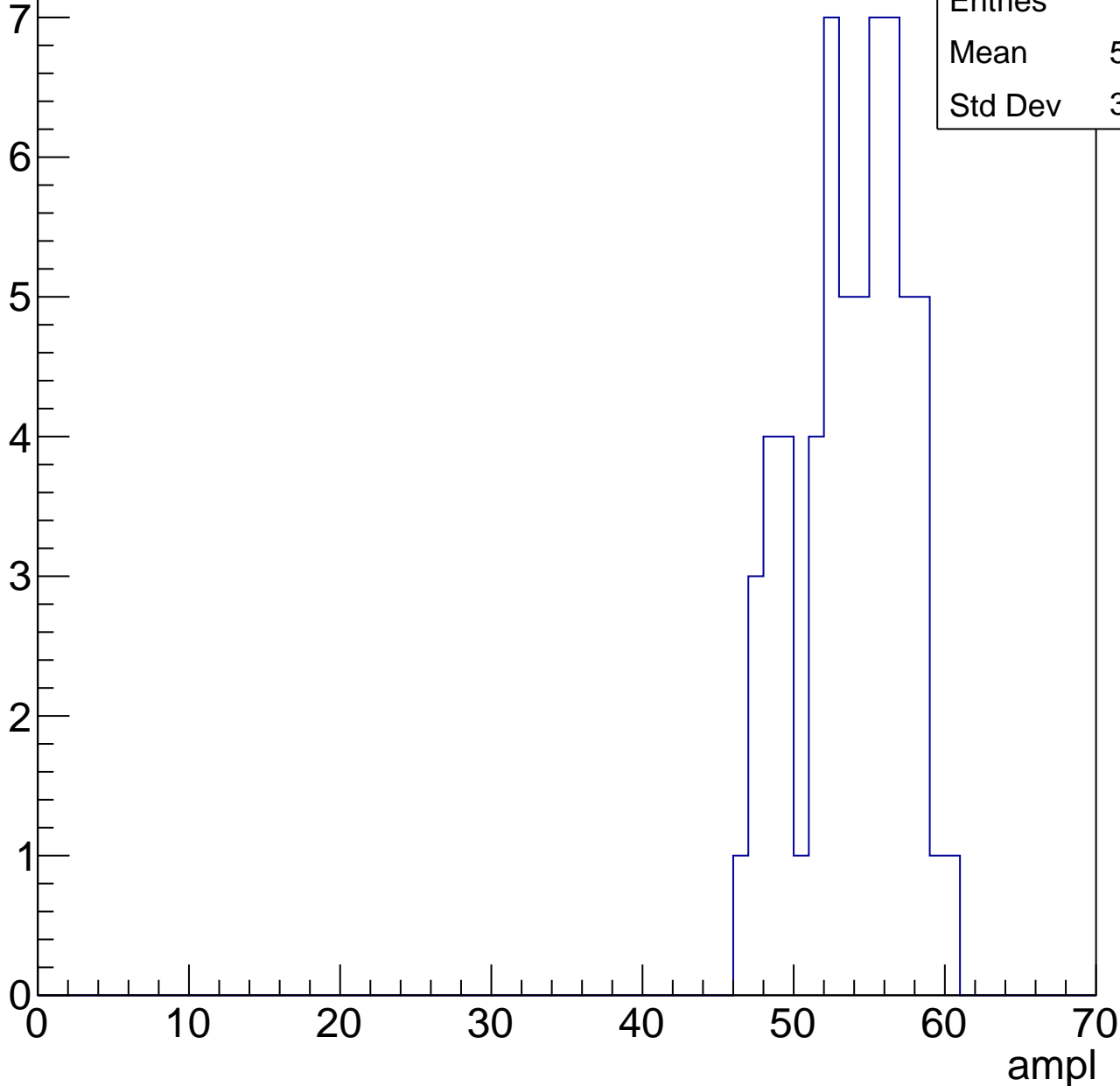


B1L102S, U2-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.32
Std Dev	3.495

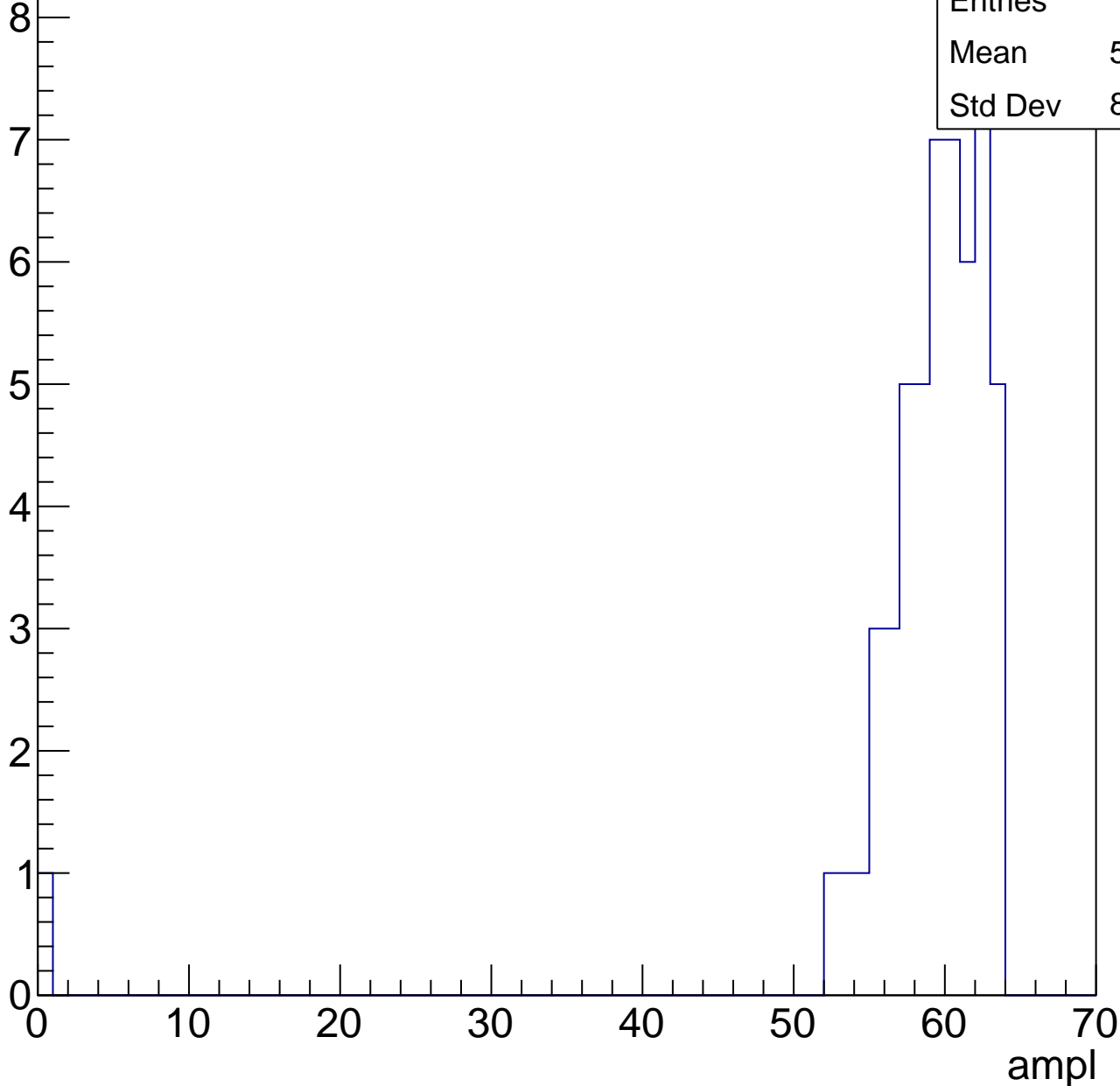


B1L102S, U2-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.06
Std Dev	8.497

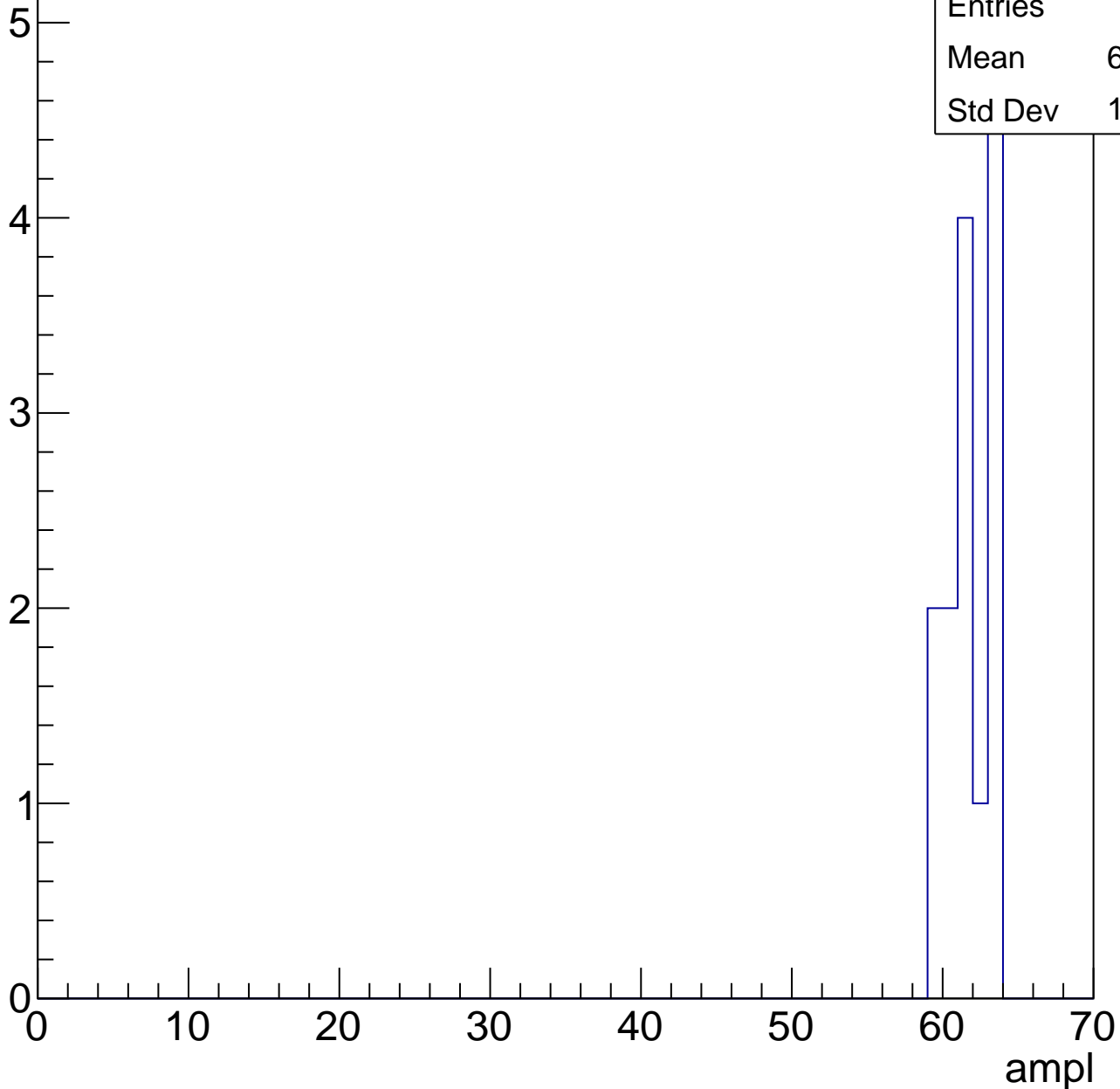


B1L102S, U2-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.36
Std Dev	1.445



B1L102S, U2-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	62
Std Dev	0

B1L102S, U2-ch93, adc0

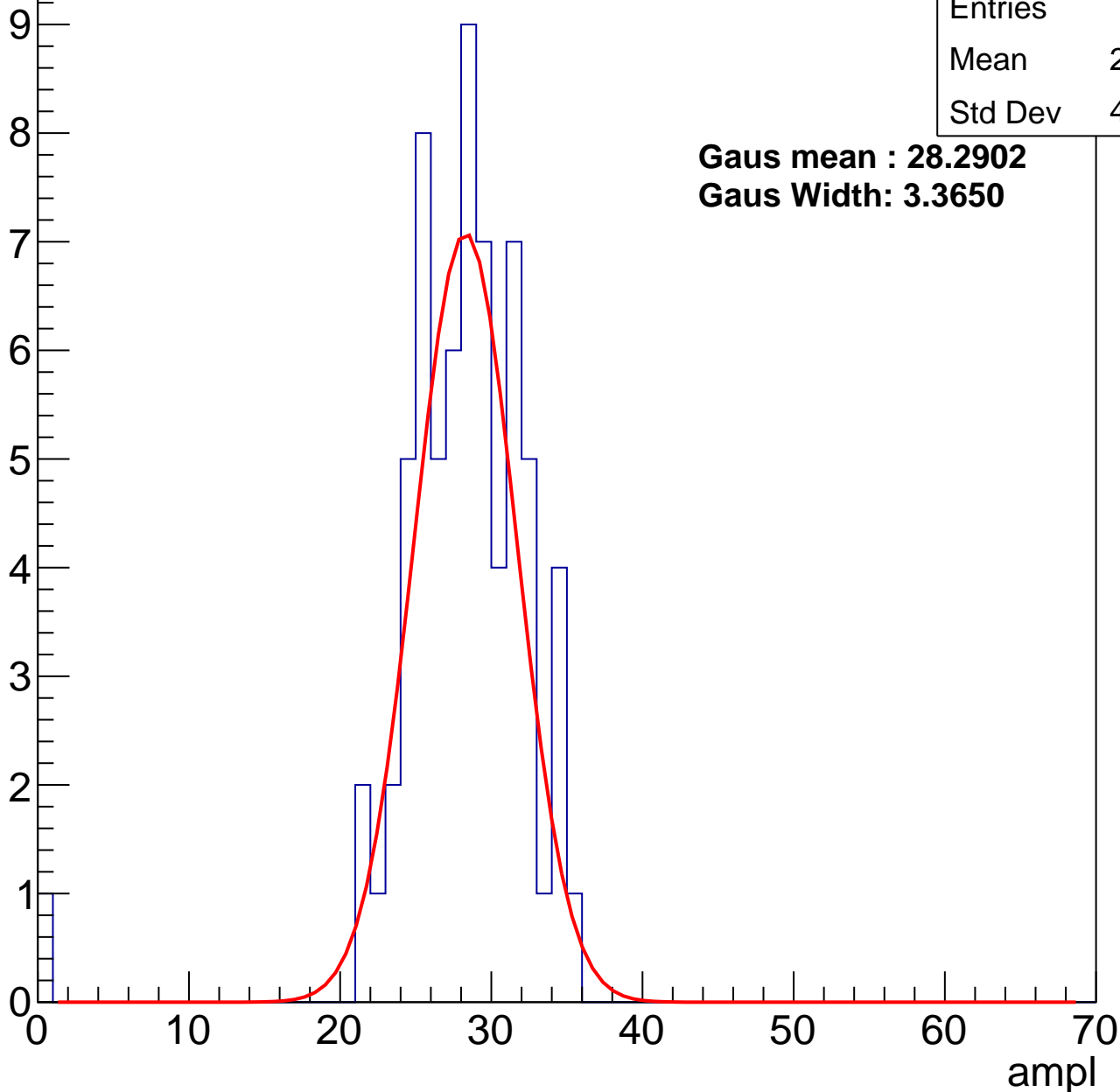
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.62
Std Dev	4.728

Gaus mean : 28.2902

Gaus Width: 3.3650



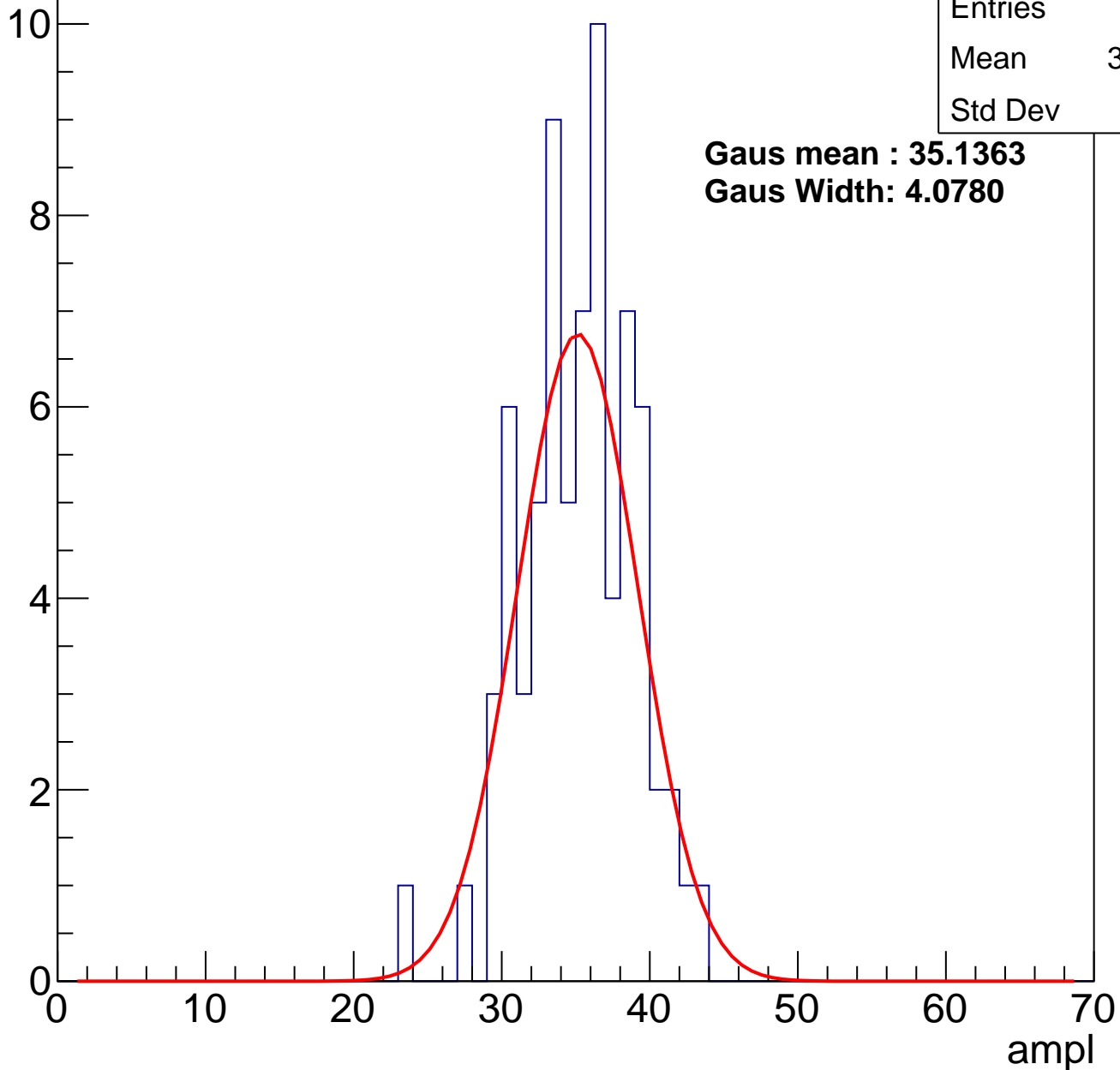
B1L102S, U2-ch93, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	34.75
Std Dev	3.73

Gaus mean : 35.1363
Gaus Width: 4.0780

Entry



B1L102S, U2-ch93, adc2

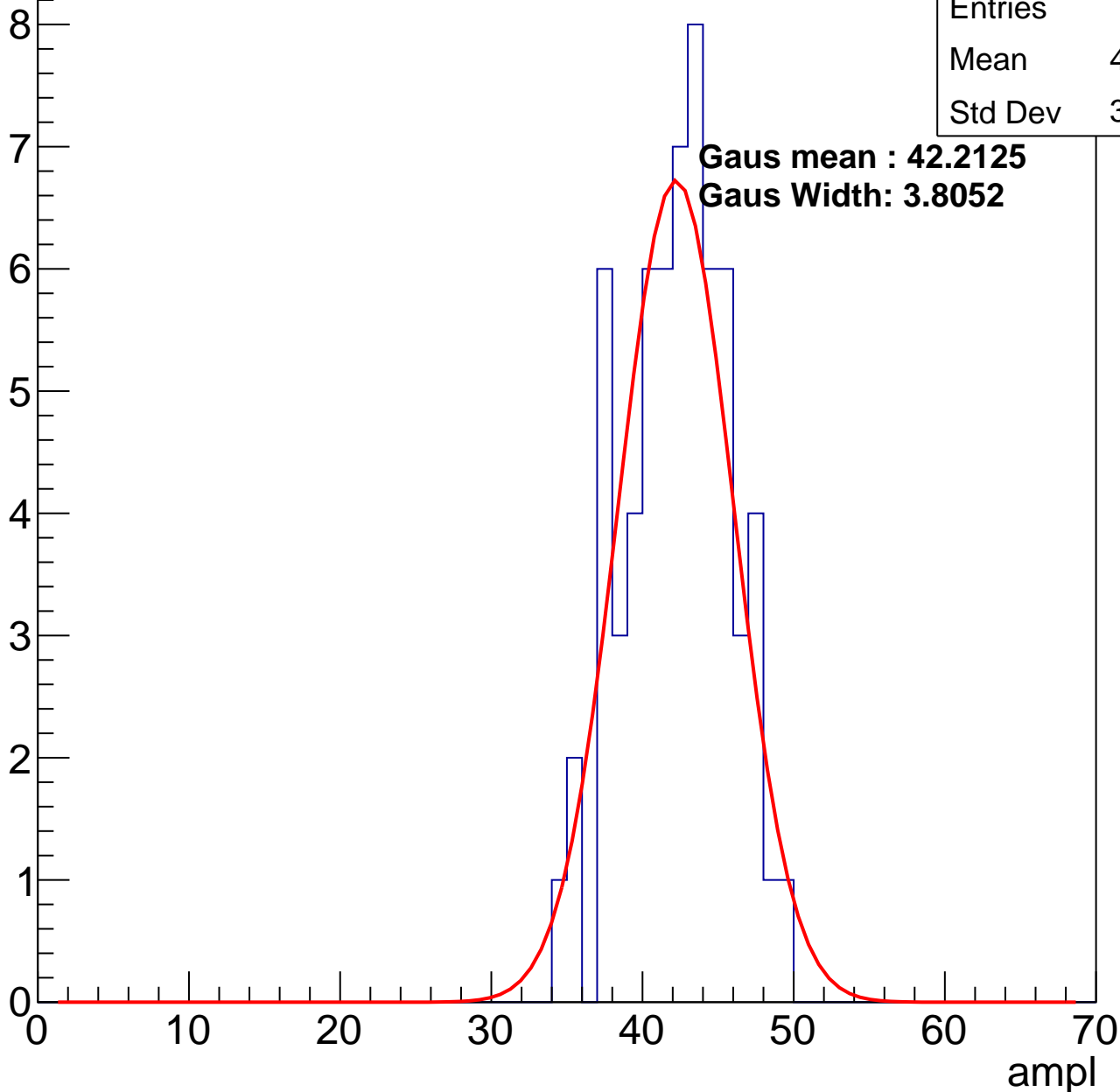
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.83
Std Dev	3.408

Gaus mean : 42.2125

Gaus Width: 3.8052

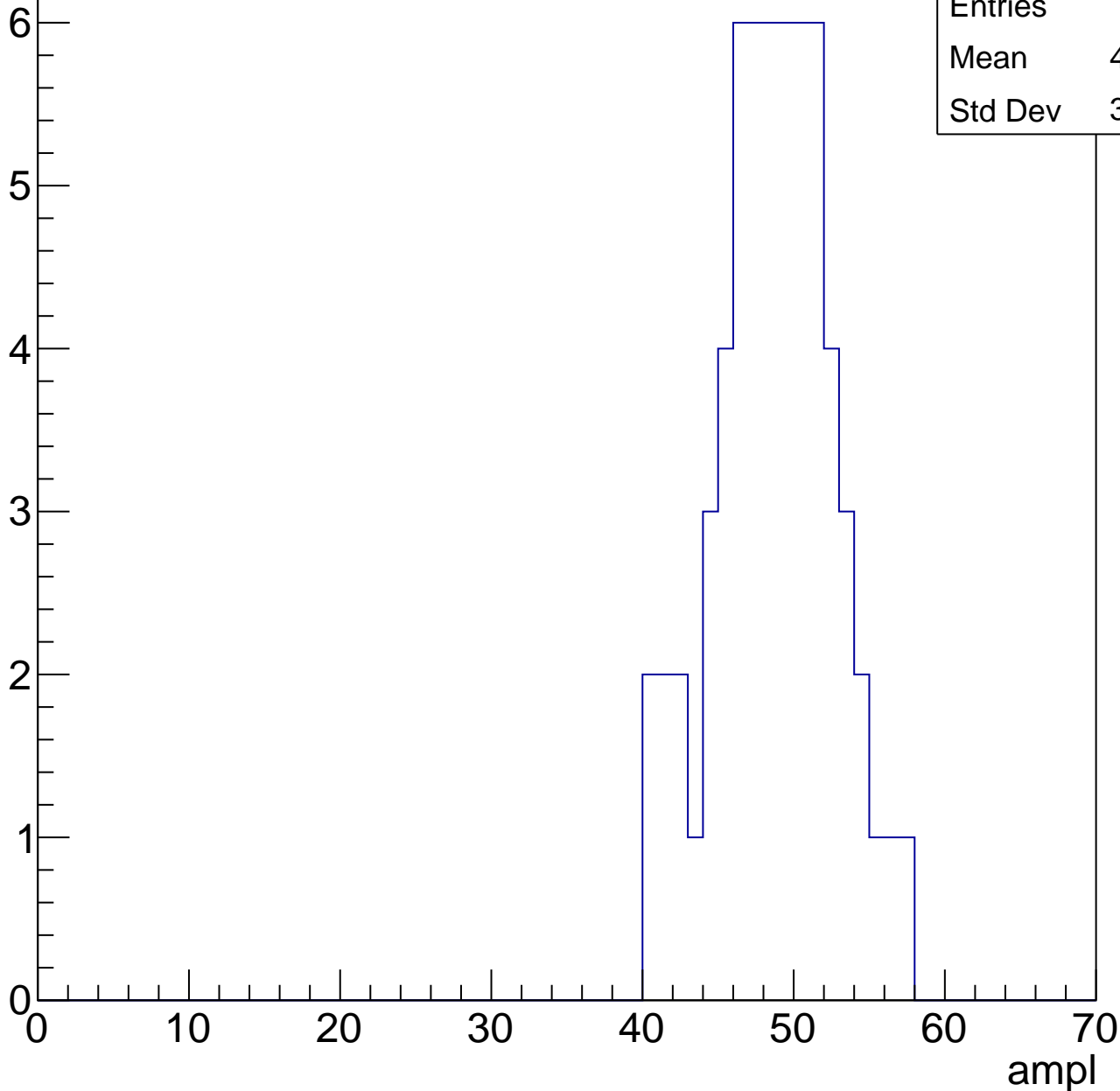


B1L102S, U2-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.23
Std Dev	3.858

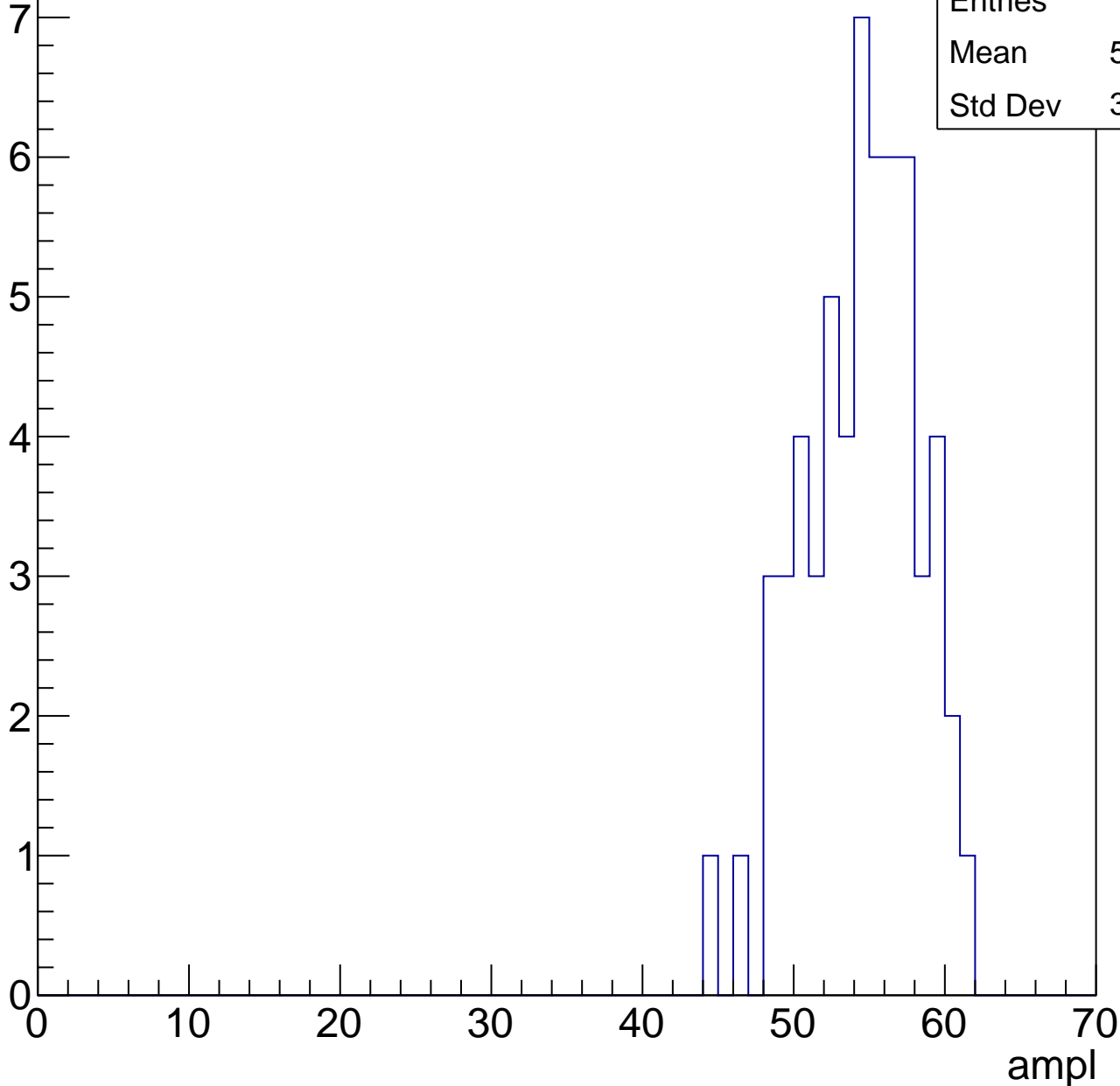


B1L102S, U2-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.95
Std Dev	3.716

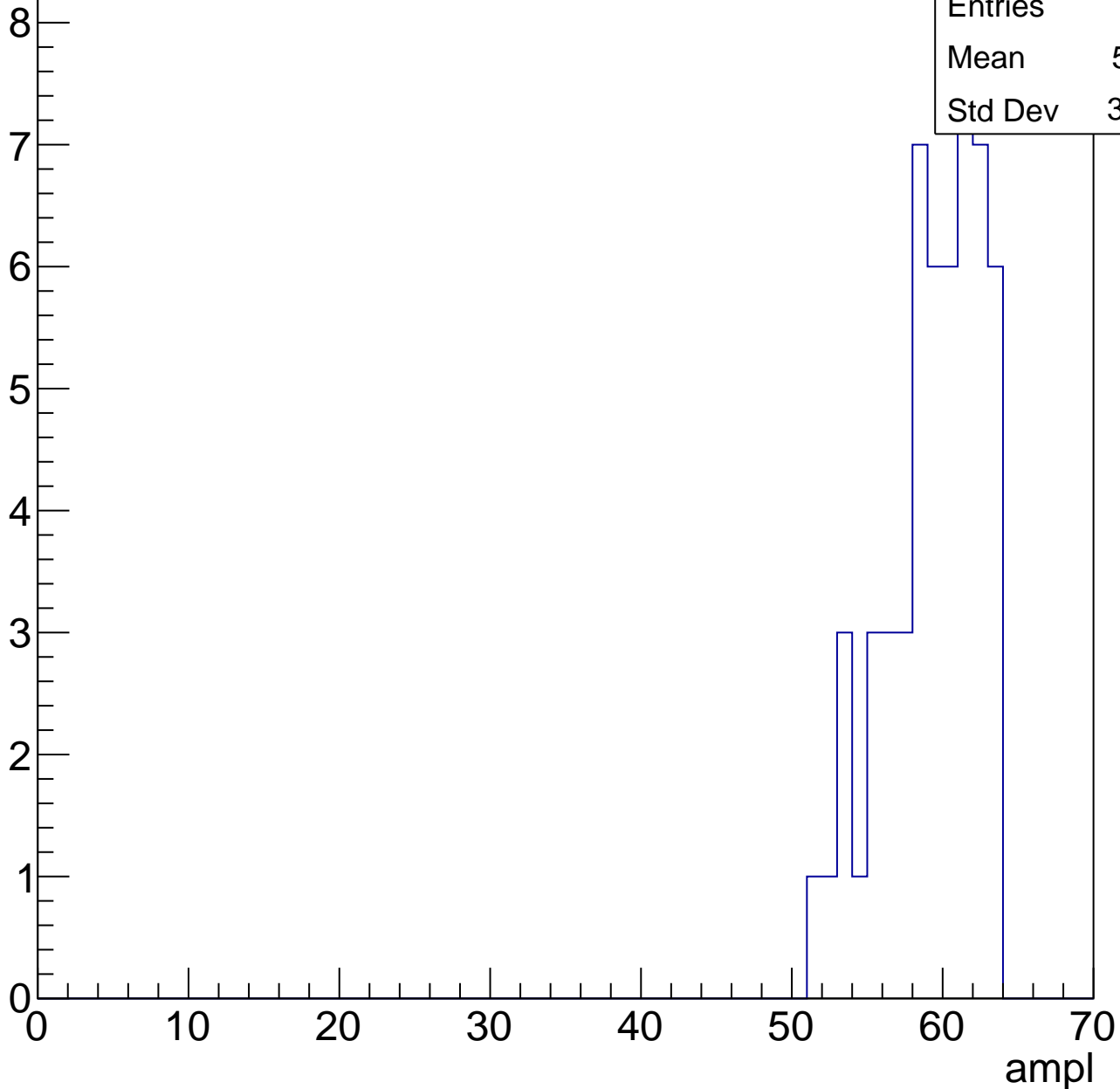


B1L102S, U2-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

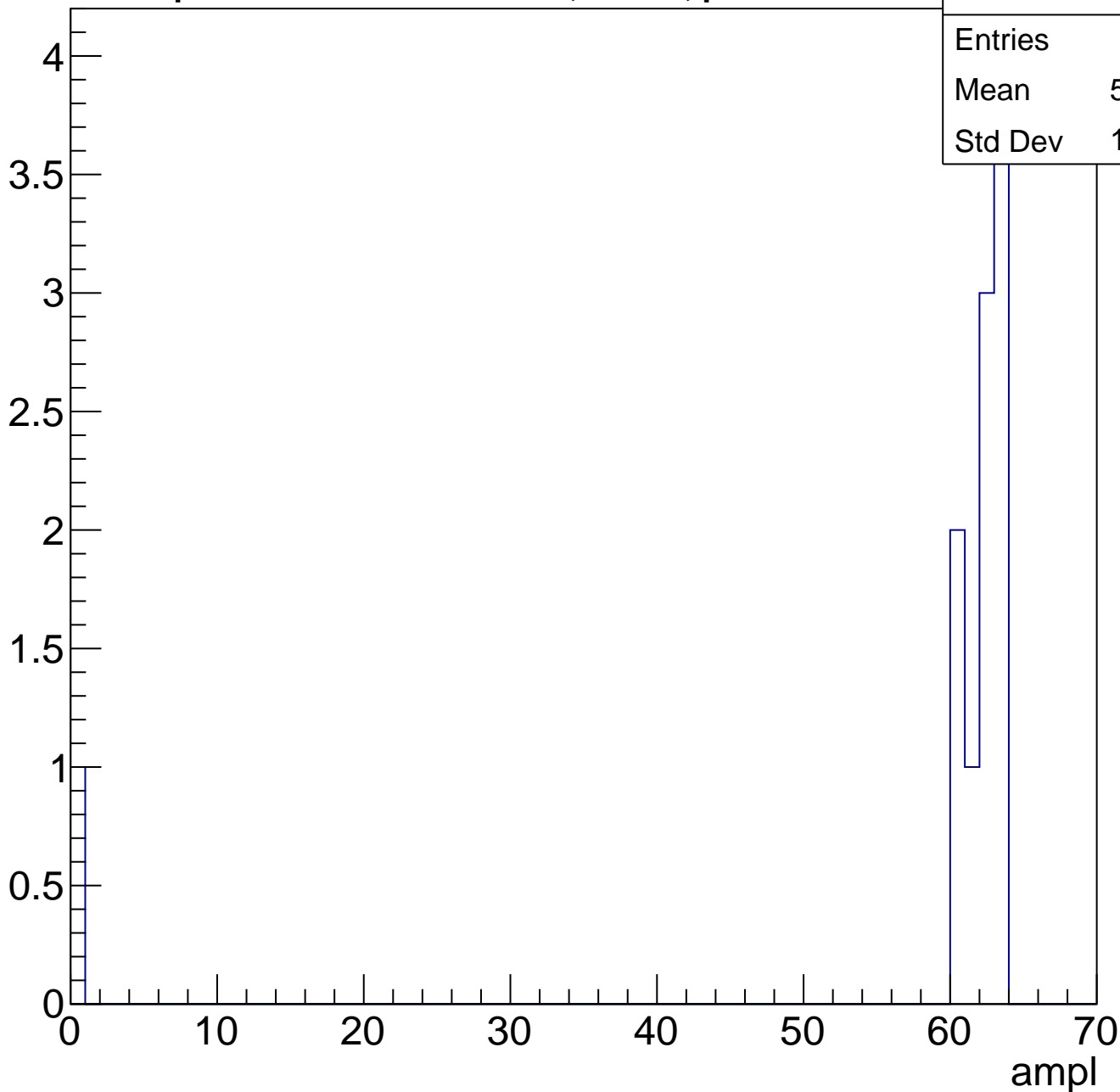
Entries	55
Mean	58.91
Std Dev	3.123



B1L102S, U2-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch94, adc0

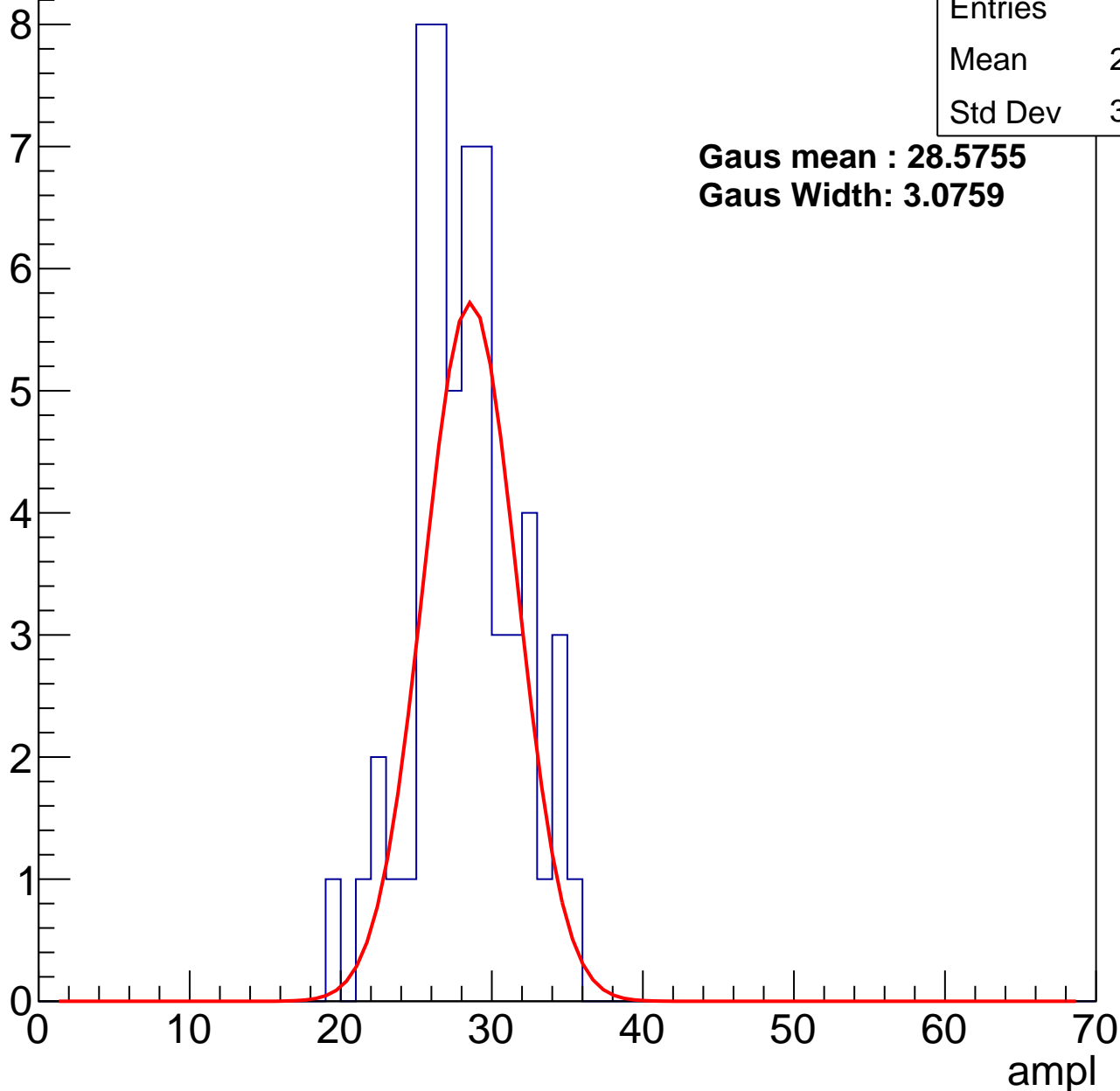
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.75
Std Dev	3.382

Gaus mean : 28.5755

Gaus Width: 3.0759



B1L102S, U2-ch94, adc1

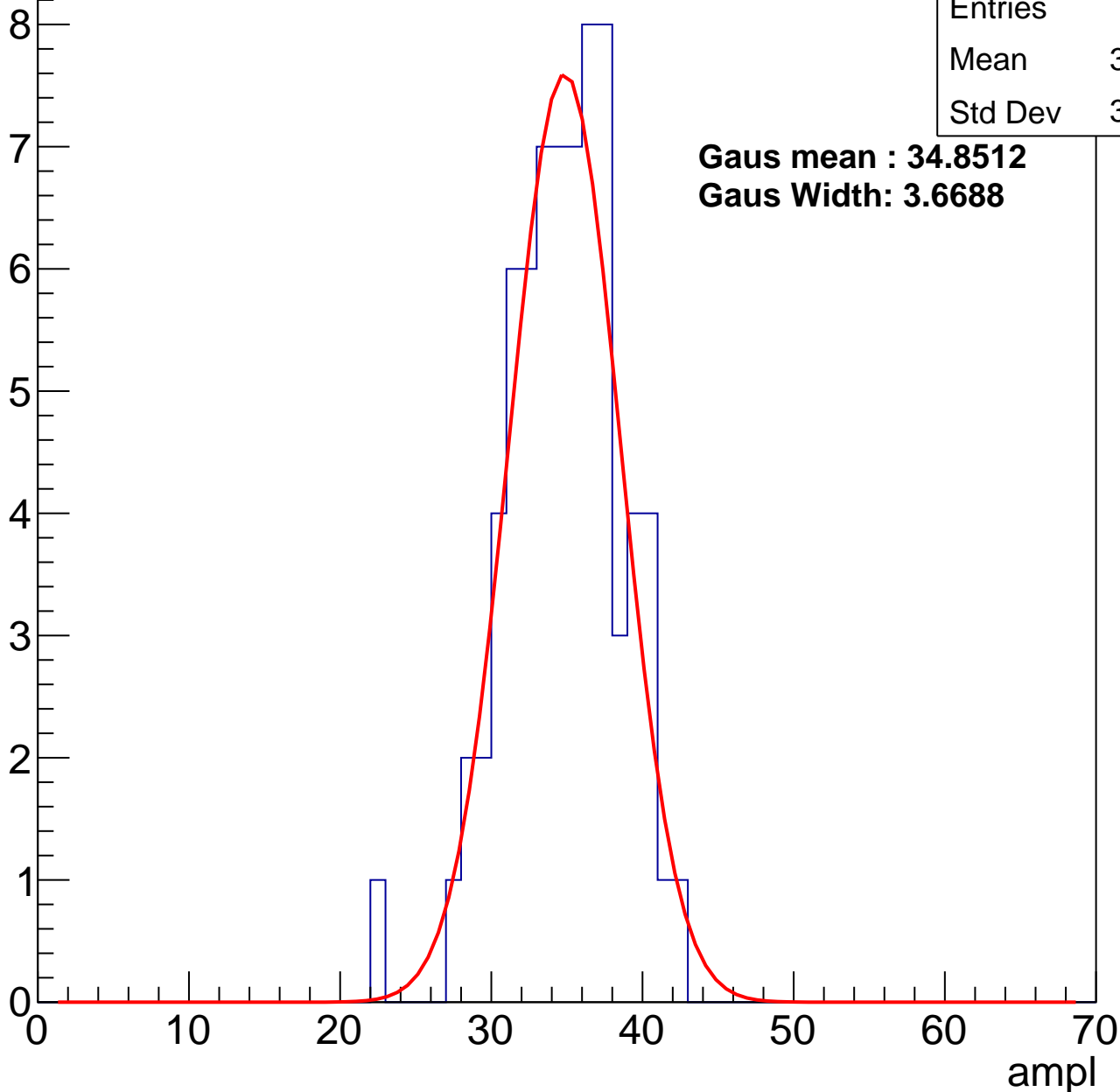
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.33
Std Dev	3.674

Gaus mean : 34.8512

Gaus Width: 3.6688



B1L102S, U2-ch94, adc2

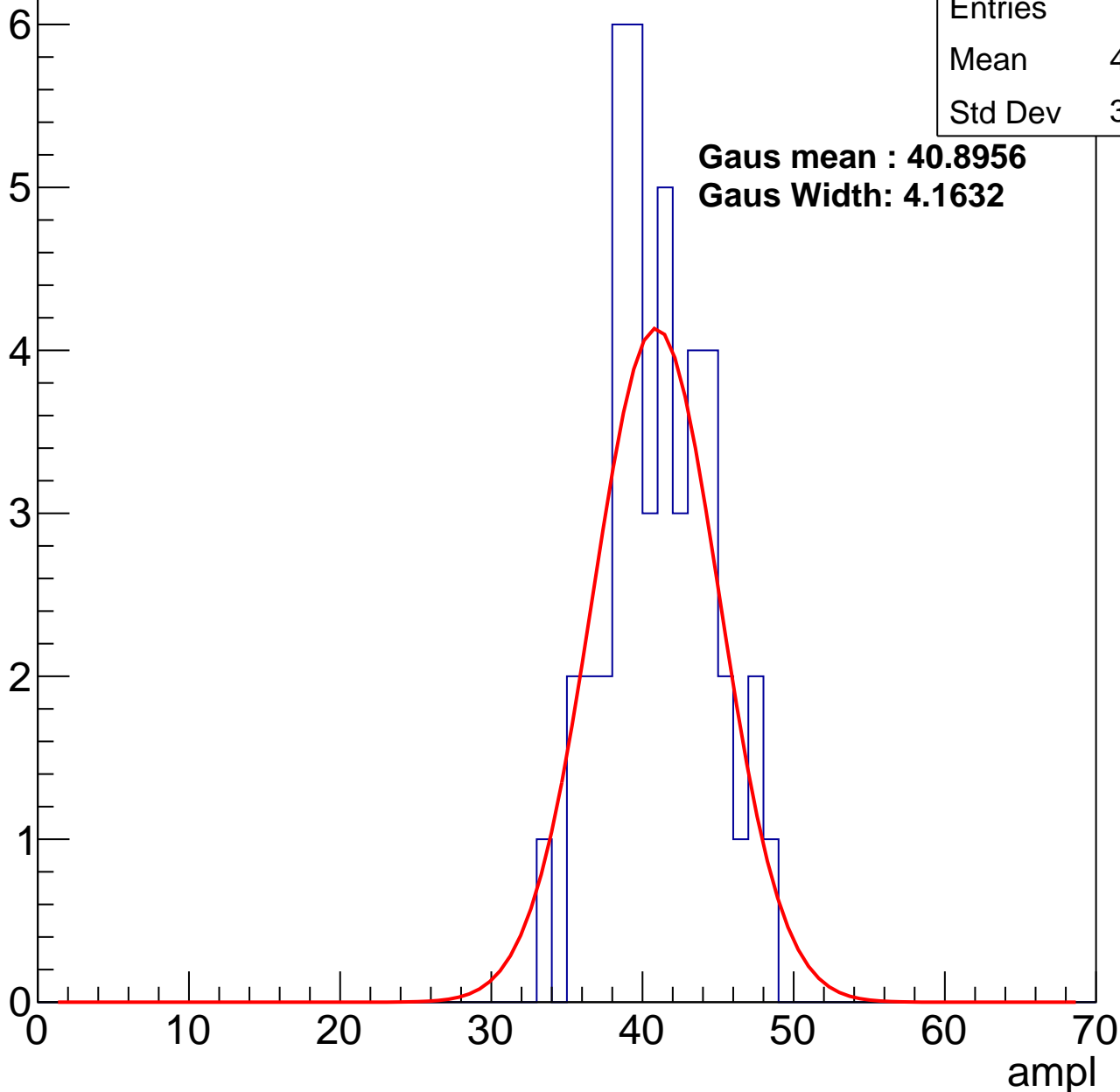
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	40.64
Std Dev	3.458

Gaus mean : 40.8956

Gaus Width: 4.1632

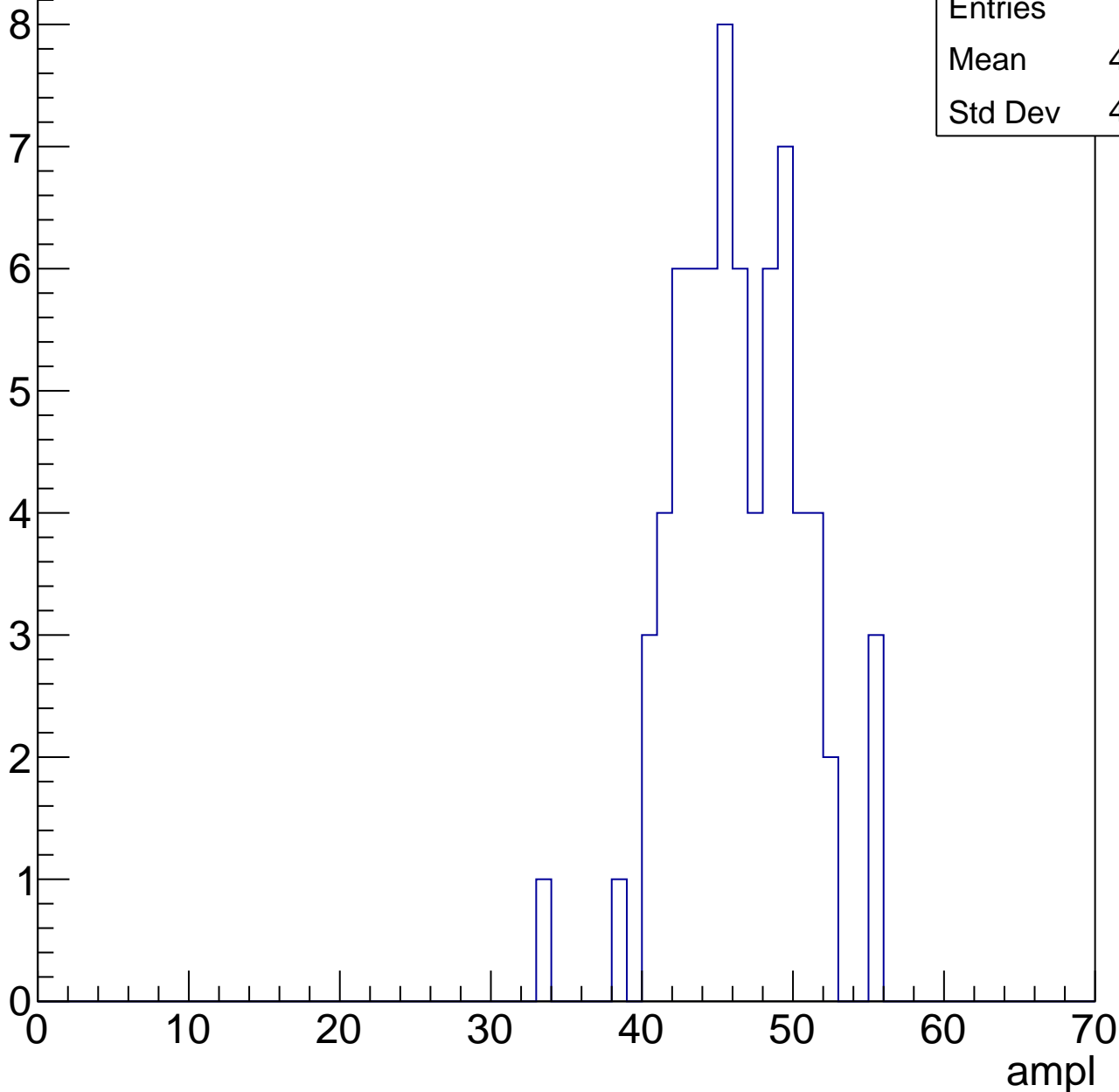


B1L102S, U2-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	45.87
Std Dev	4.097

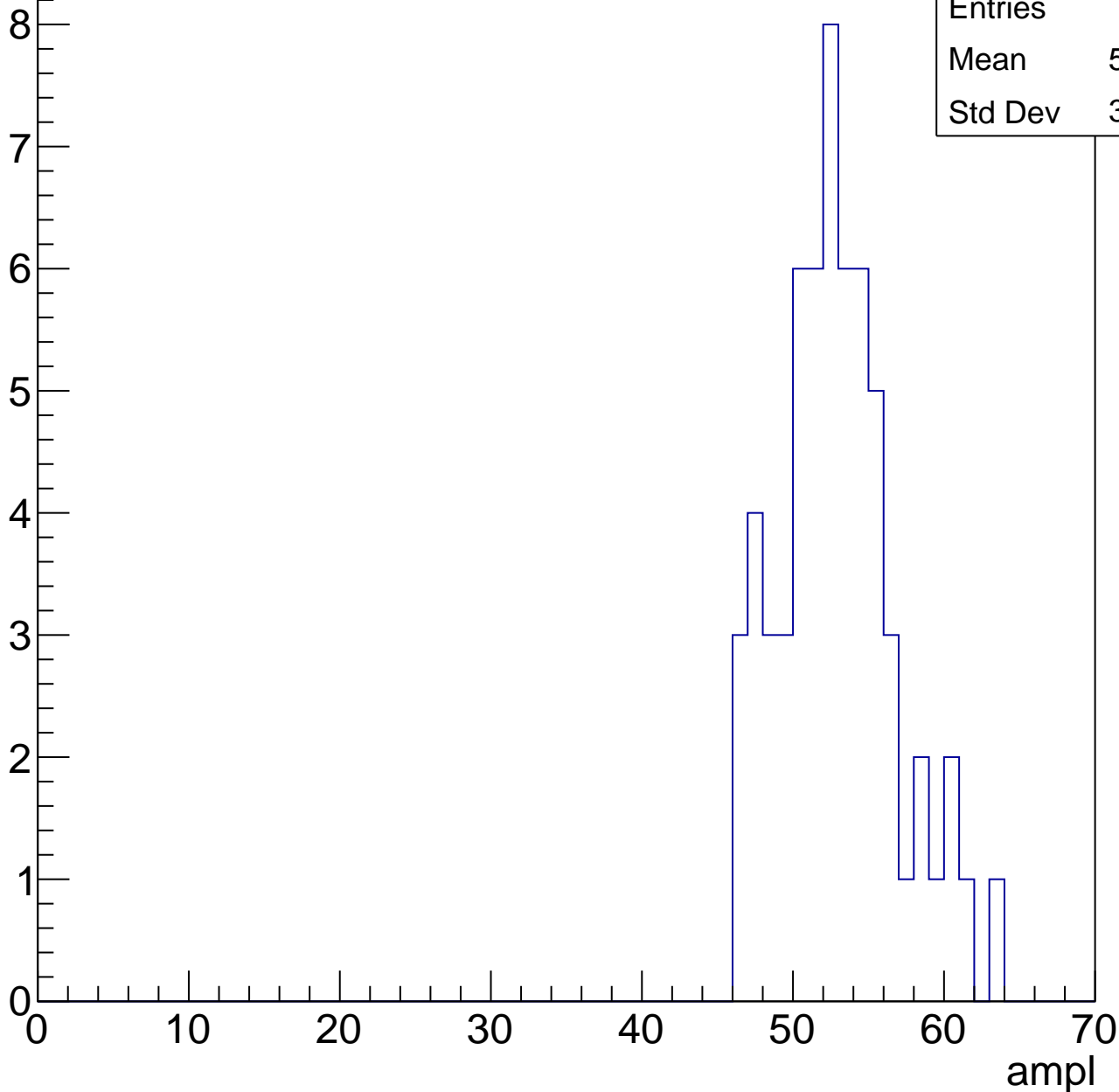


B1L102S, U2-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

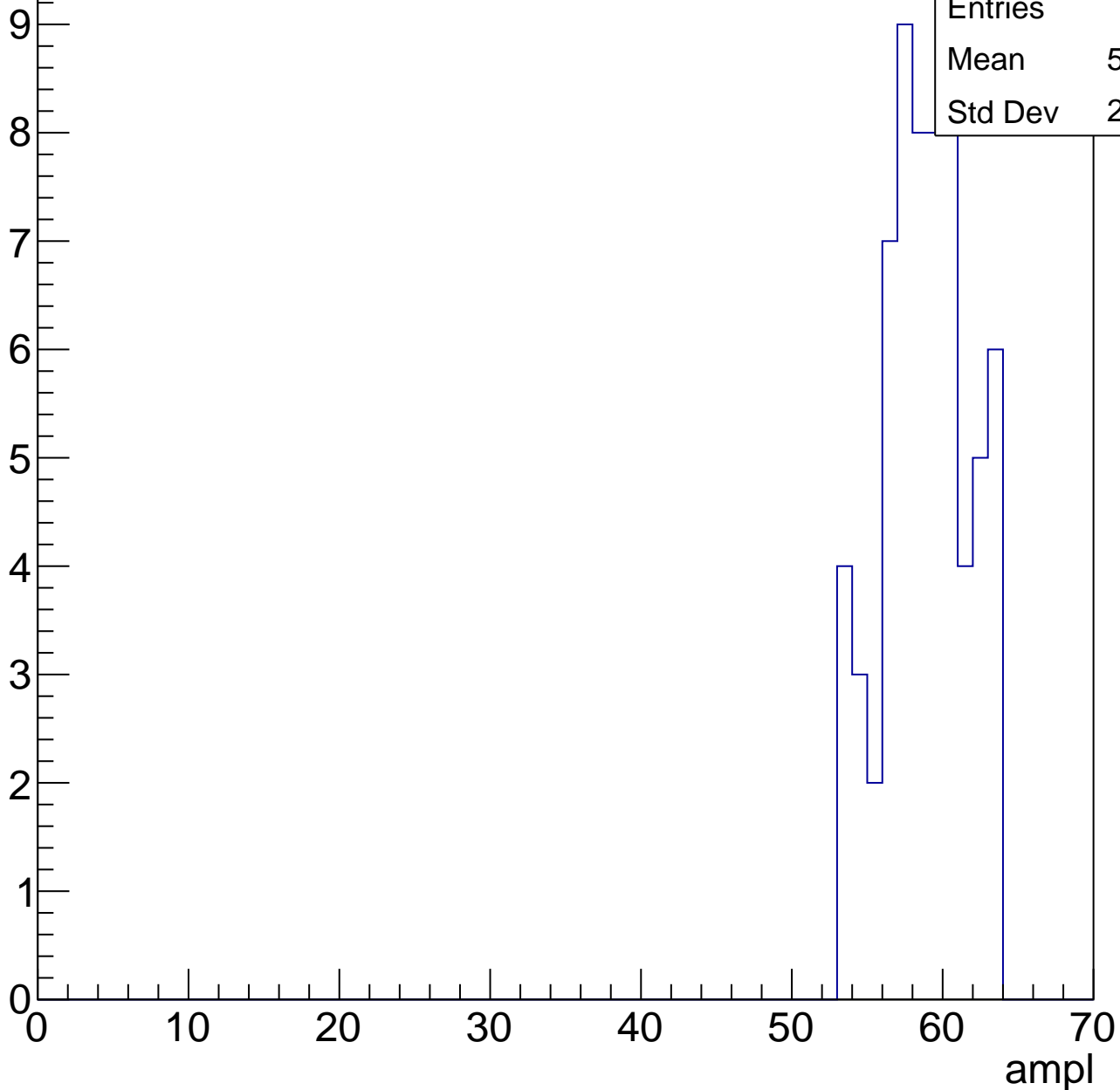
Entries	61
Mean	52.46
Std Dev	3.839



B1L102S, U2-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

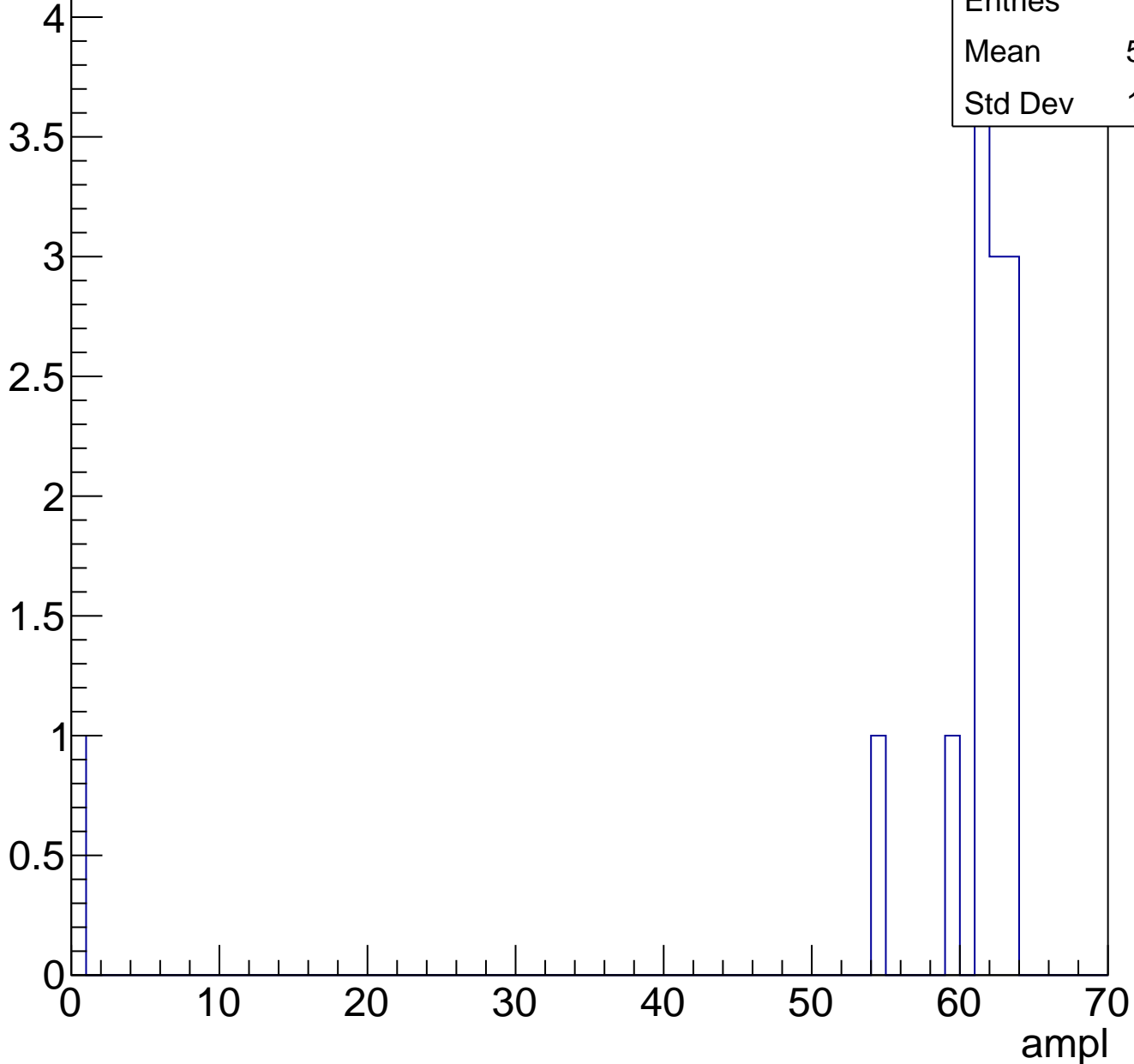
Entry



B1L102S, U2-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

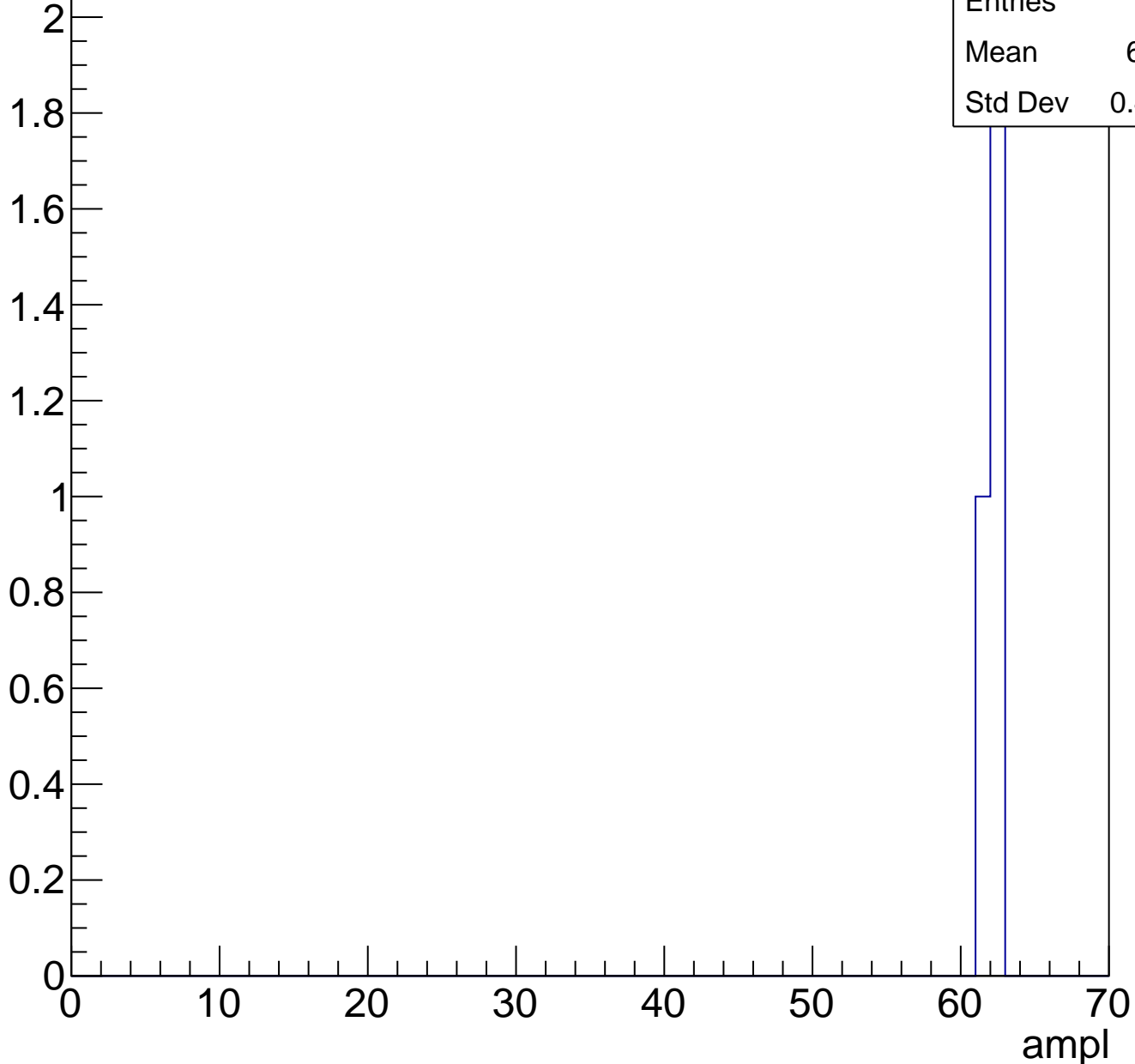
Entry



B1L102S, U2-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch95, adc0

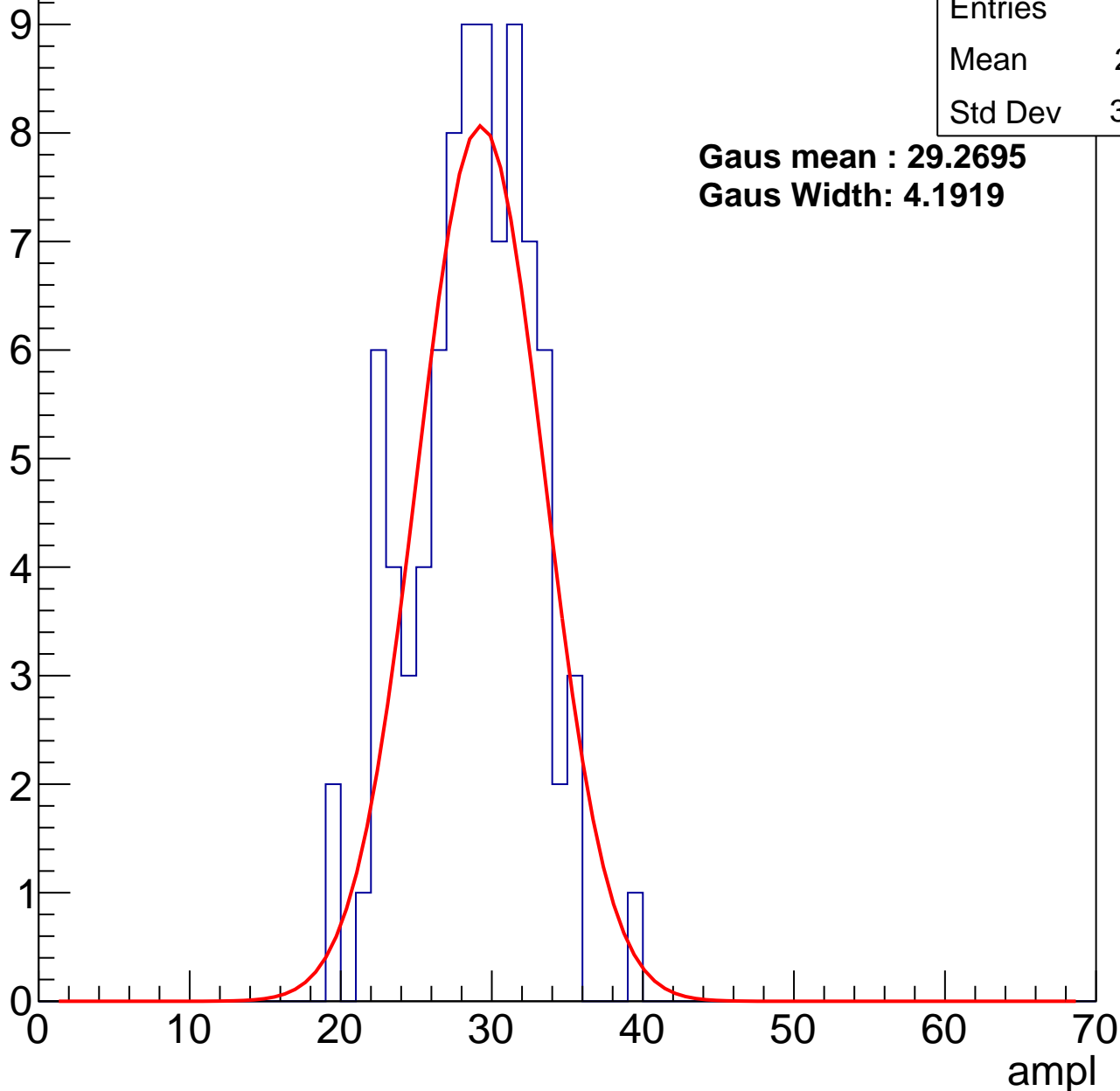
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	28.31
Std Dev	3.946

Gaus mean : 29.2695

Gaus Width: 4.1919



B1L102S, U2-ch95, adc1

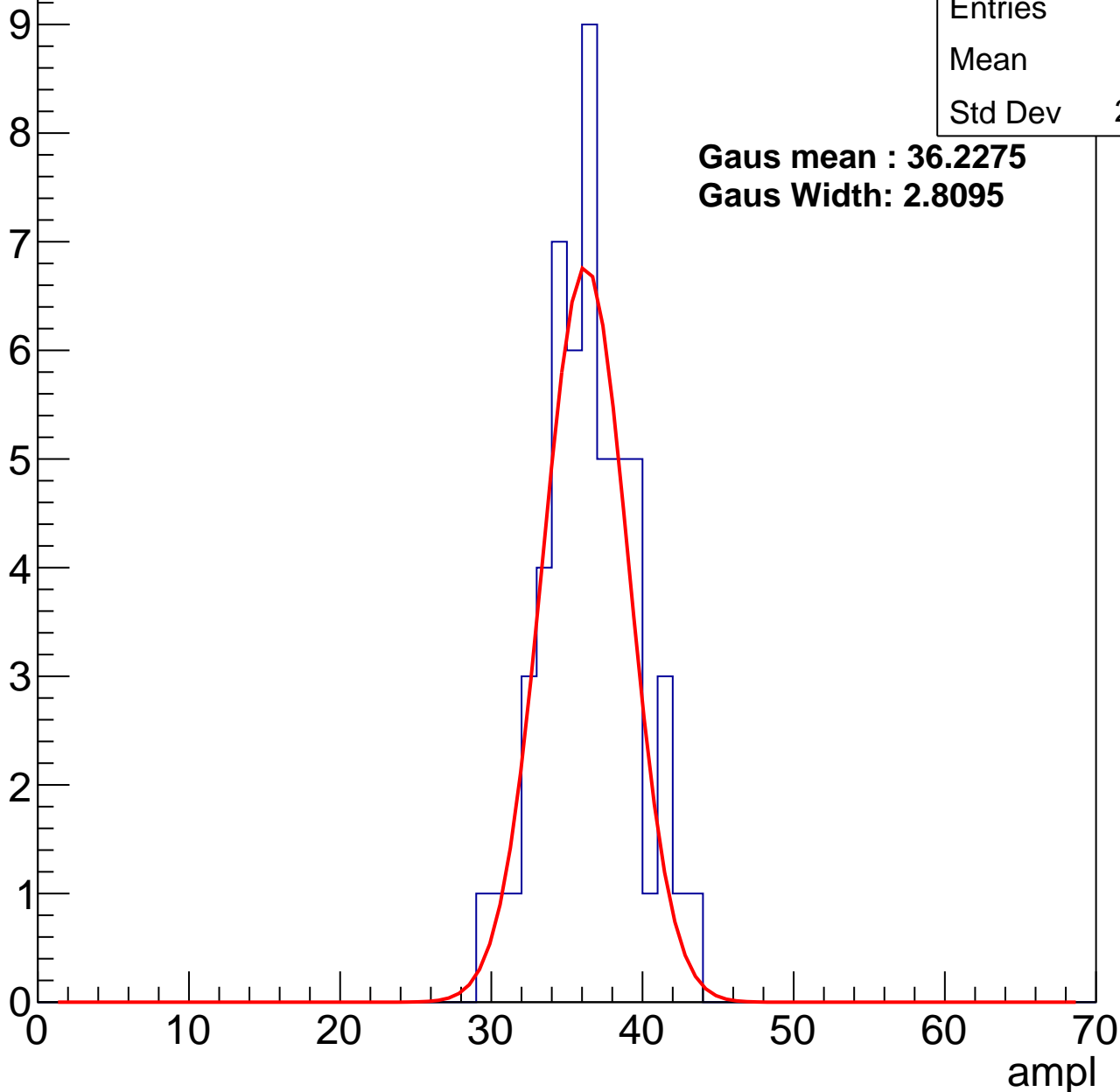
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	36
Std Dev	2.991

Gaus mean : 36.2275

Gaus Width: 2.8095



B1L102S, U2-ch95, adc2

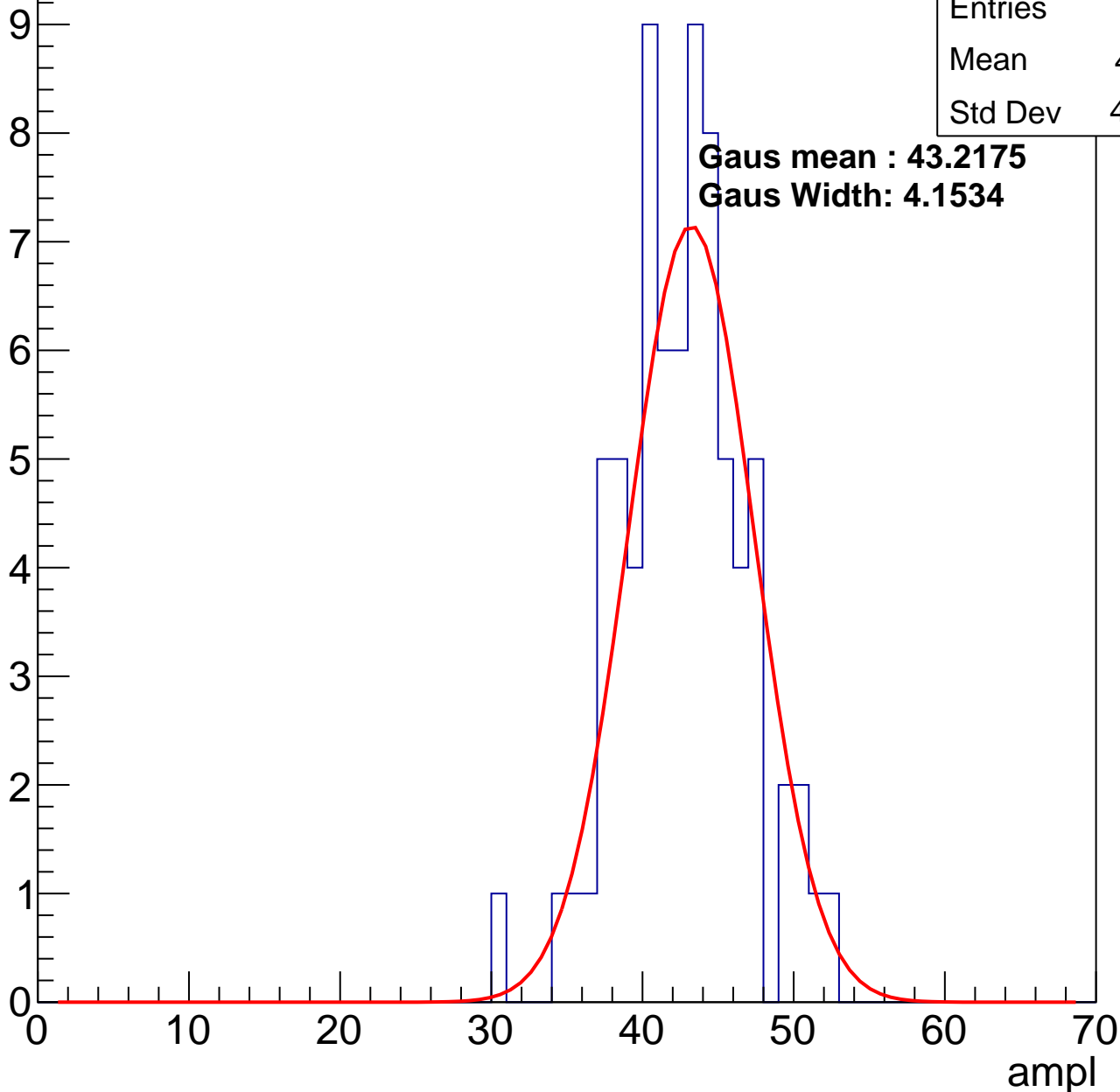
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.21
Std Dev	4.063

Gaus mean : 43.2175

Gaus Width: 4.1534

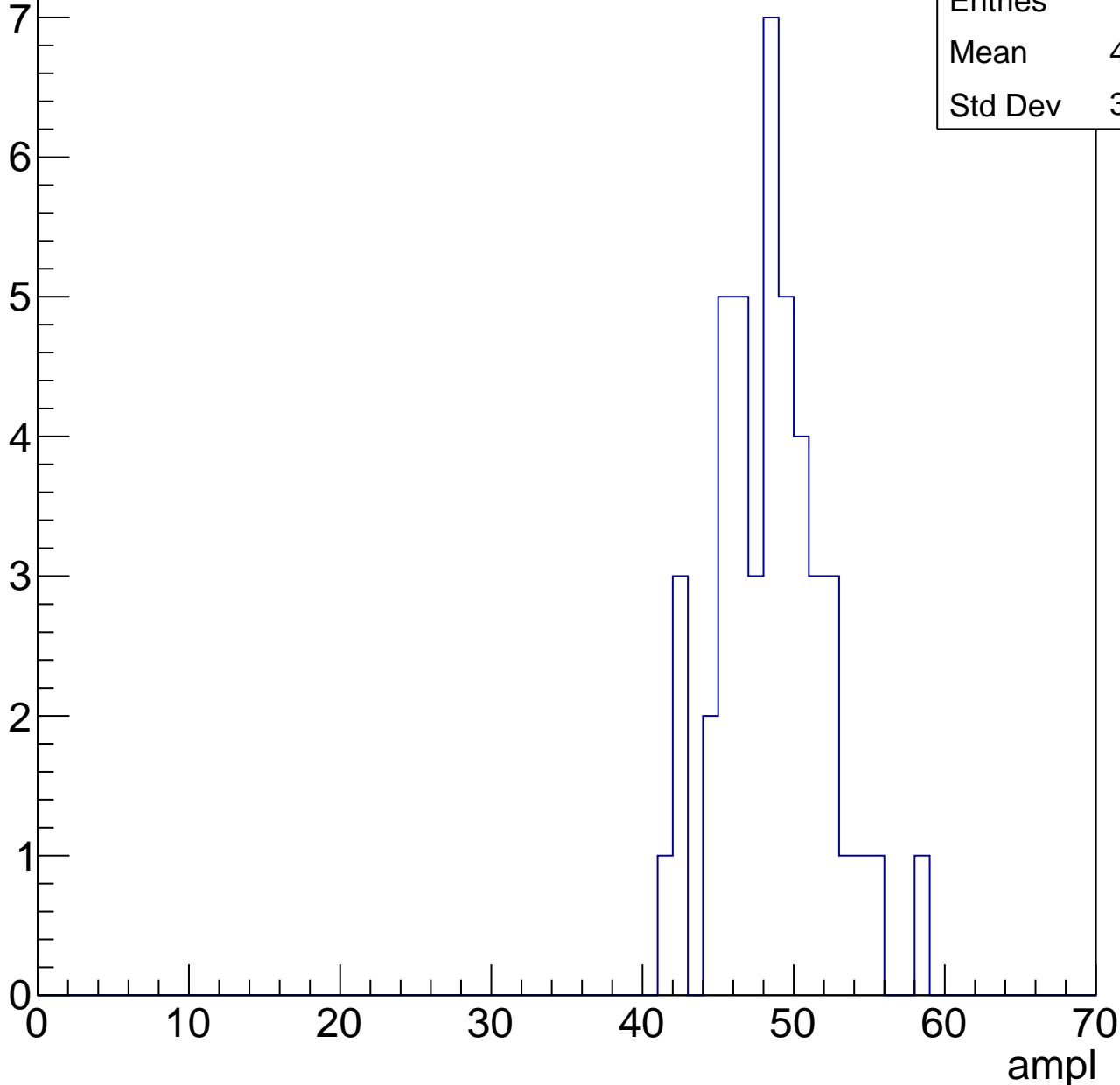


B1L102S, U2-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

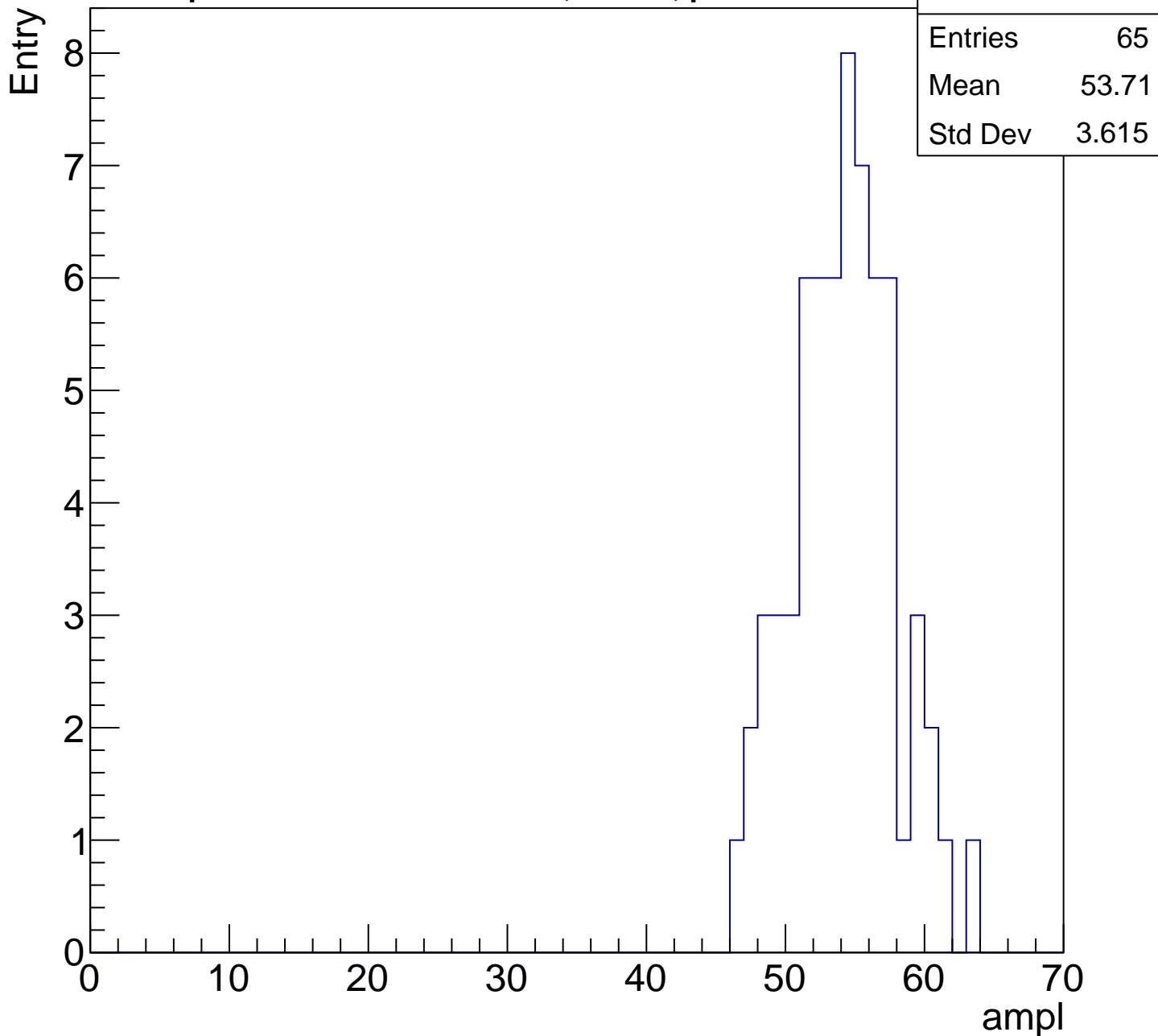
Entry

Entries	45
Mean	48.02
Std Dev	3.537



B1L102S, U2-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

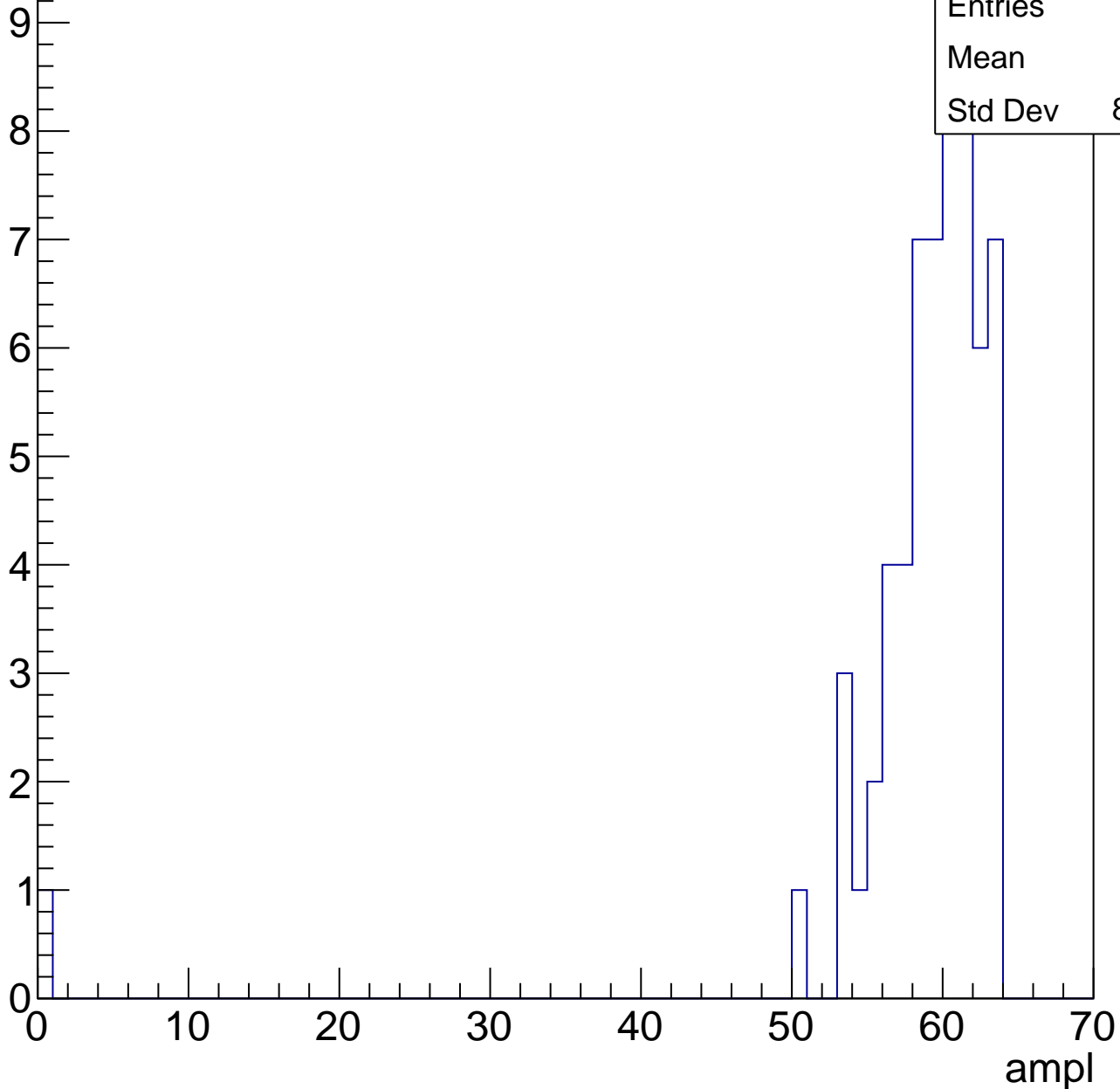


B1L102S, U2-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

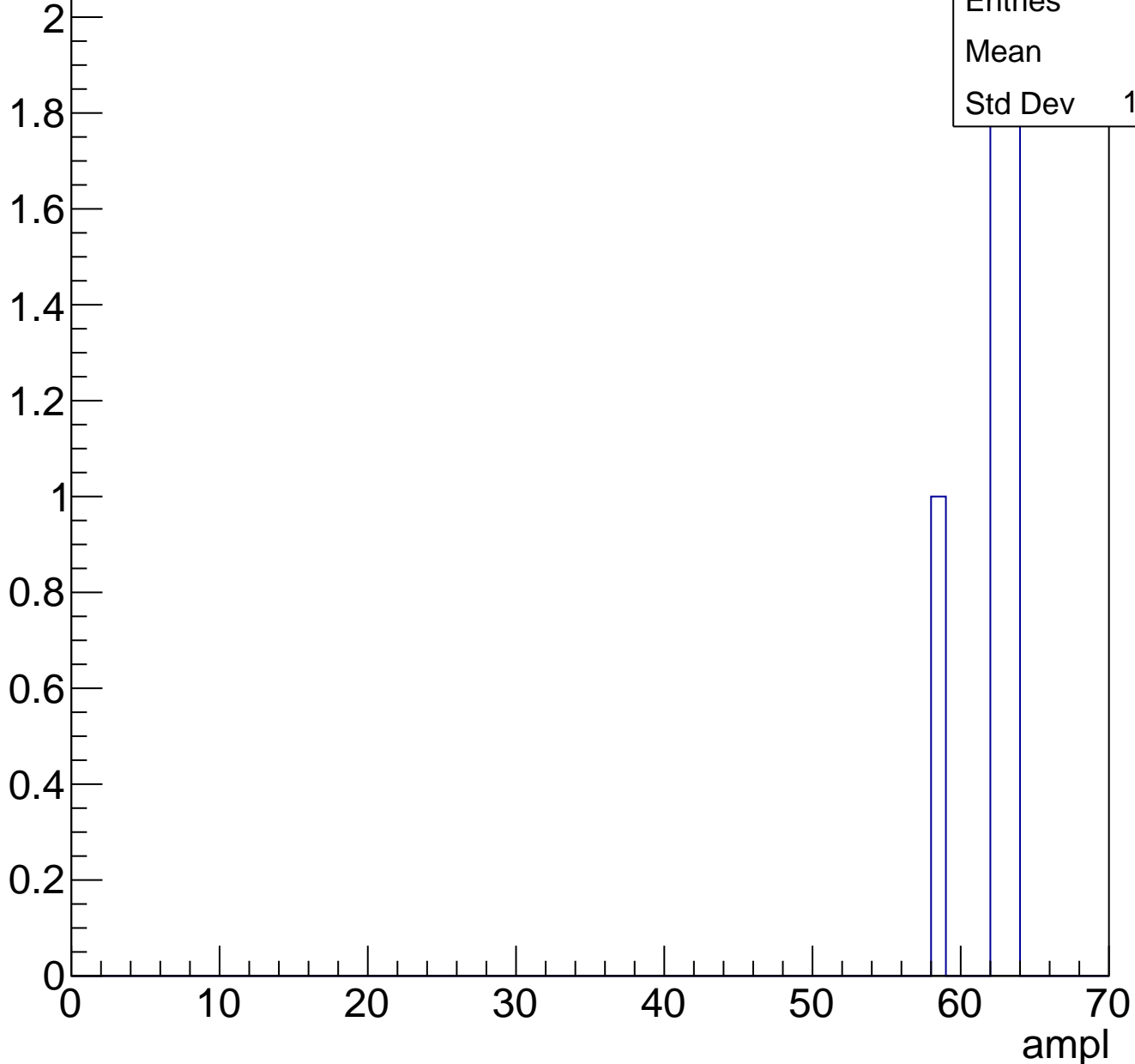
Entries	60
Mean	58.1
Std Dev	8.111



B1L102S, U2-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	47
Std Dev	21.21

0 10 20 30 40 50 60 70

ampl

1

B1L102S, U2-ch96, adc0

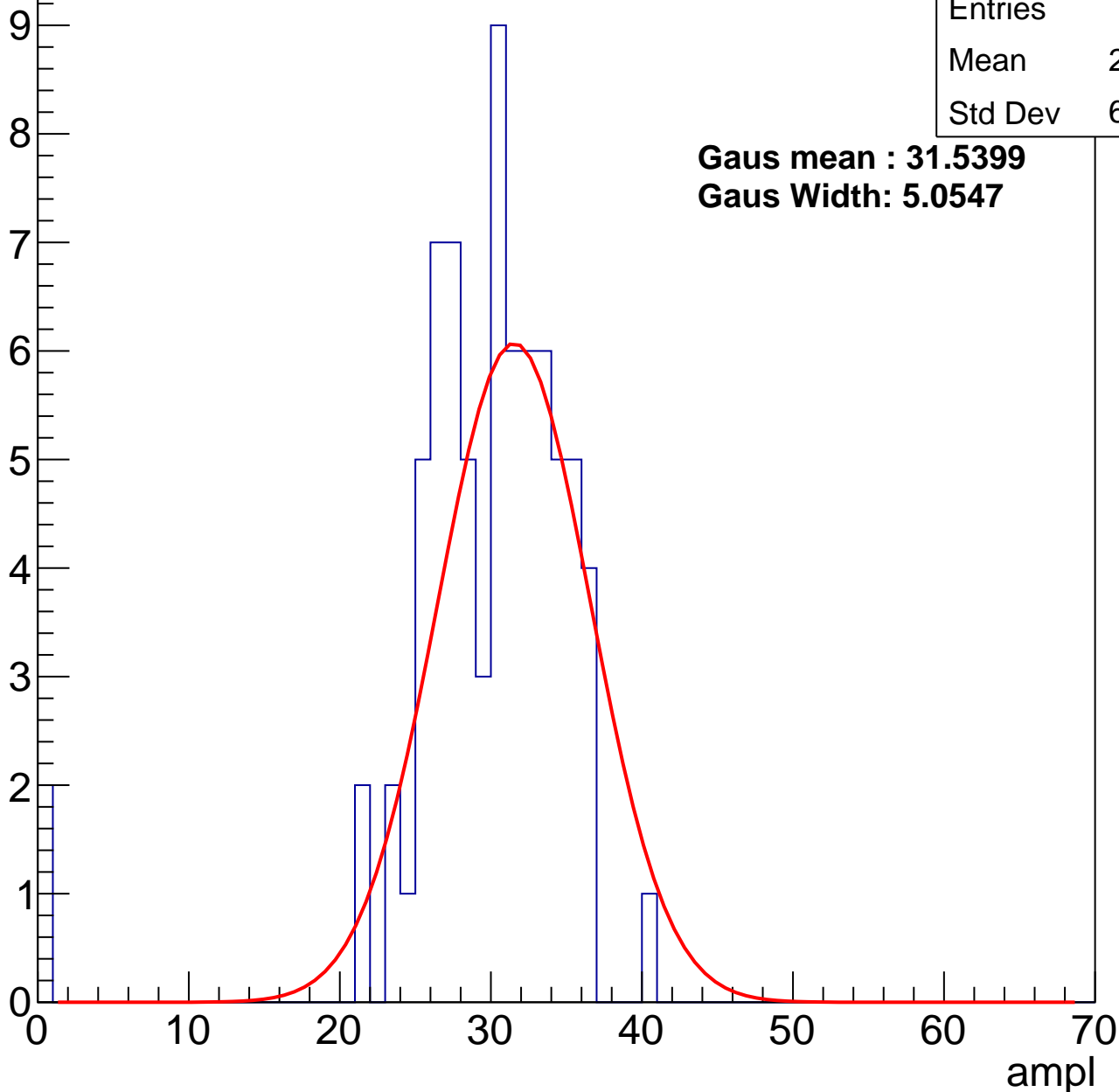
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	29.08
Std Dev	6.164

Gaus mean : 31.5399

Gaus Width: 5.0547



B1L102S, U2-ch96, adc1

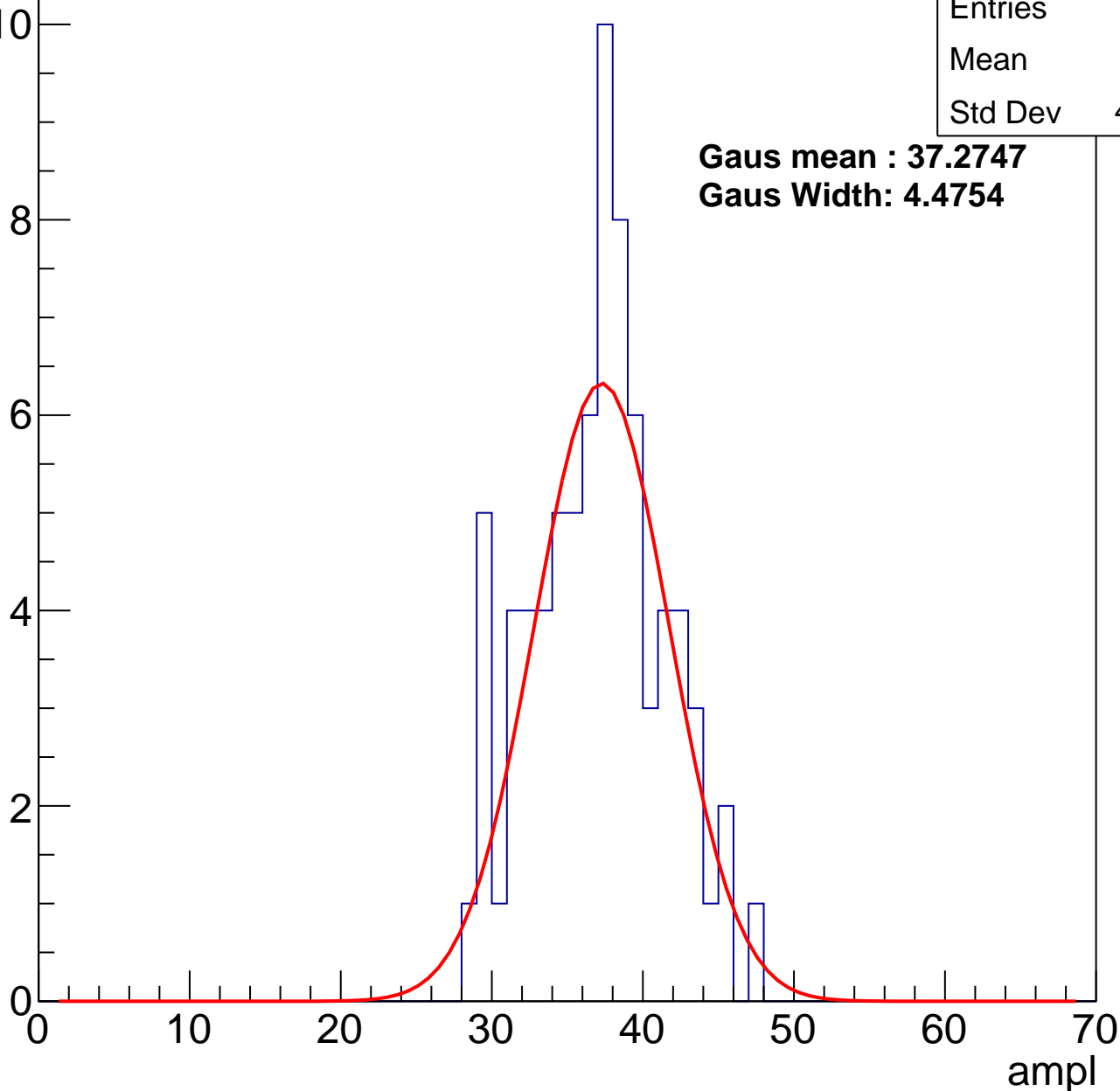
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	36.6
Std Dev	4.301

Gaus mean : 37.2747

Gaus Width: 4.4754



B1L102S, U2-ch96, adc2

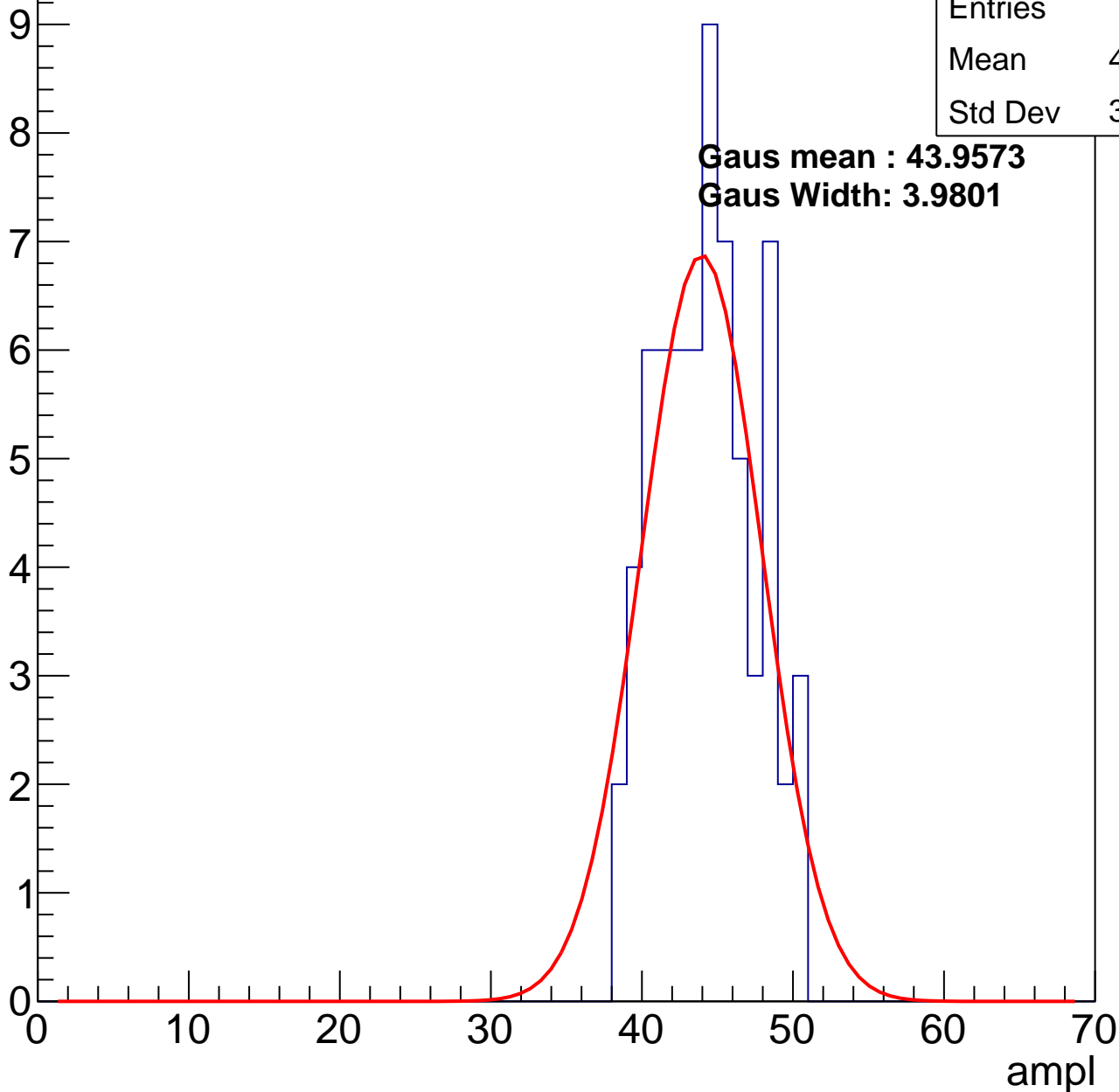
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.85
Std Dev	3.197

Gaus mean : 43.9573

Gaus Width: 3.9801

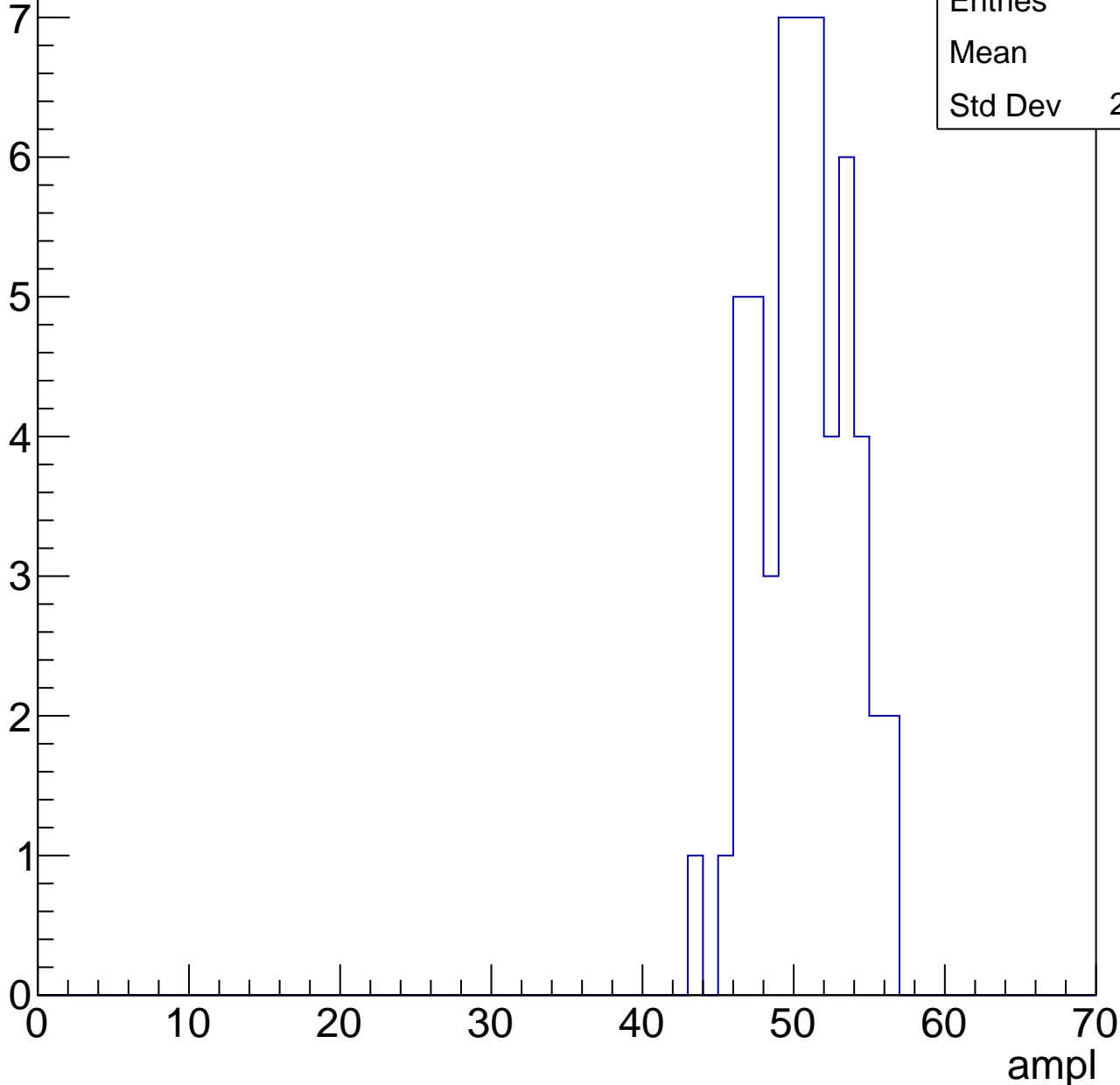


B1L102S, U2-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

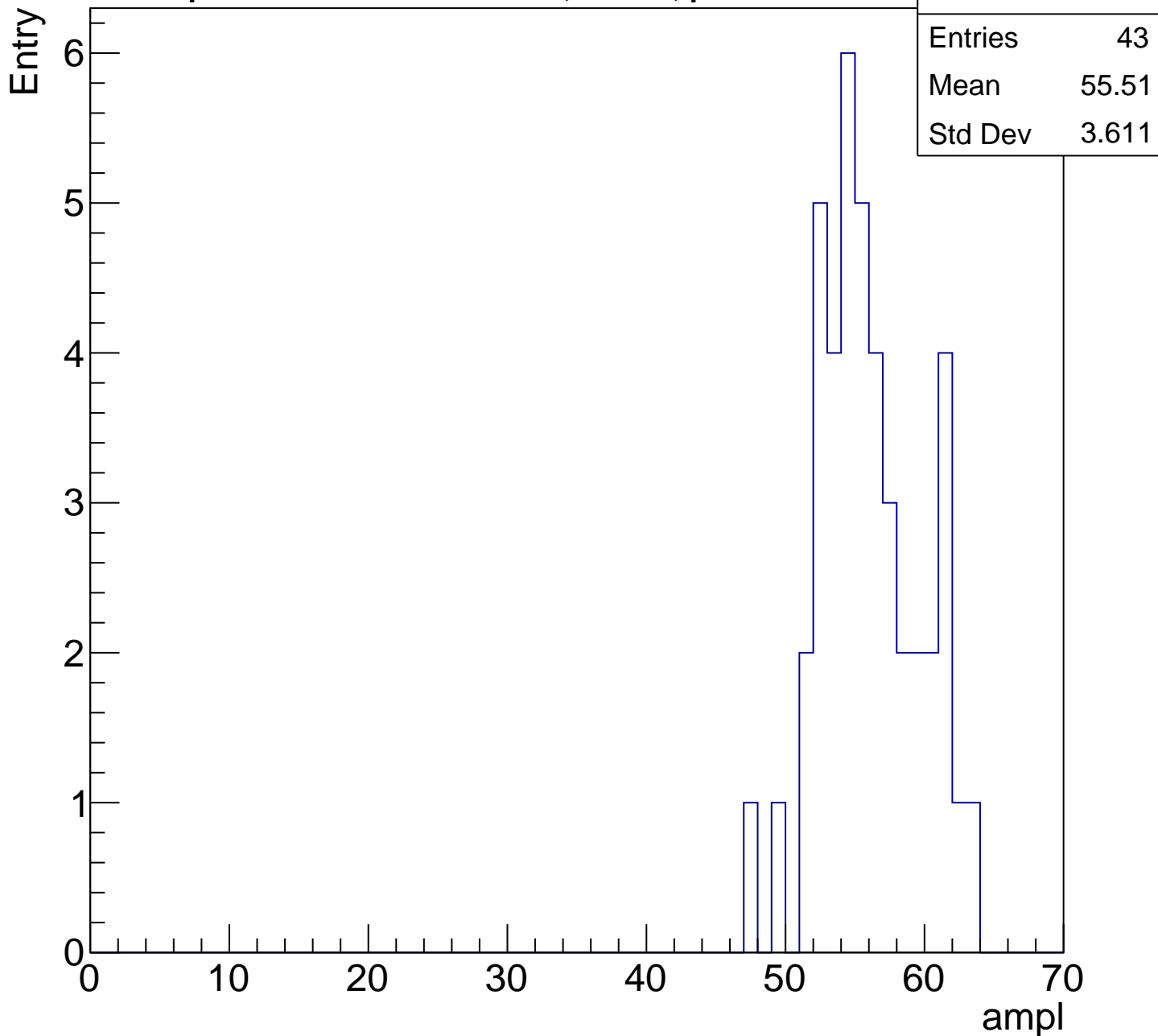
Entry

Entries	54
Mean	50.2
Std Dev	2.978



B1L102S, U2-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

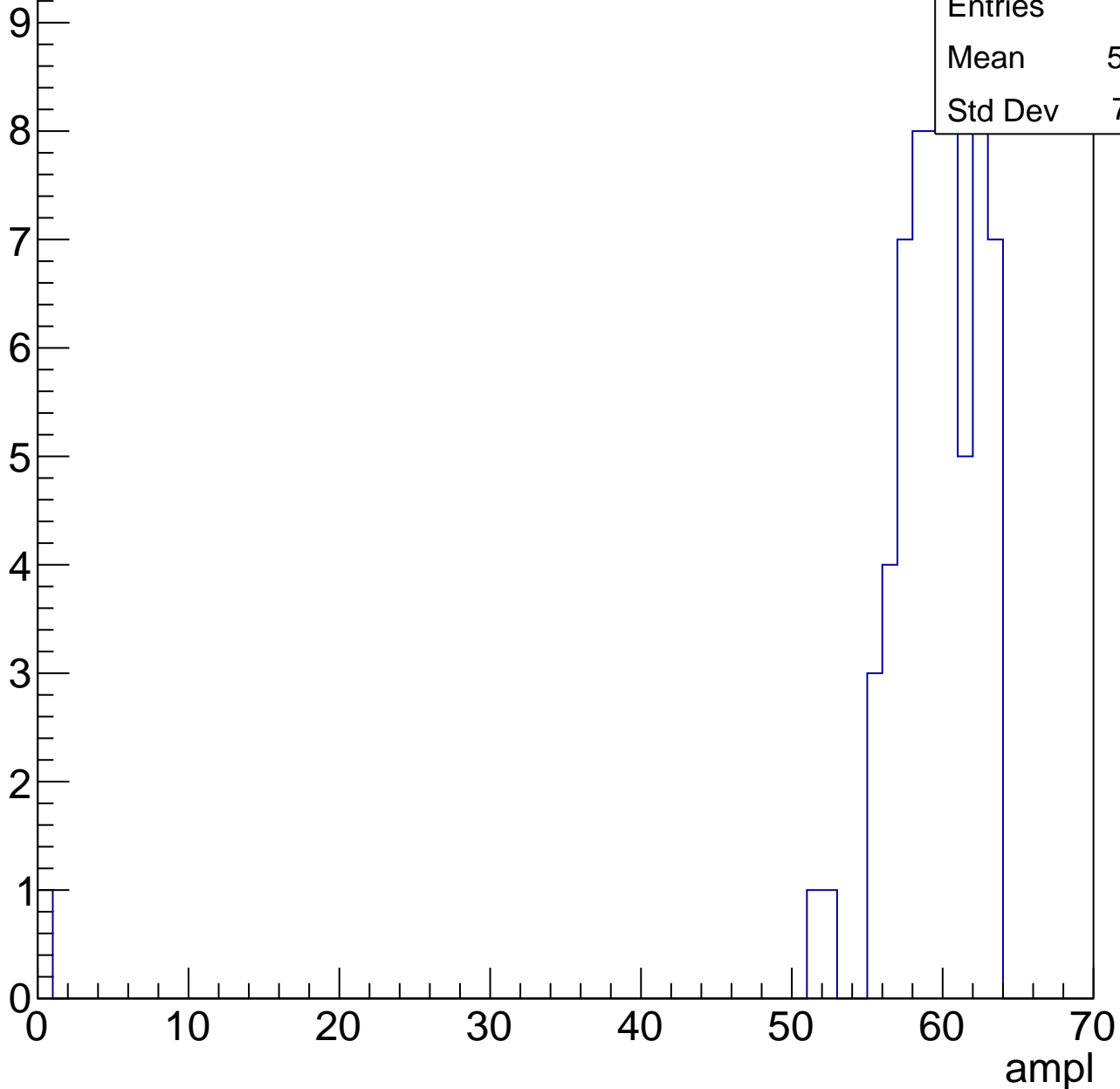


B1L102S, U2-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	58.24
Std Dev	7.931



B1L102S, U2-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	62.33
Std Dev	0.9428

B1L102S, U2-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch97, adc0

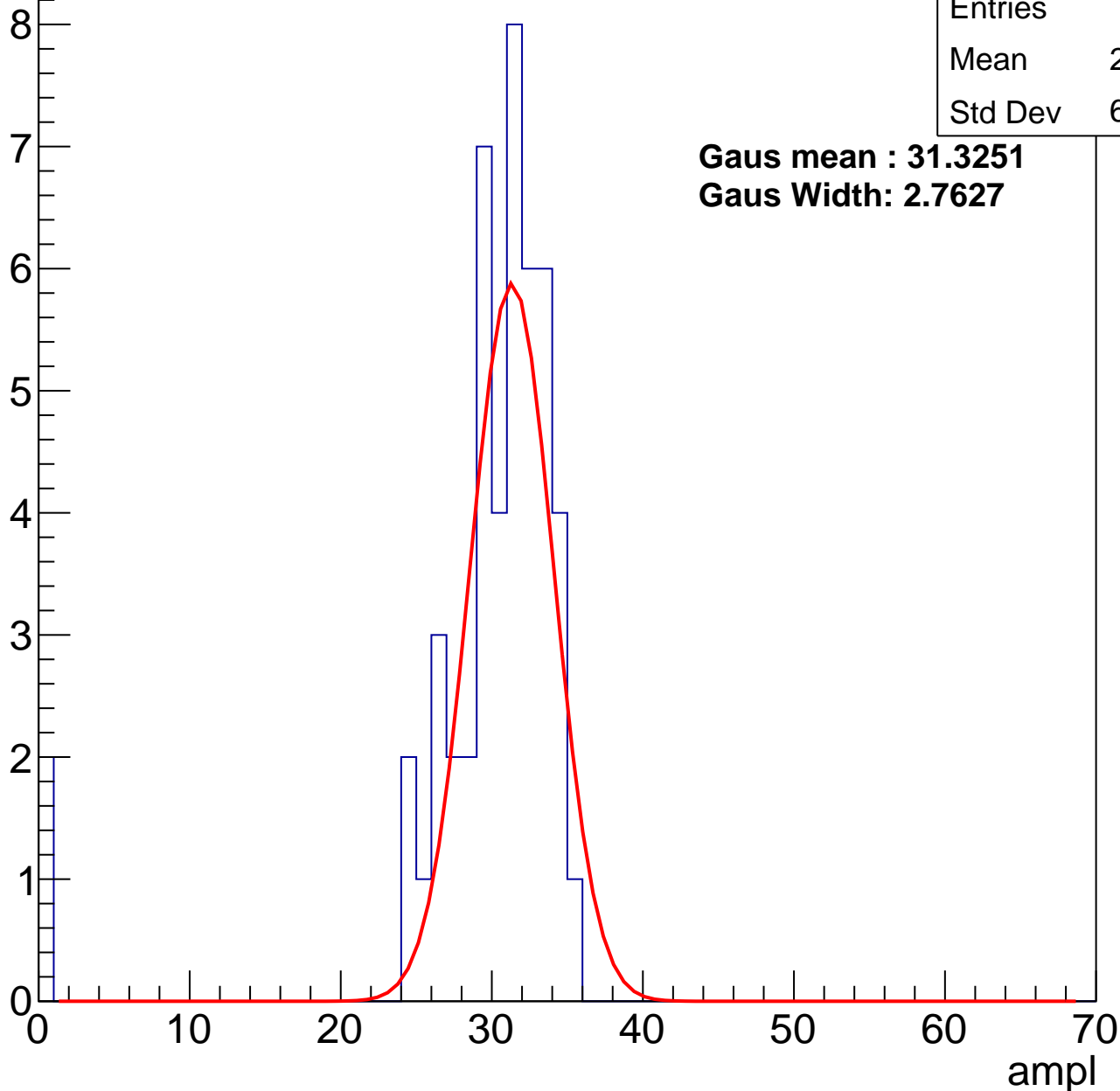
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	29.02
Std Dev	6.625

Gaus mean : 31.3251

Gaus Width: 2.7627



B1L102S, U2-ch97, adc1

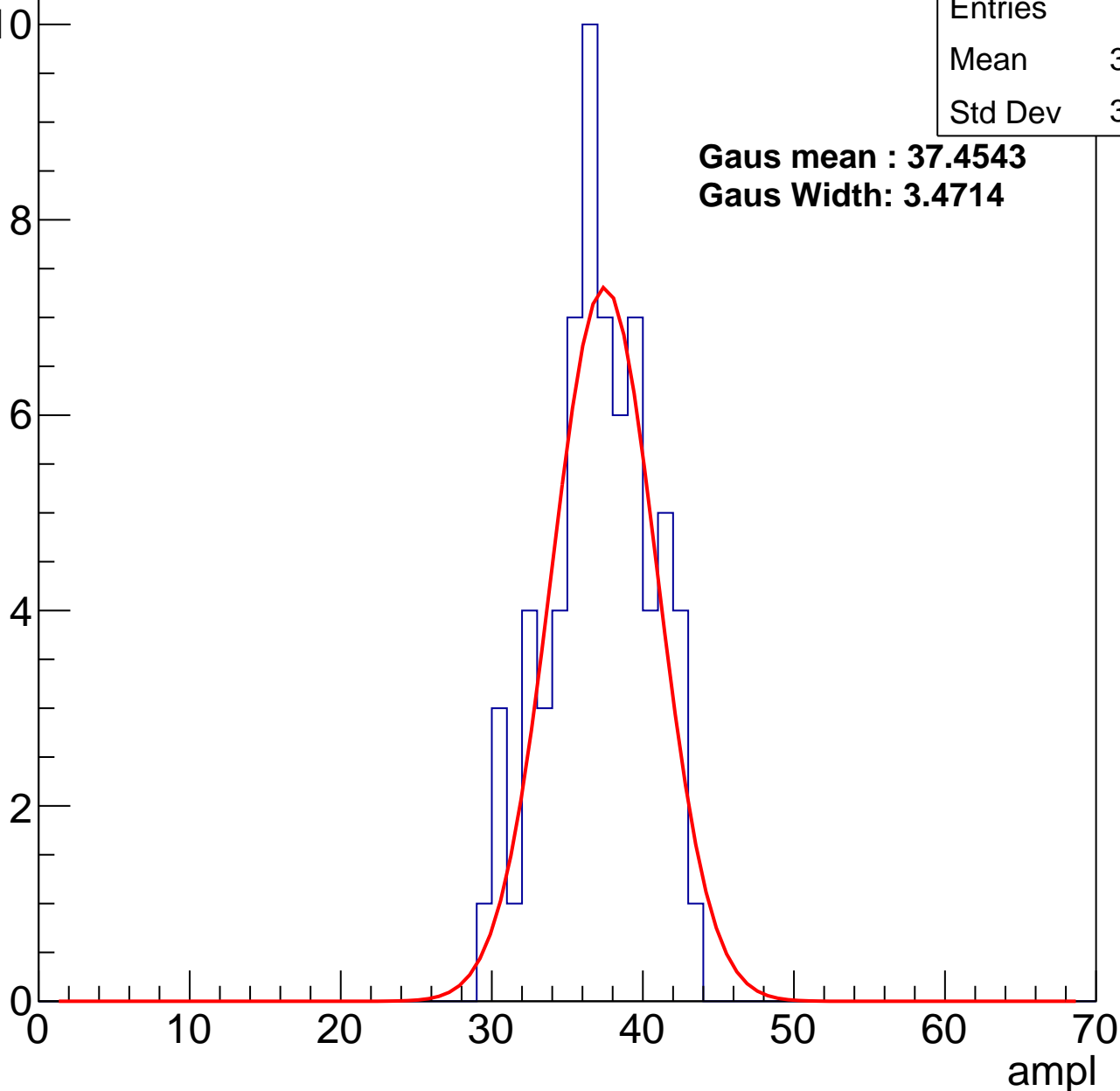
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	36.63
Std Dev	3.336

Gaus mean : 37.4543

Gaus Width: 3.4714



B1L102S, U2-ch97, adc2

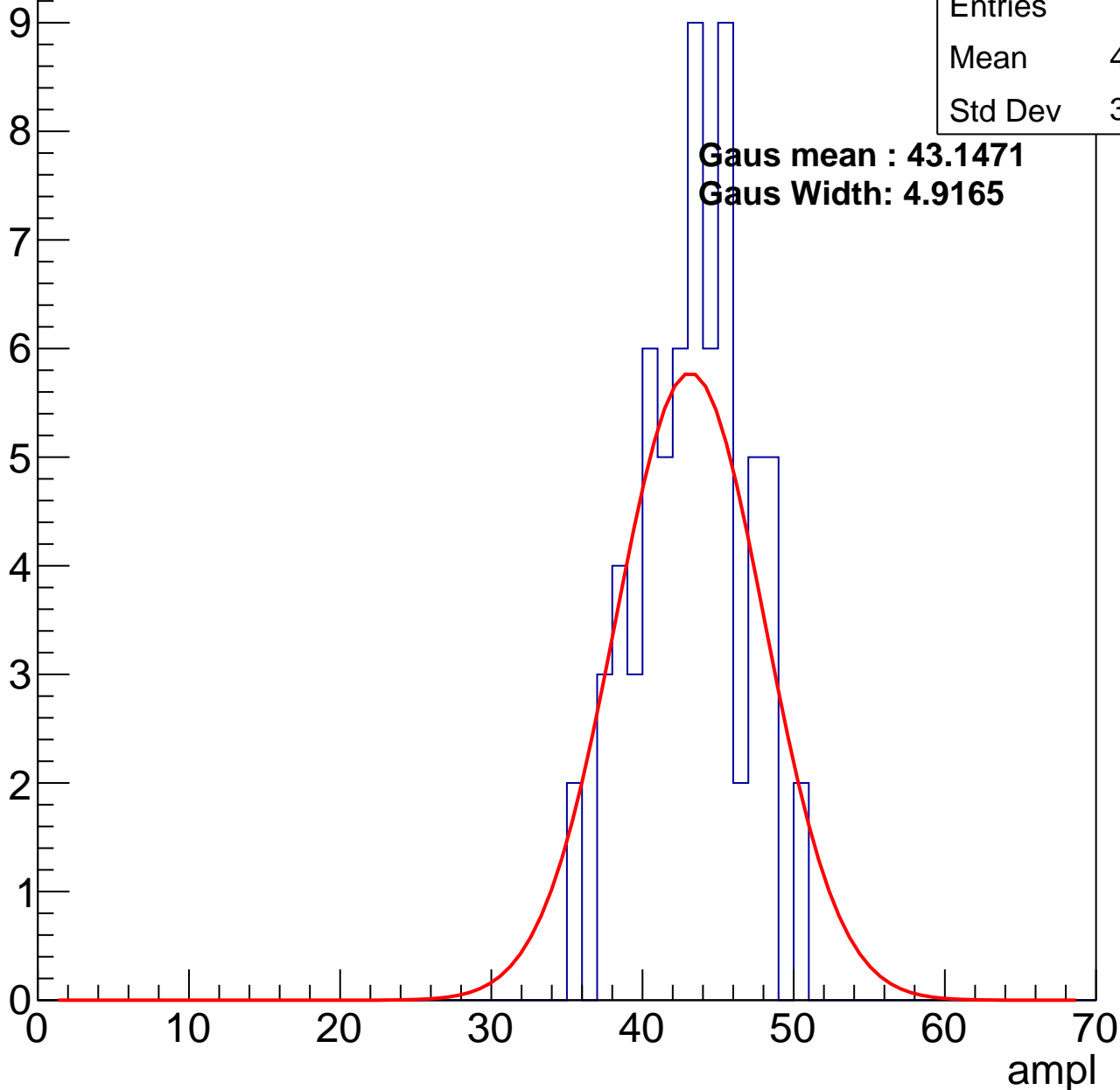
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.84
Std Dev	3.509

Gaus mean : 43.1471

Gaus Width: 4.9165

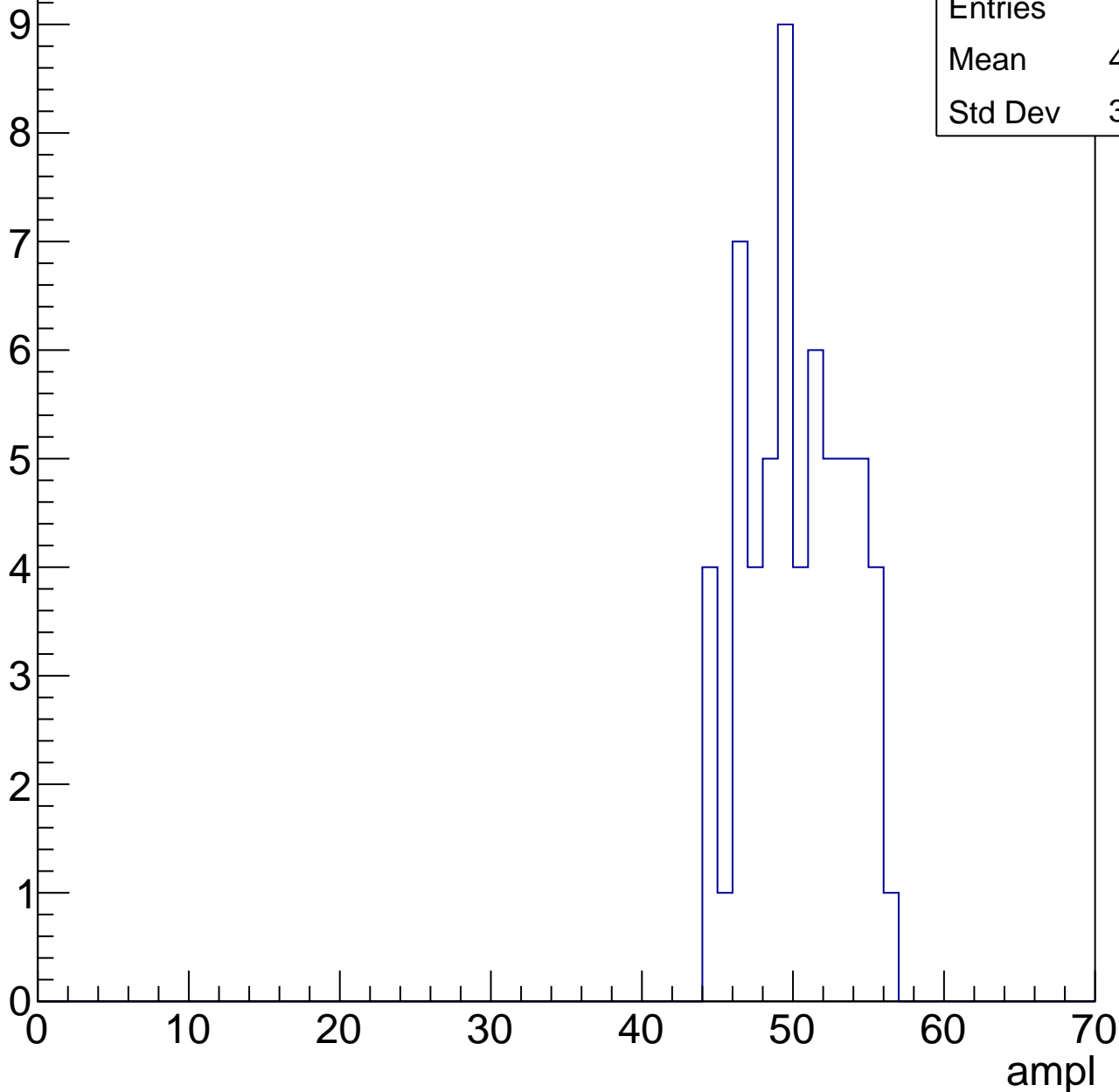


B1L102S, U2-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	49.82
Std Dev	3.243

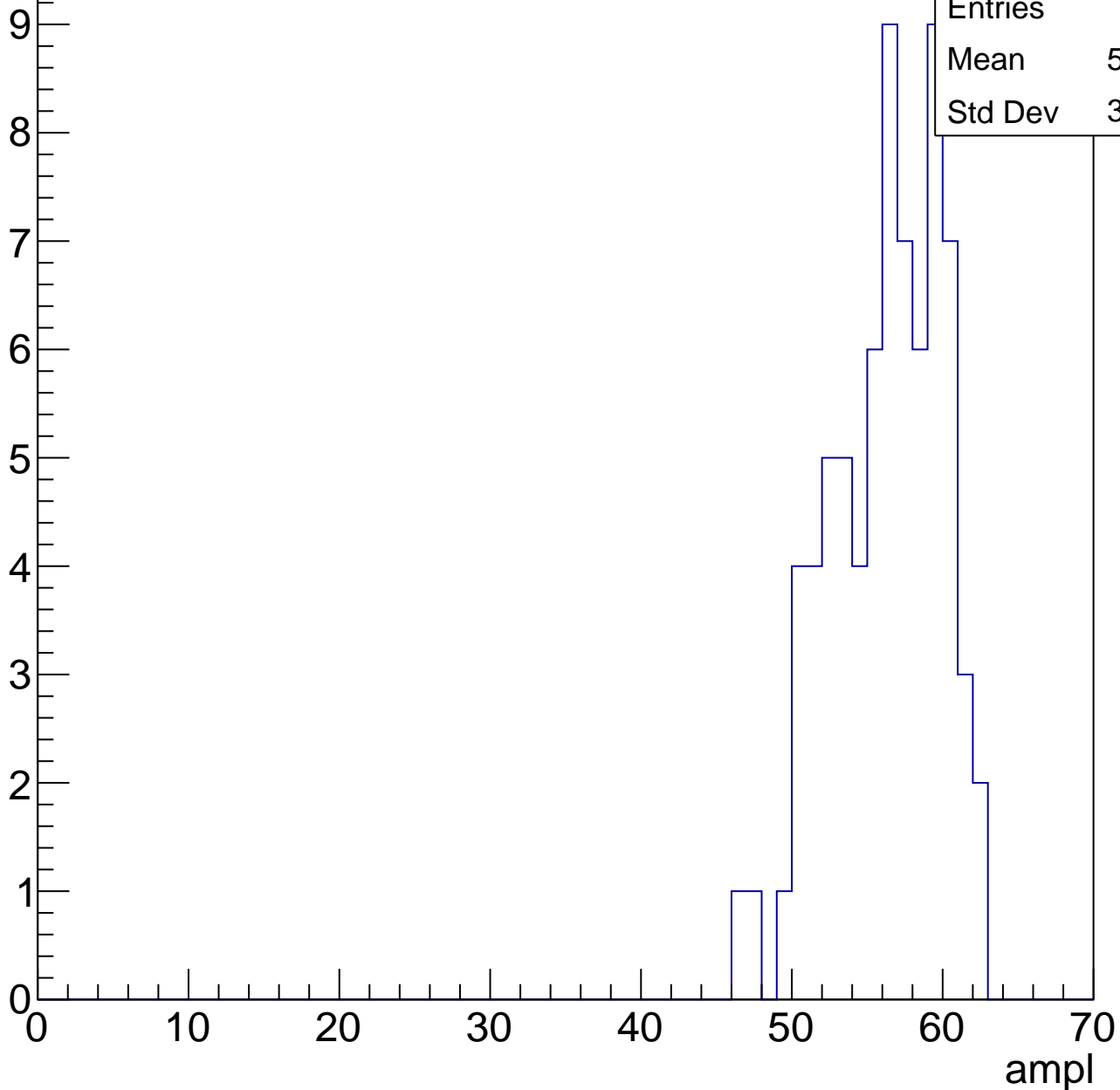


B1L102S, U2-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	55.76
Std Dev	3.653

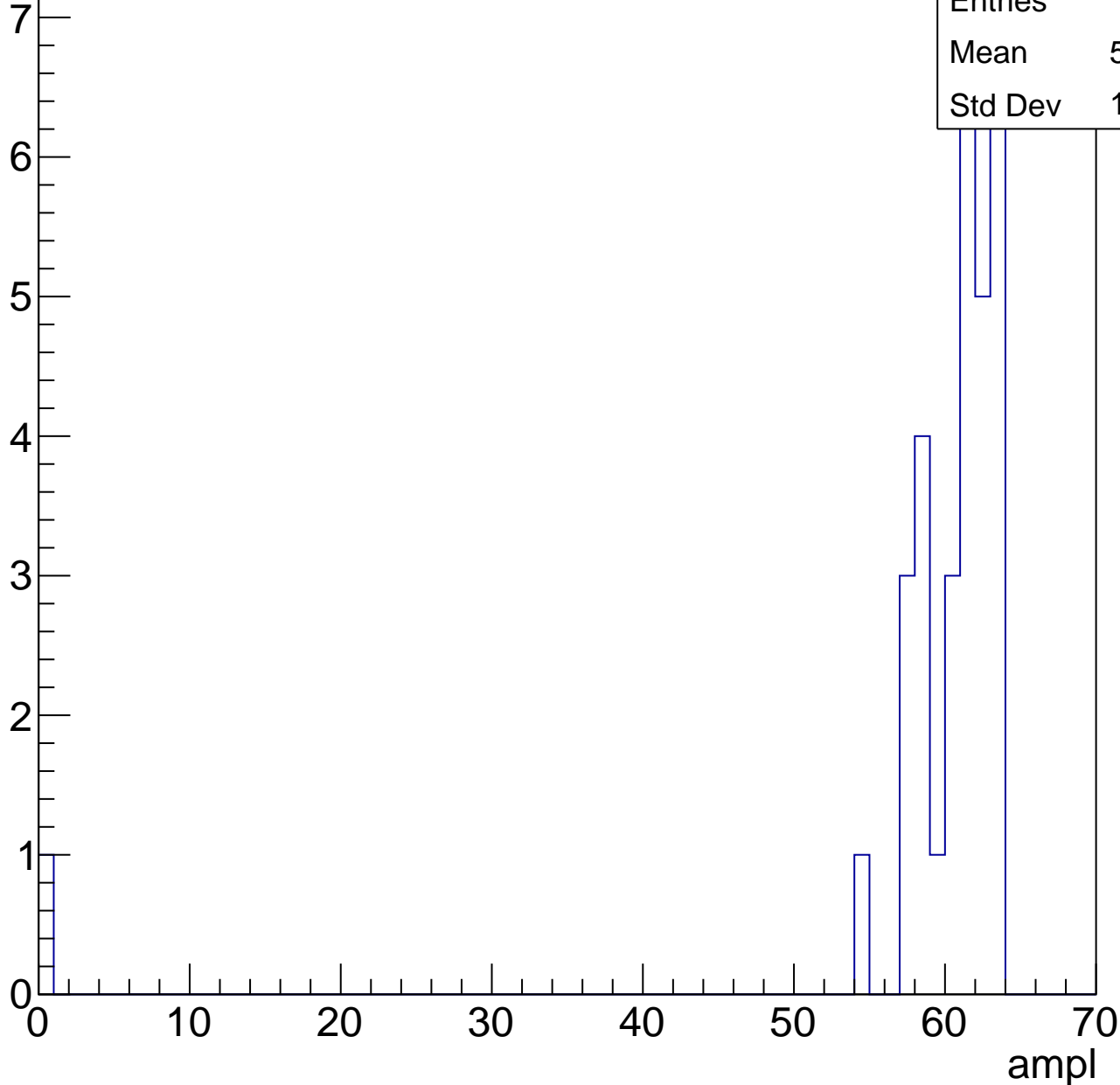


B1L102S, U2-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

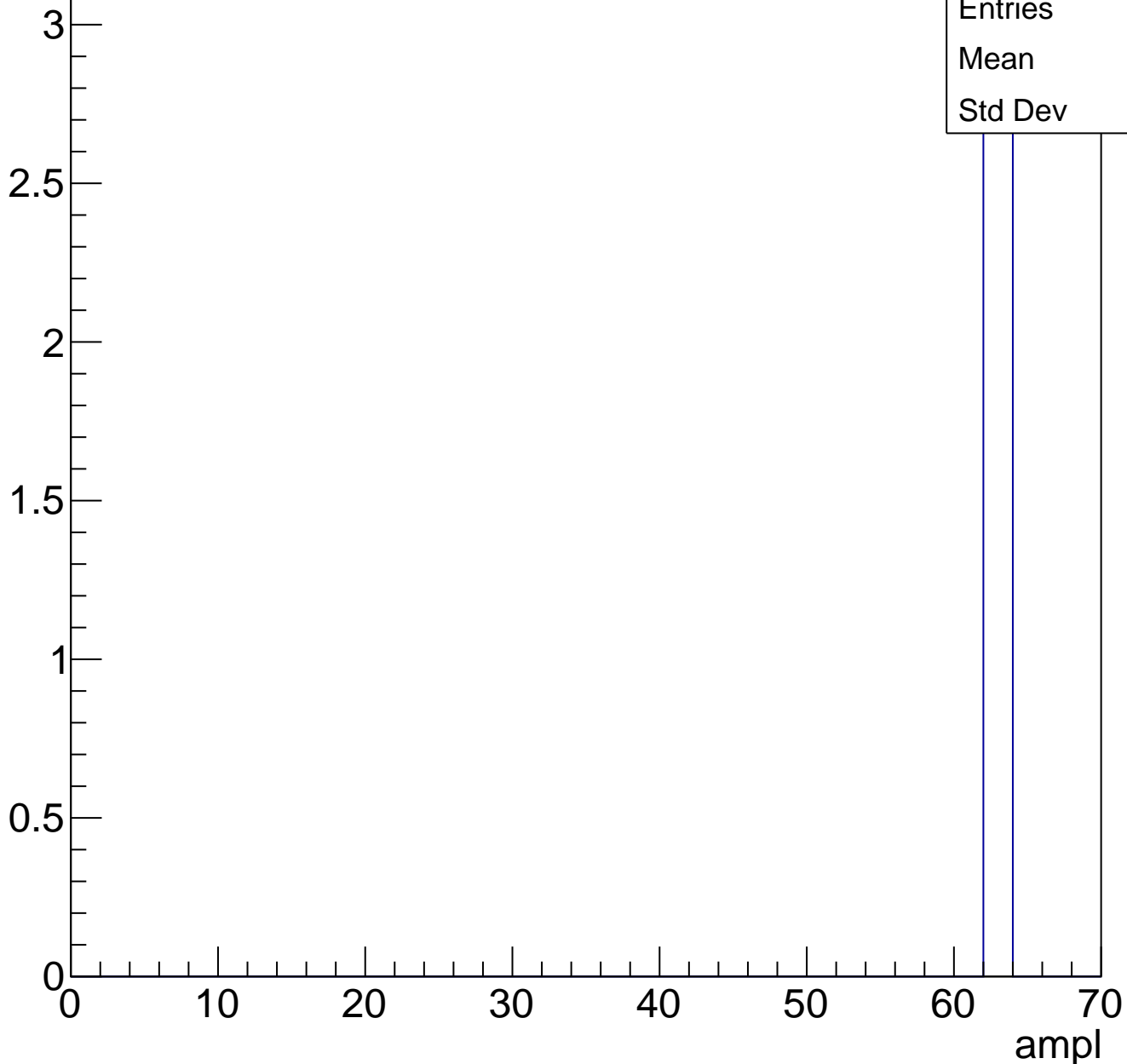
Entries	32
Mean	58.56
Std Dev	10.76



B1L102S, U2-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch98, adc0

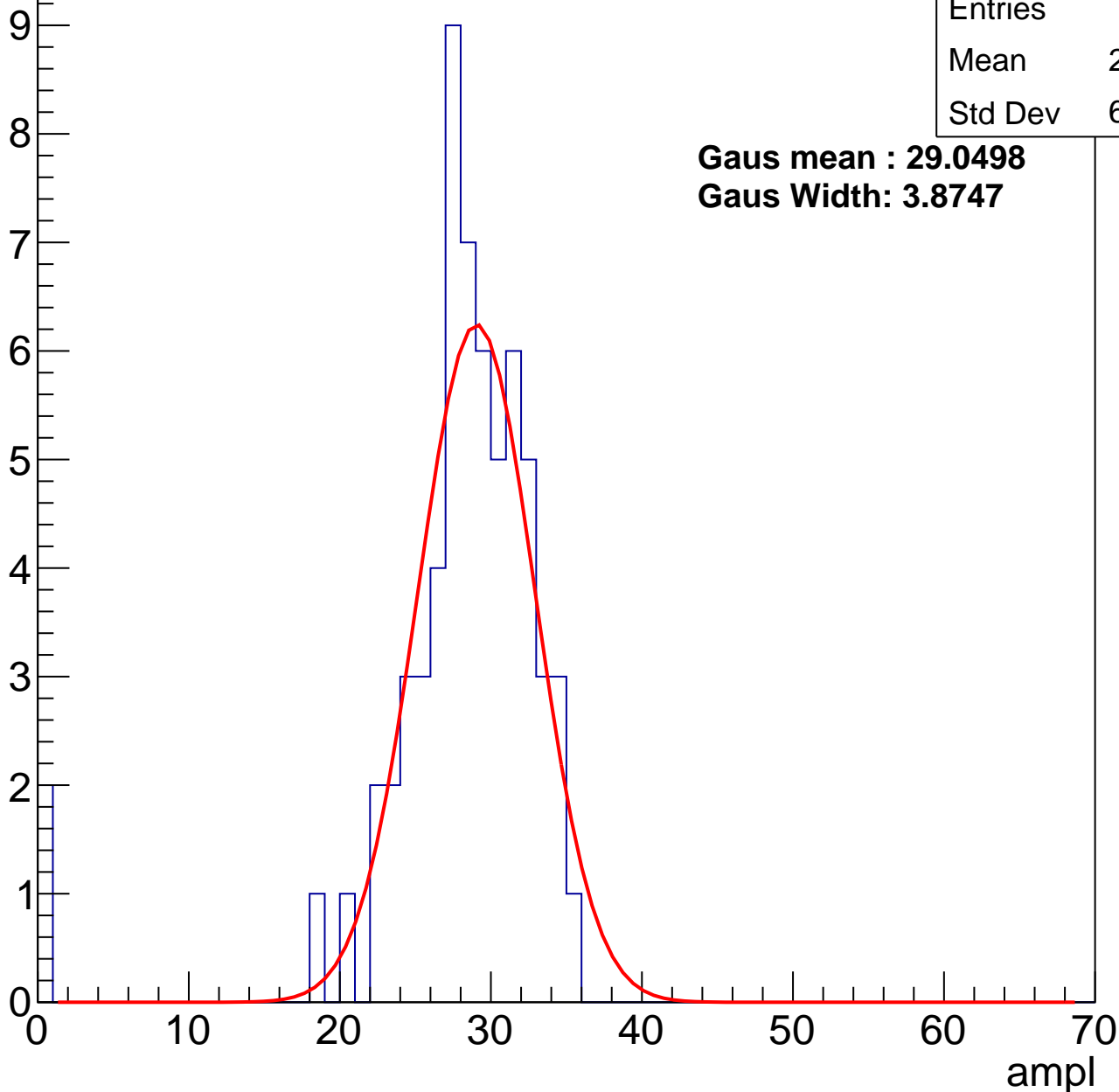
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.37
Std Dev	6.069

Gaus mean : 29.0498

Gaus Width: 3.8747



B1L102S, U2-ch98, adc1

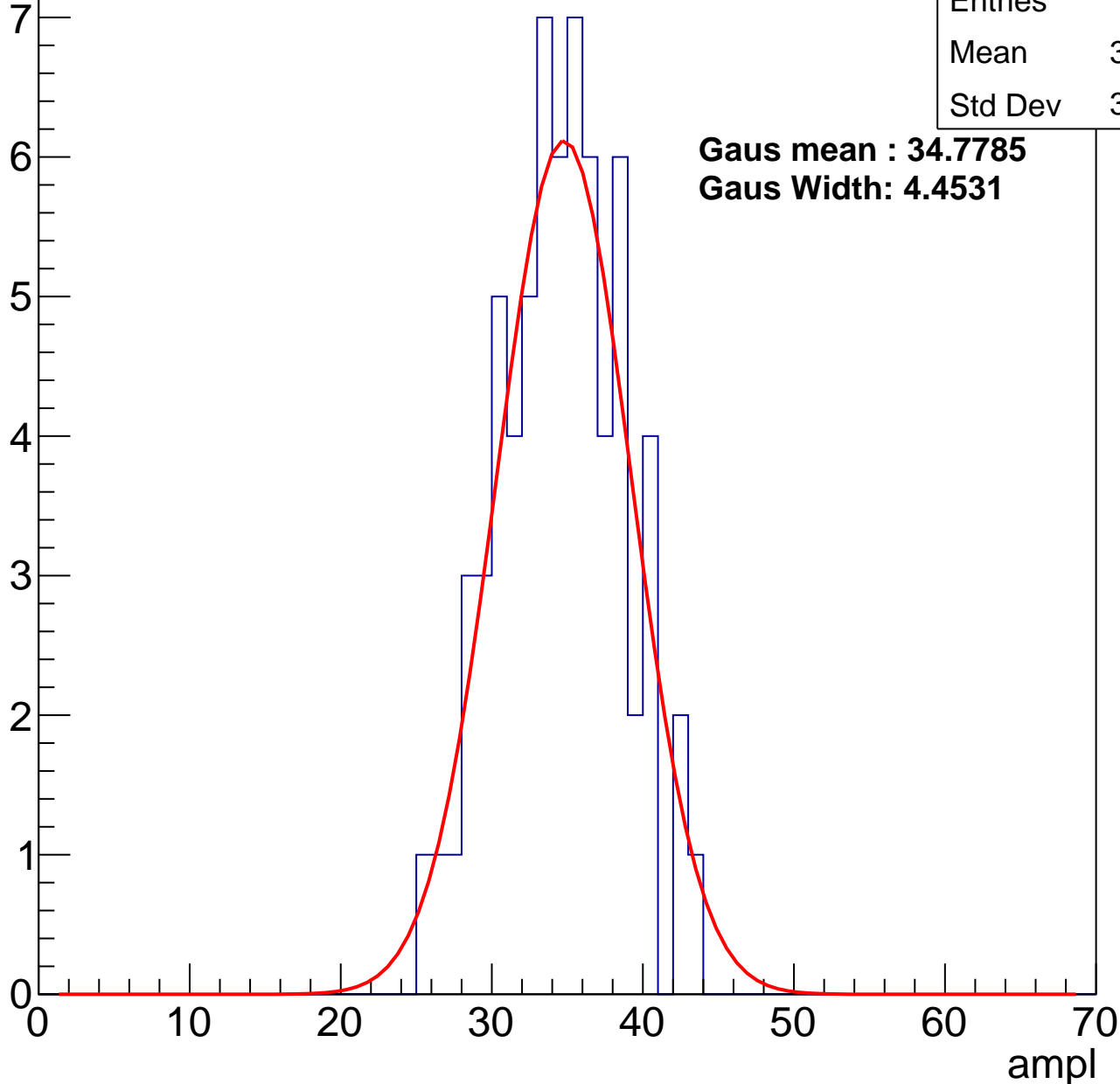
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.12
Std Dev	3.995

Gaus mean : 34.7785

Gaus Width: 4.4531



B1L102S, U2-ch98, adc2

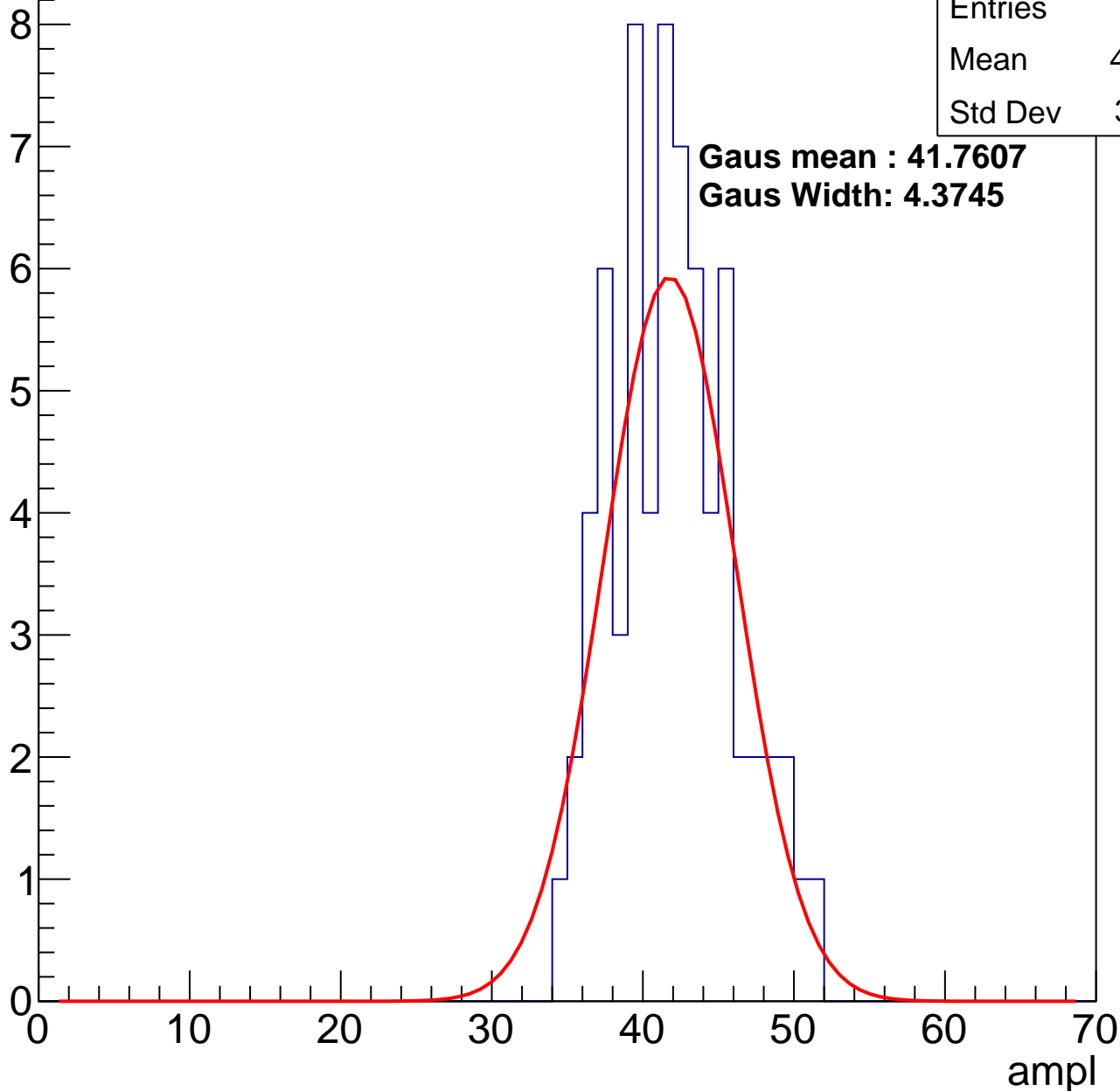
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.49
Std Dev	3.911

Gaus mean : 41.7607

Gaus Width: 4.3745

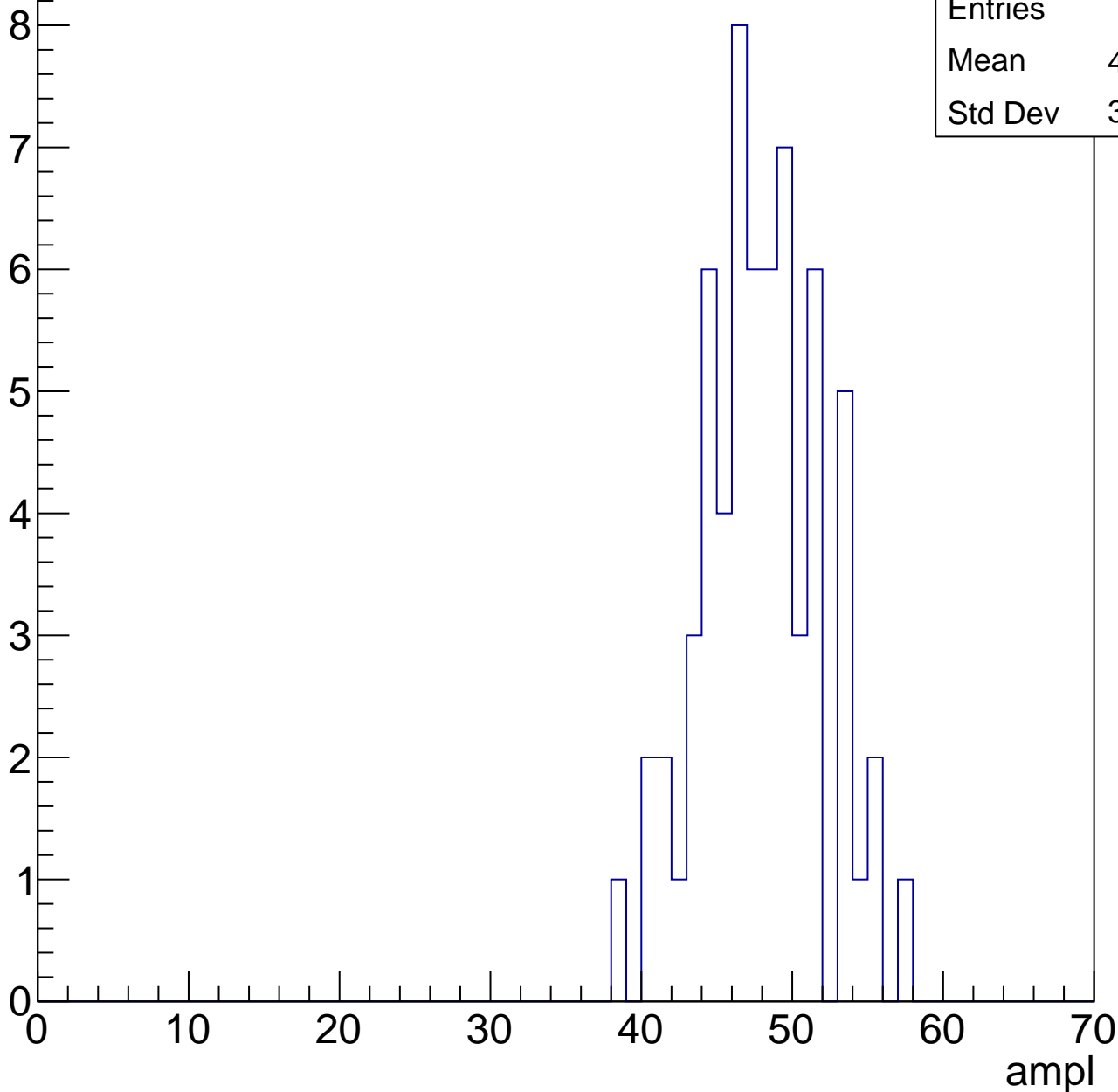


B1L102S, U2-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.47
Std Dev	3.957

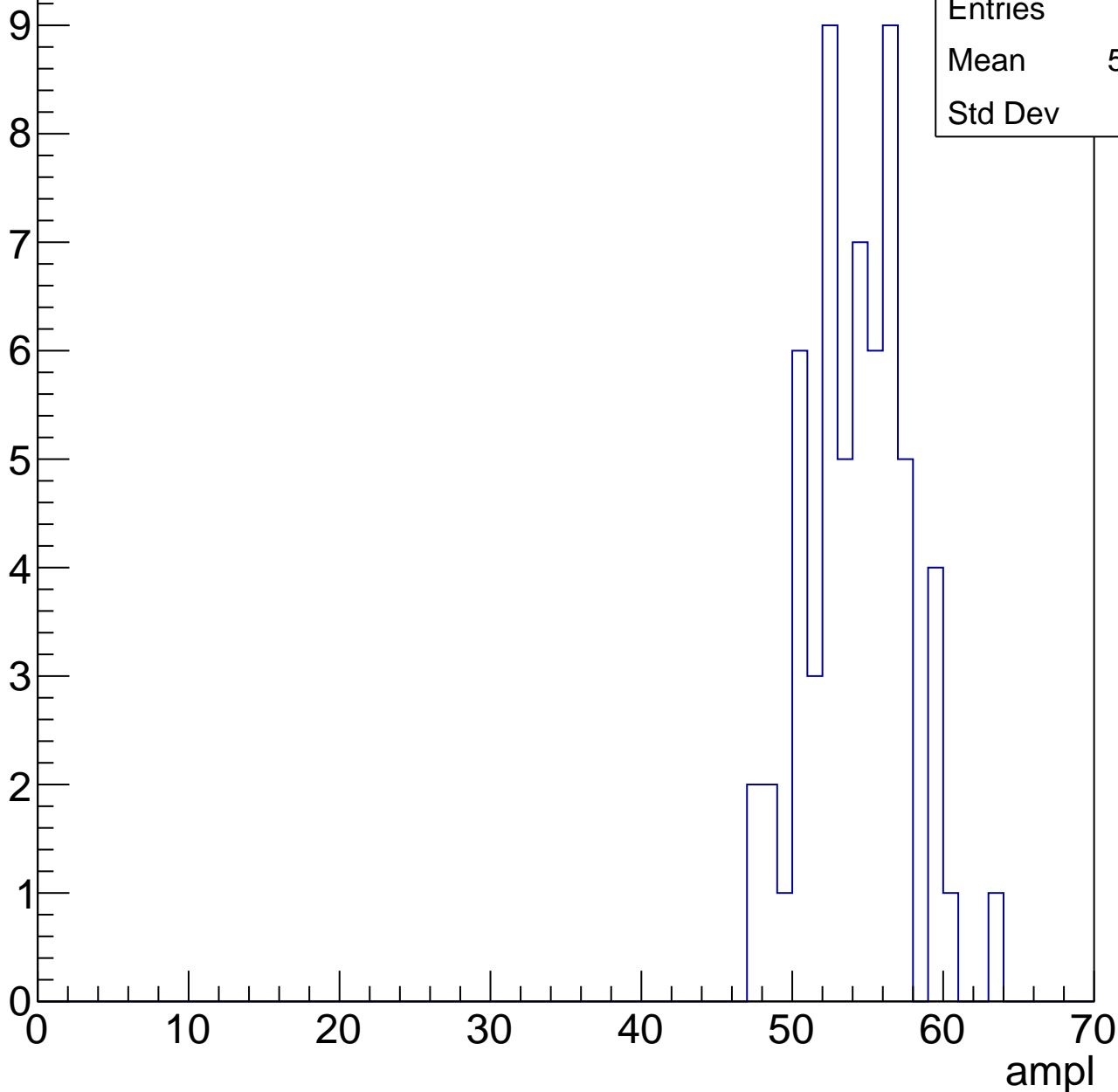


B1L102S, U2-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	53.79
Std Dev	3.29

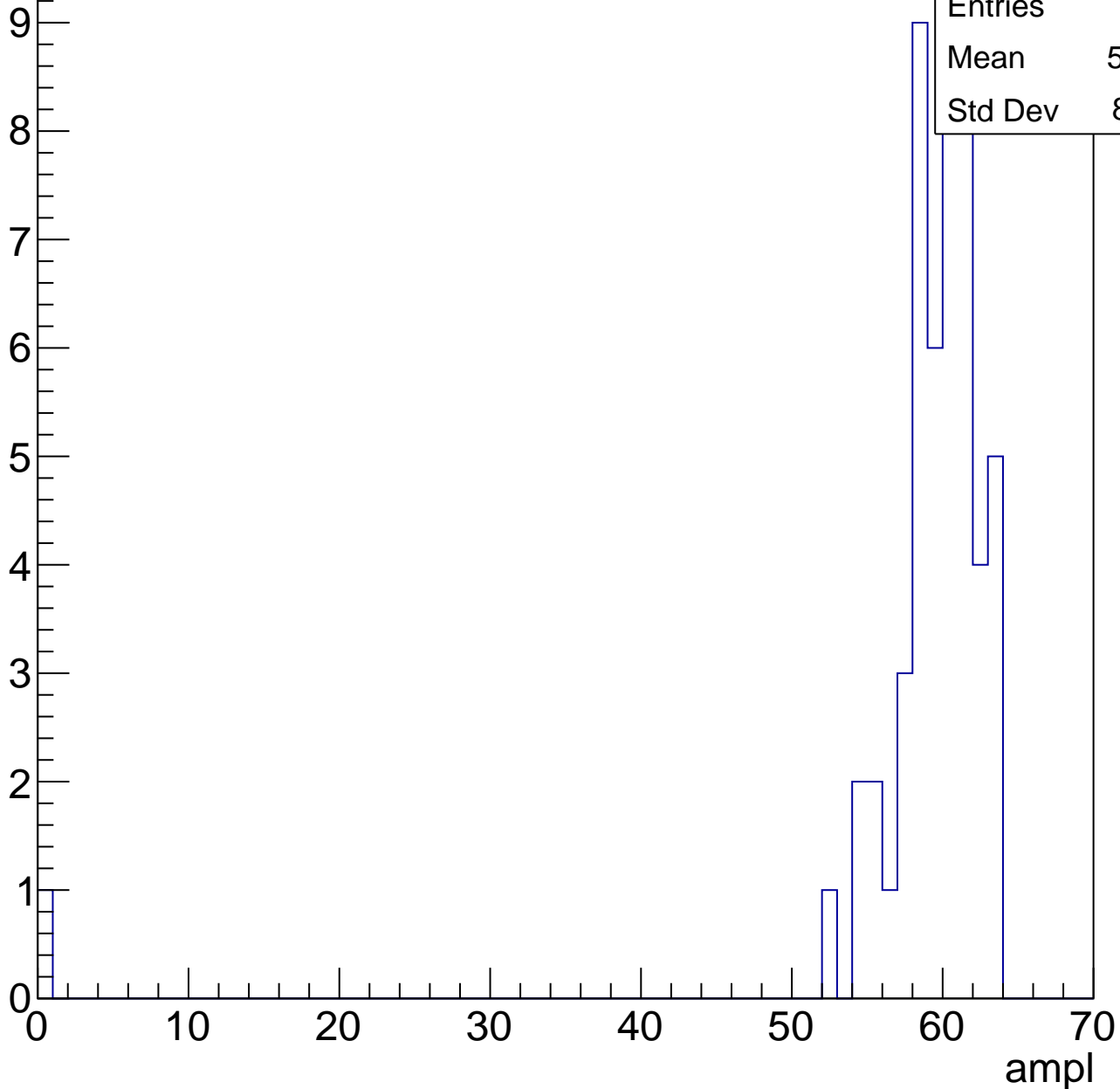


B1L102S, U2-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	58.14
Std Dev	8.591

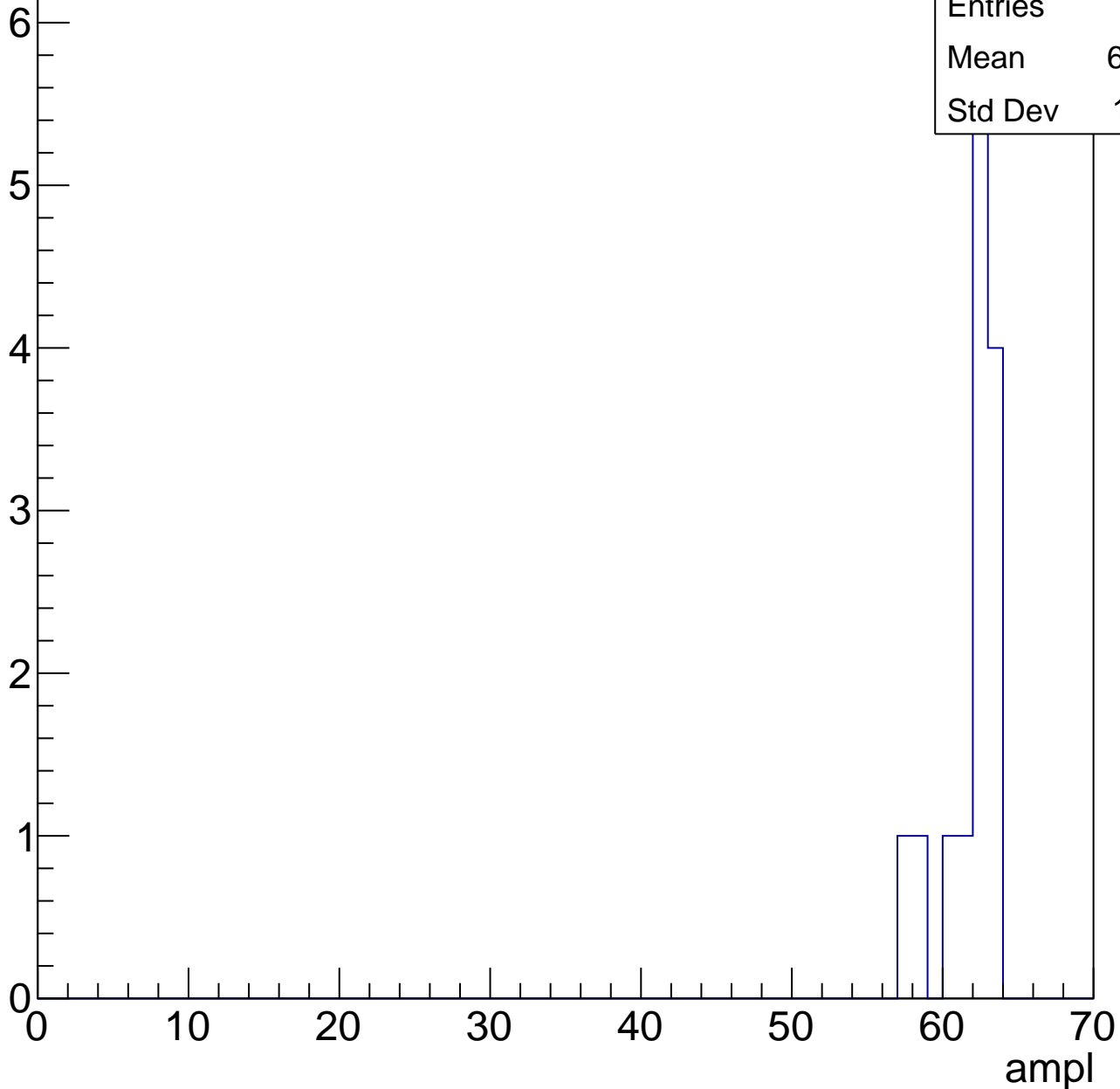


B1L102S, U2-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

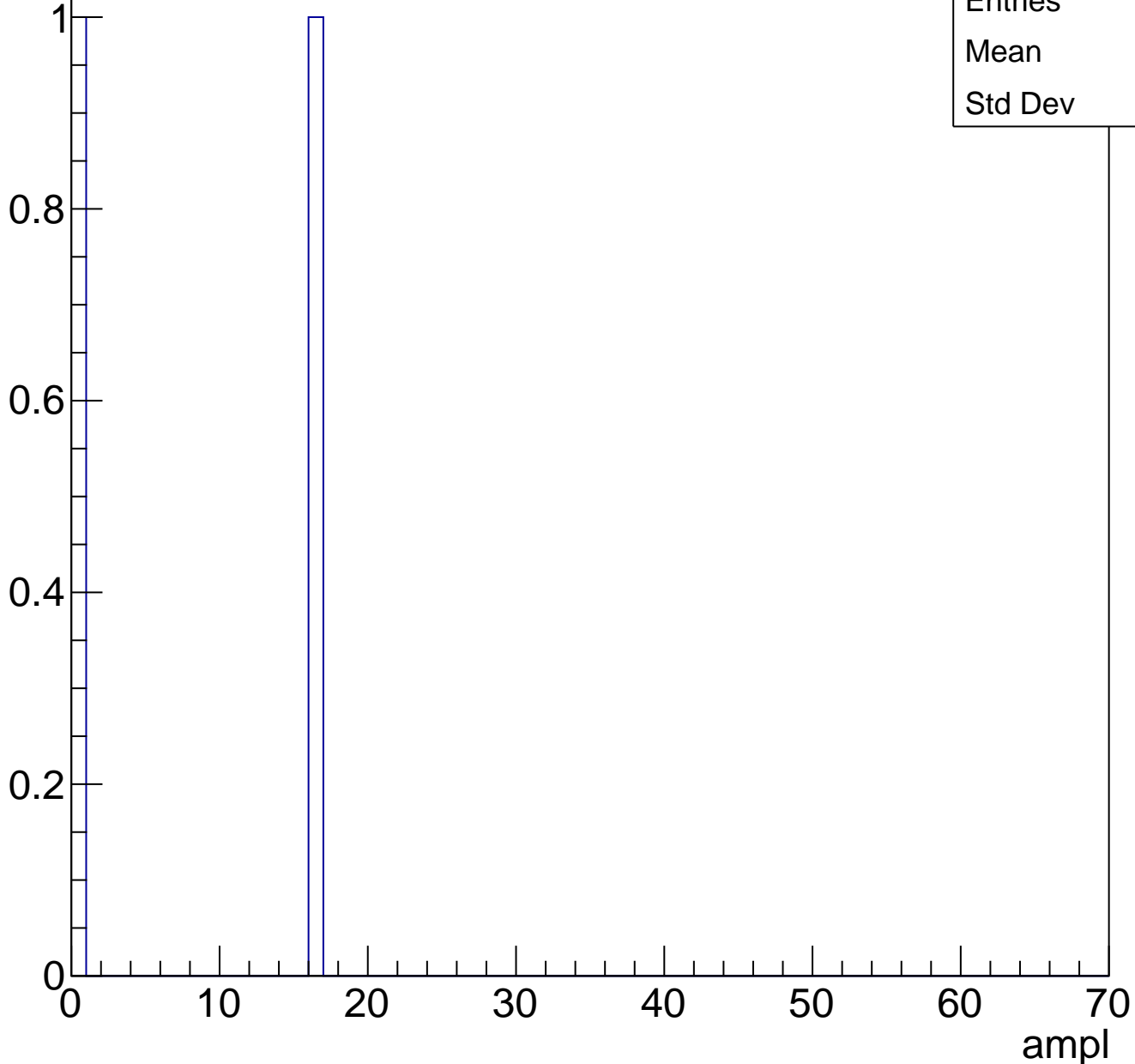
Entries	14
Mean	61.43
Std Dev	1.801



B1L102S, U2-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8
Std Dev	8

B1L102S, U2-ch99, adc0

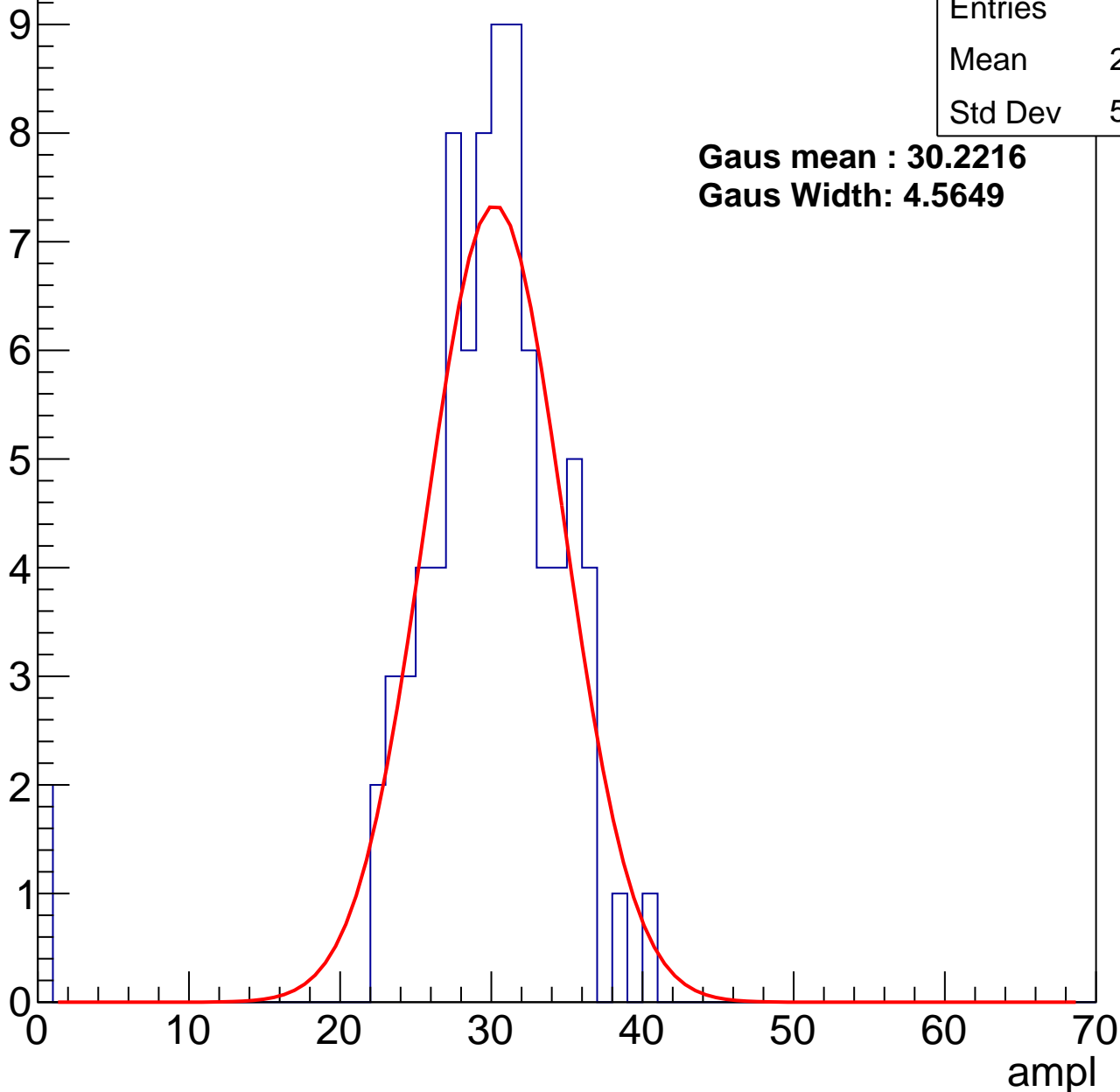
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	29.05
Std Dev	5.953

Gaus mean : 30.2216

Gaus Width: 4.5649



B1L102S, U2-ch99, adc1

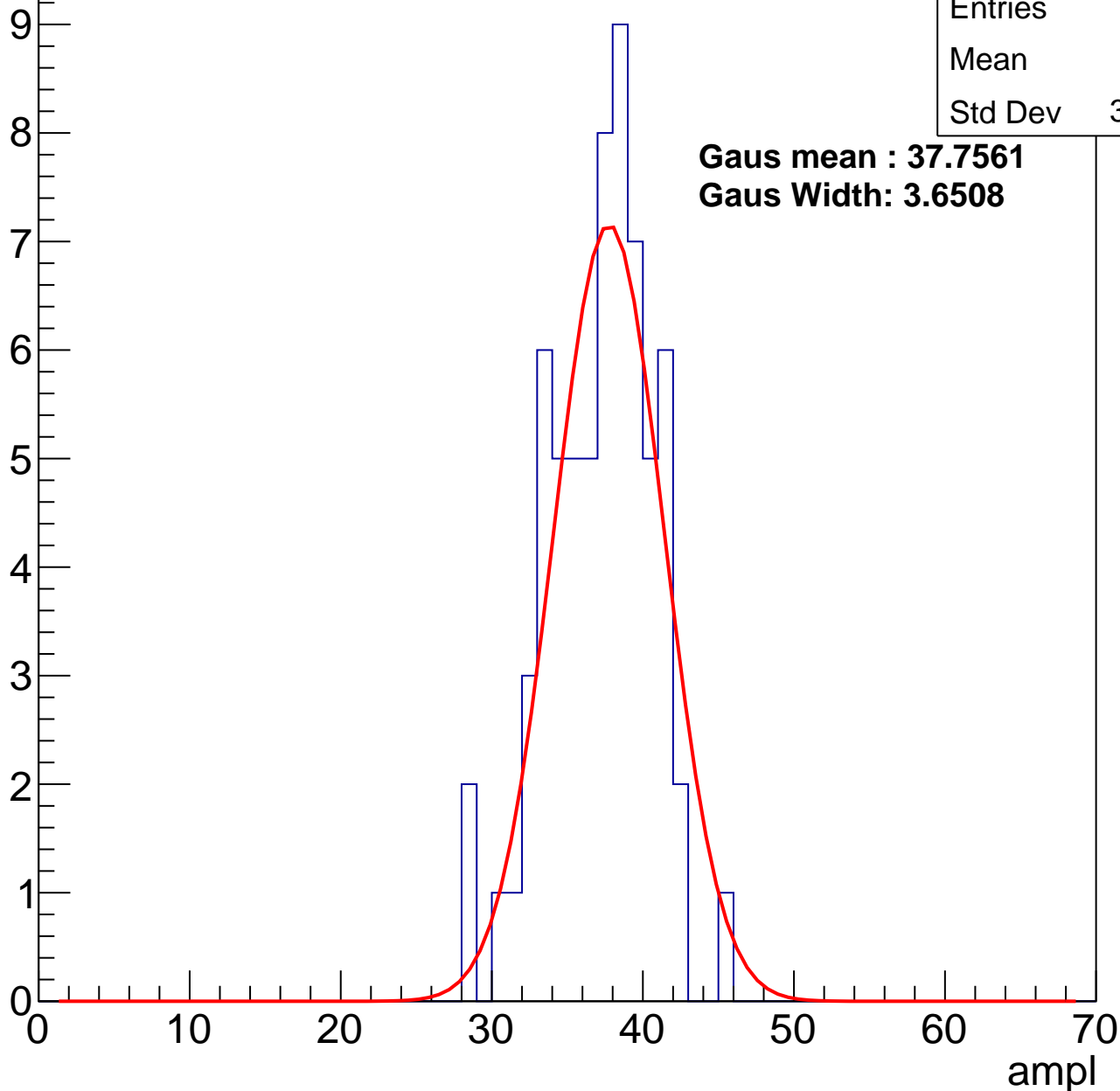
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.7
Std Dev	3.424

Gaus mean : 37.7561

Gaus Width: 3.6508



B1L102S, U2-ch99, adc2

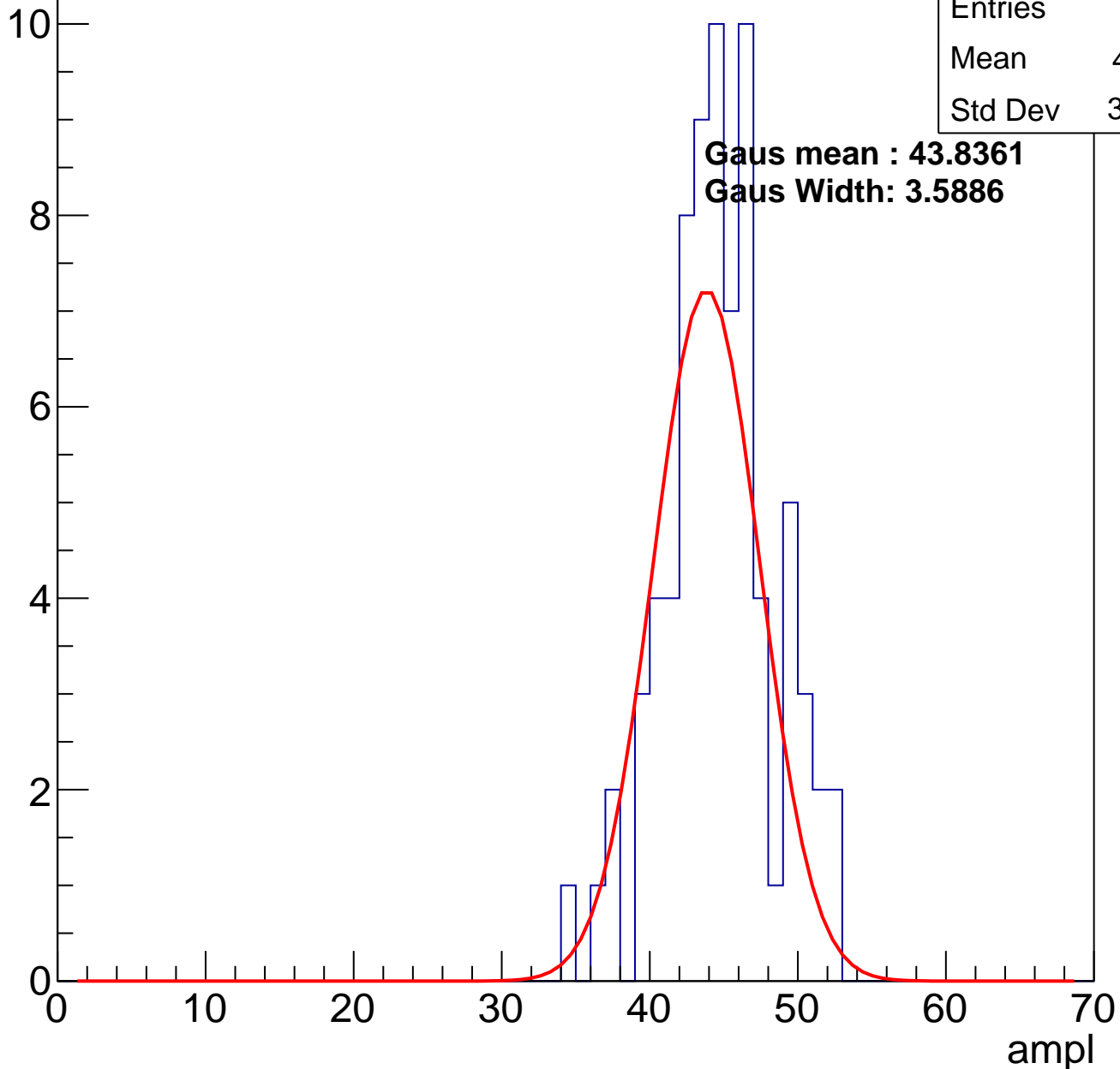
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	44.21
Std Dev	3.686

Gaus mean : 43.8361

Gaus Width: 3.5886

Entry

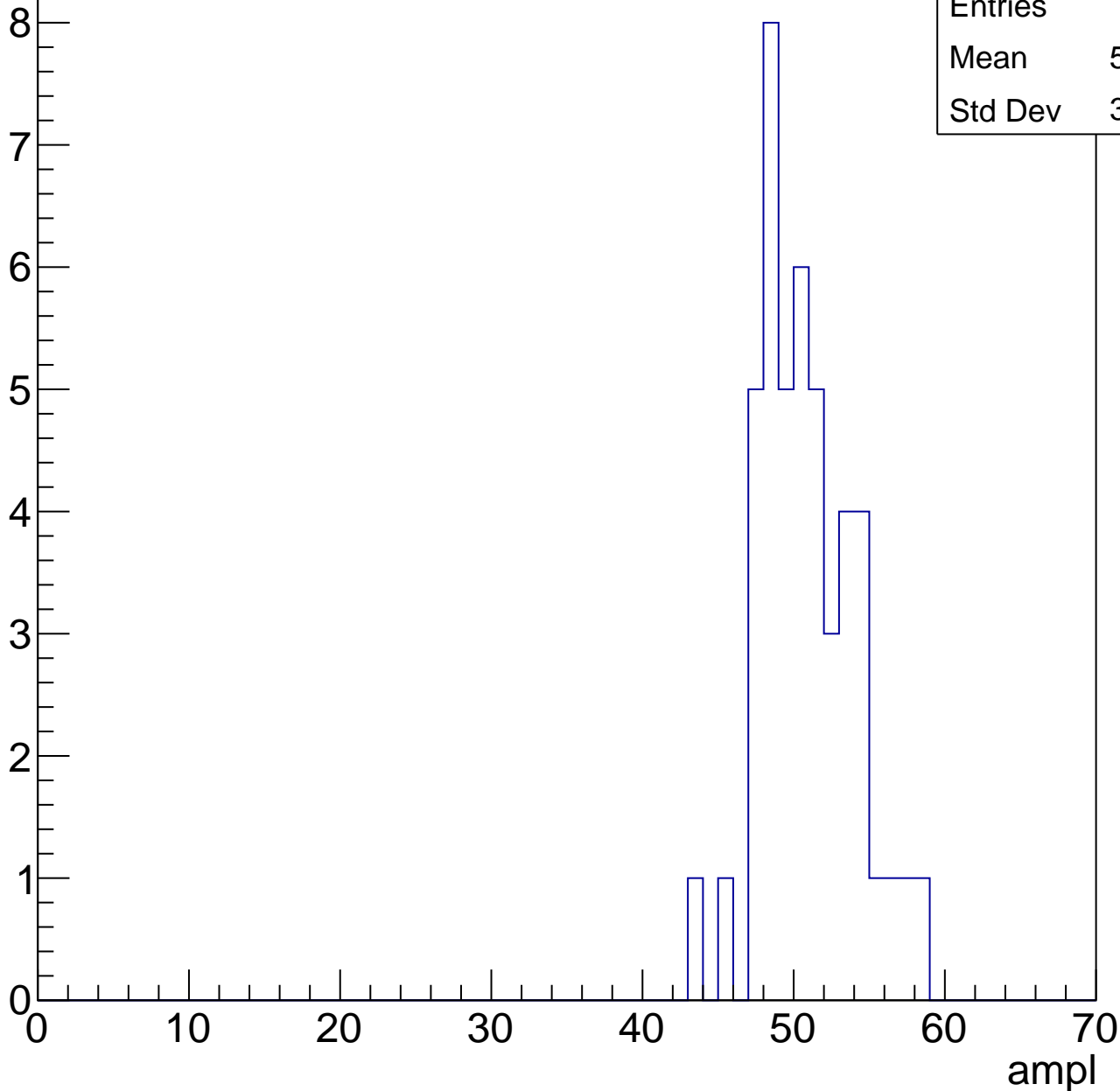


B1L102S, U2-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	50.37
Std Dev	3.095

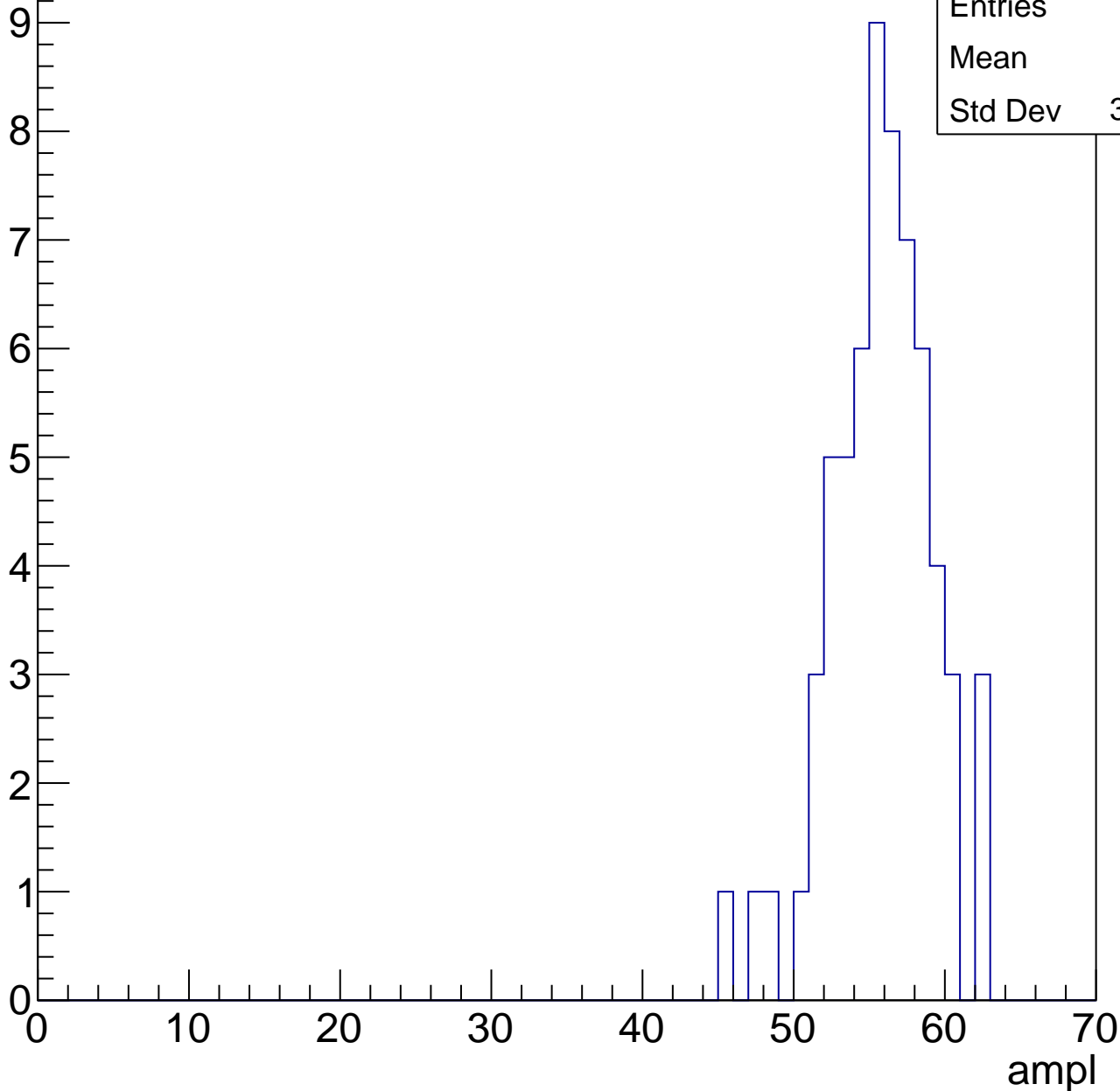


B1L102S, U2-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.3
Std Dev	3.398

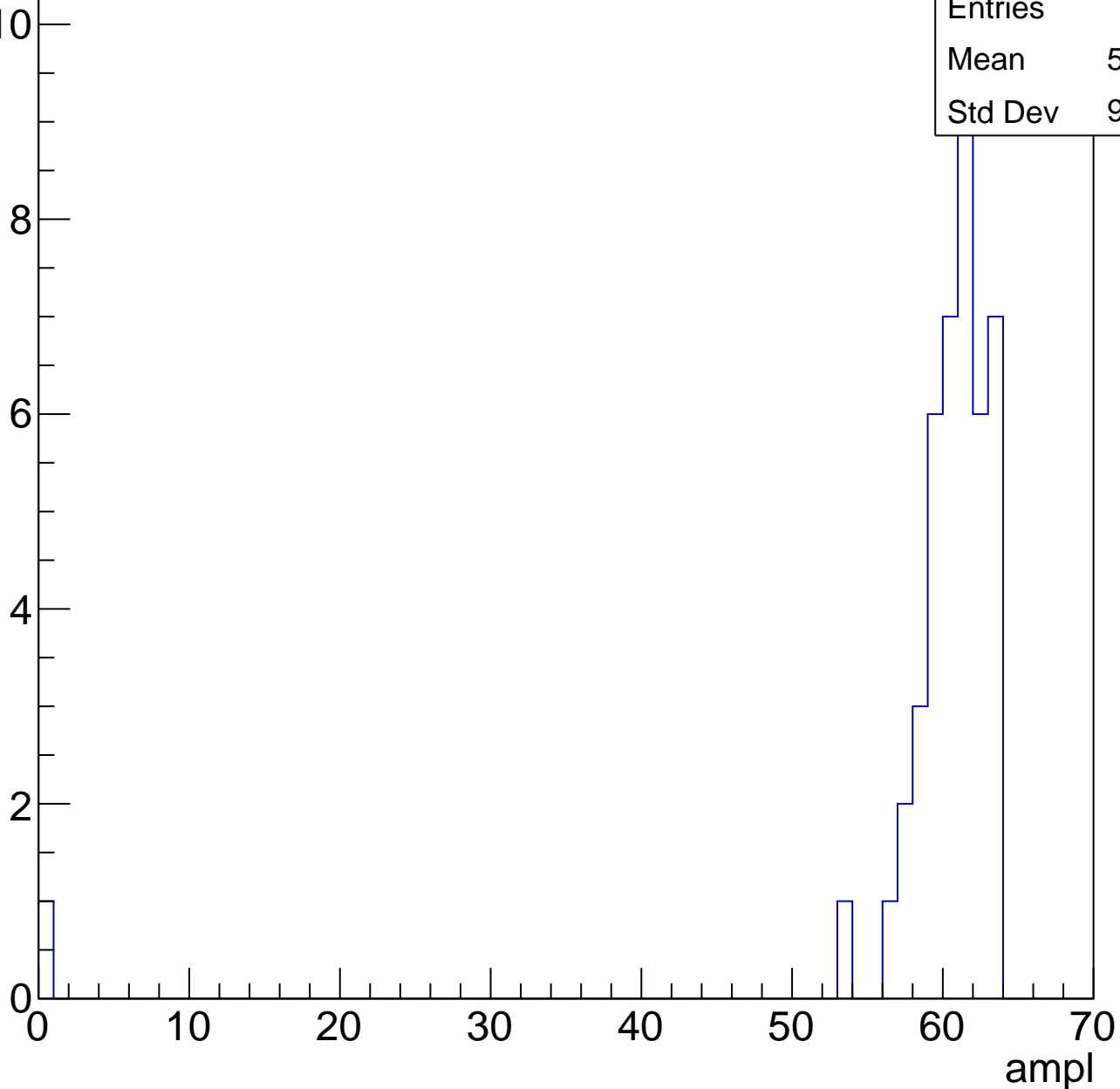


B1L102S, U2-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

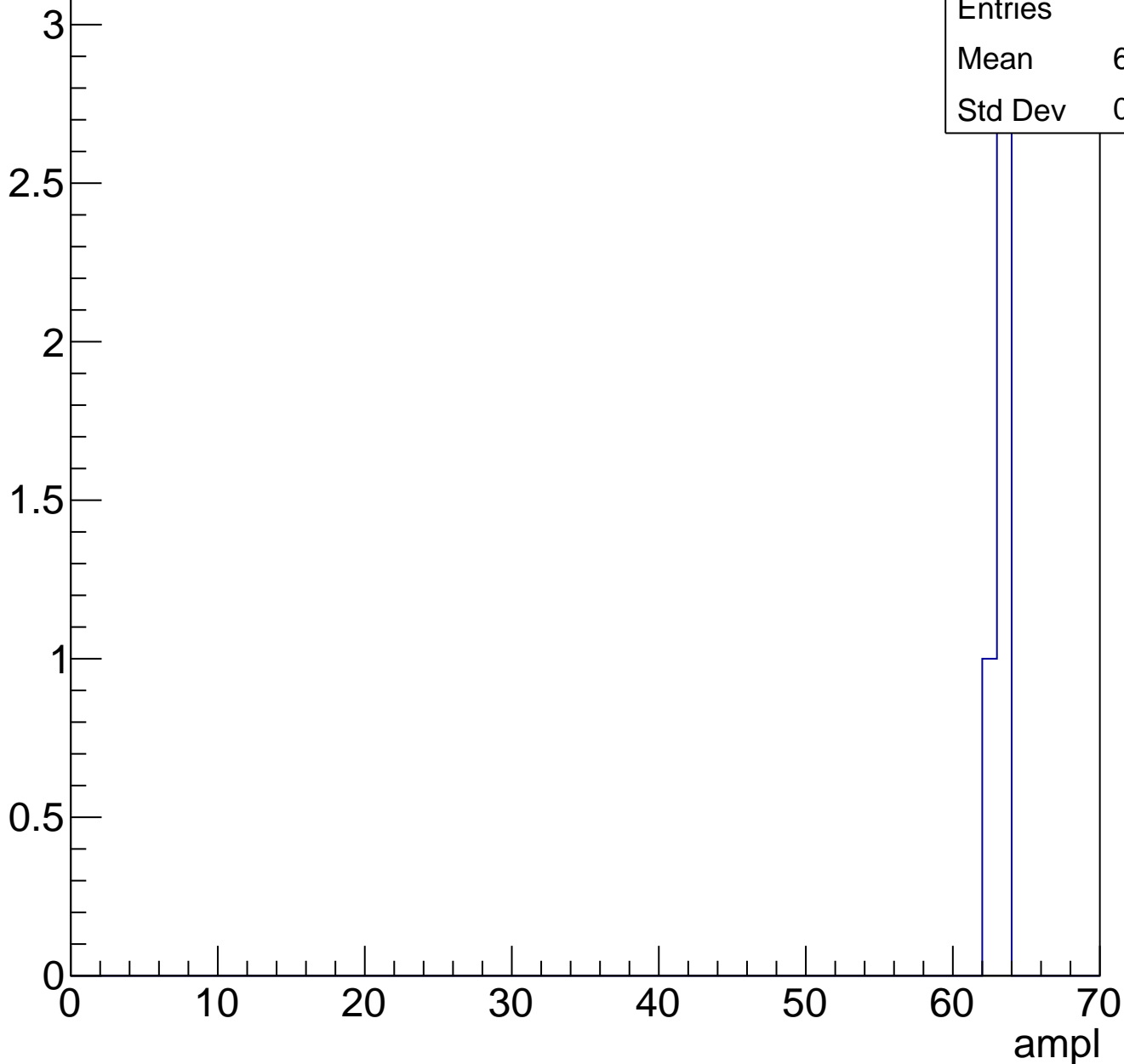
Entries	44
Mean	58.95
Std Dev	9.232



B1L102S, U2-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch100, adc0

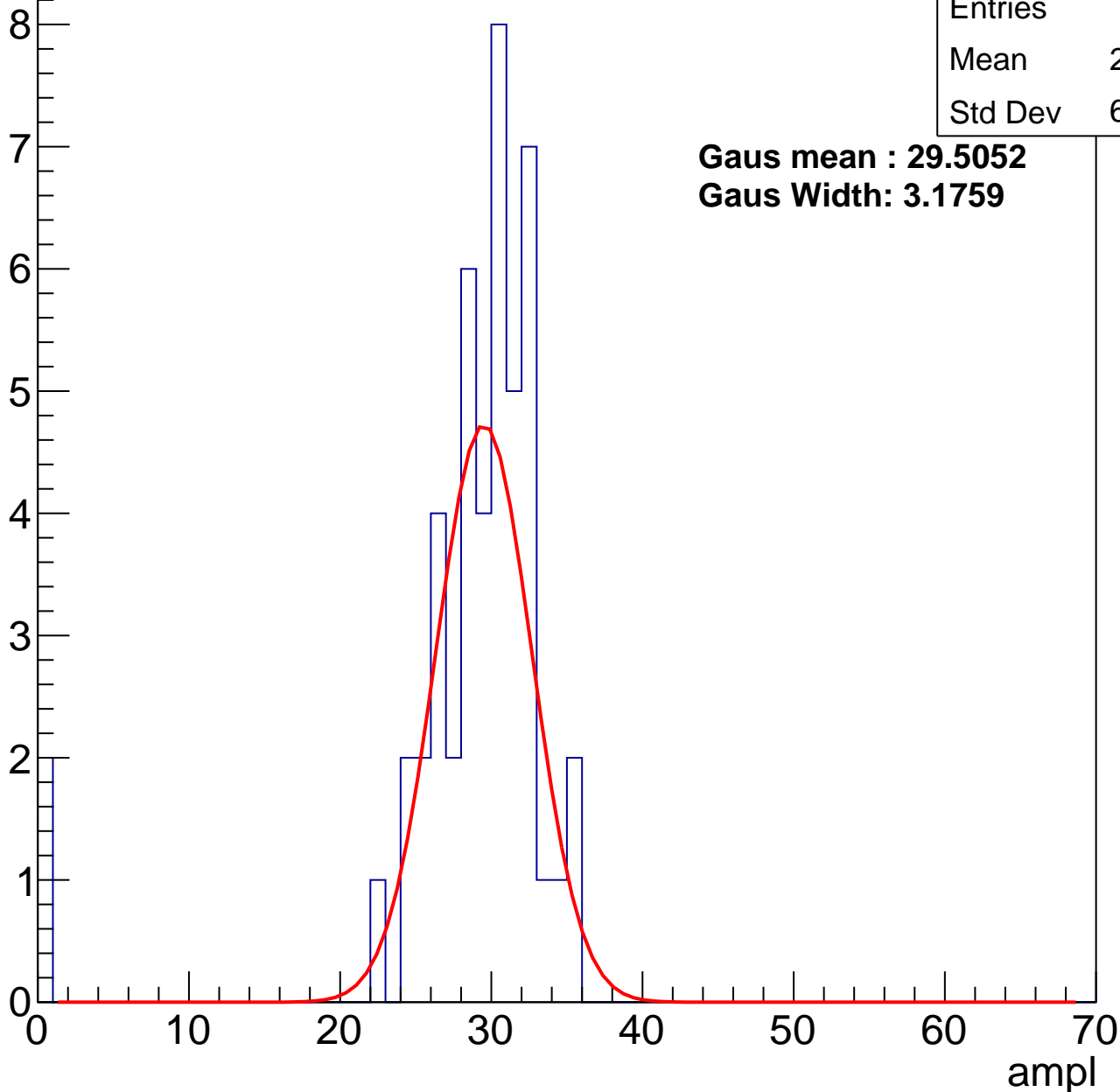
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	28.04
Std Dev	6.562

Gaus mean : 29.5052

Gaus Width: 3.1759



B1L102S, U2-ch100, adc1

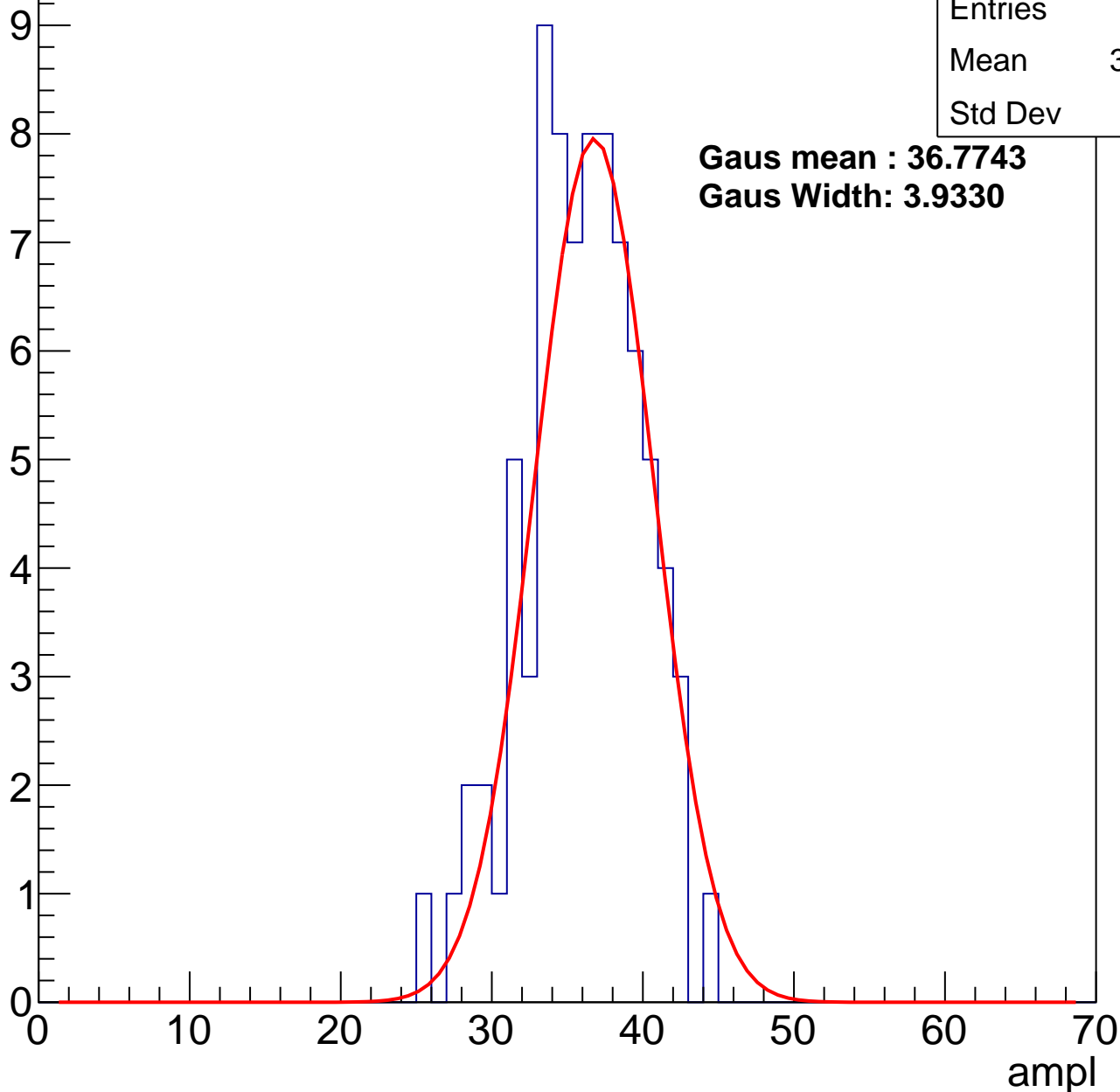
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.54
Std Dev	3.82

Gaus mean : 36.7743

Gaus Width: 3.9330



B1L102S, U2-ch100, adc2

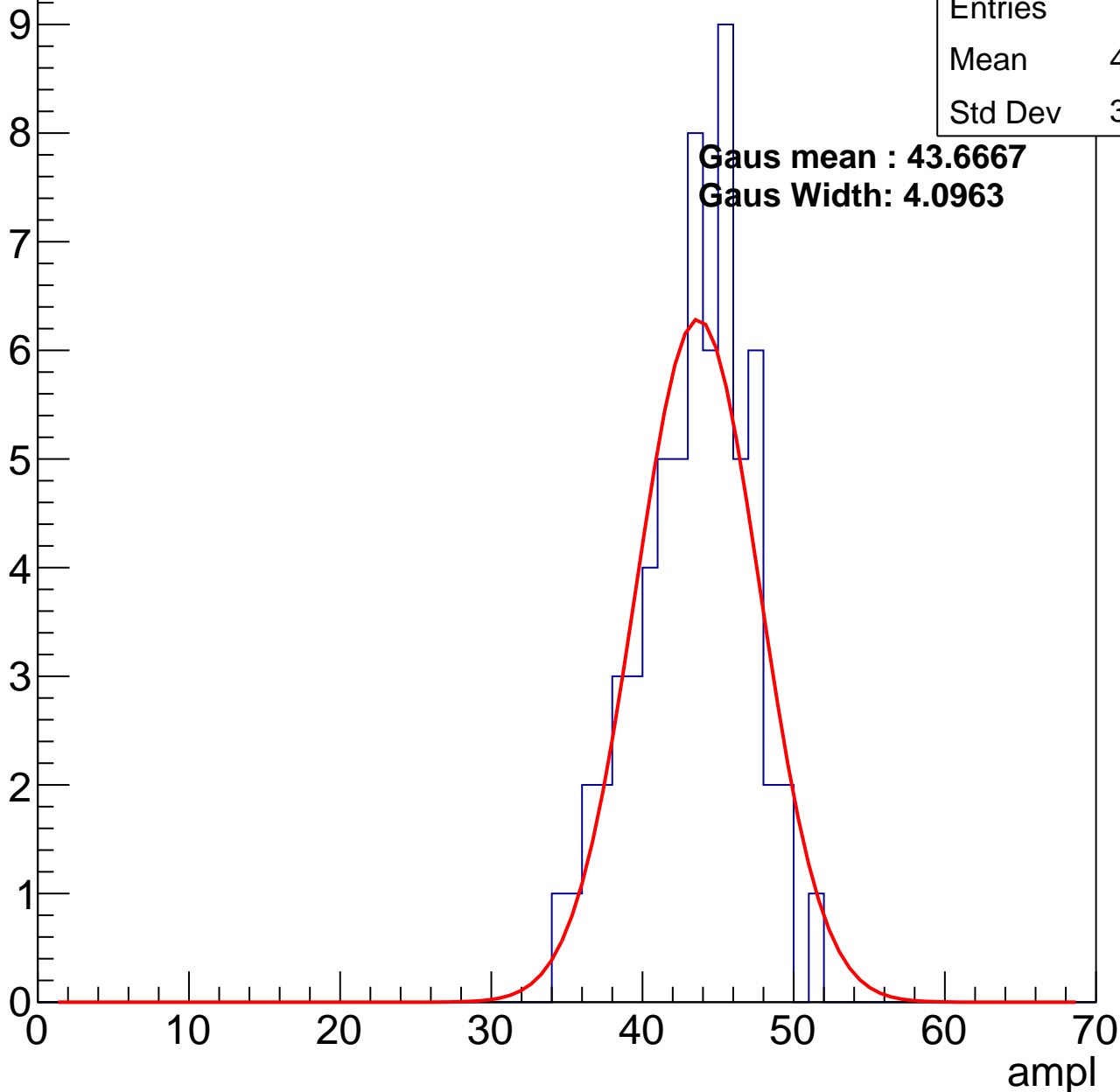
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.94
Std Dev	3.654

Gaus mean : 43.6667

Gaus Width: 4.0963

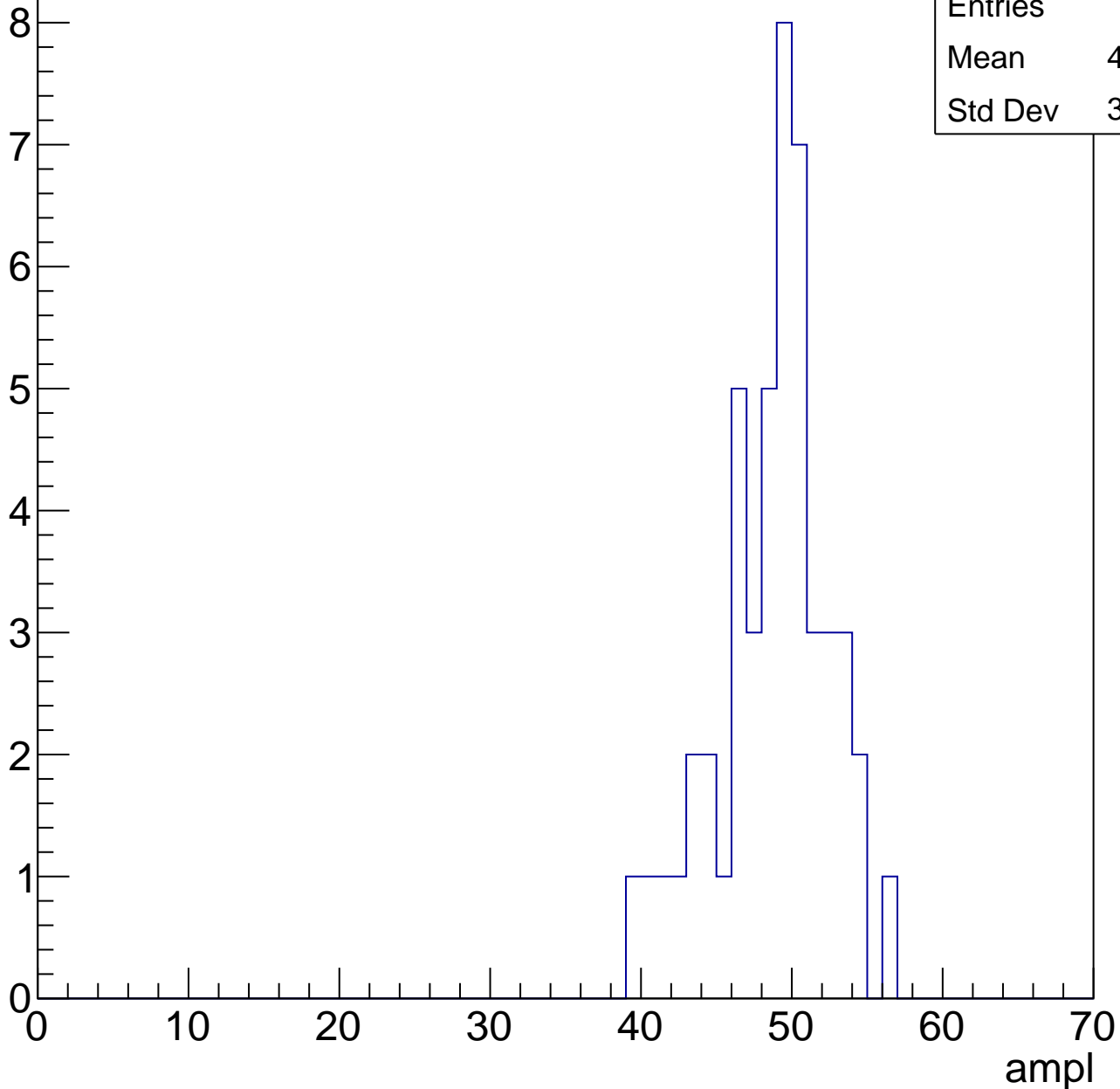


B1L102S, U2-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	48.29
Std Dev	3.676

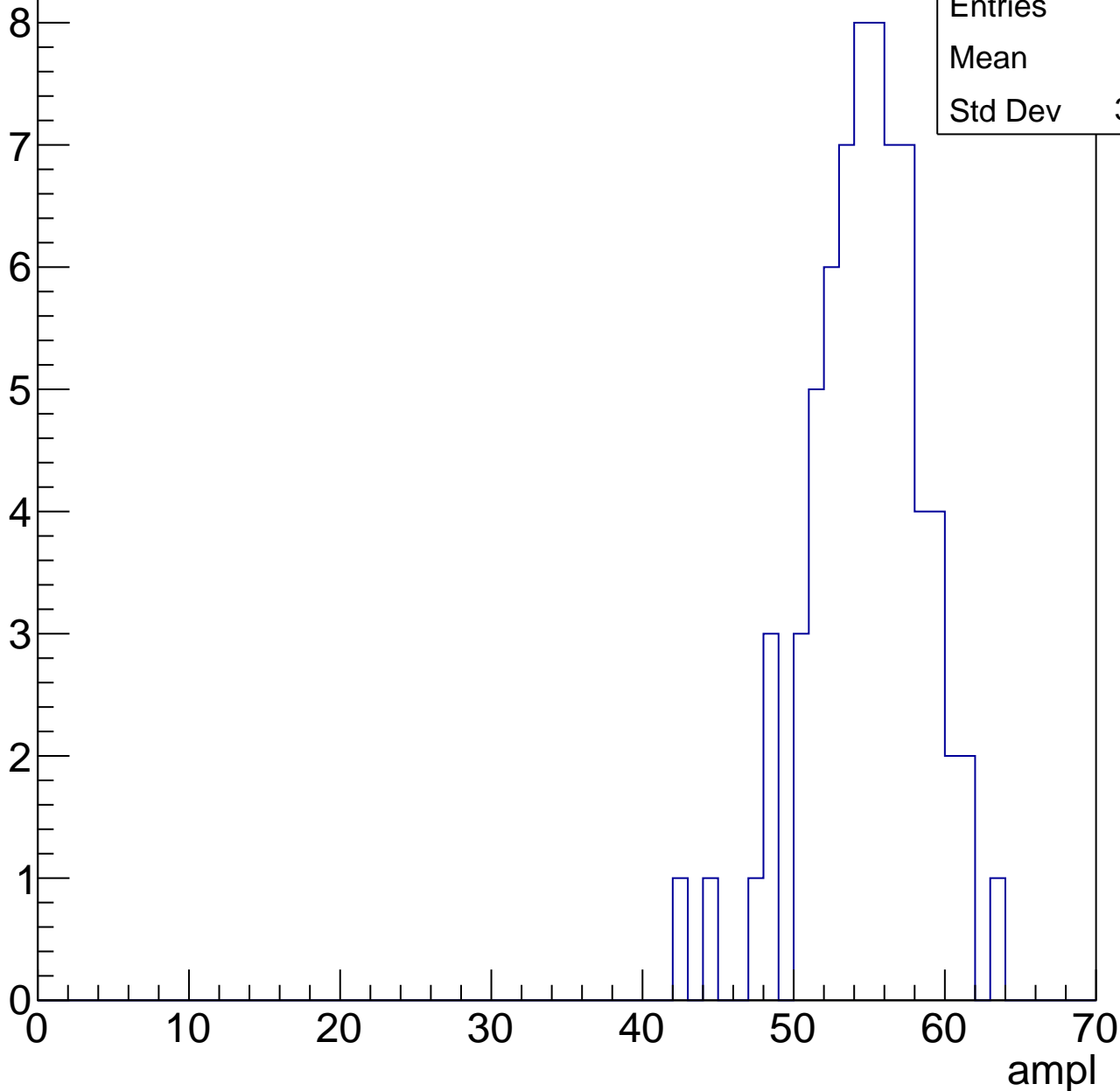


B1L102S, U2-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.3
Std Dev	3.841



B1L102S, U2-ch100, adc5

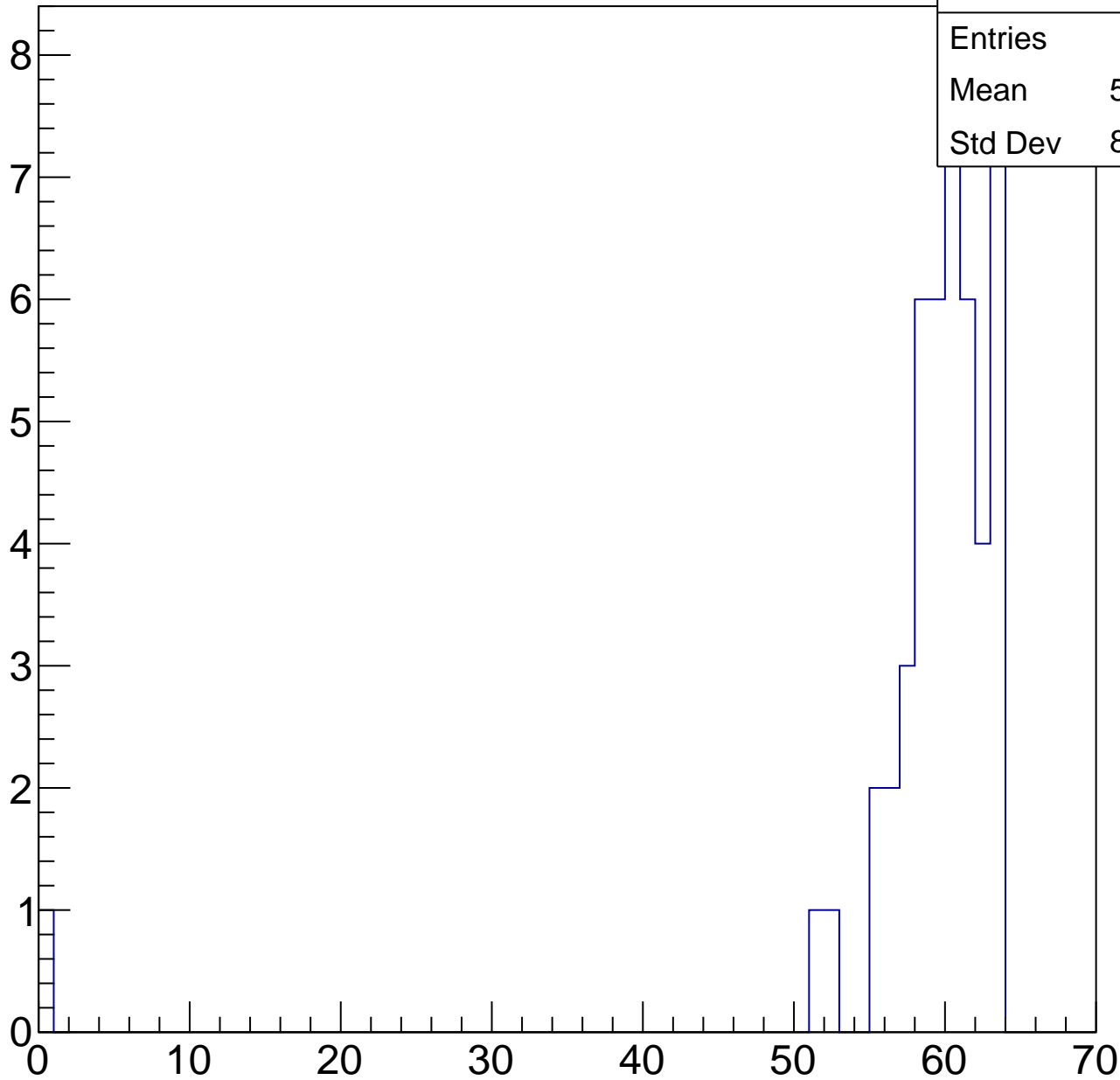
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.25
Std Dev	8.934

ampl

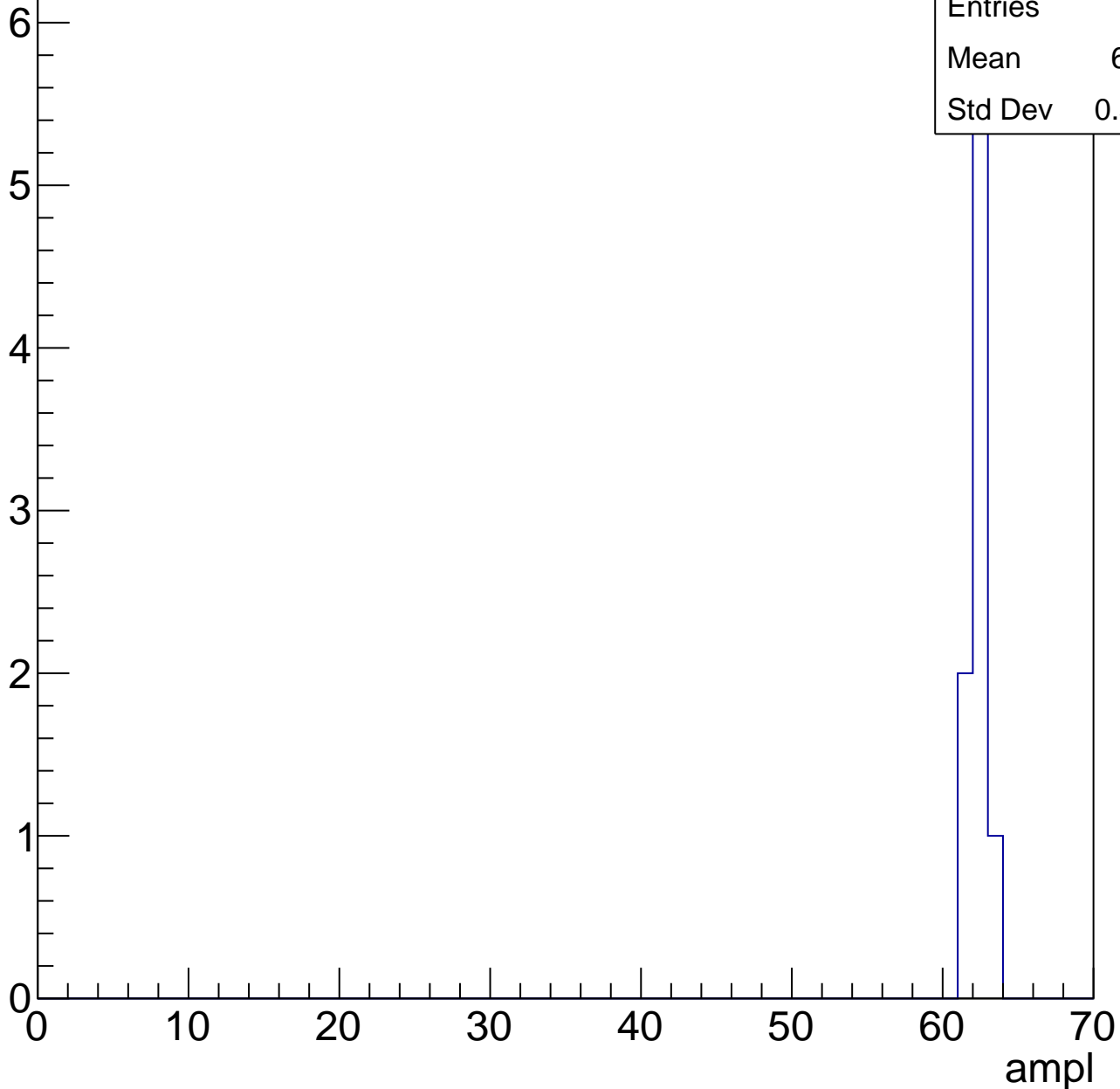


B1L102S, U2-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.89
Std Dev	0.5666



B1L102S, U2-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch101, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	27.71
Std Dev	5.751

Gaus mean : 28.8759

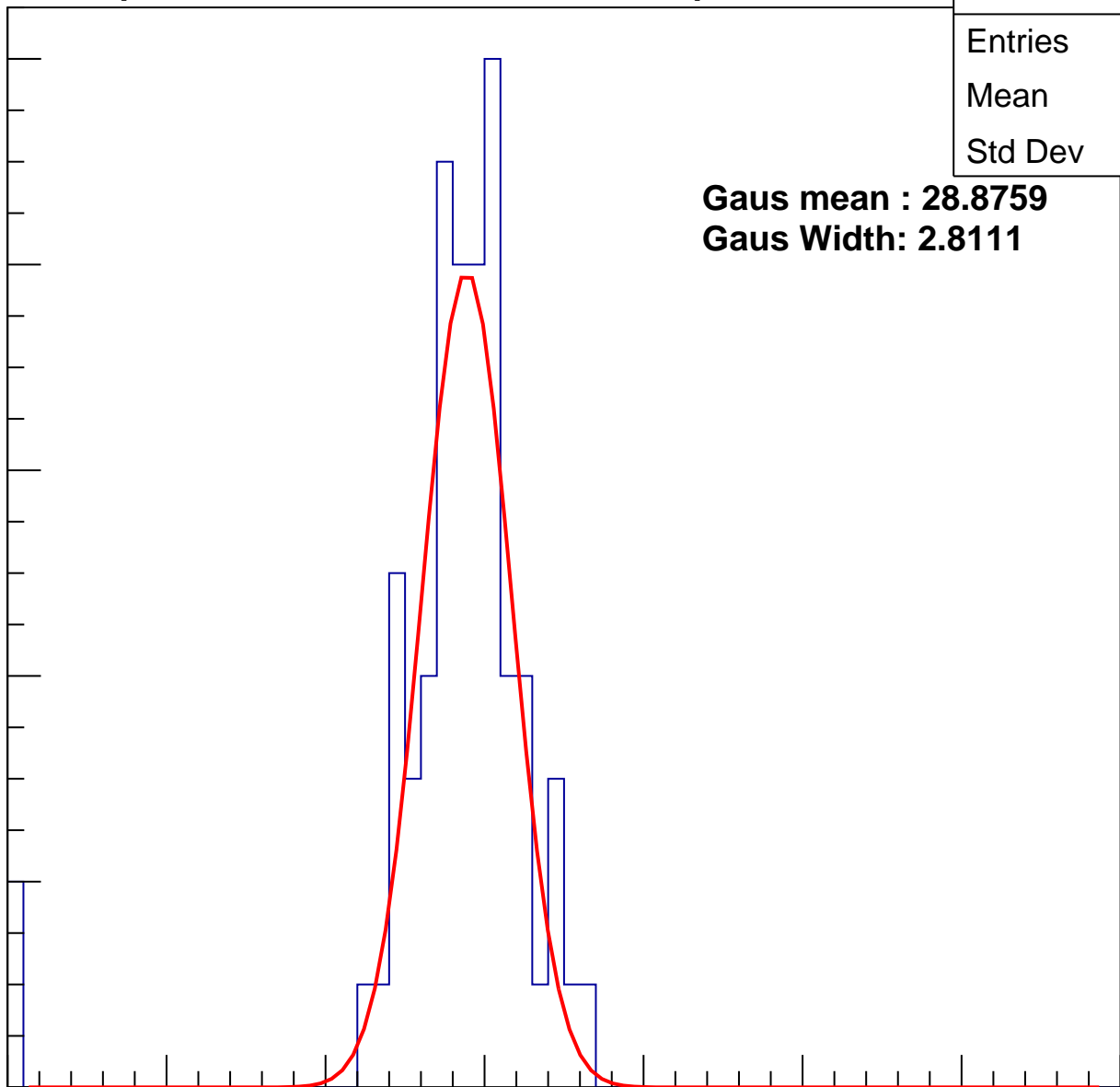
Gaus Width: 2.8111

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch101, adc1

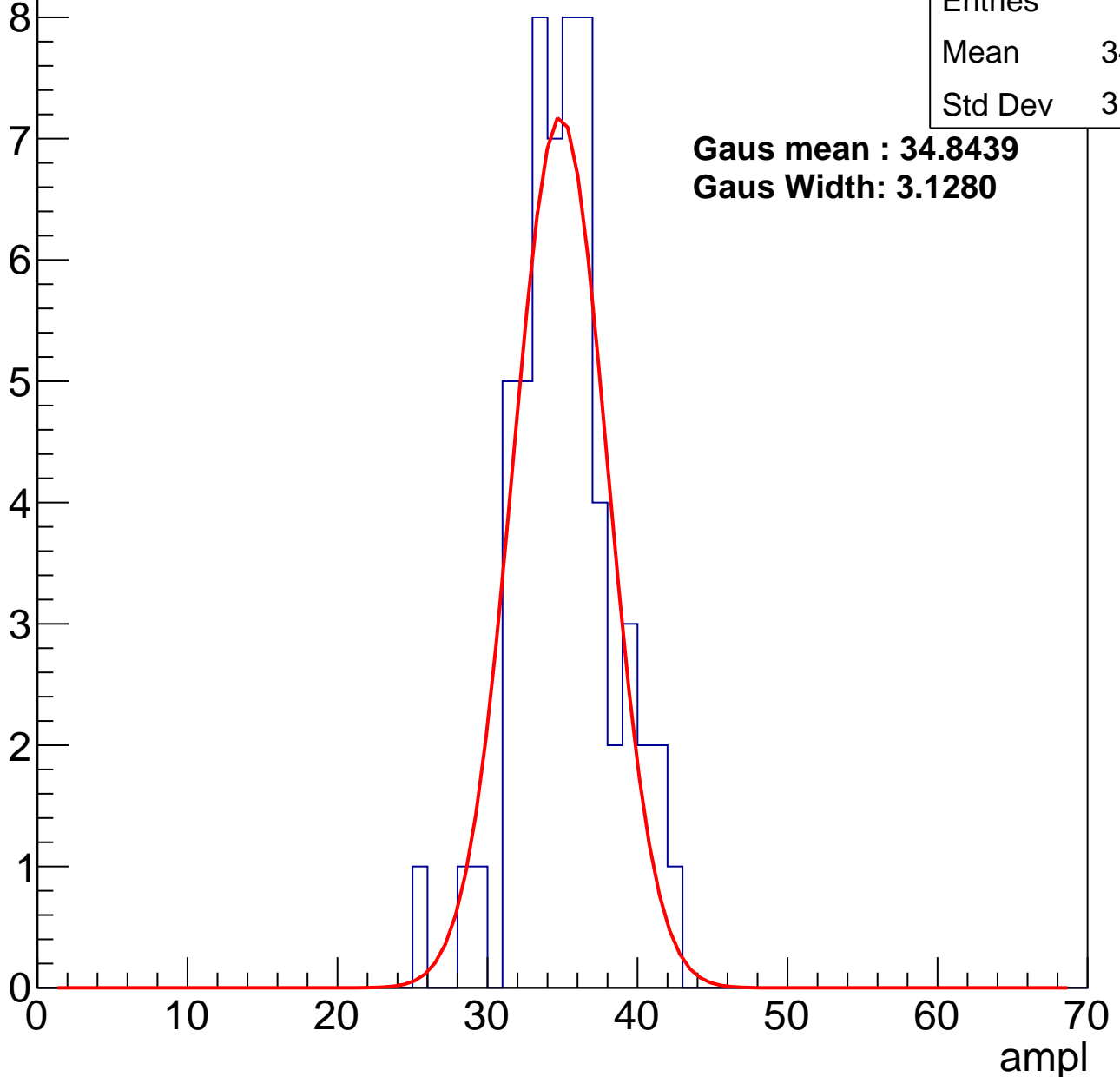
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.69
Std Dev	3.228

Gaus mean : 34.8439

Gaus Width: 3.1280



B1L102S, U2-ch101, adc2

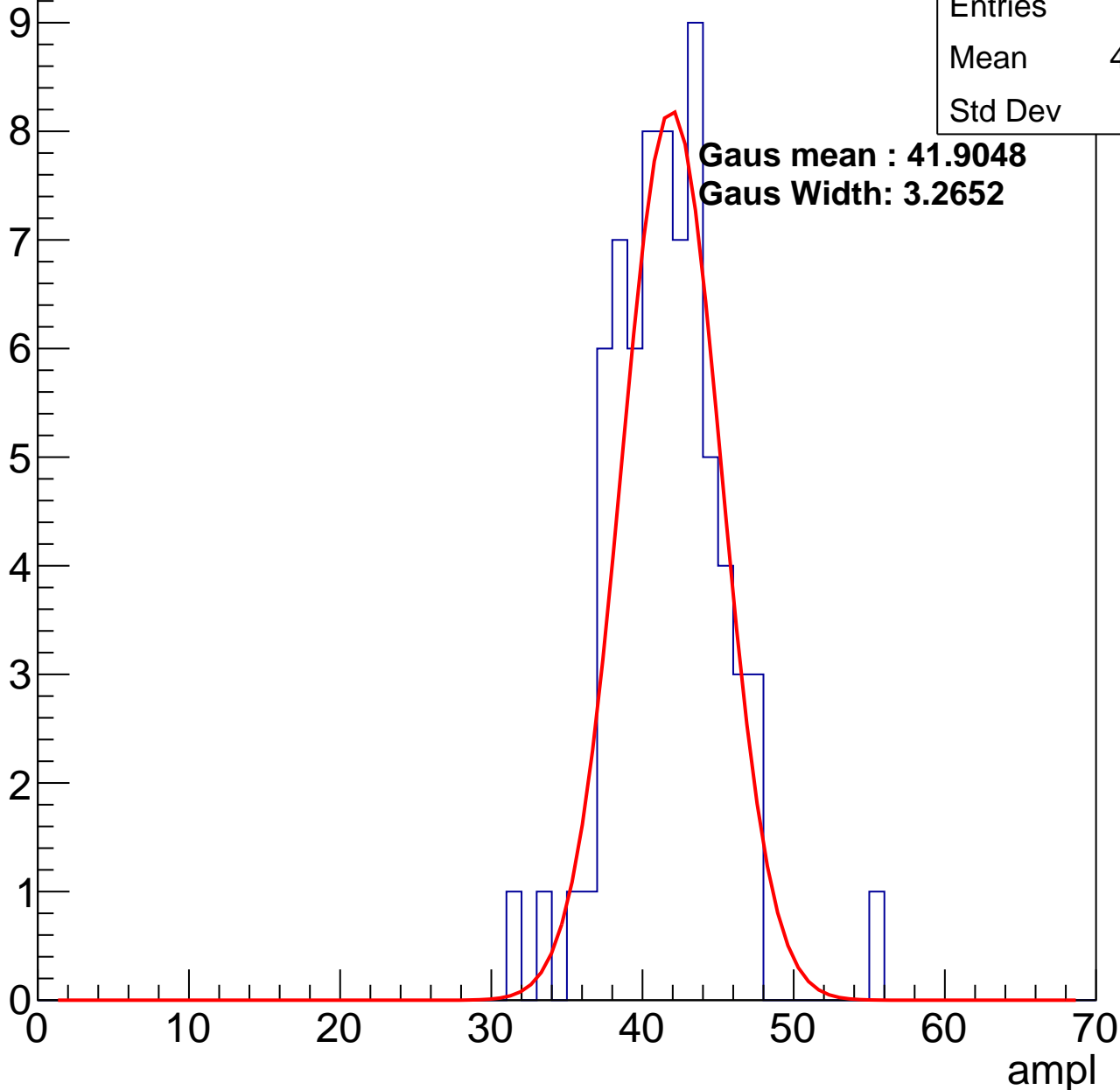
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.13
Std Dev	3.65

Gaus mean : 41.9048

Gaus Width: 3.2652

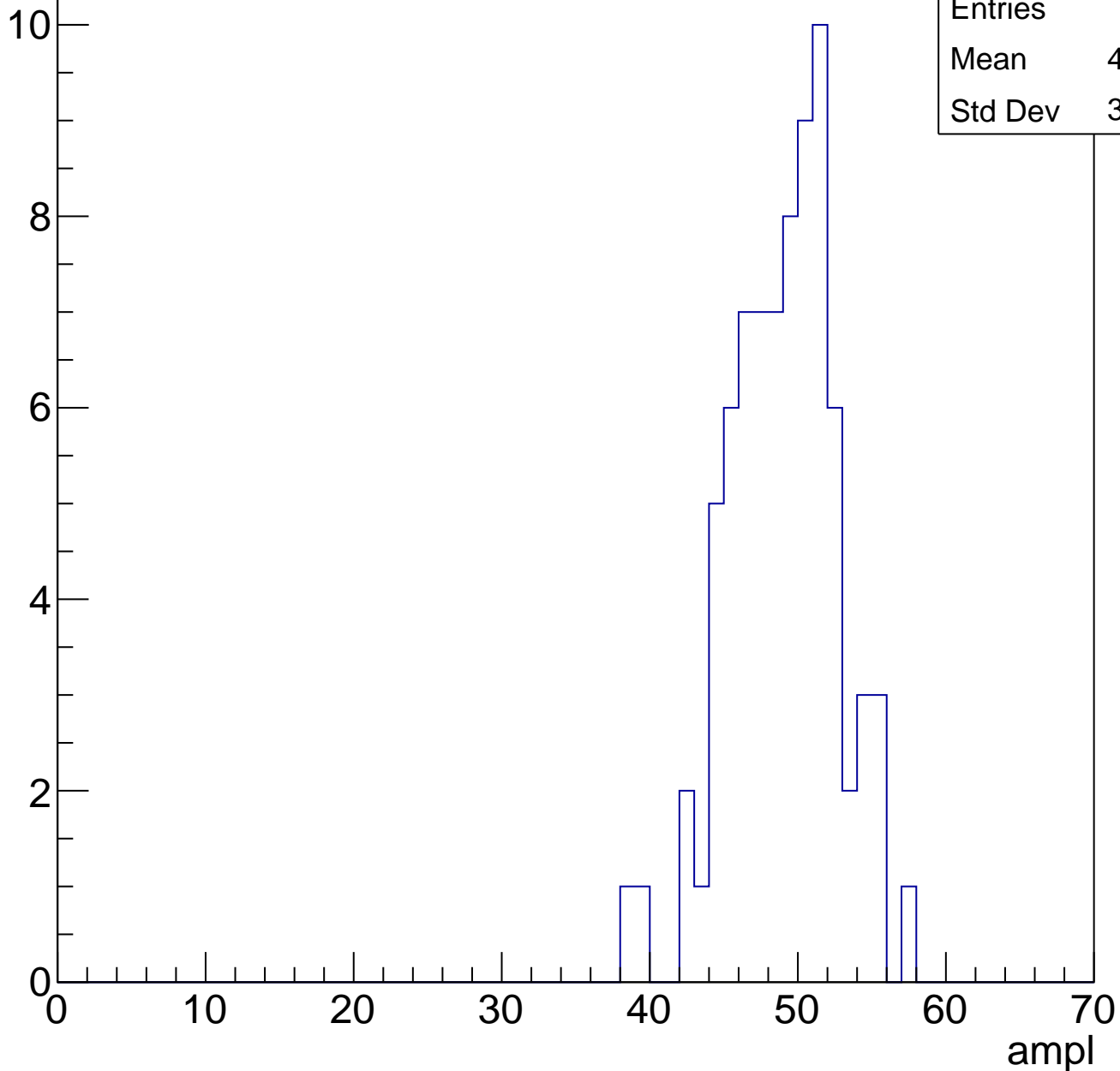


B1L102S, U2-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	48.54
Std Dev	3.628

Entry

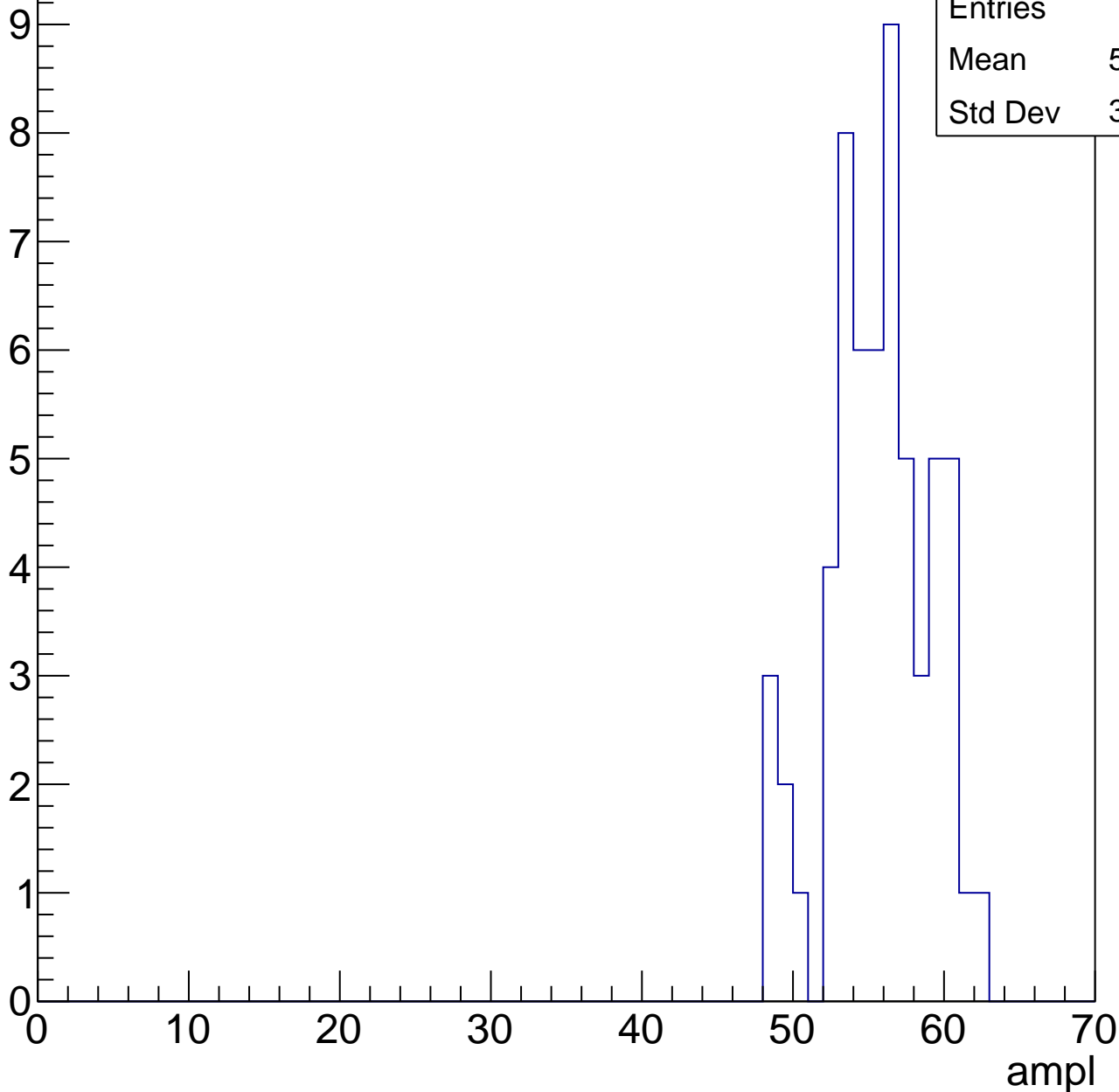


B1L102S, U2-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.24
Std Dev	3.346

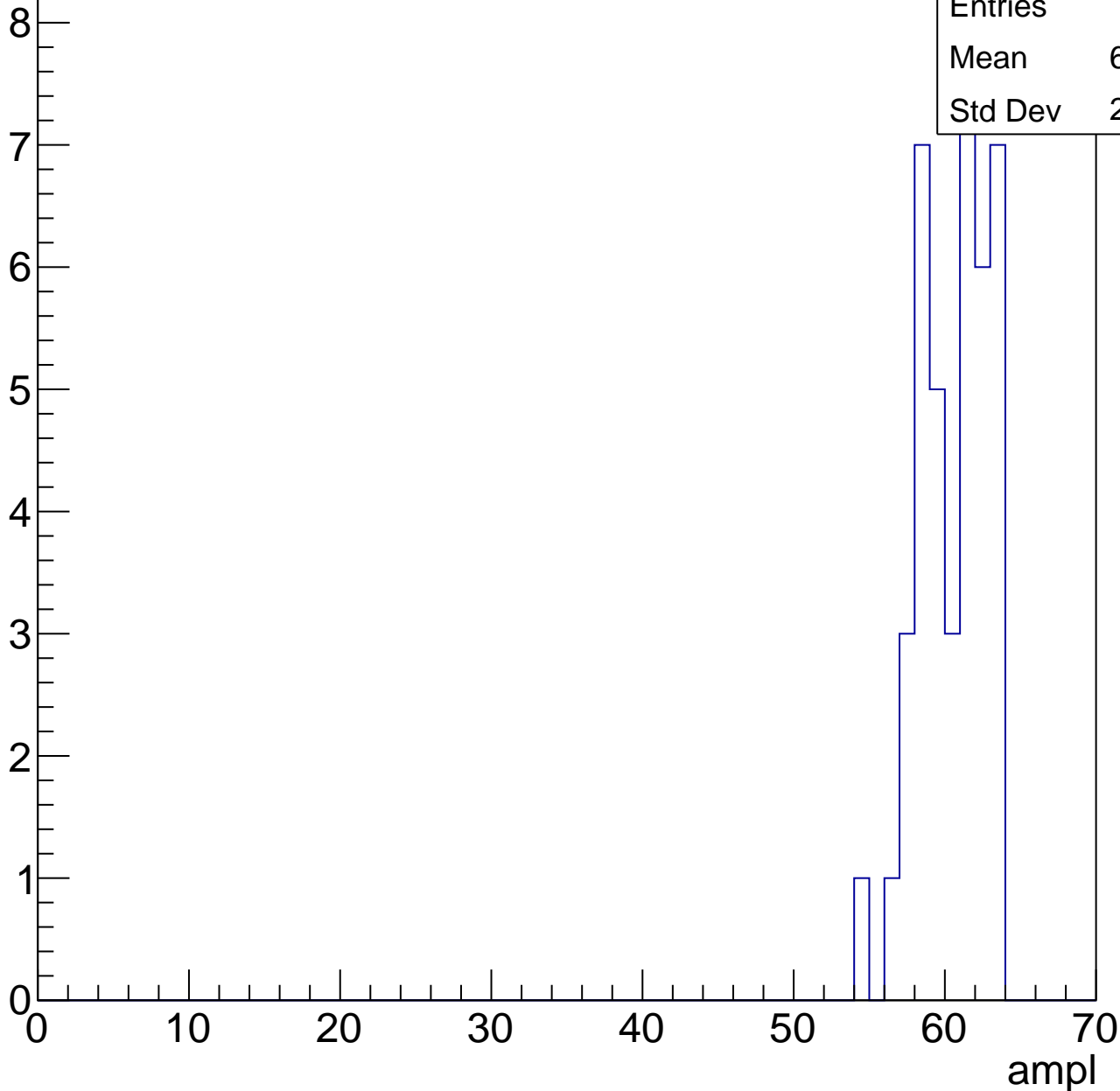


B1L102S, U2-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

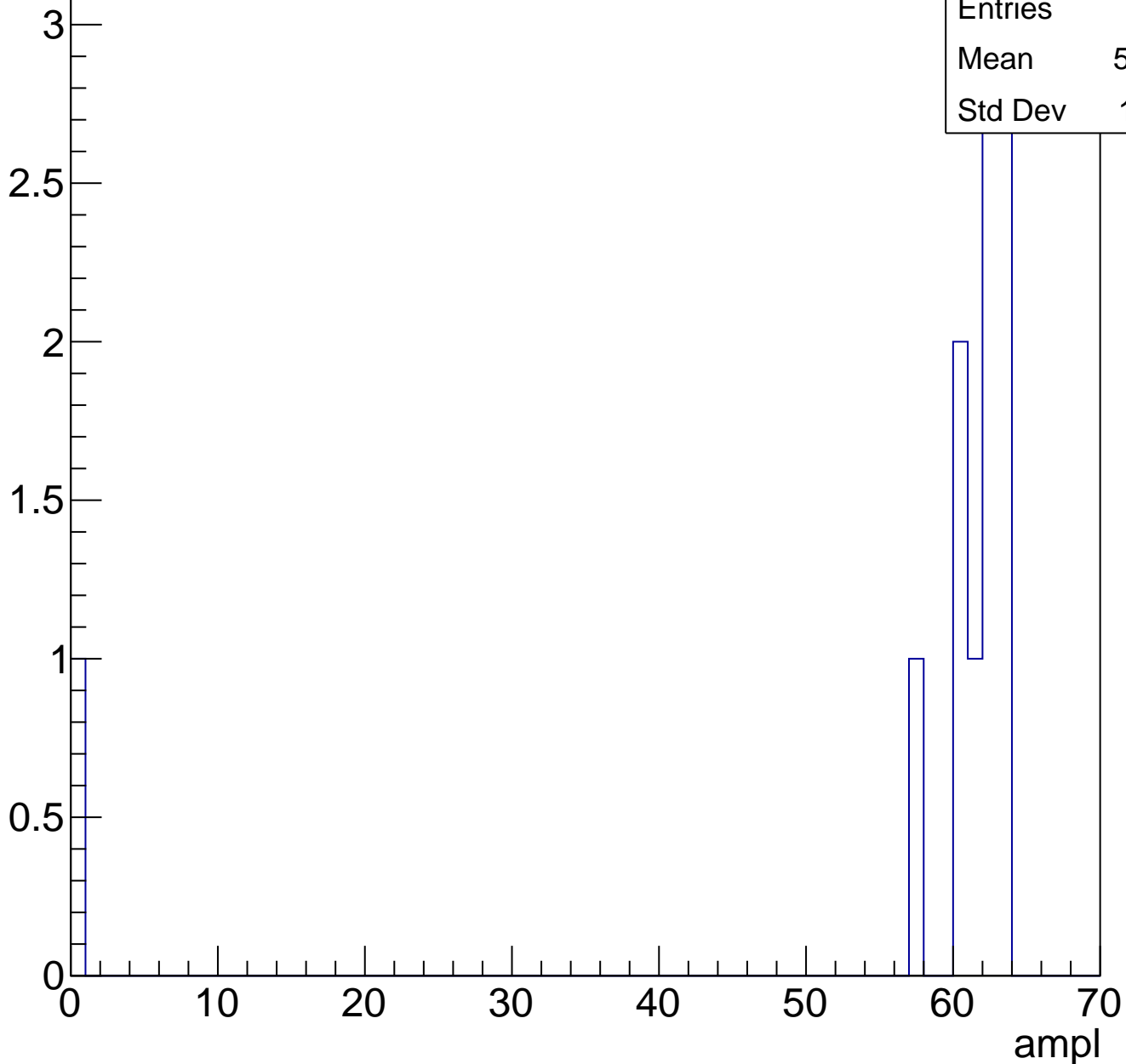
Entries	41
Mean	60.07
Std Dev	2.246



B1L102S, U2-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U2-ch102, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	27.11
Std Dev	5.024

Gaus mean : 27.9807

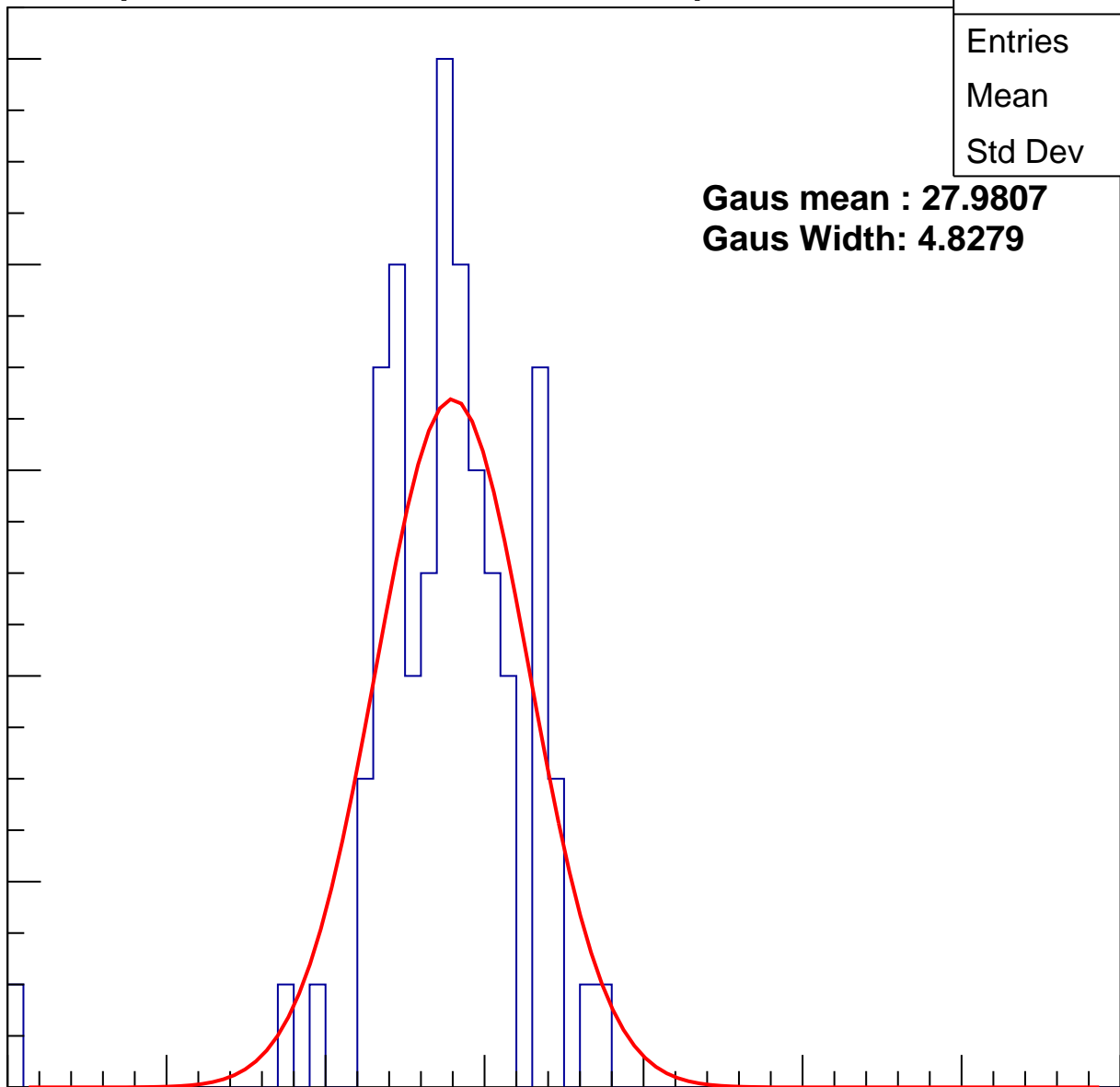
Gaus Width: 4.8279

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch102, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	34.62
Std Dev	3.769

Gaus mean : 34.8640

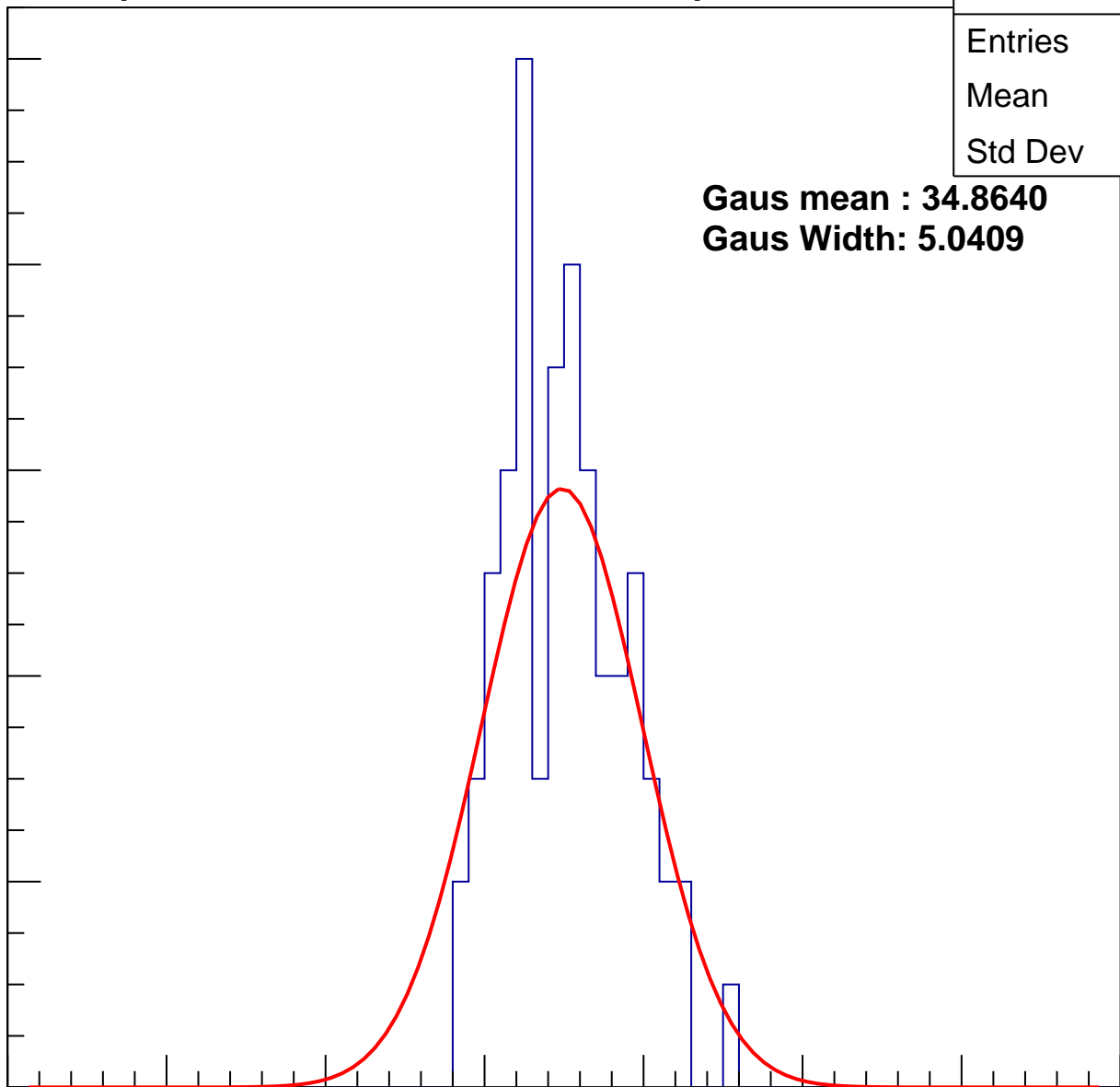
Gaus Width: 5.0409

Entry

10
8
6
4
2
0

ampl

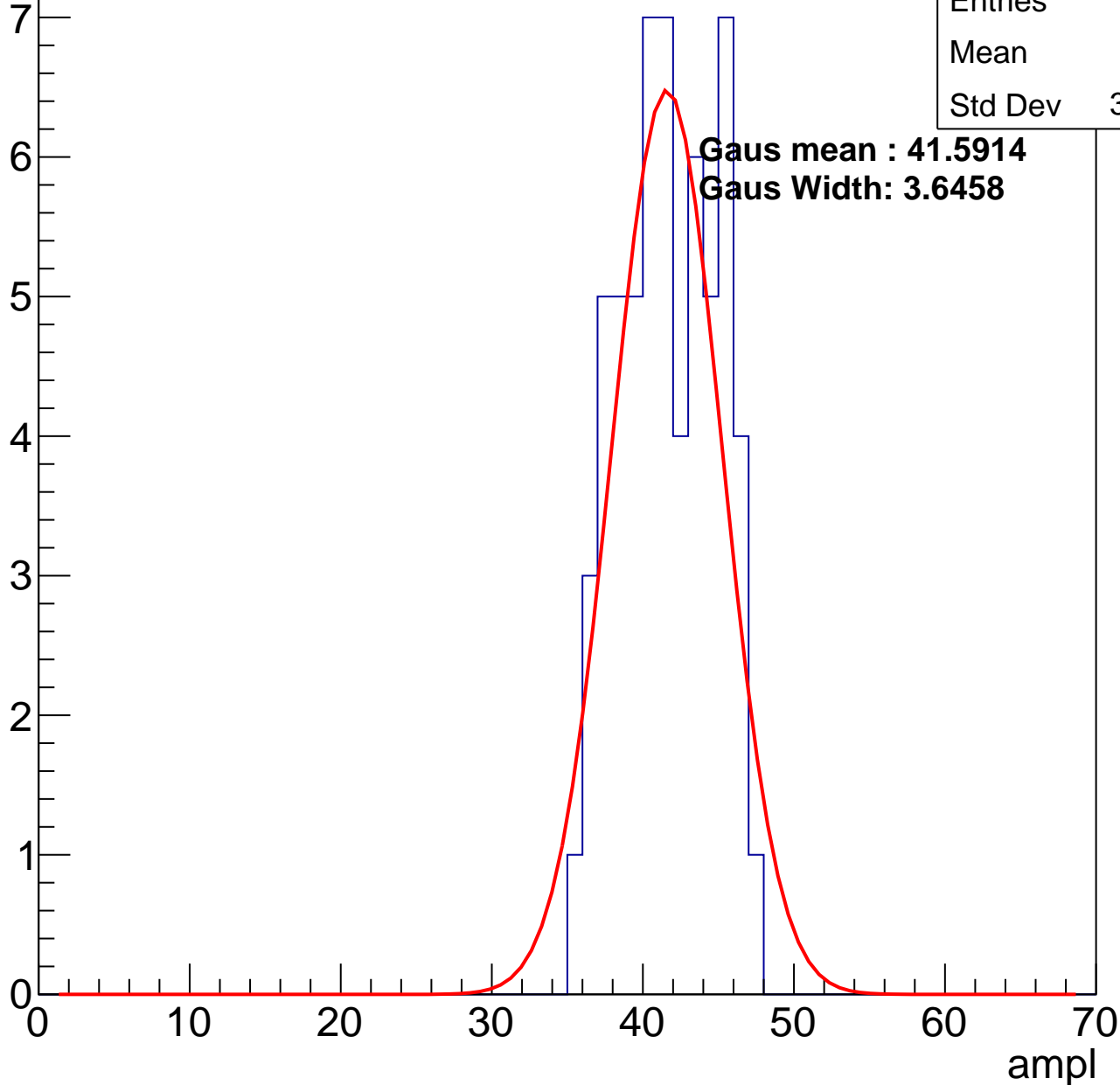
0 10 20 30 40 50 60 70



B1L102S, U2-ch102, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

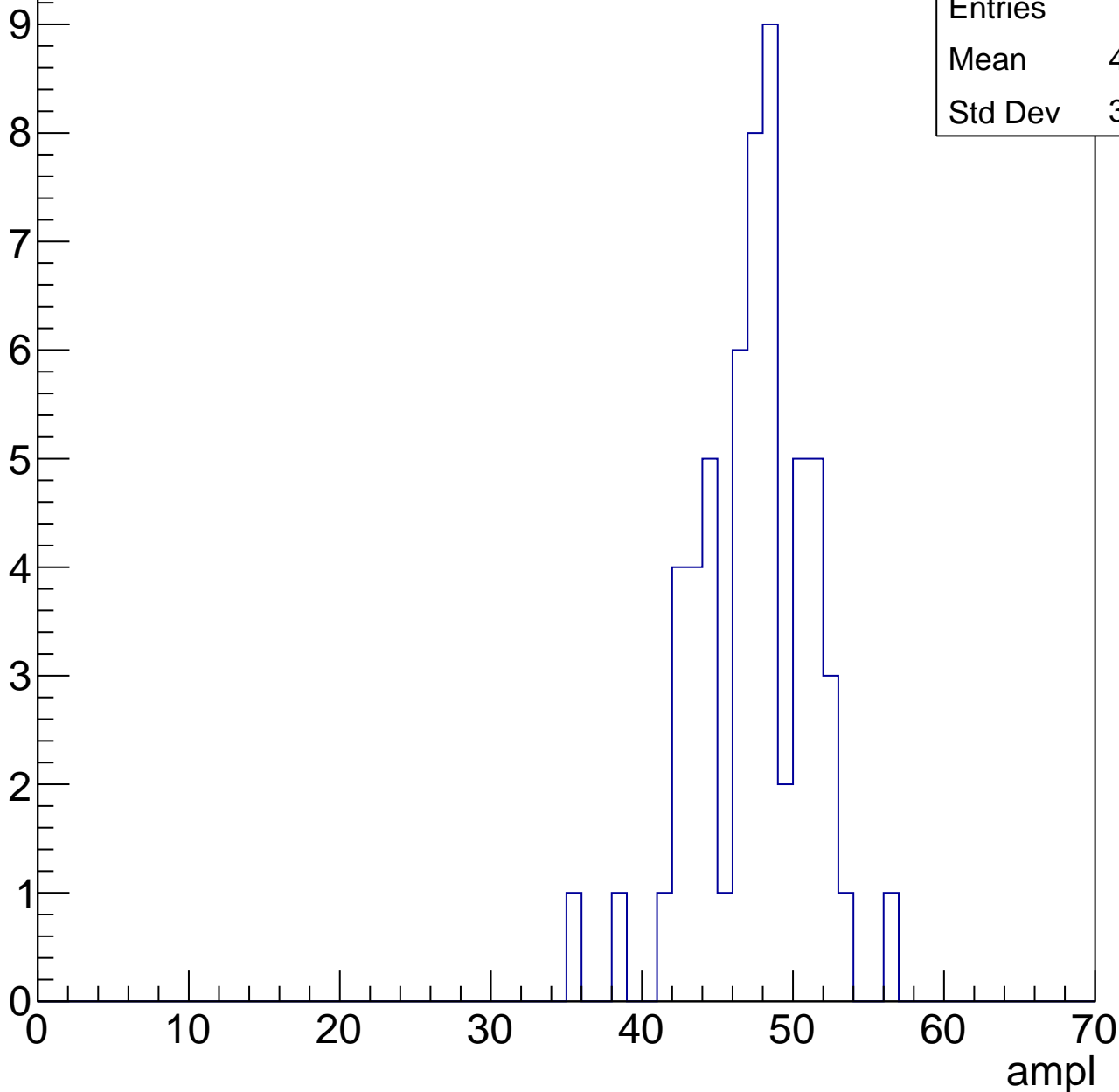


B1L102S, U2-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	46.86
Std Dev	3.776

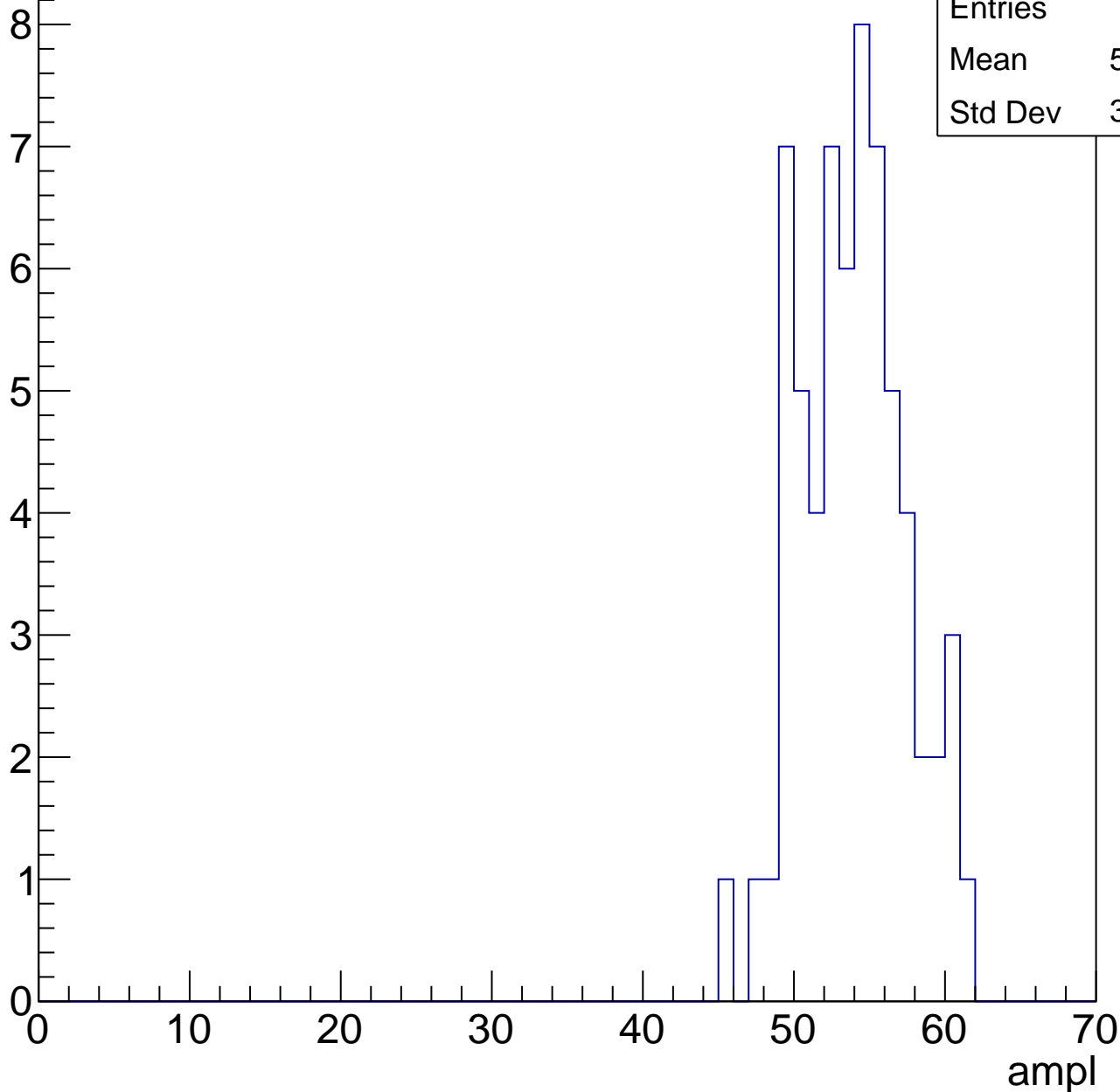


B1L102S, U2-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	53.42
Std Dev	3.472

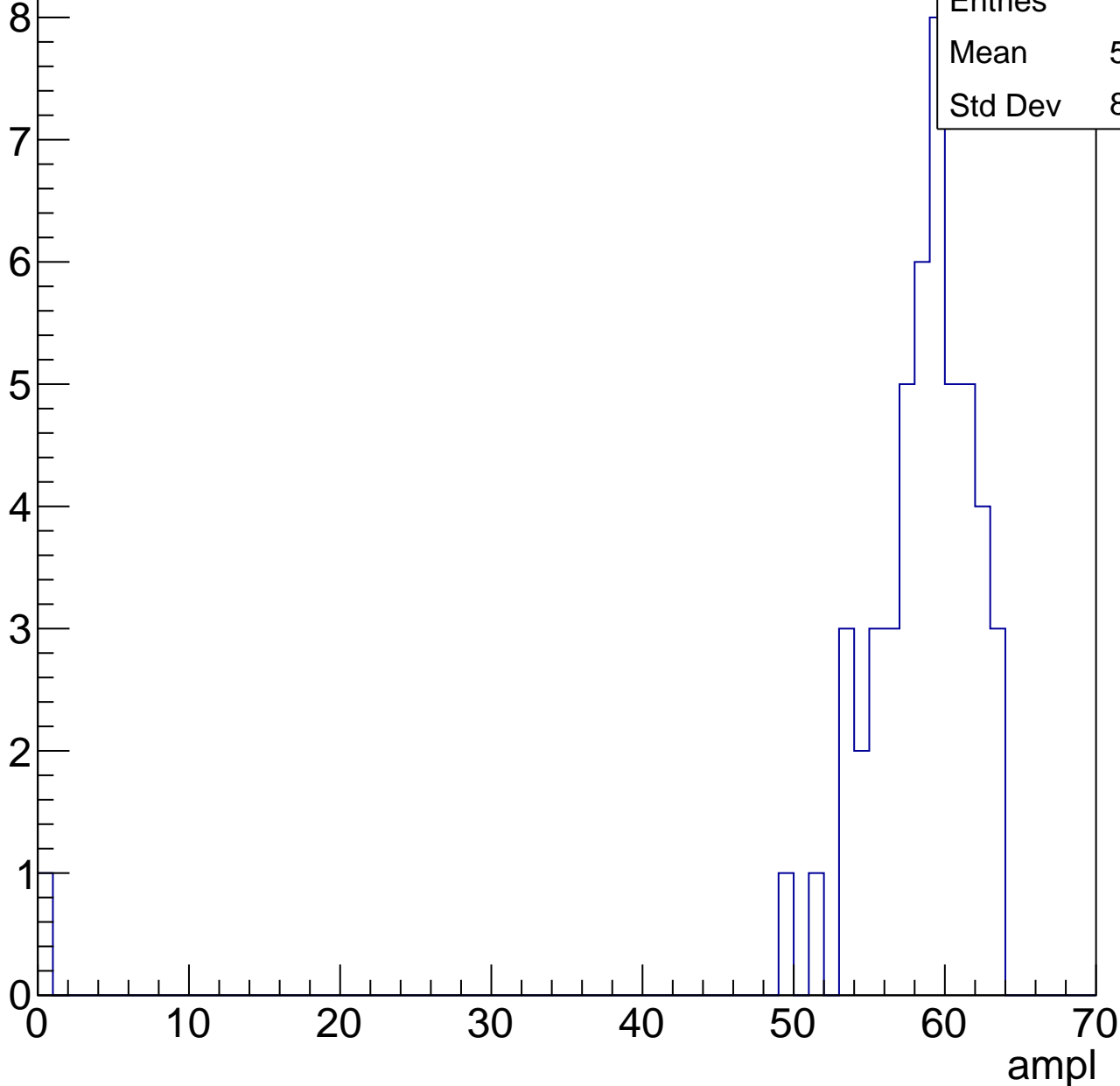


B1L102S, U2-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	56.94
Std Dev	8.719

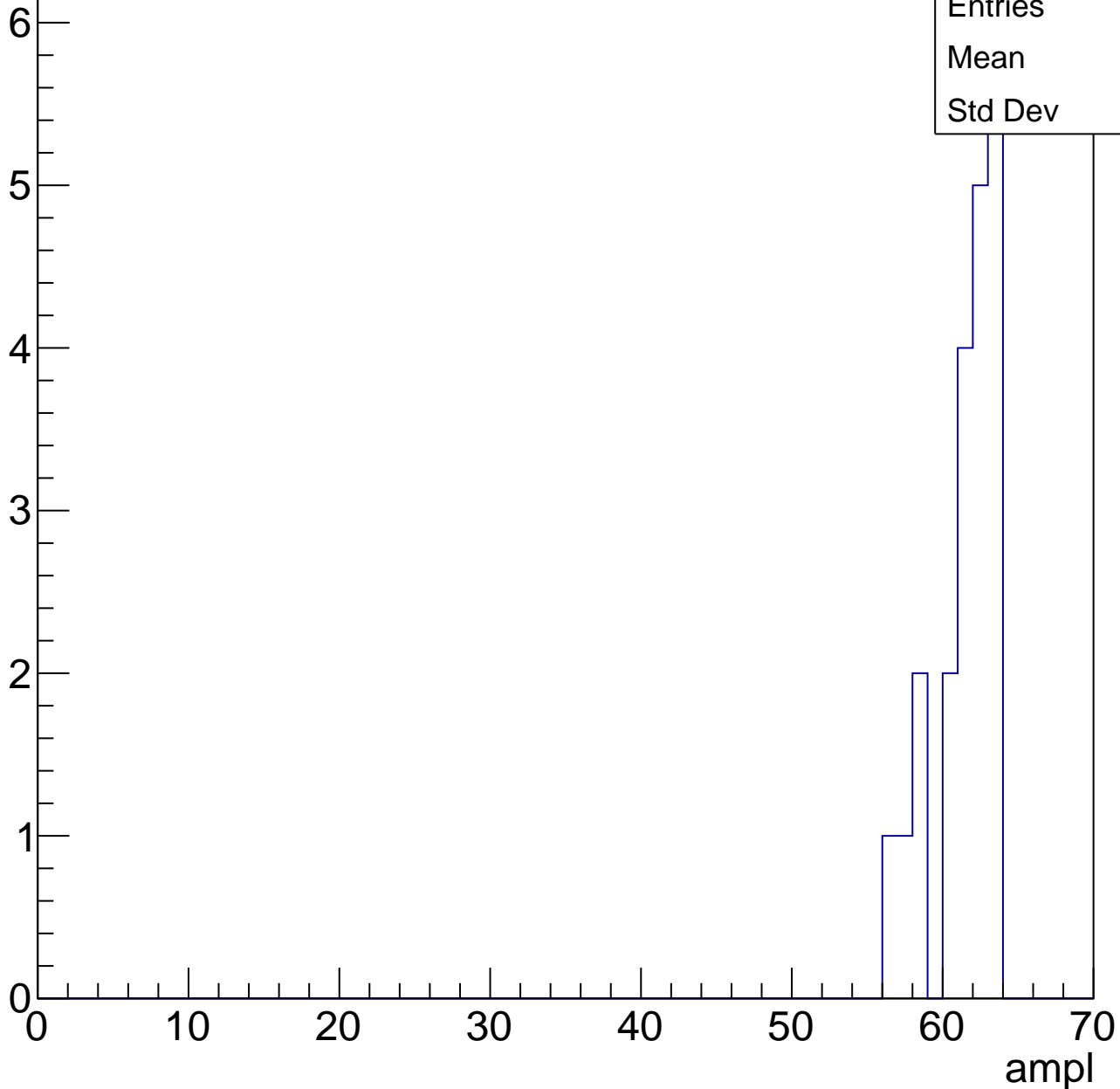


B1L102S, U2-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61
Std Dev	2.07



B1L102S, U2-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch103, adc0

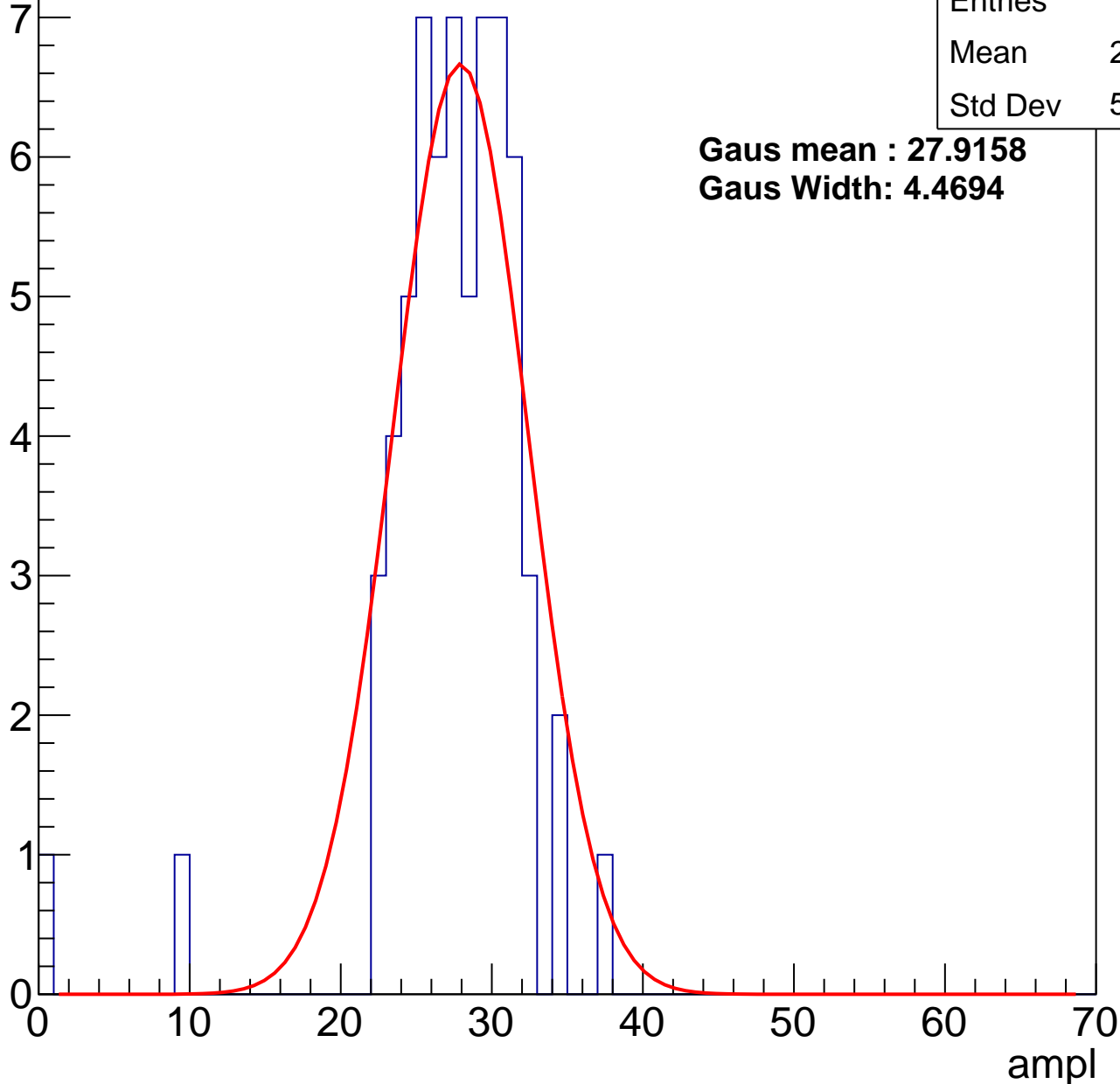
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	26.88
Std Dev	5.166

Gaus mean : 27.9158

Gaus Width: 4.4694



B1L102S, U2-ch103, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	33.88
Std Dev	4.51

Gaus mean : 34.8746

Gaus Width: 3.4305

Entry

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

70

B1L102S, U2-ch103, adc2

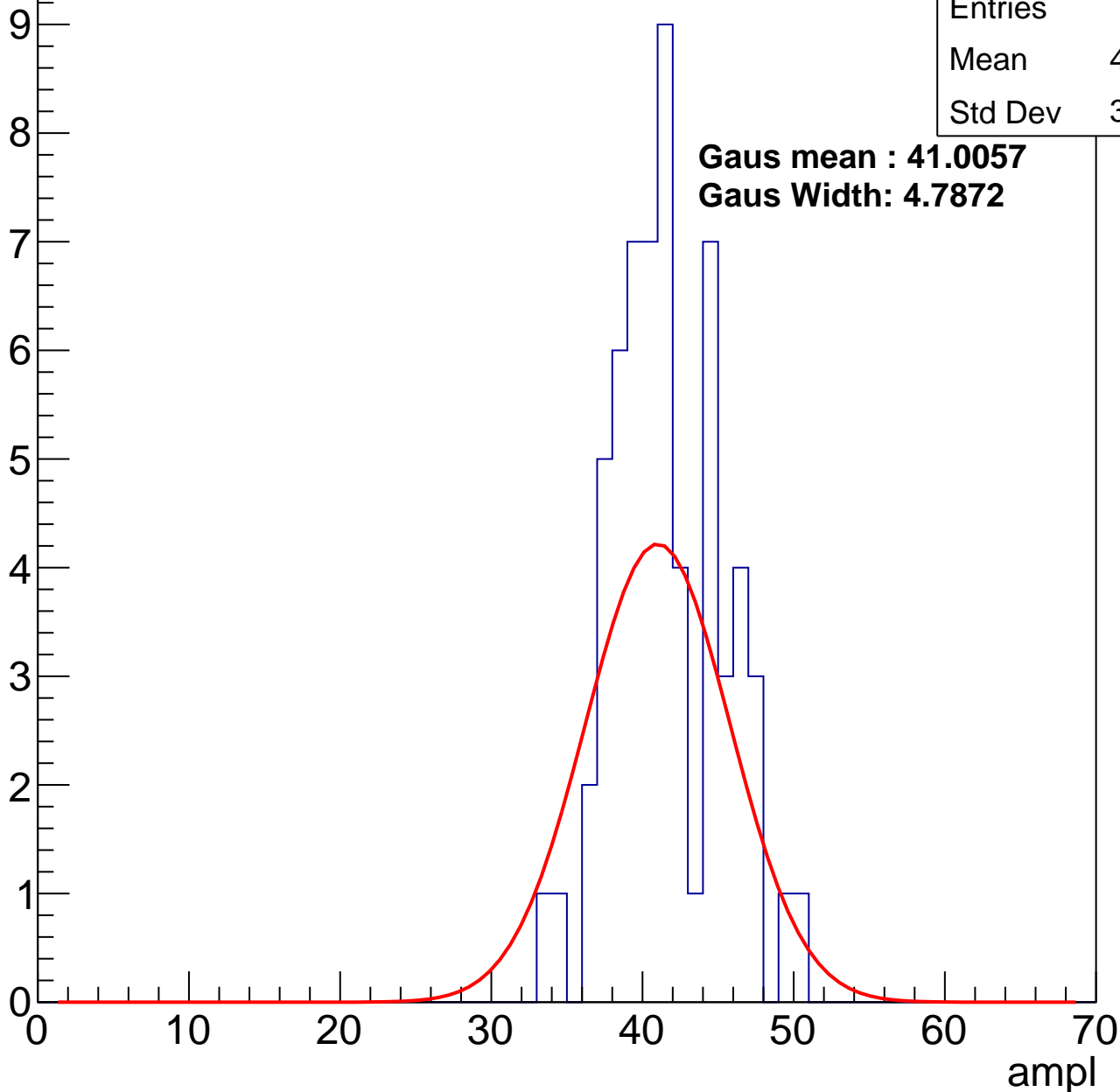
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.16
Std Dev	3.597

Gaus mean : 41.0057

Gaus Width: 4.7872

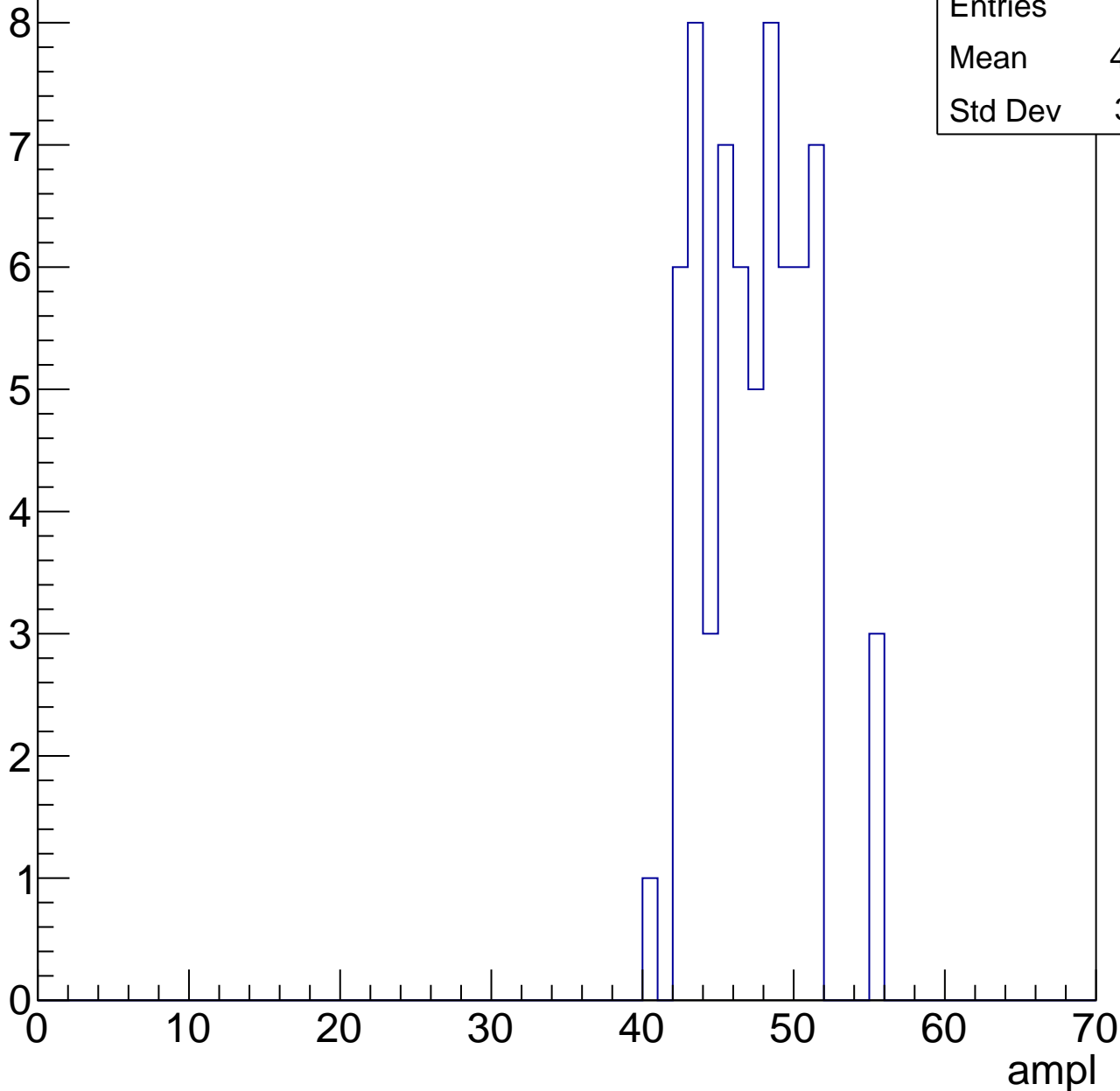


B1L102S, U2-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

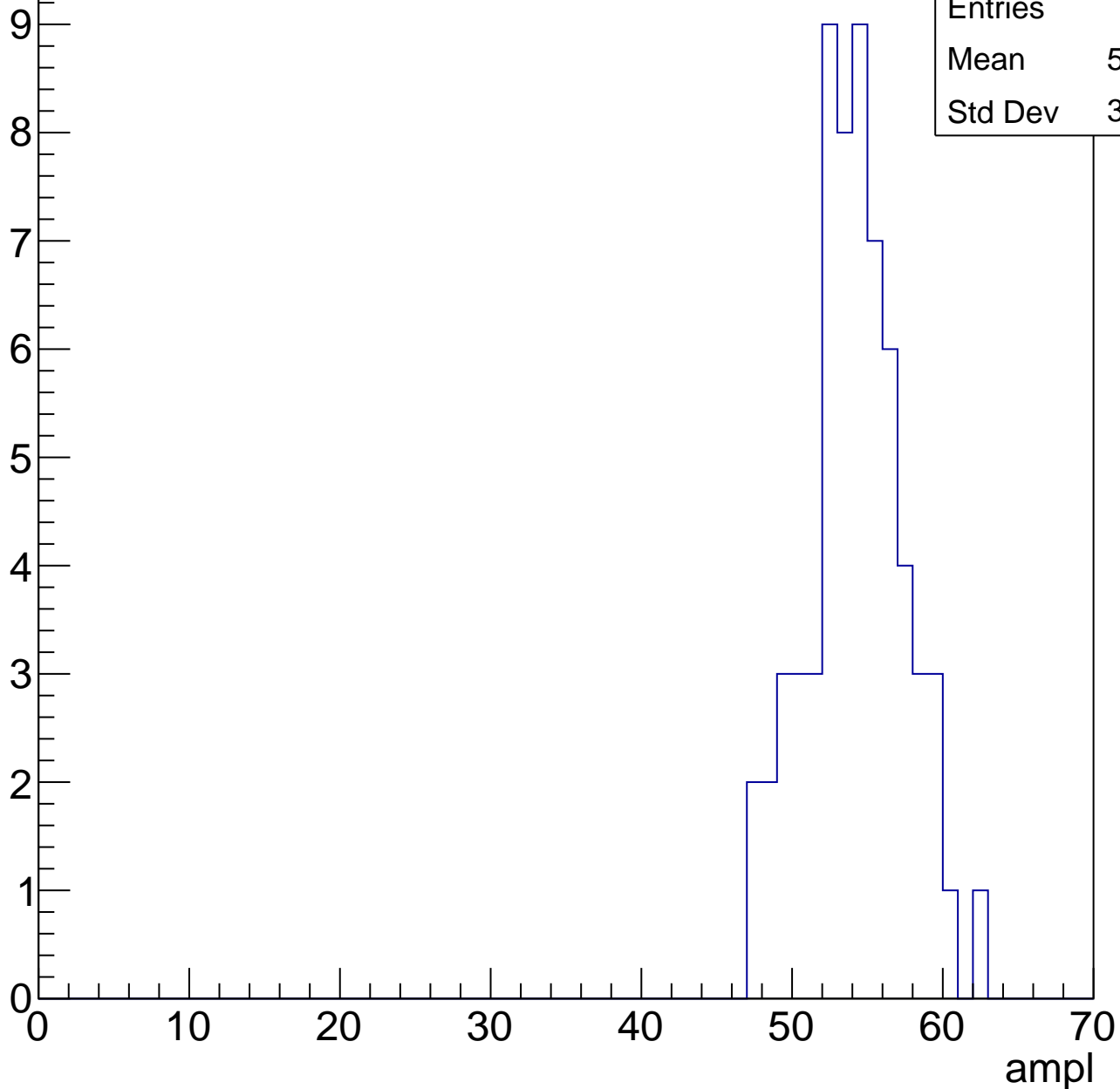
Entries	66
Mean	46.88
Std Dev	3.431



B1L102S, U2-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



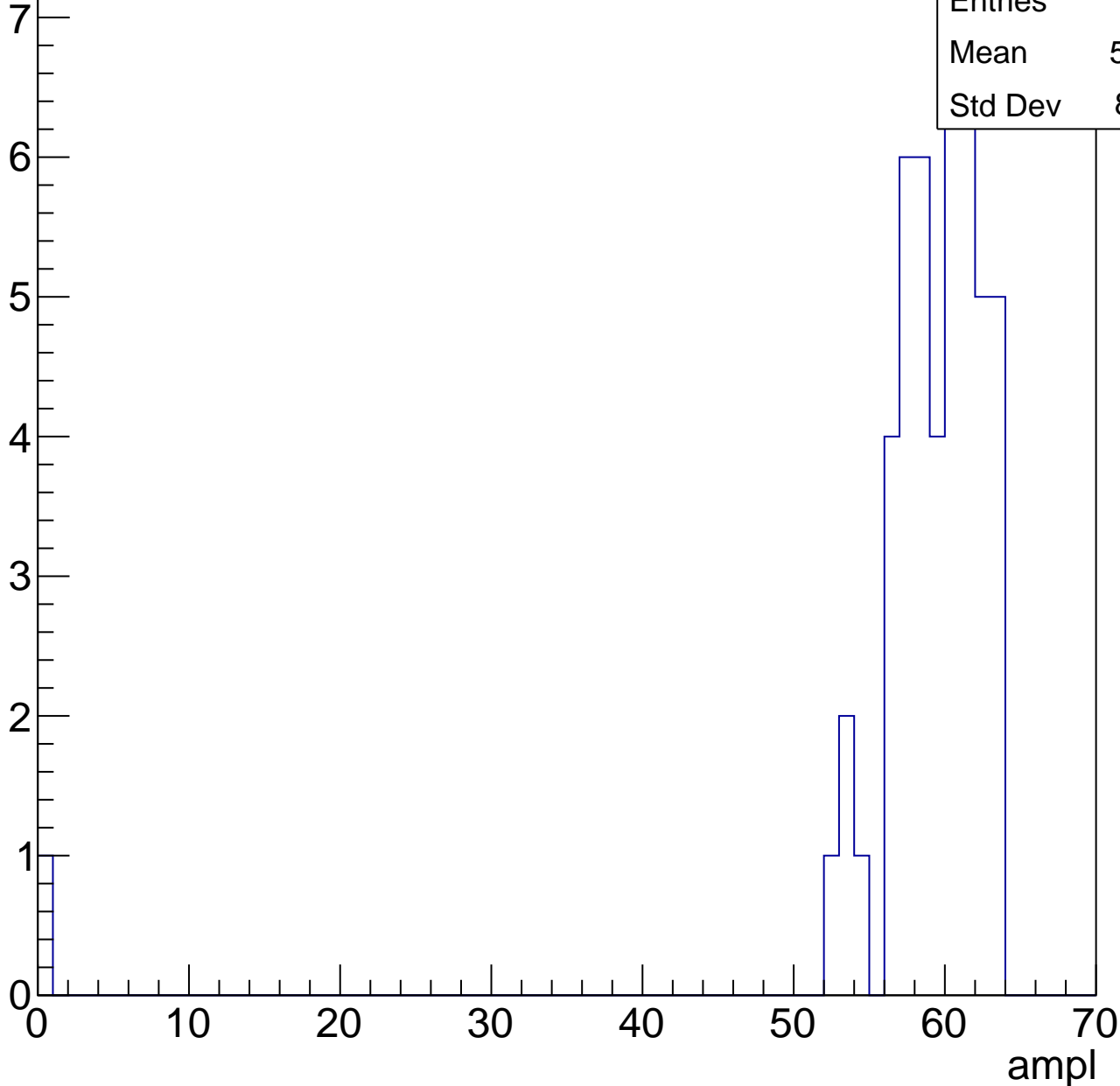
Entries	64
Mean	53.75
Std Dev	3.187

B1L102S, U2-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

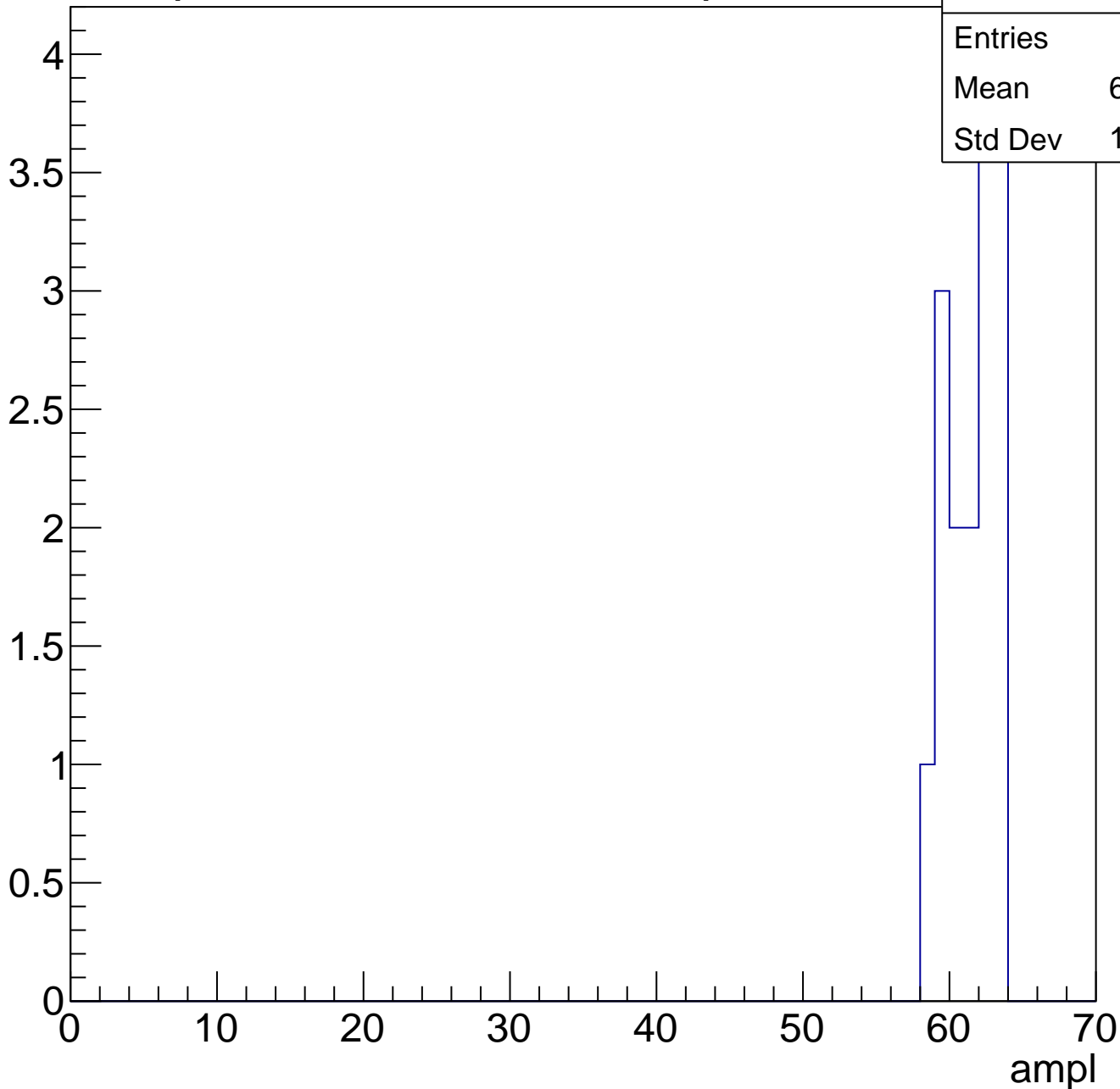
Entries	49
Mean	57.84
Std Dev	8.791



B1L102S, U2-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

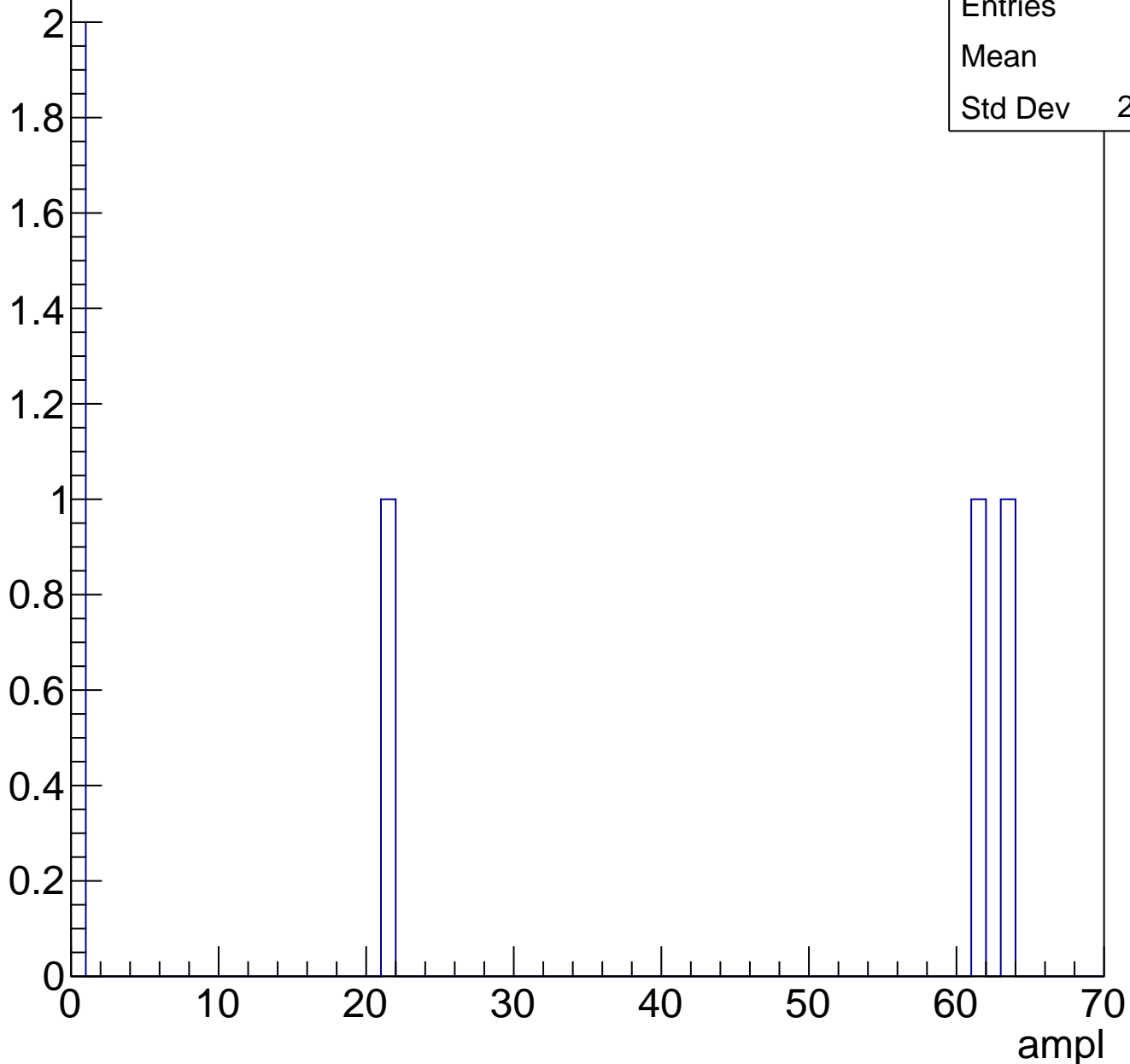
Entry



B1L102S, U2-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	29
Std Dev	28.02

B1L102S, U2-ch104, adc0

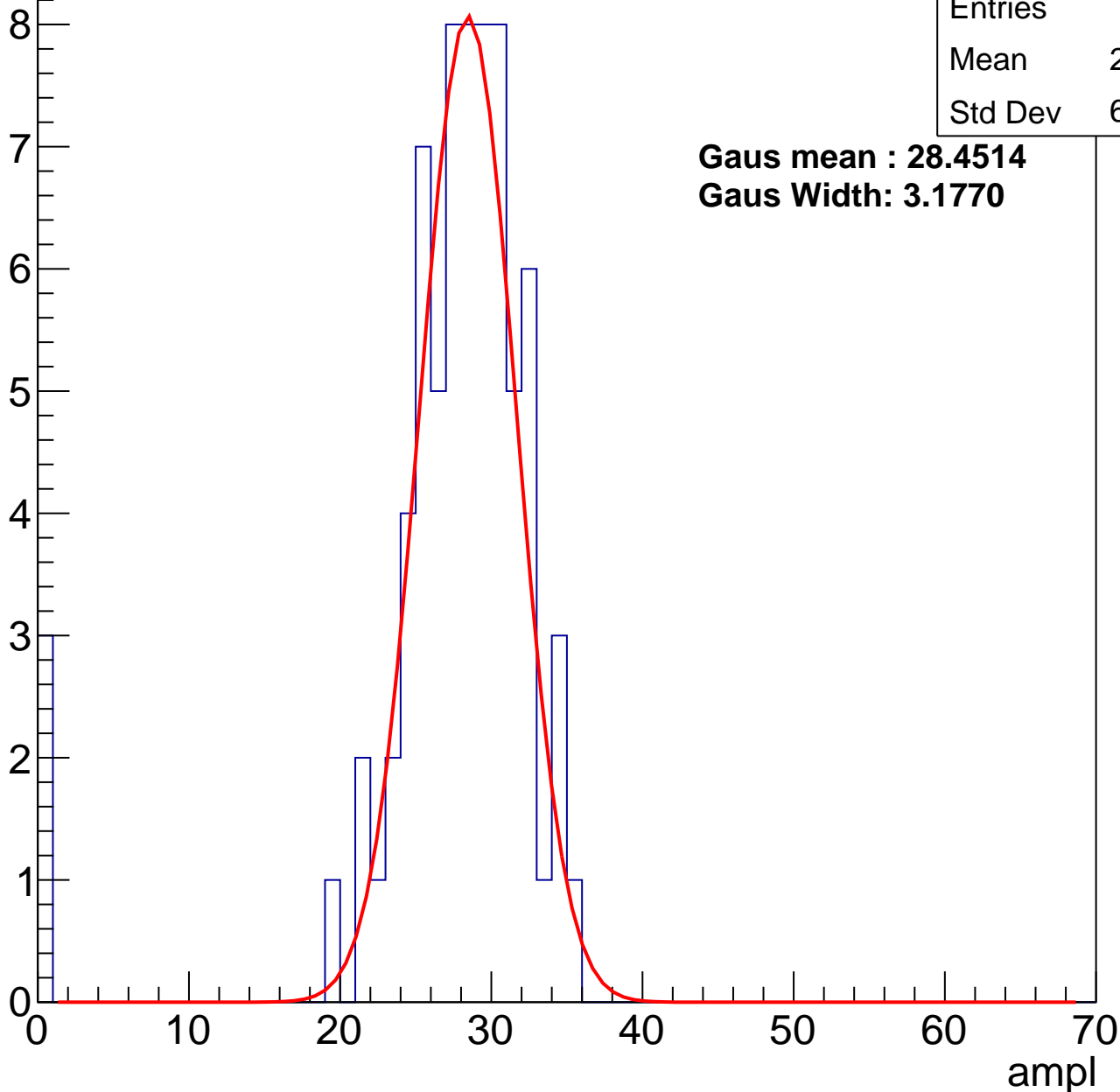
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	26.84
Std Dev	6.449

Gaus mean : 28.4514

Gaus Width: 3.1770



B1L102S, U2-ch104, adc1

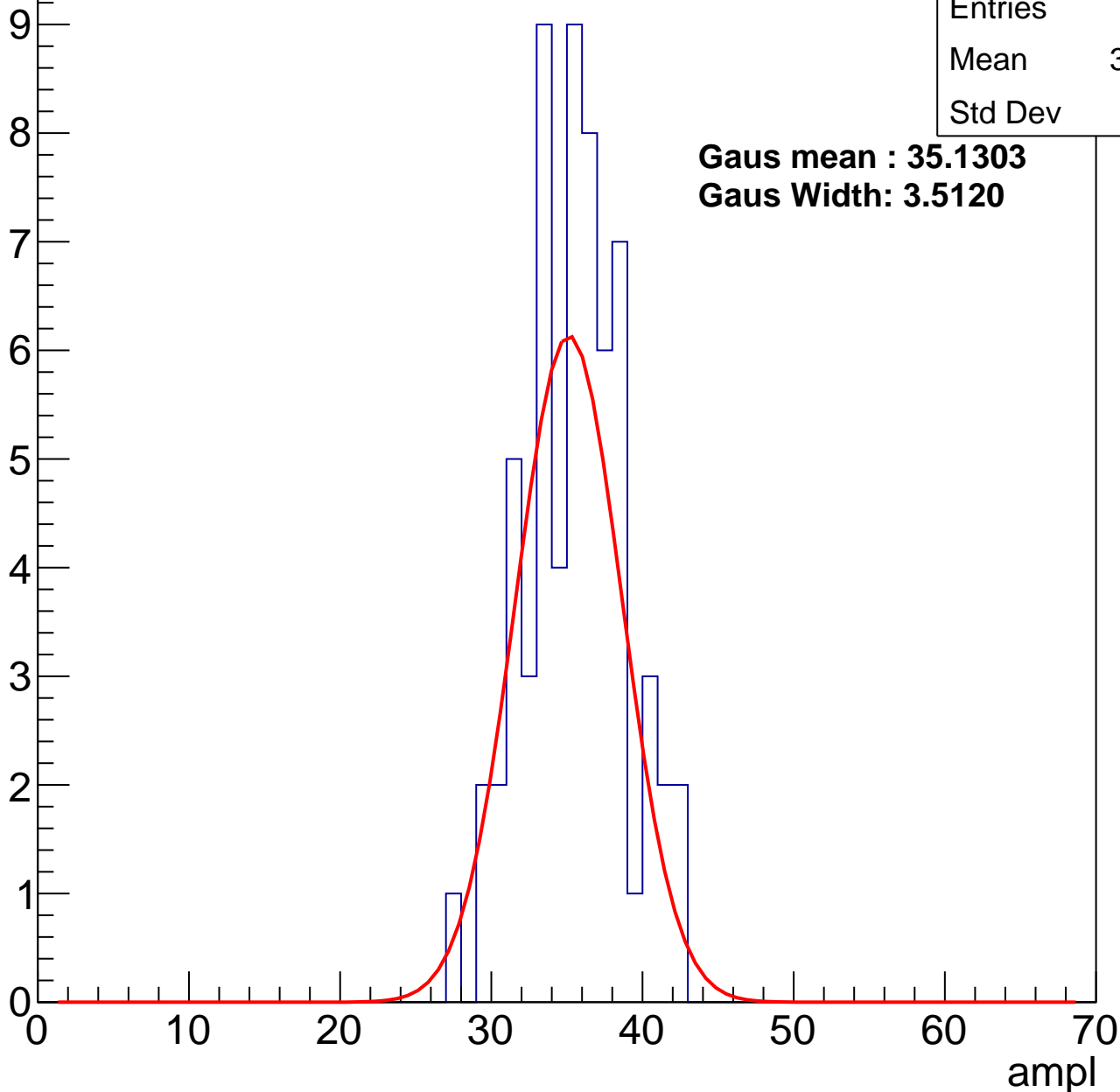
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.08
Std Dev	3.28

Gaus mean : 35.1303

Gaus Width: 3.5120



B1L102S, U2-ch104, adc2

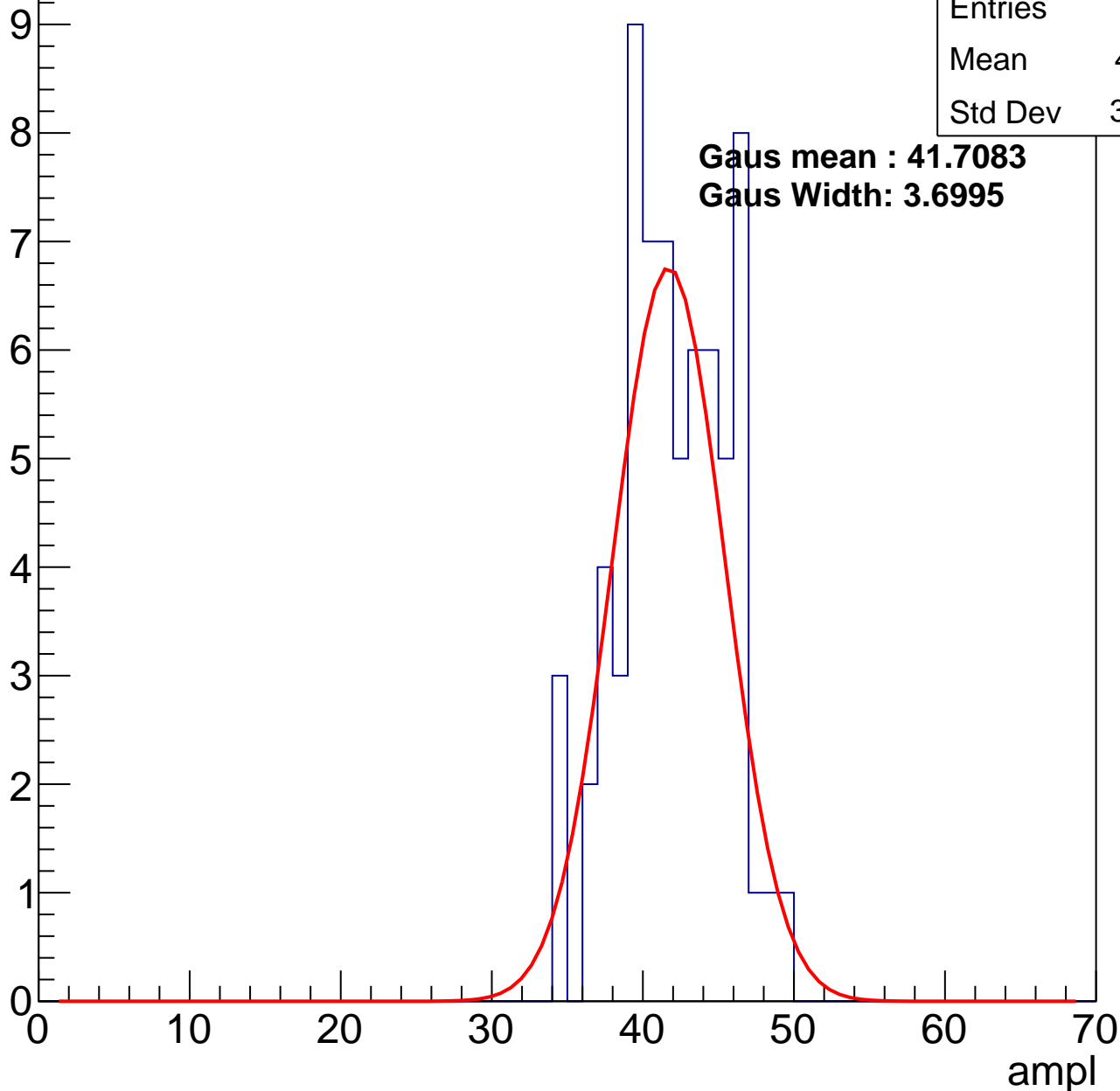
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.51
Std Dev	3.483

Gaus mean : 41.7083

Gaus Width: 3.6995

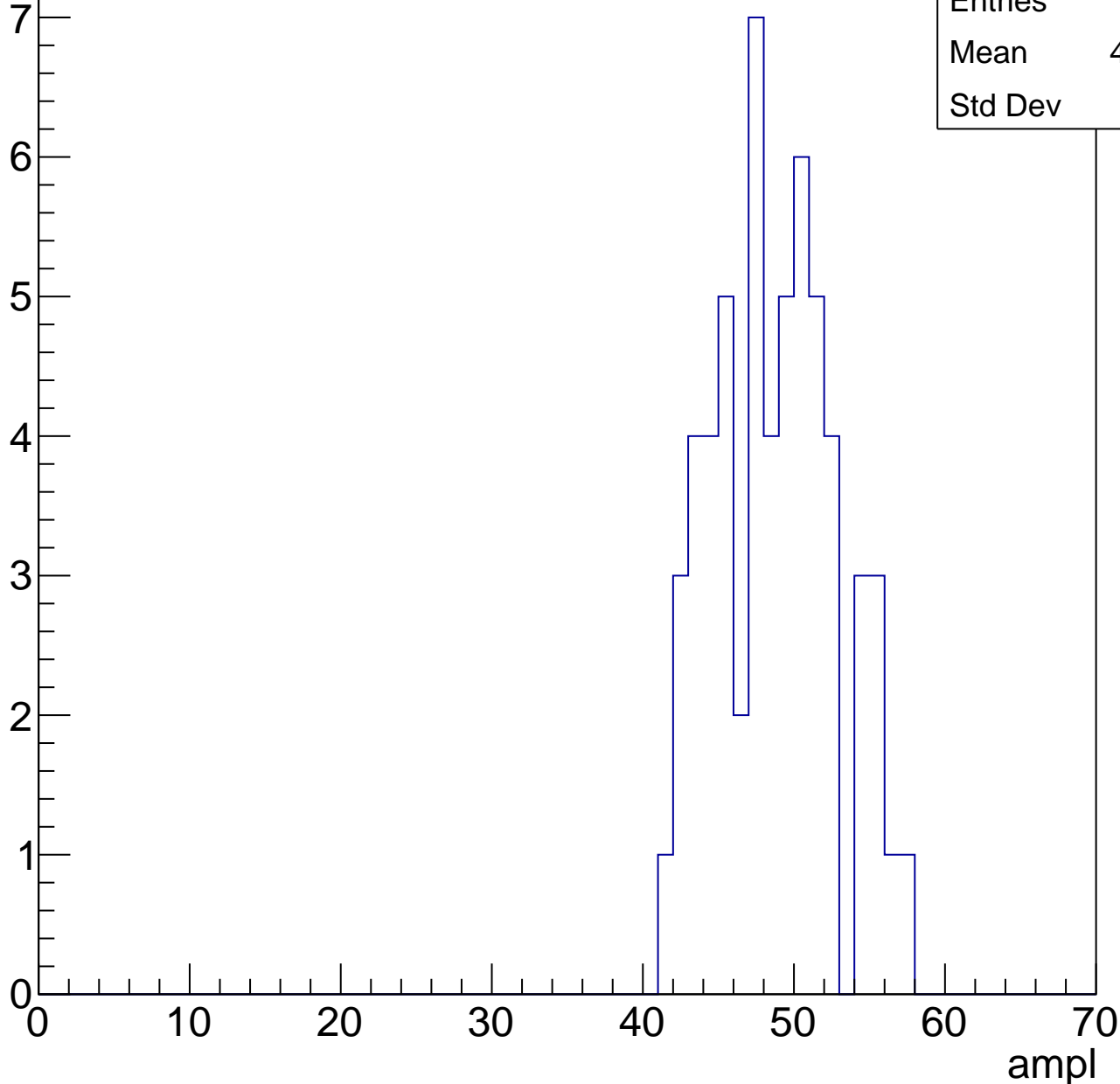


B1L102S, U2-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	48.29
Std Dev	3.97

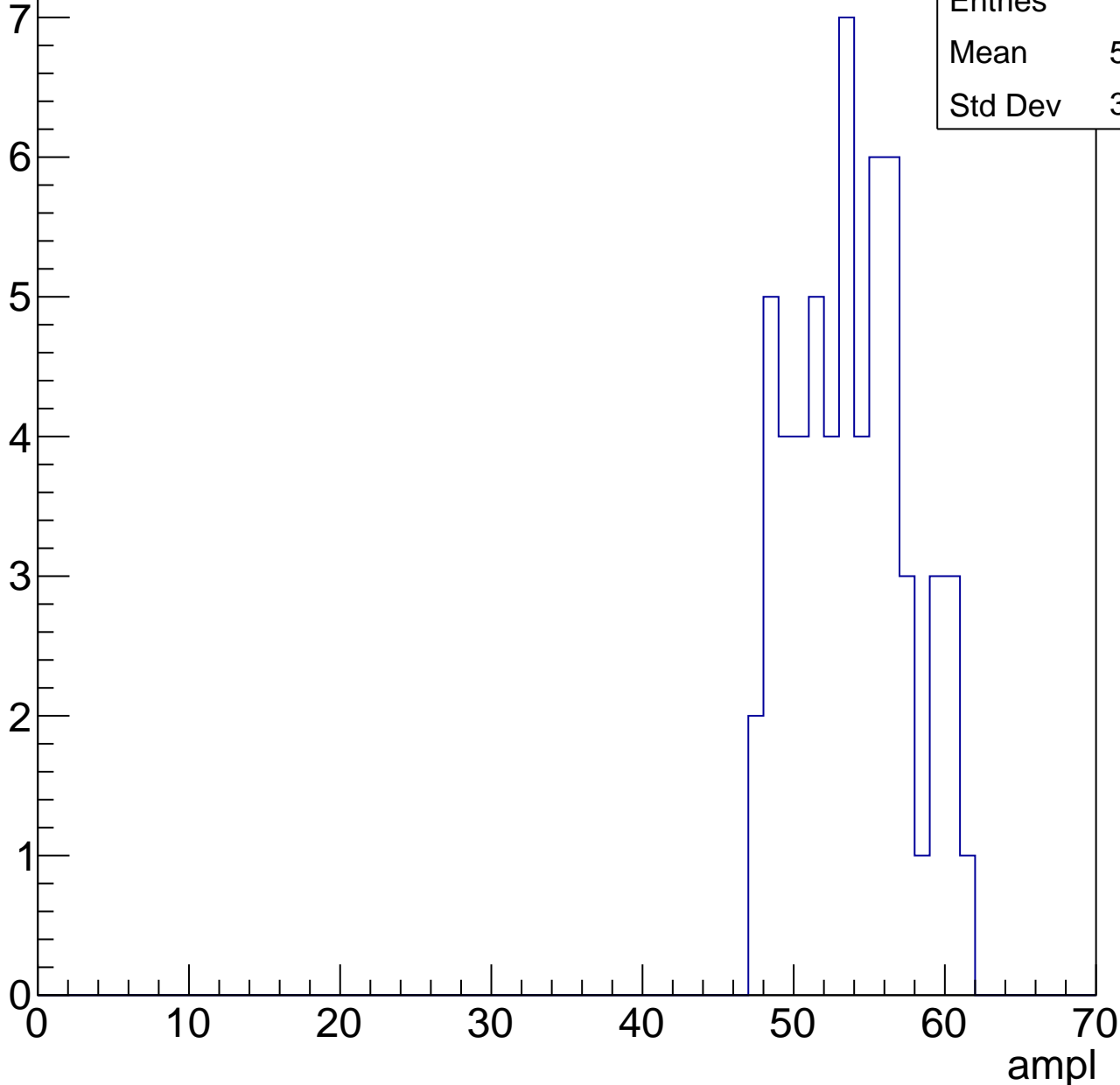


B1L102S, U2-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.33
Std Dev	3.688

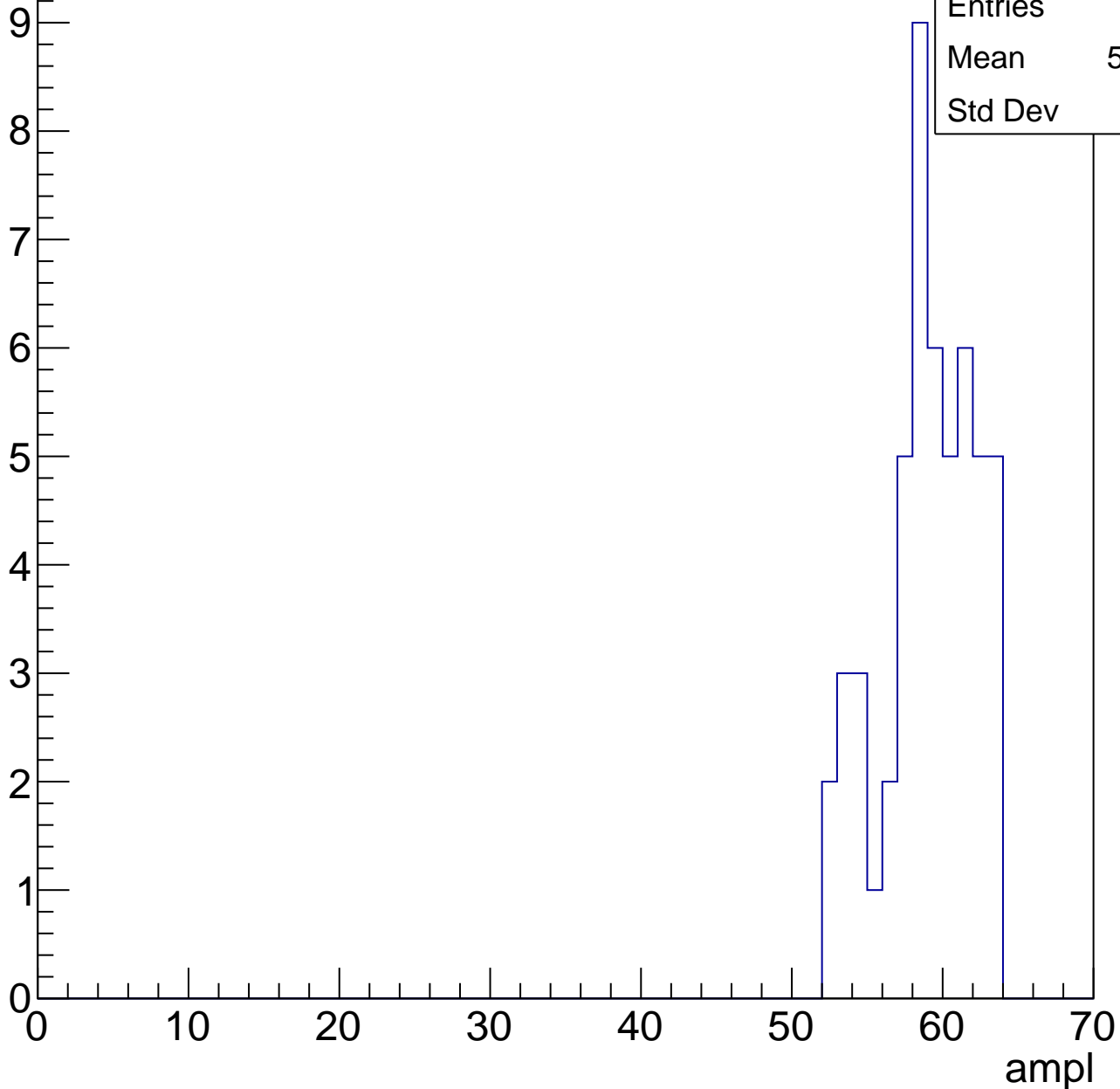


B1L102S, U2-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.54
Std Dev	3.06

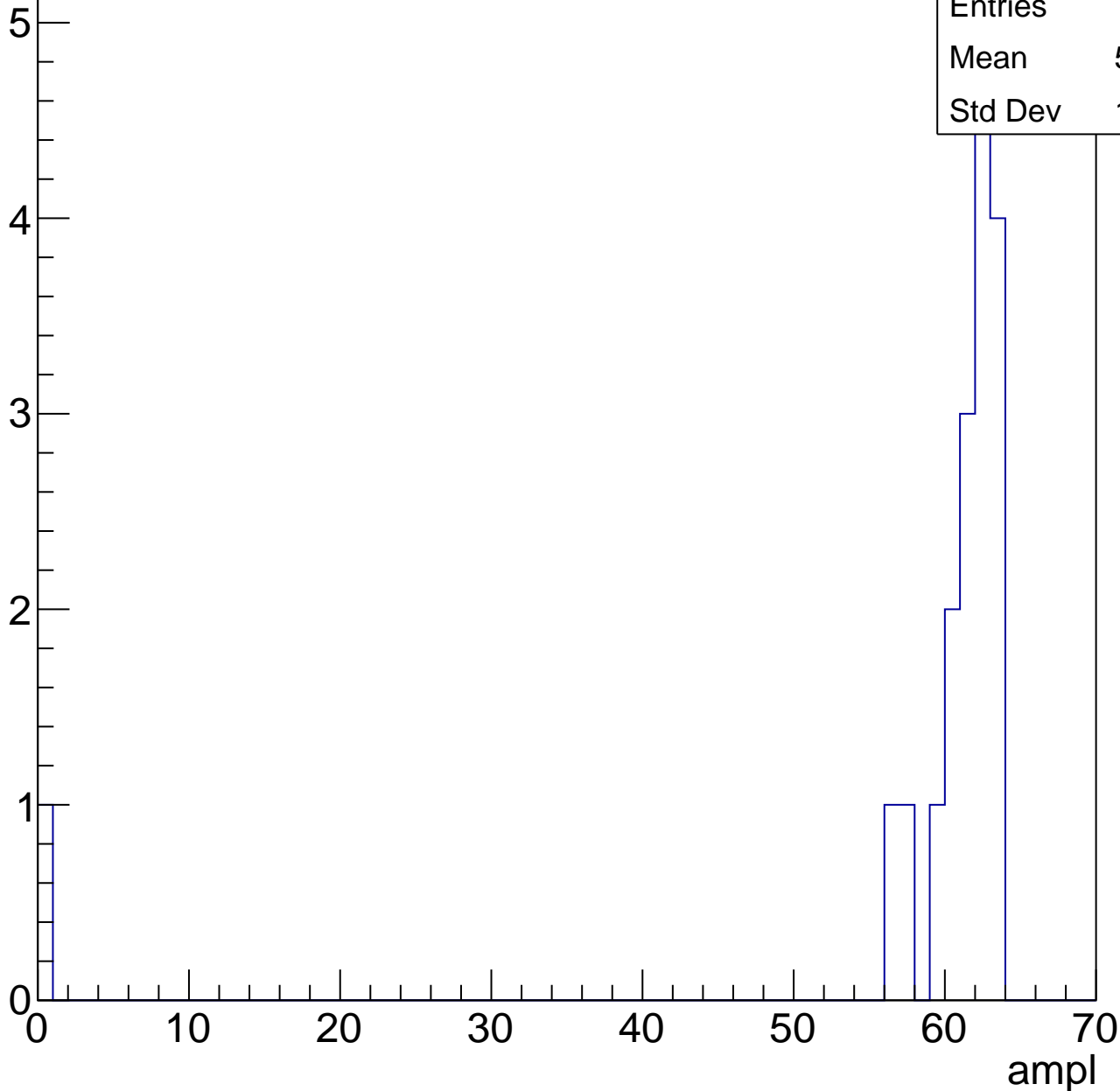


B1L102S, U2-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	57.61
Std Dev	14.11



B1L102S, U2-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch105, adc0

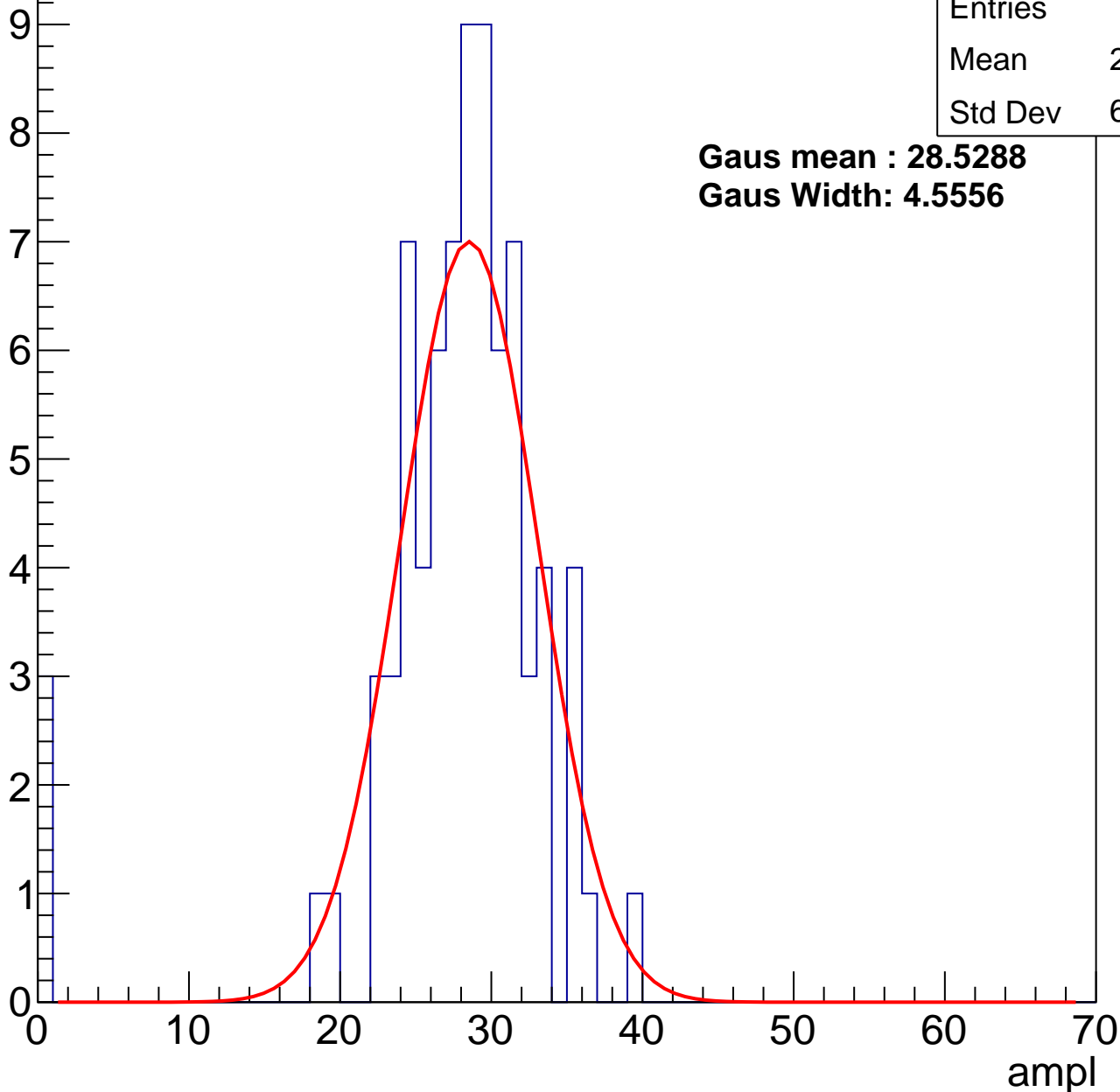
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.06
Std Dev	6.609

Gaus mean : 28.5288

Gaus Width: 4.5556



B1L102S, U2-ch105, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	64
Mean	34.72
Std Dev	3.333

Gaus mean : 35.7952

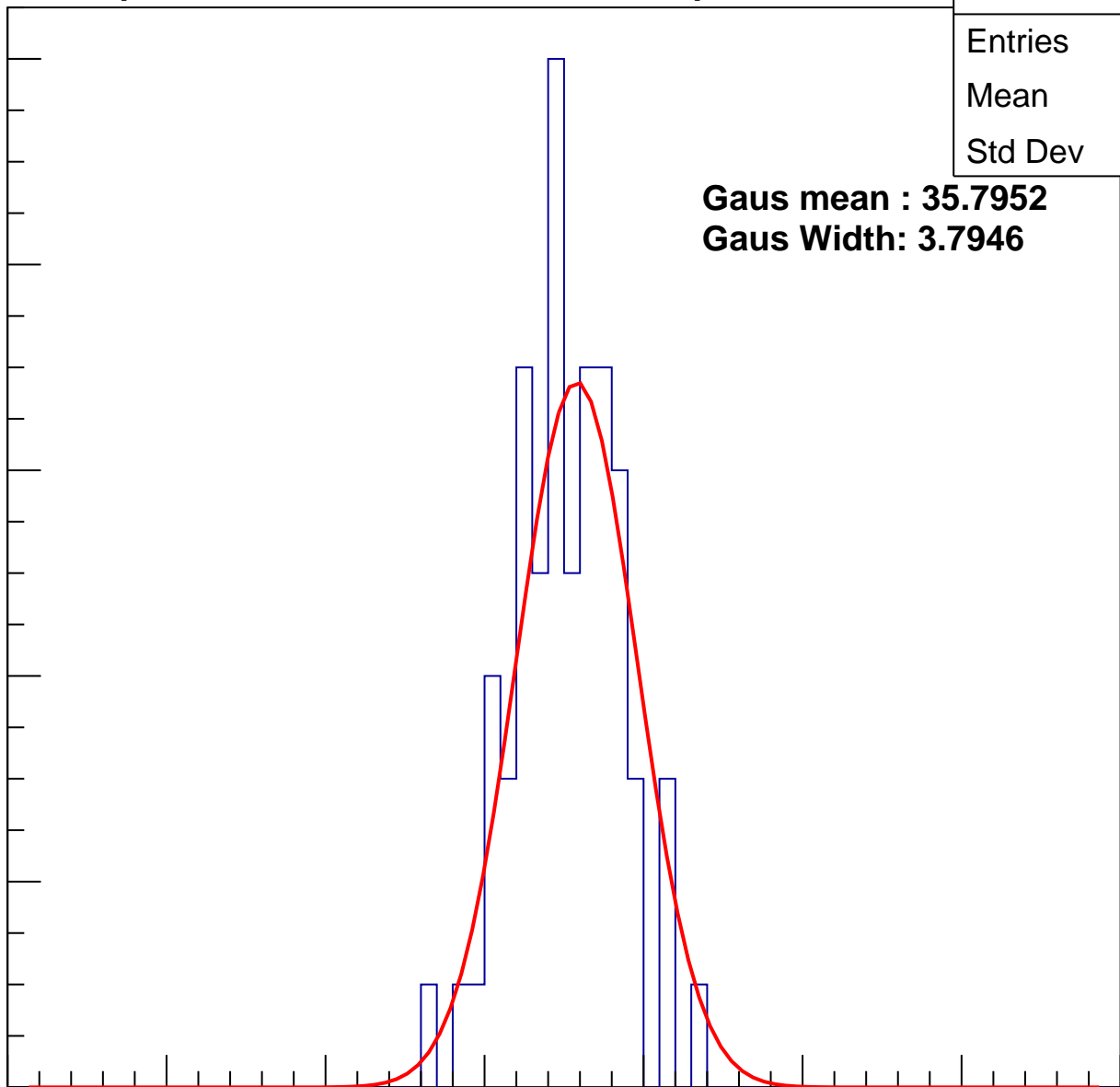
Gaus Width: 3.7946

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch105, adc2

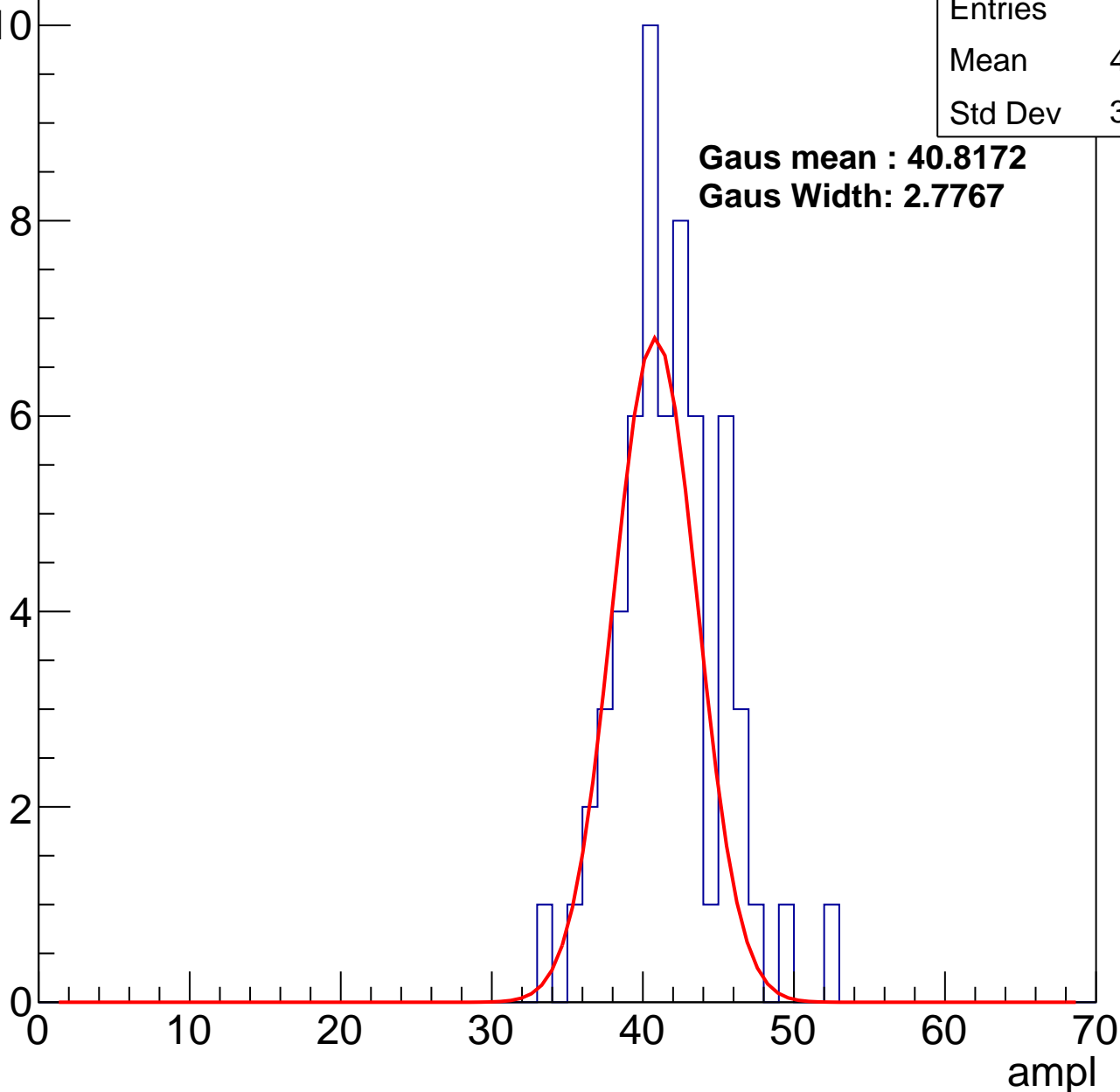
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.28
Std Dev	3.406

Gaus mean : 40.8172

Gaus Width: 2.7767

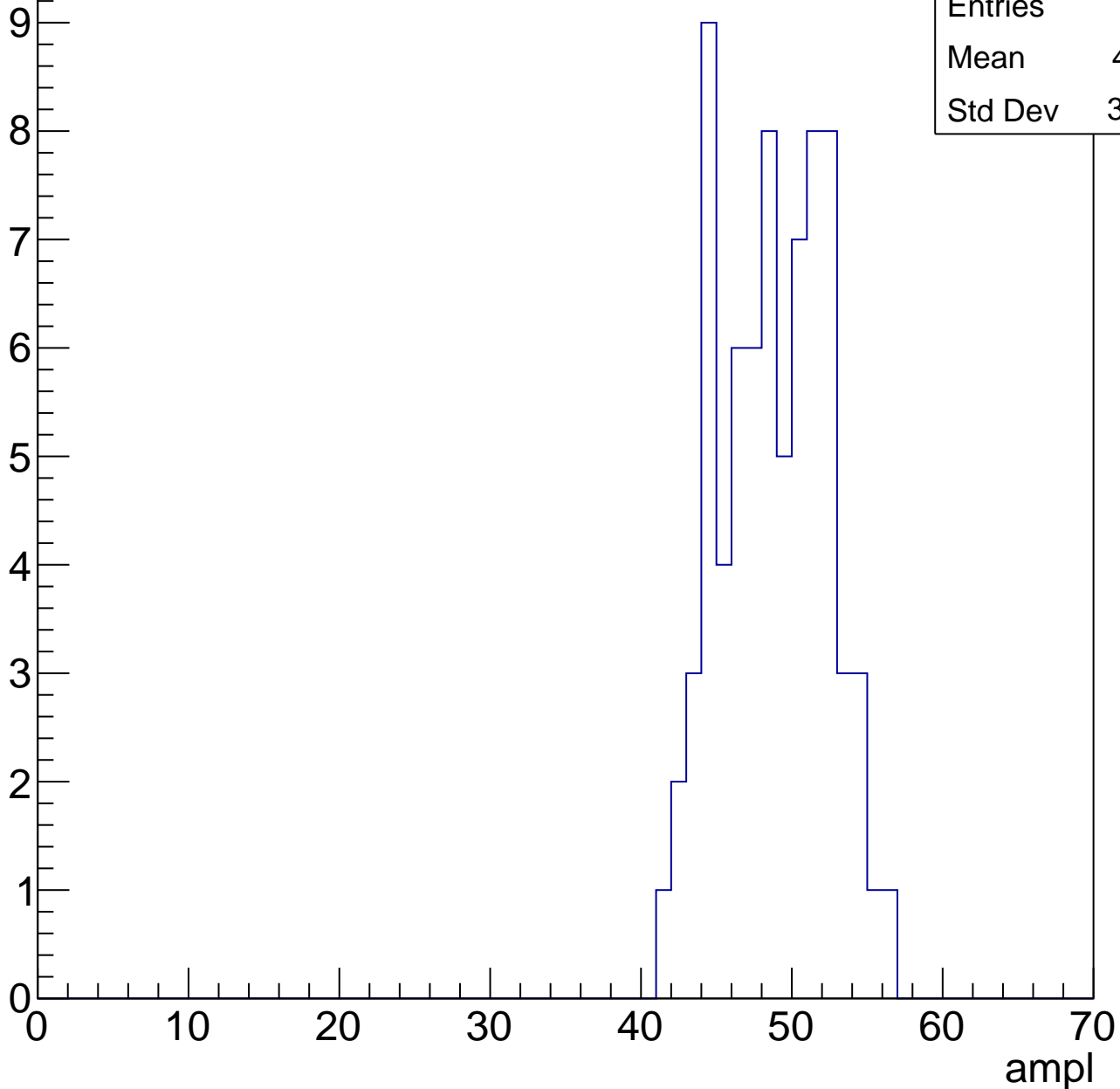


B1L102S, U2-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.31
Std Dev	3.517

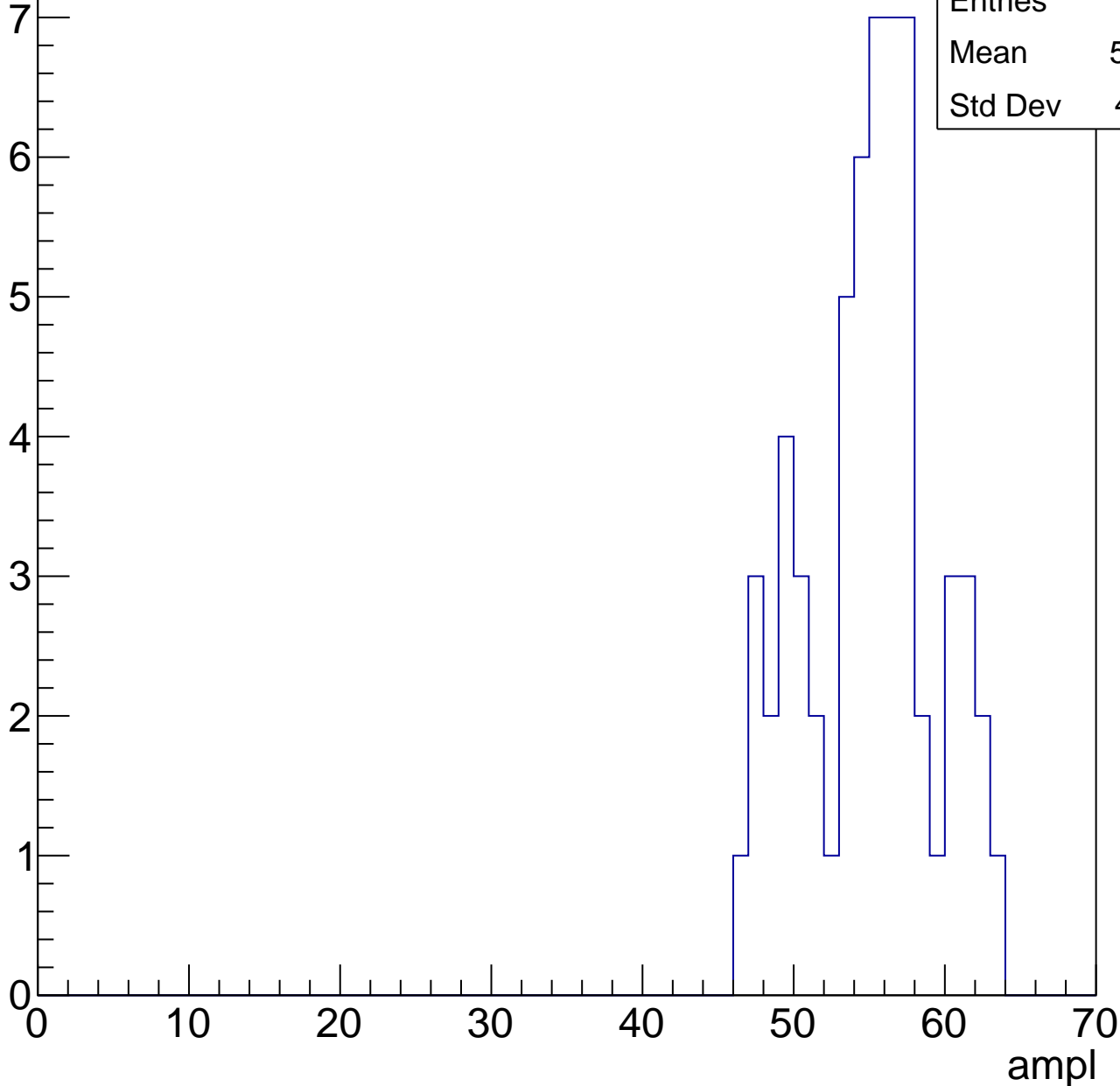


B1L102S, U2-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

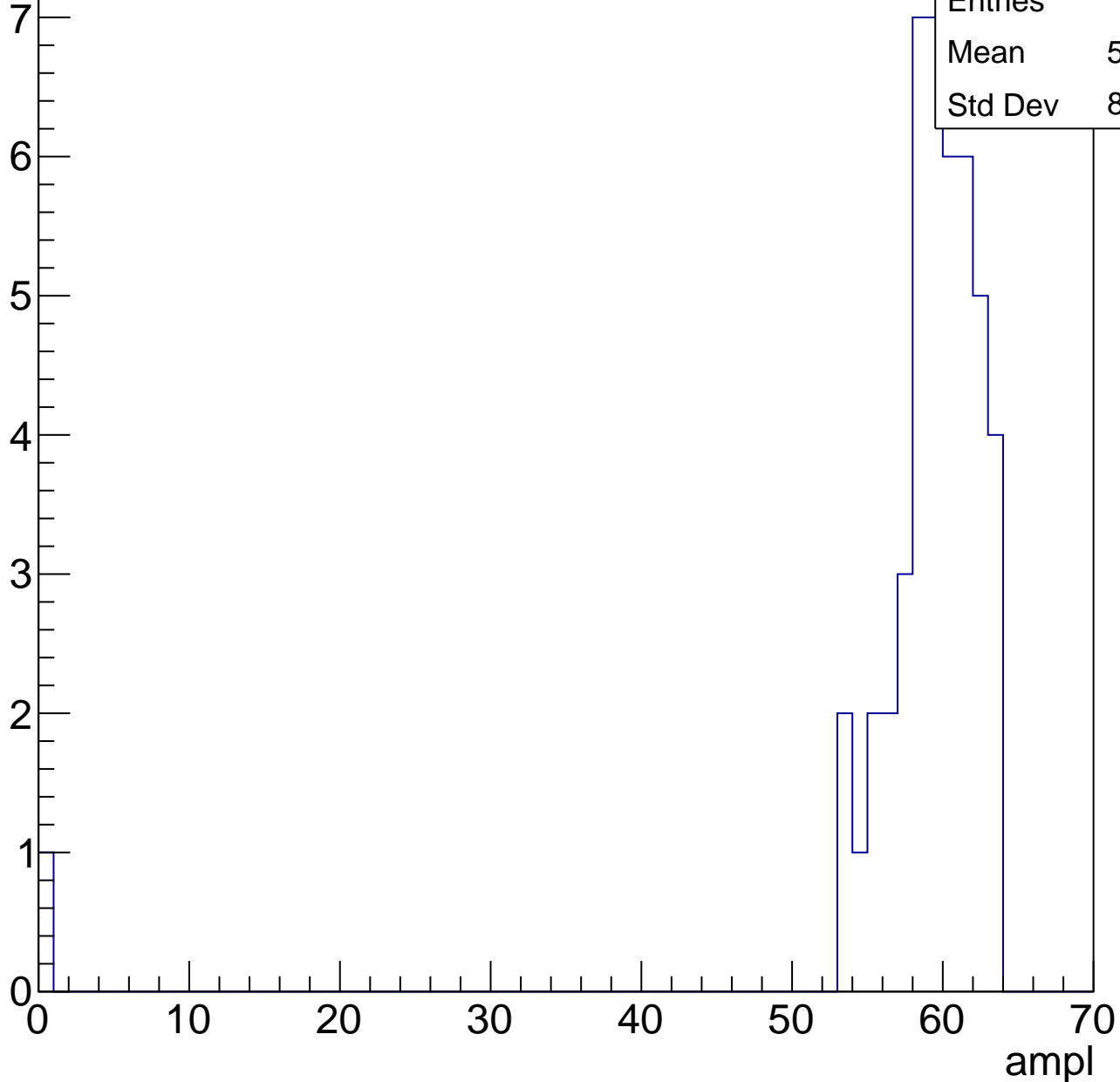
Entries	60
Mean	54.55
Std Dev	4.201



B1L102S, U2-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

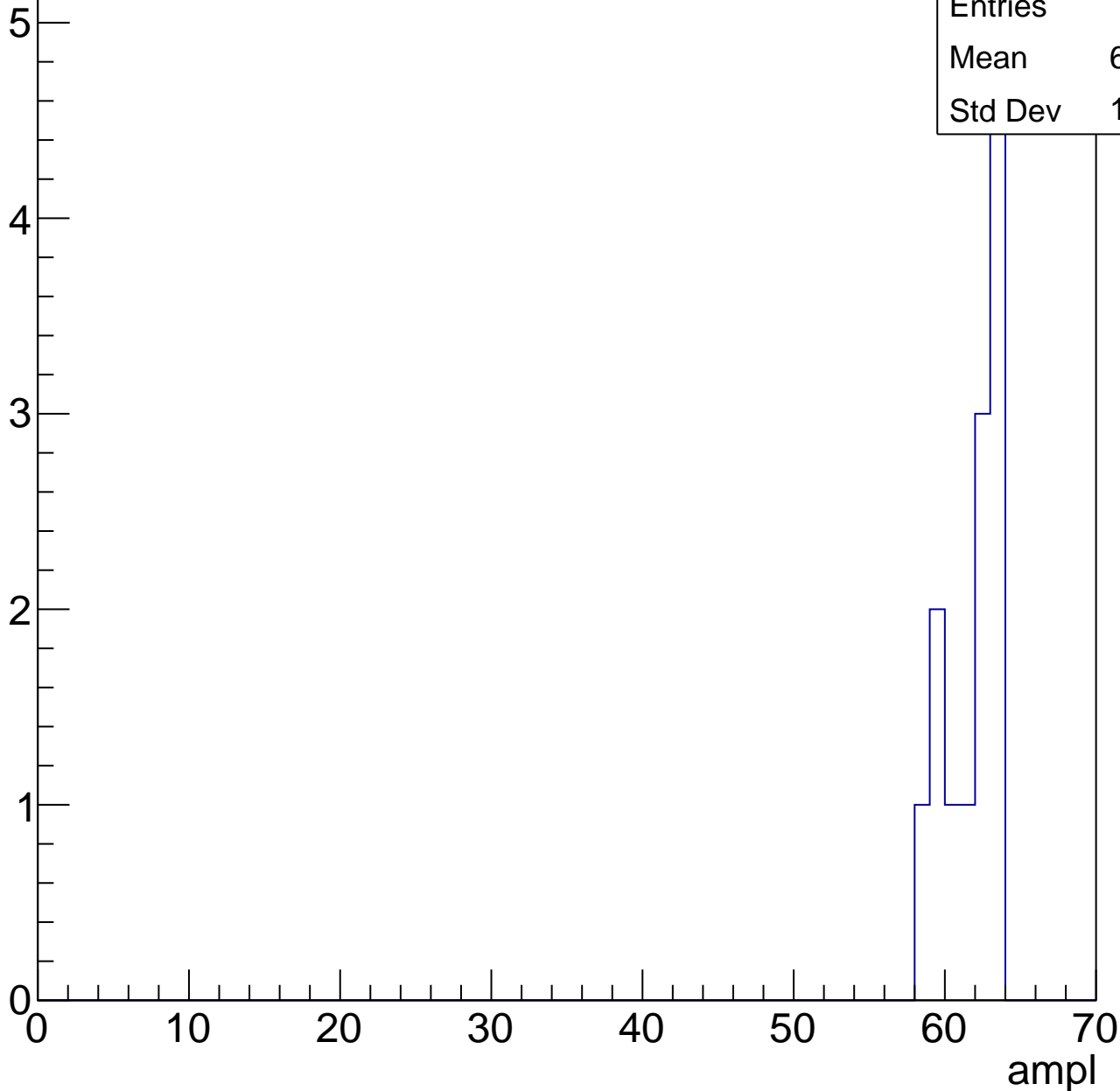


B1L102S, U2-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	61.38
Std Dev	1.734



B1L102S, U2-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch106, adc0

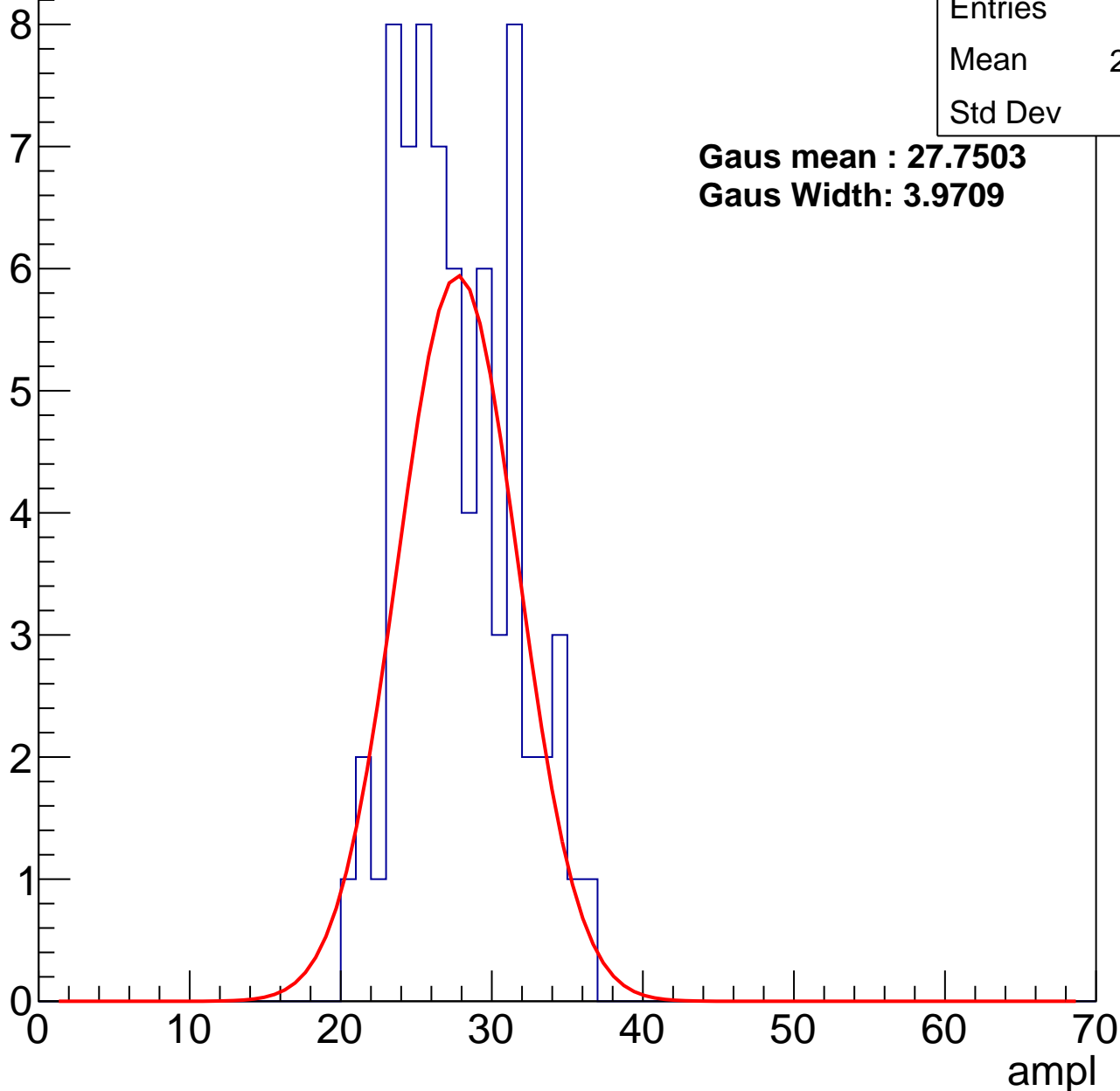
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.24
Std Dev	3.72

Gaus mean : 27.7503

Gaus Width: 3.9709



B1L102S, U2-ch106, adc1

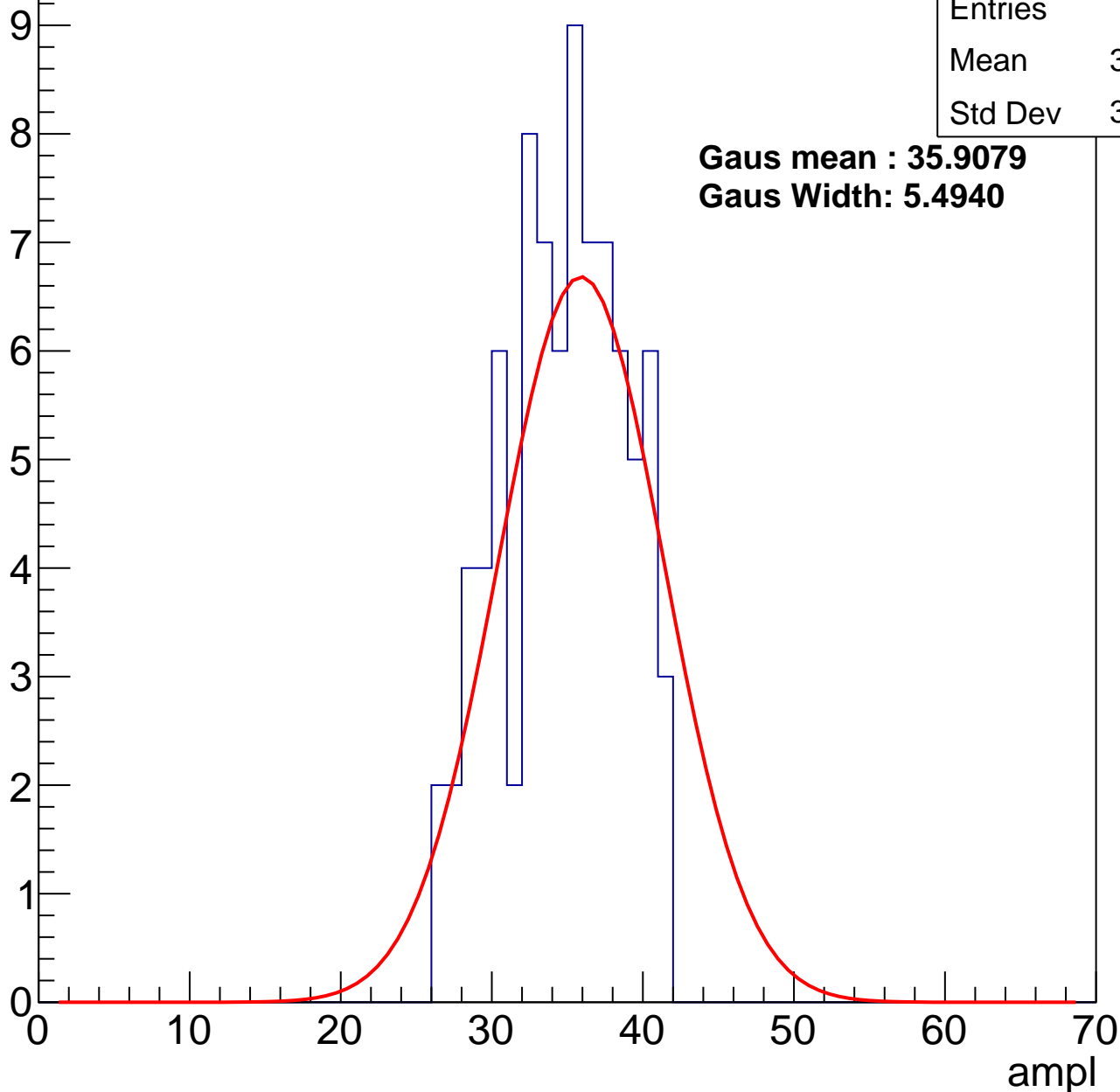
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	34.27
Std Dev	3.935

Gaus mean : 35.9079

Gaus Width: 5.4940



B1L102S, U2-ch106, adc2

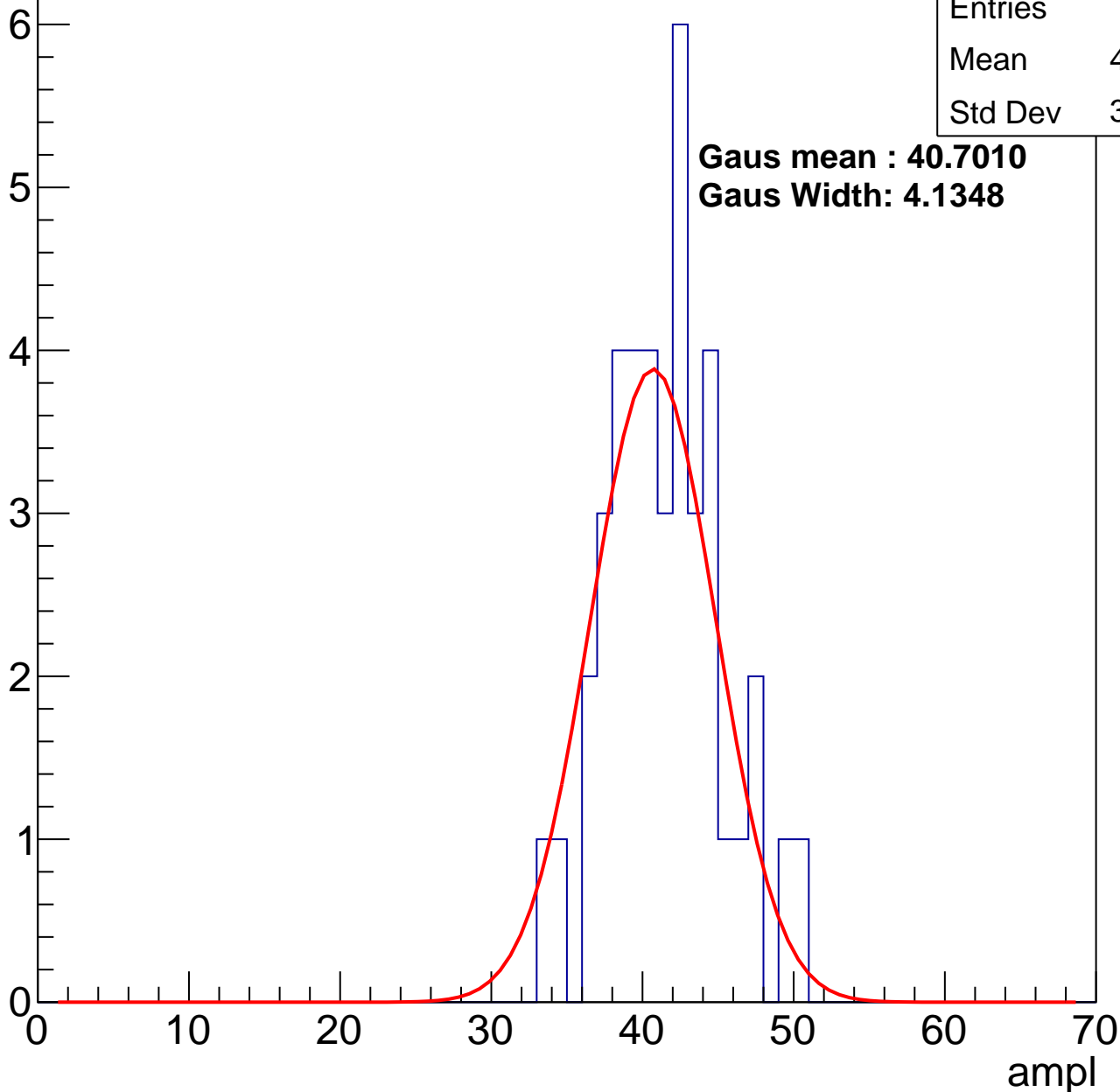
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	41.02
Std Dev	3.758

Gaus mean : 40.7010

Gaus Width: 4.1348

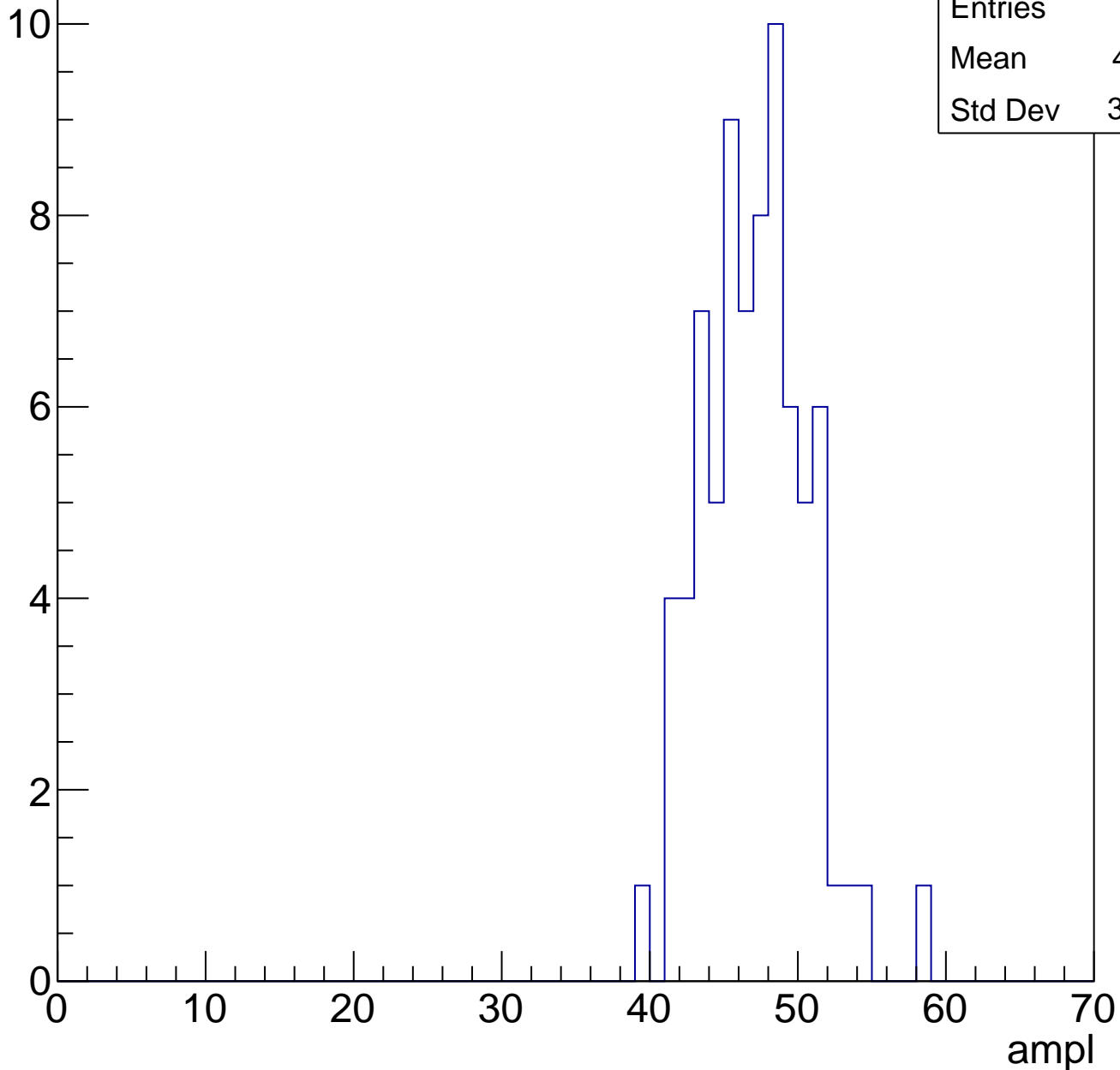


B1L102S, U2-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	46.61
Std Dev	3.445

Entry

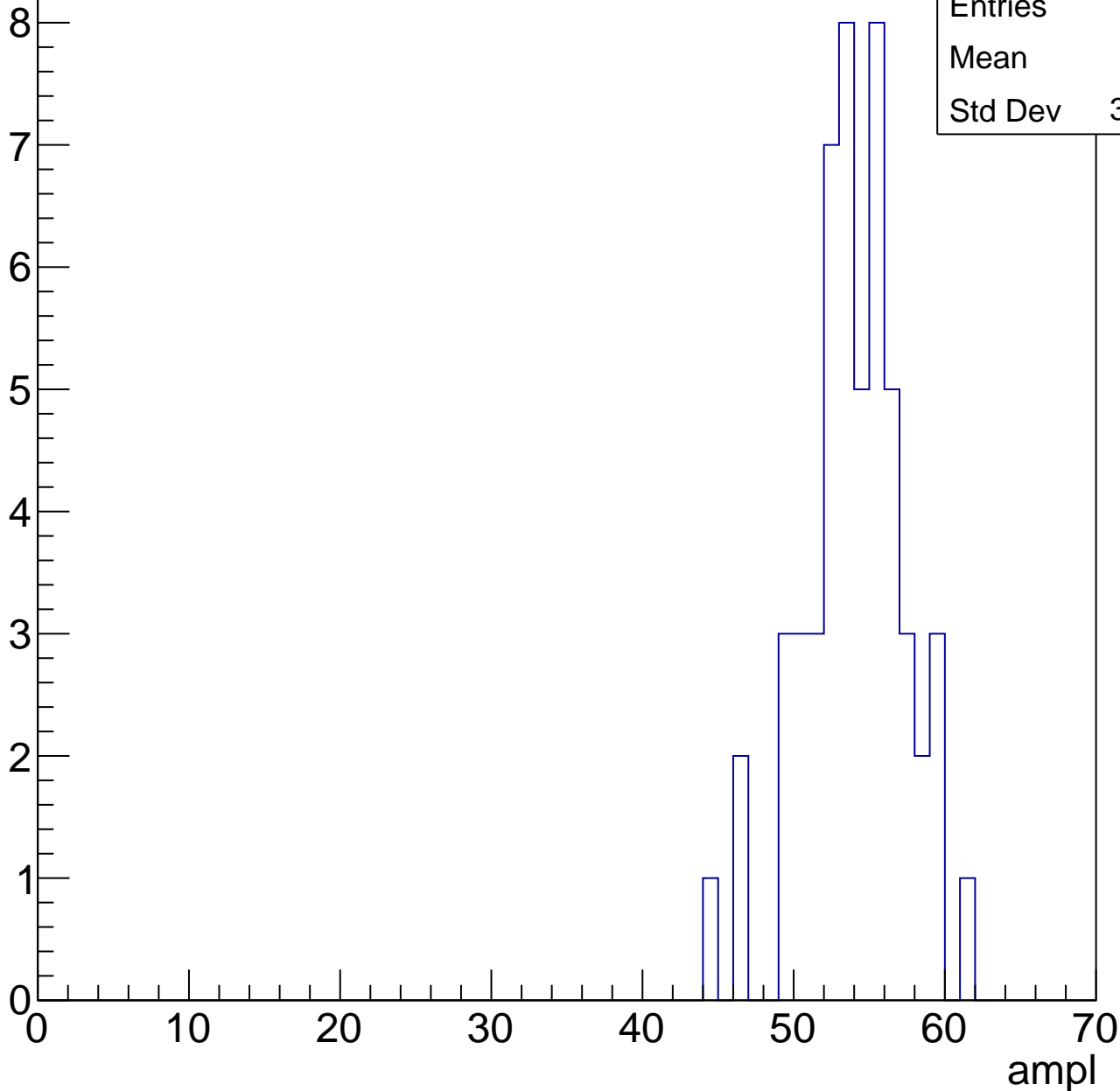


B1L102S, U2-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.5
Std Dev	3.365

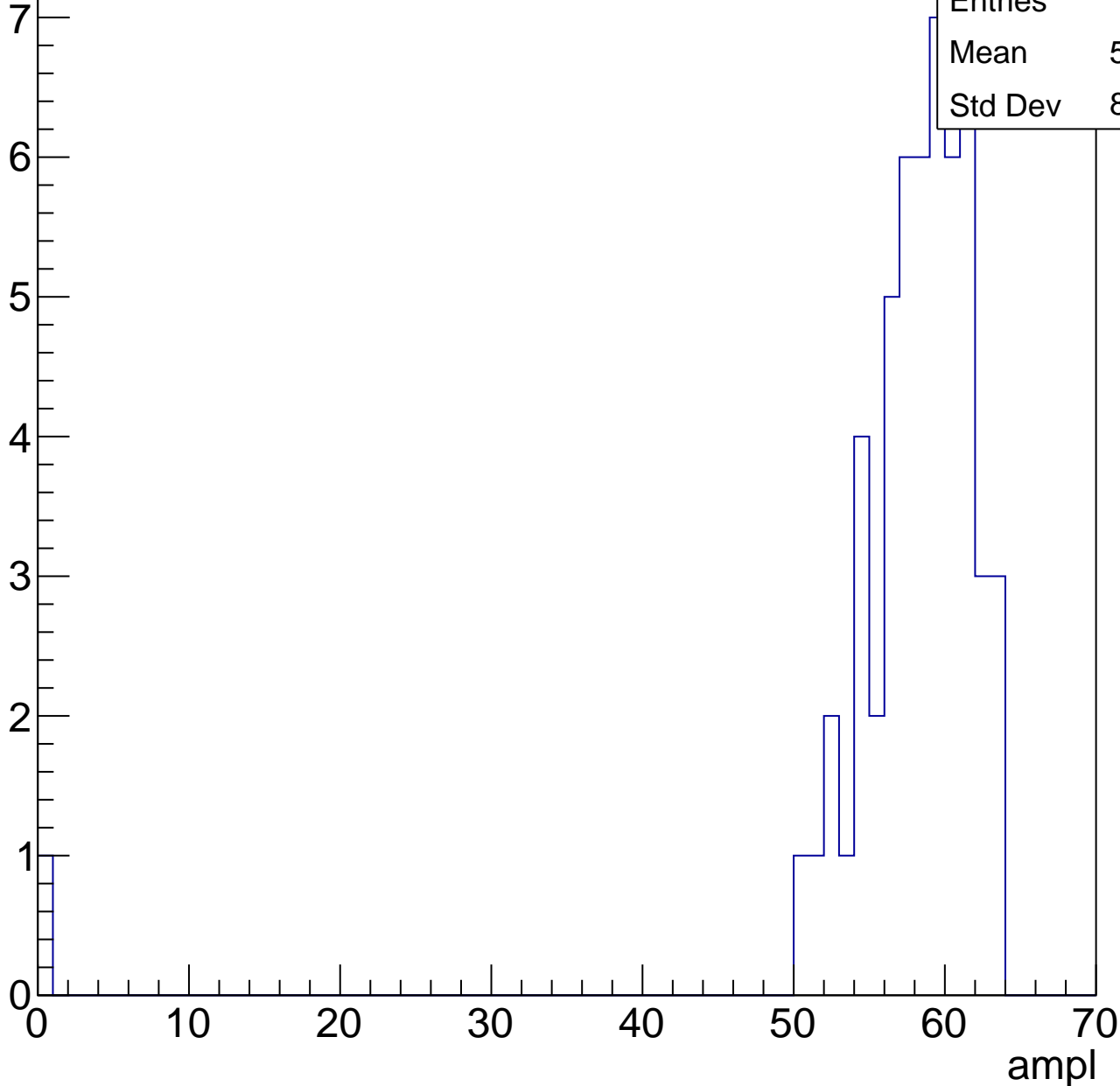


B1L102S, U2-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.89
Std Dev	8.346

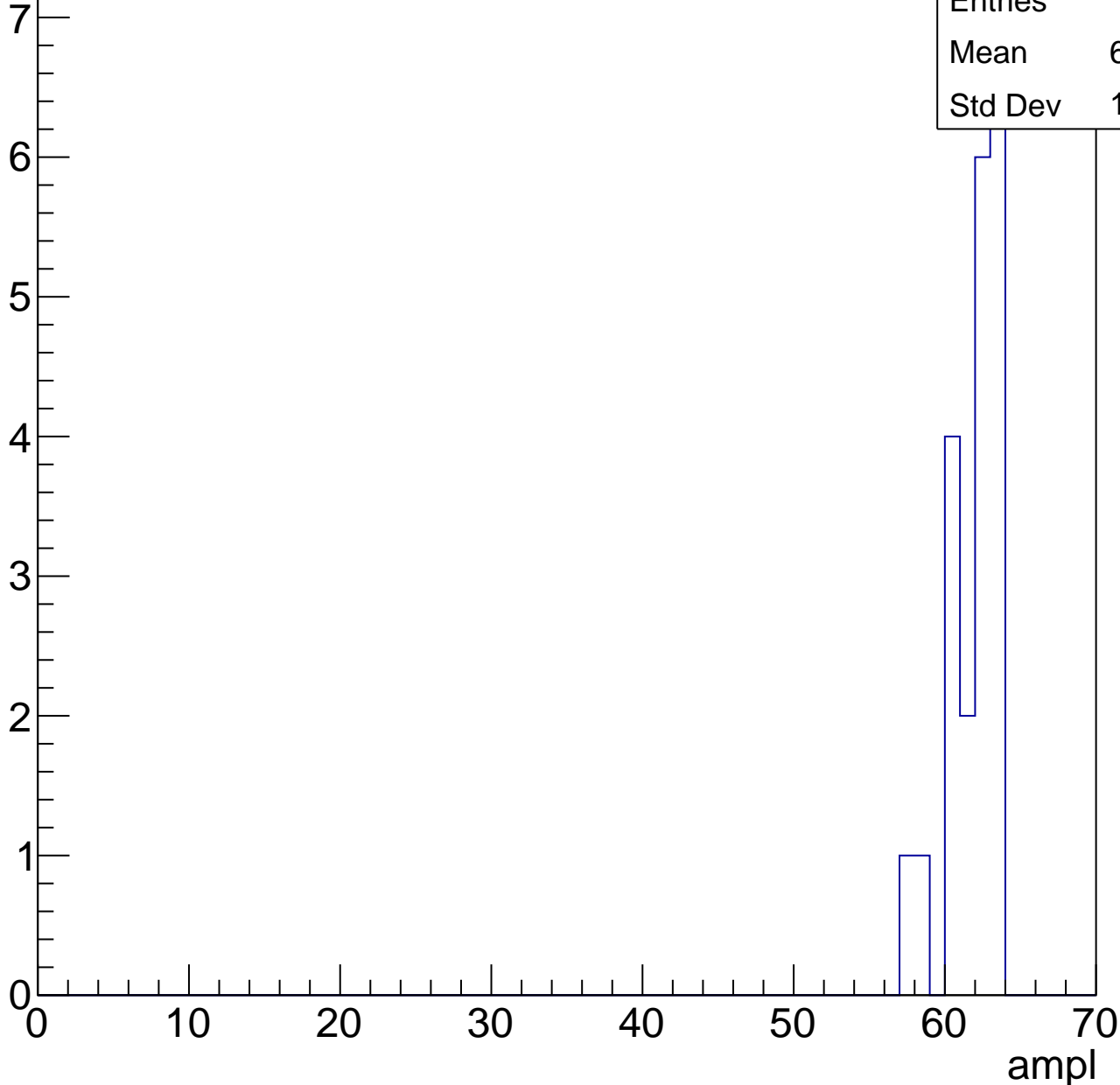


B1L102S, U2-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61.43
Std Dev	1.678



B1L102S, U2-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch107, adc0

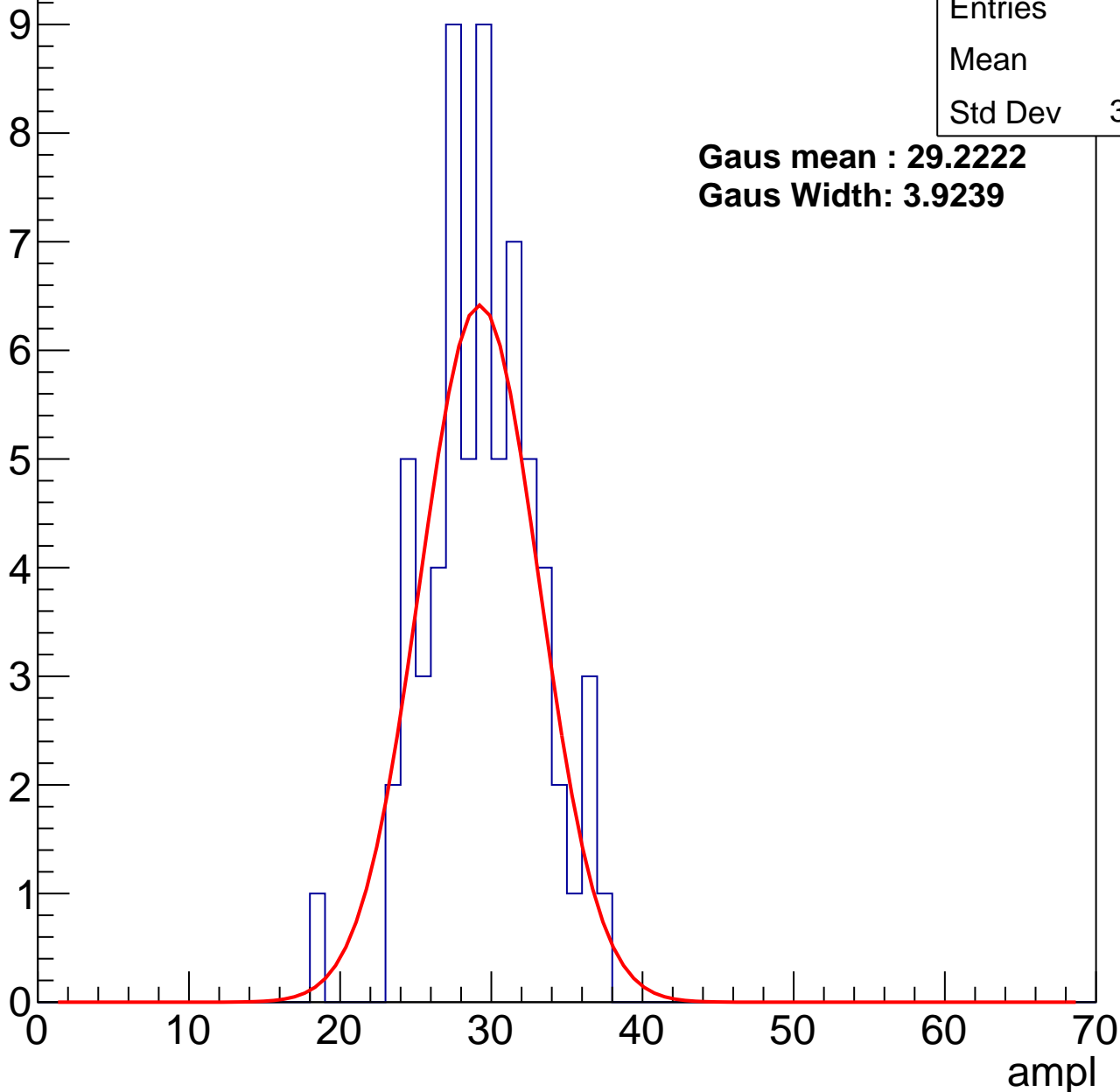
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29
Std Dev	3.656

Gaus mean : 29.2222

Gaus Width: 3.9239



B1L102S, U2-ch107, adc1

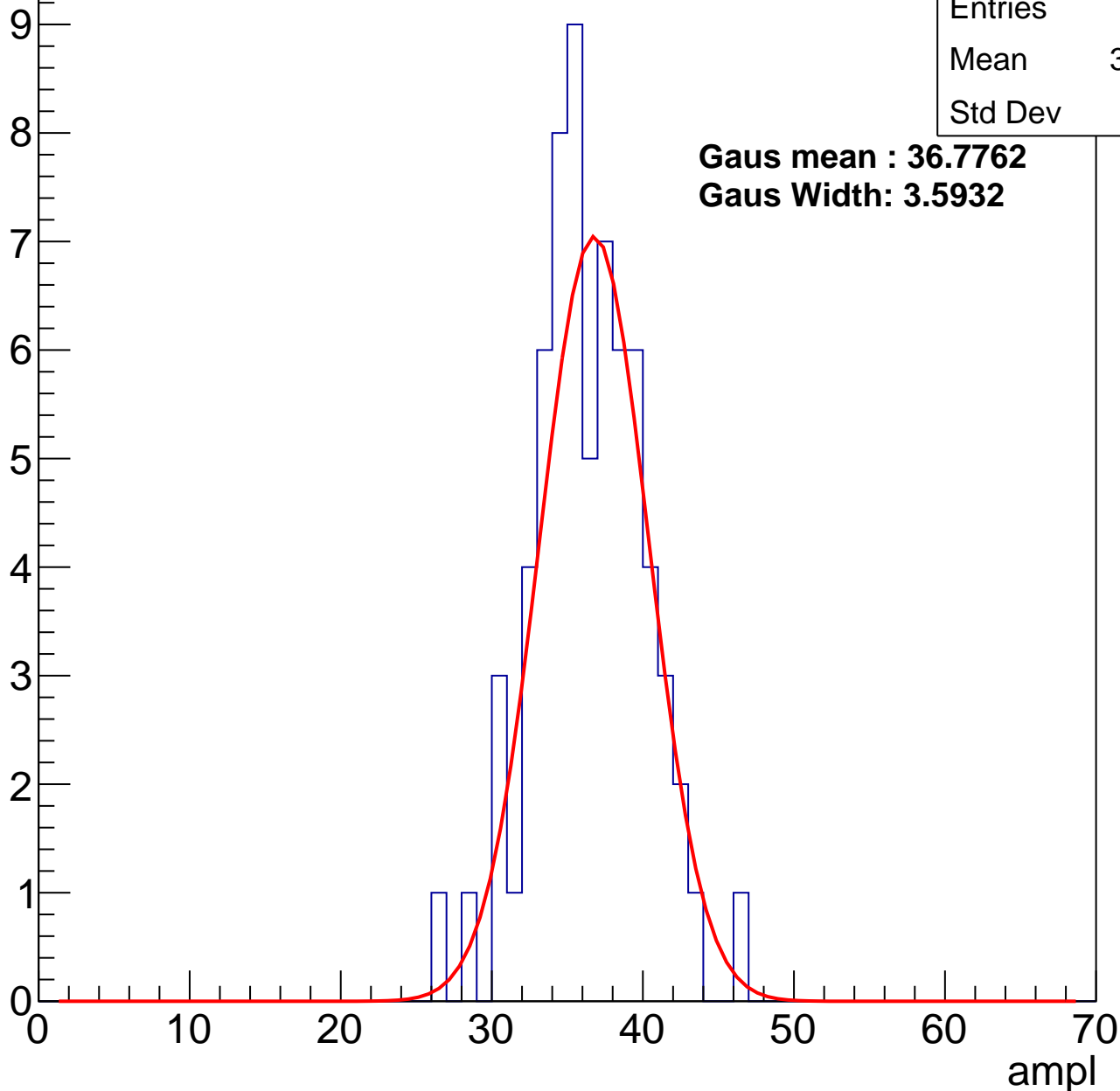
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.96
Std Dev	3.64

Gaus mean : 36.7762

Gaus Width: 3.5932



B1L102S, U2-ch107, adc2

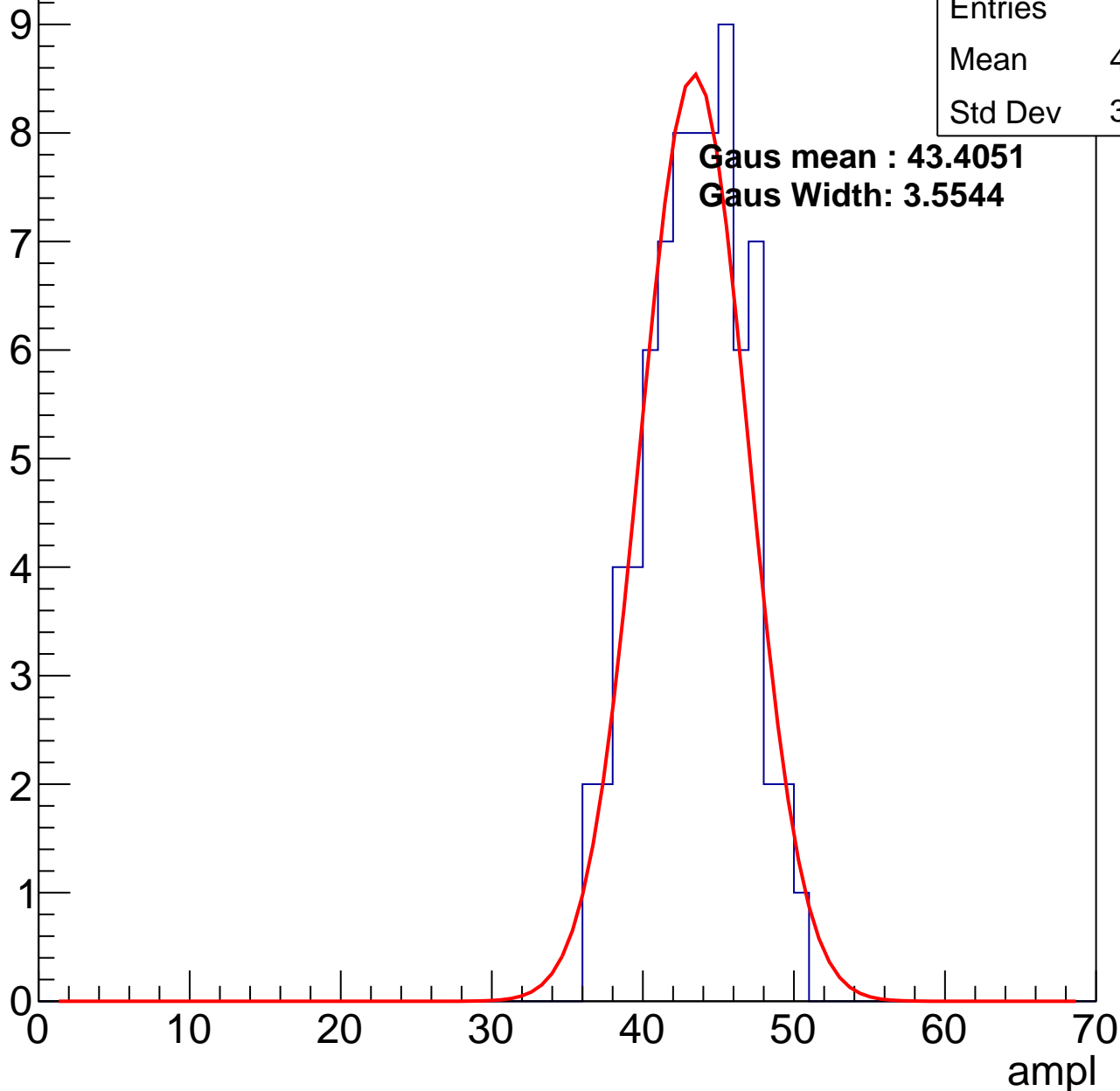
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.99
Std Dev	3.255

Gaus mean : 43.4051

Gaus Width: 3.5544

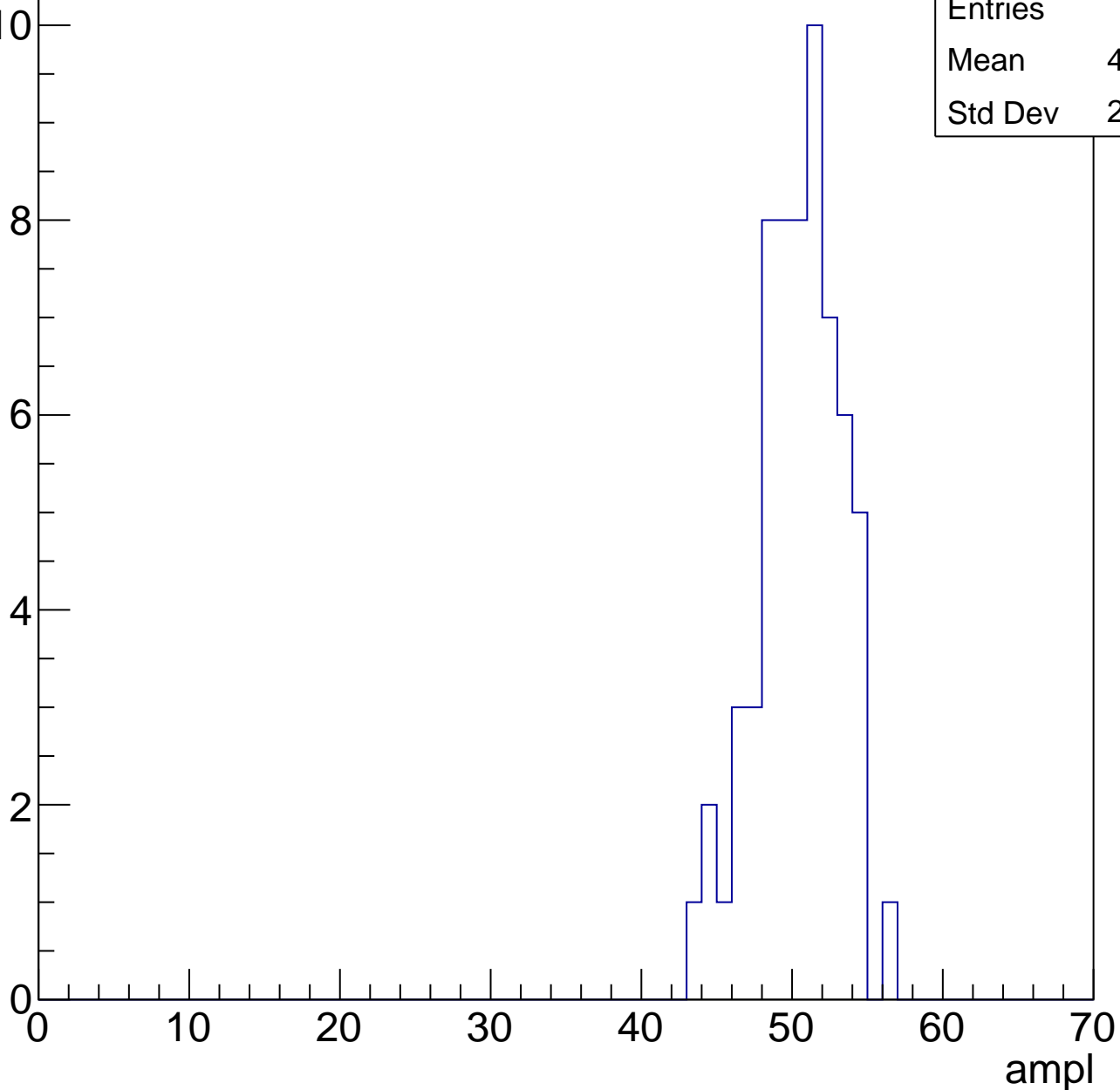


B1L102S, U2-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.98
Std Dev	2.728

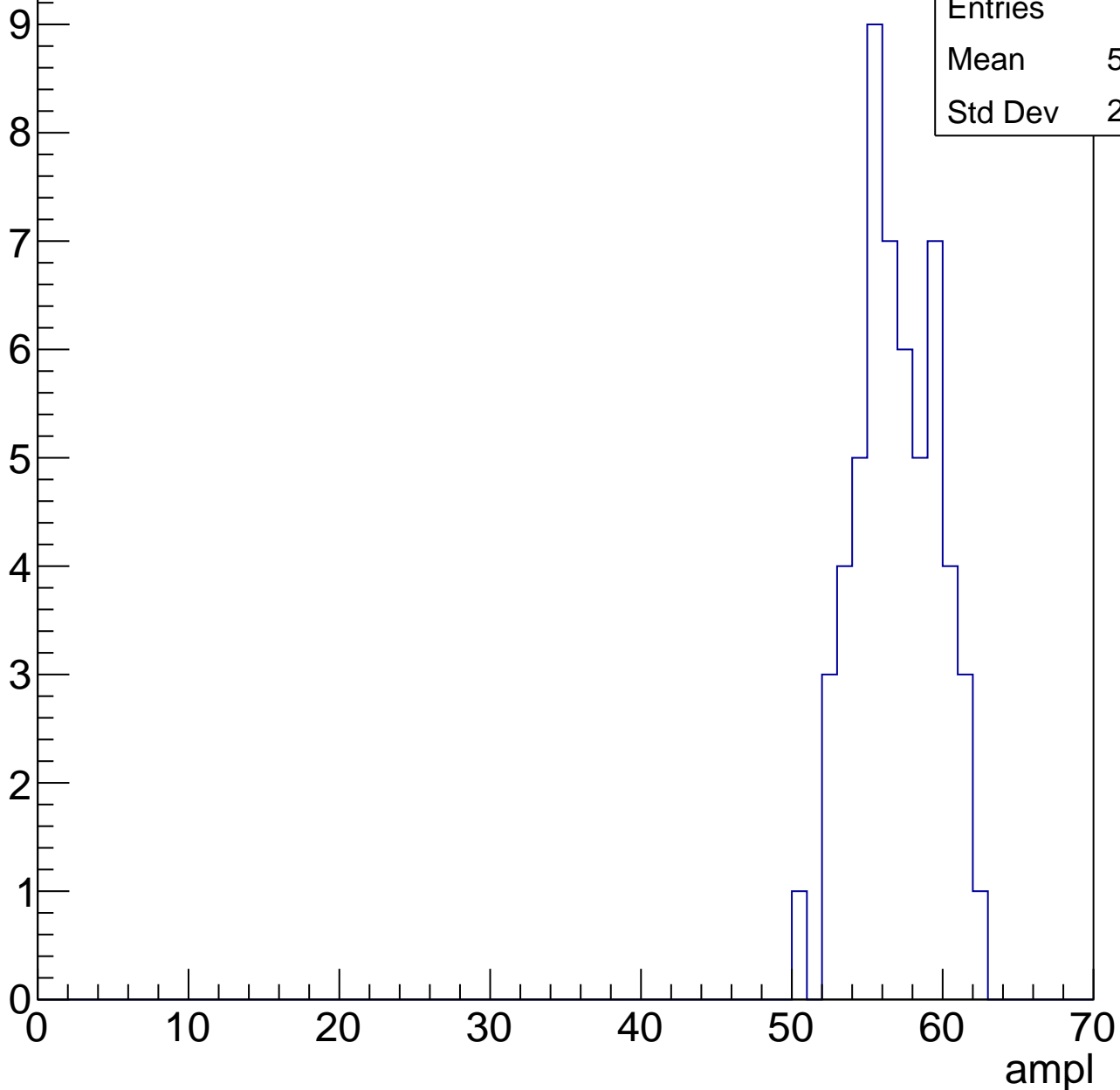


B1L102S, U2-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.45
Std Dev	2.702

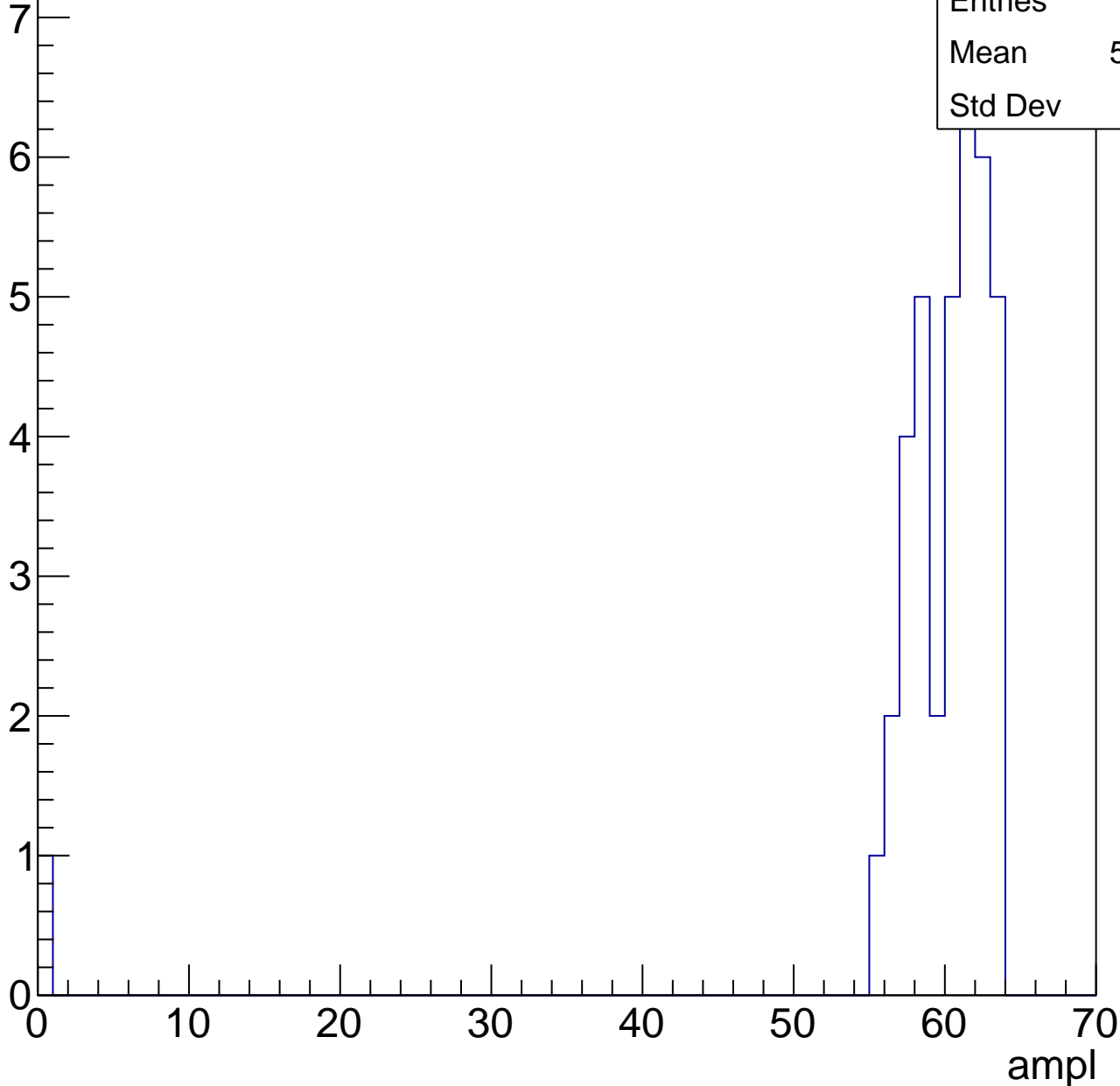


B1L102S, U2-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.34
Std Dev	9.85

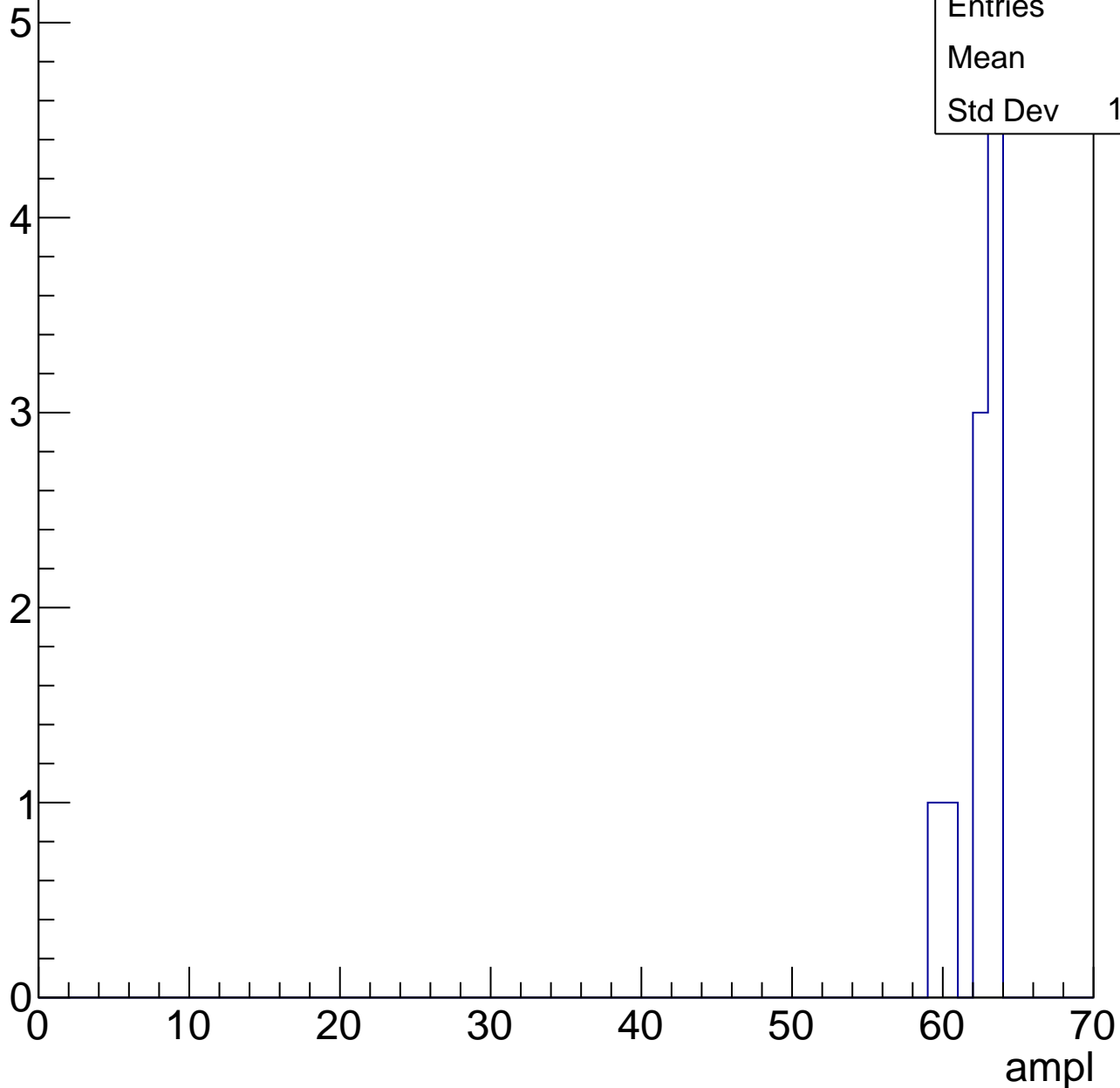


B1L102S, U2-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

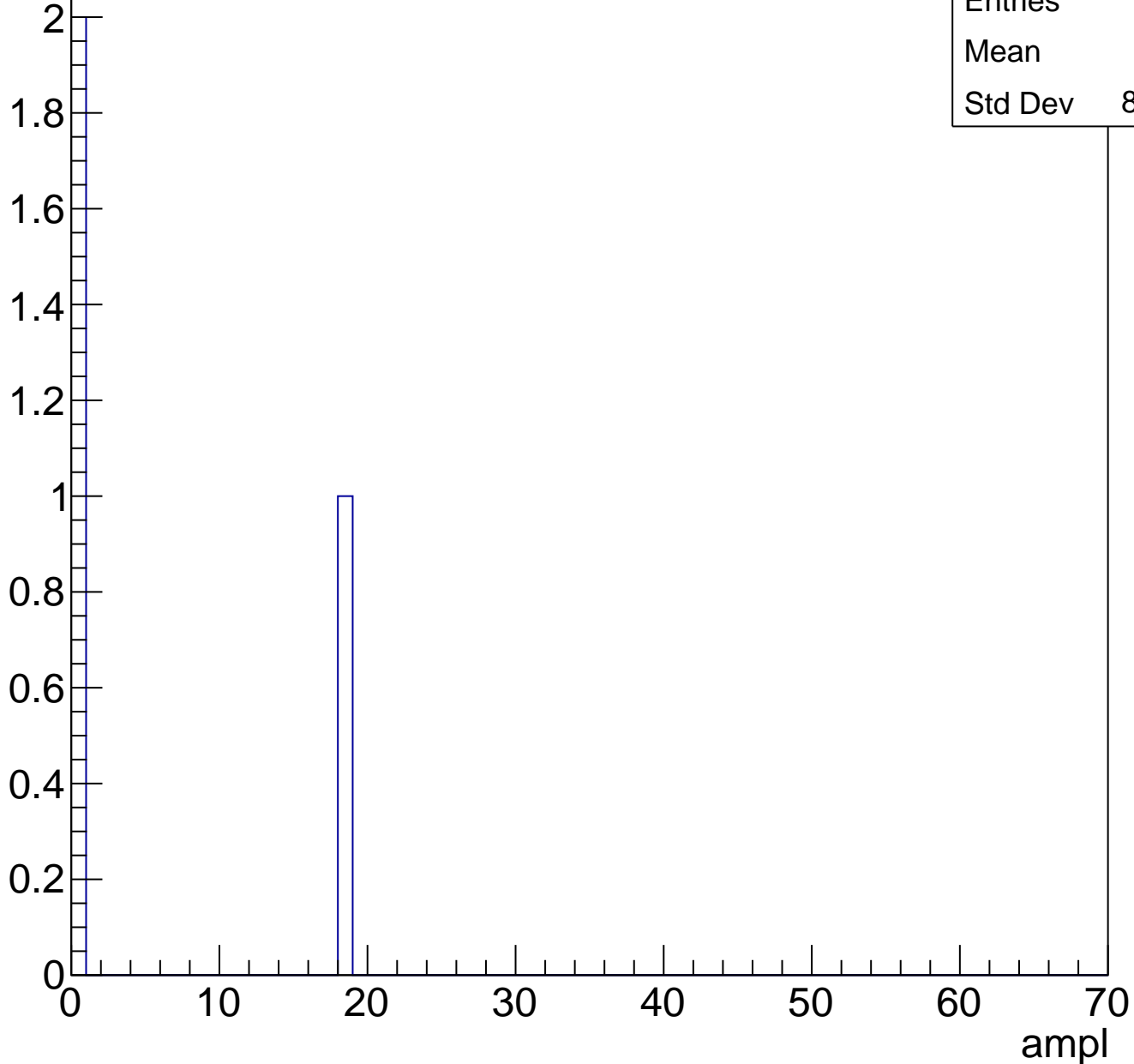
Entries	10
Mean	62
Std Dev	1.342



B1L102S, U2-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L102S, U2-ch108, adc0

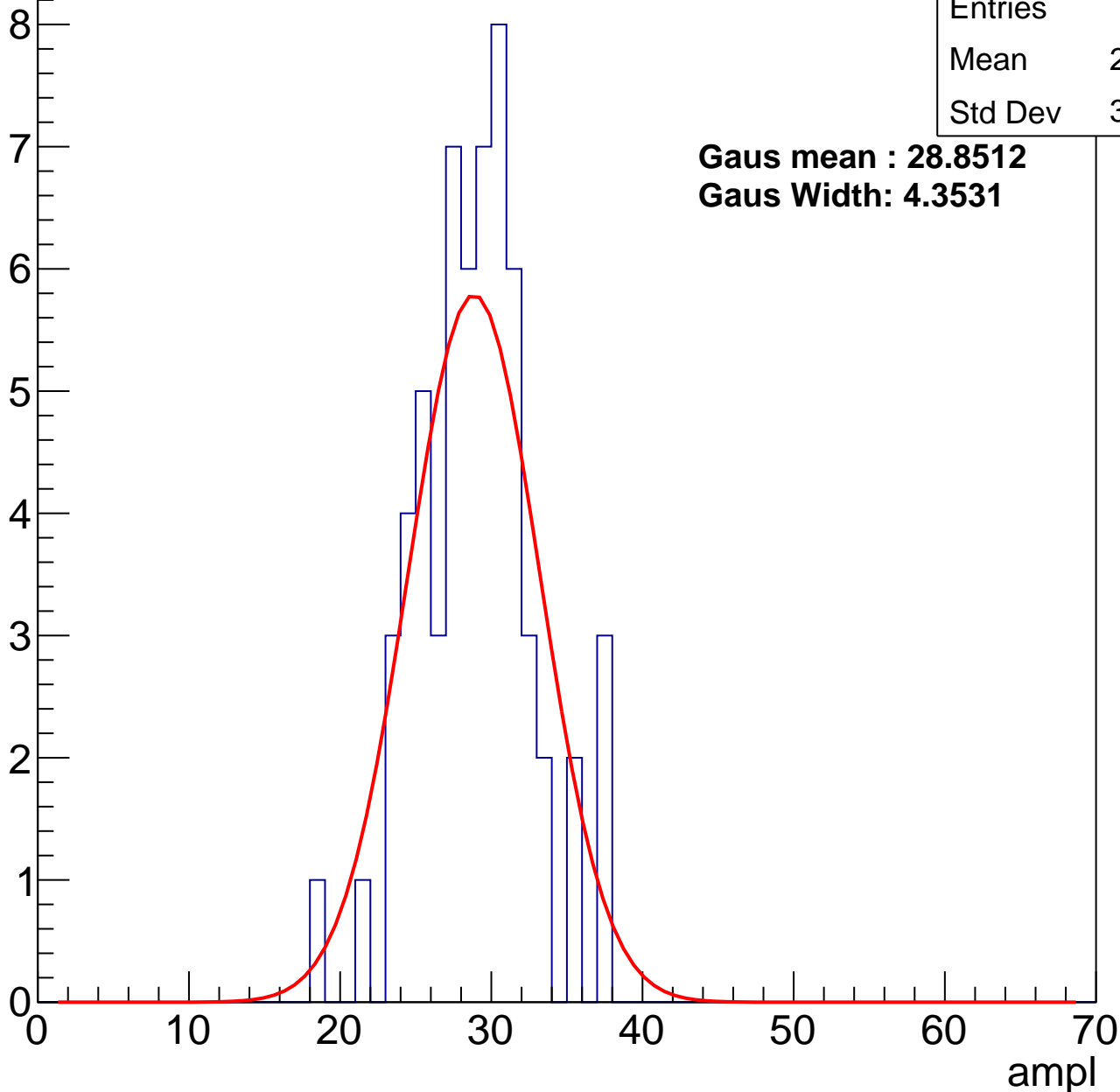
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.46
Std Dev	3.779

Gaus mean : 28.8512

Gaus Width: 4.3531



B1L102S, U2-ch108, adc1

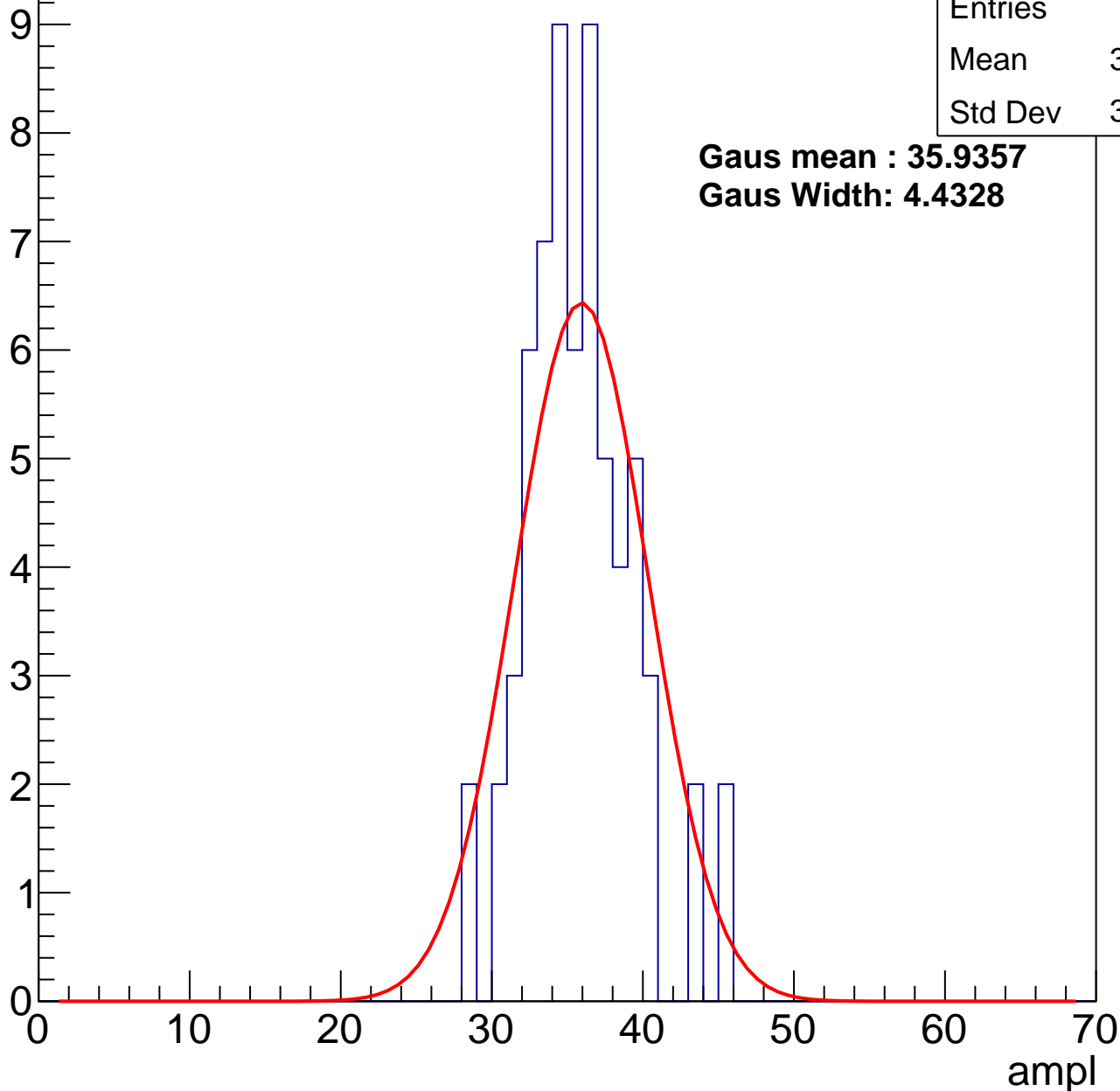
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.38
Std Dev	3.563

Gaus mean : 35.9357

Gaus Width: 4.4328



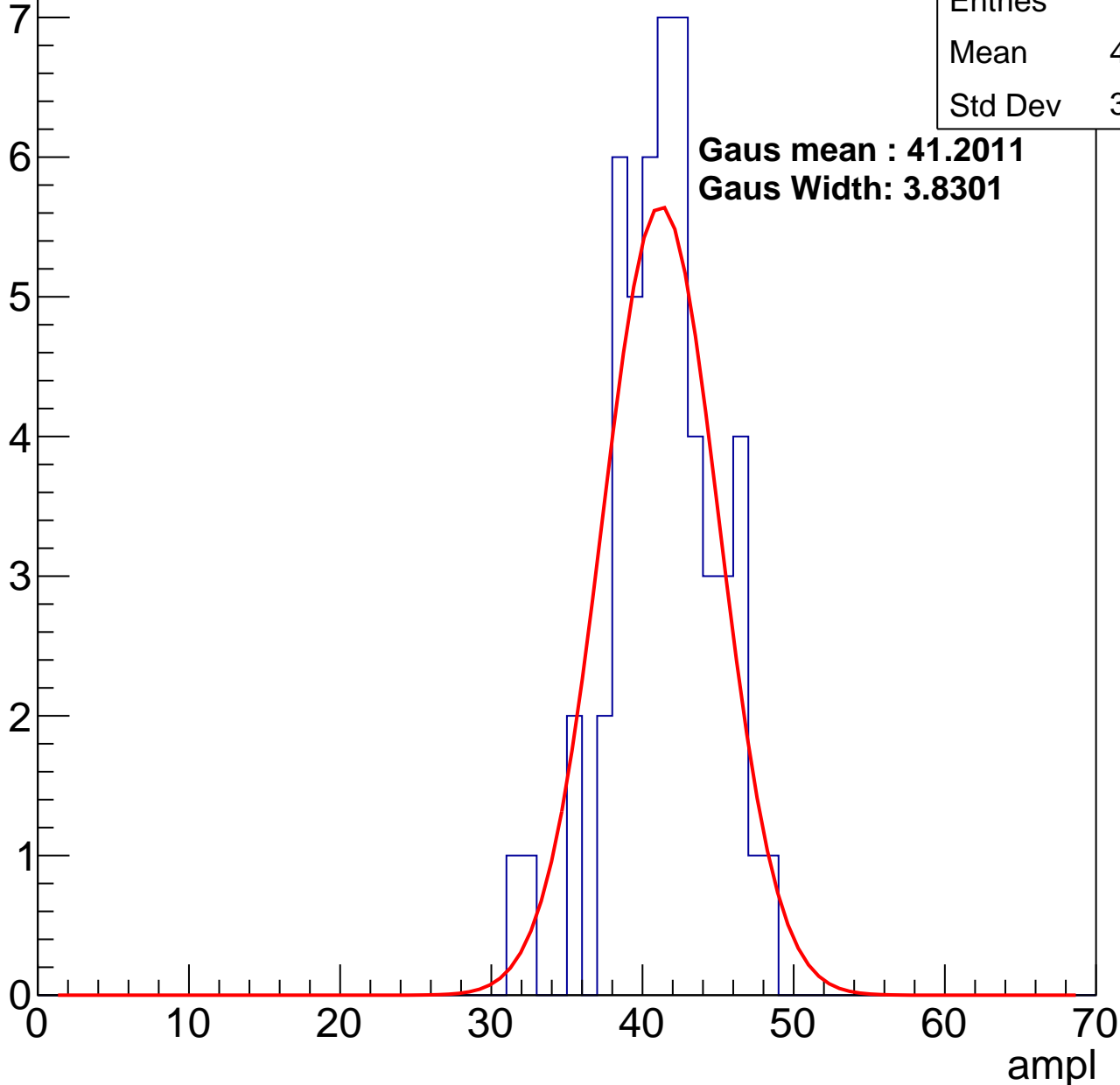
B1L102S, U2-ch108, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	40.92
Std Dev	3.496

Gaus mean : 41.2011
Gaus Width: 3.8301

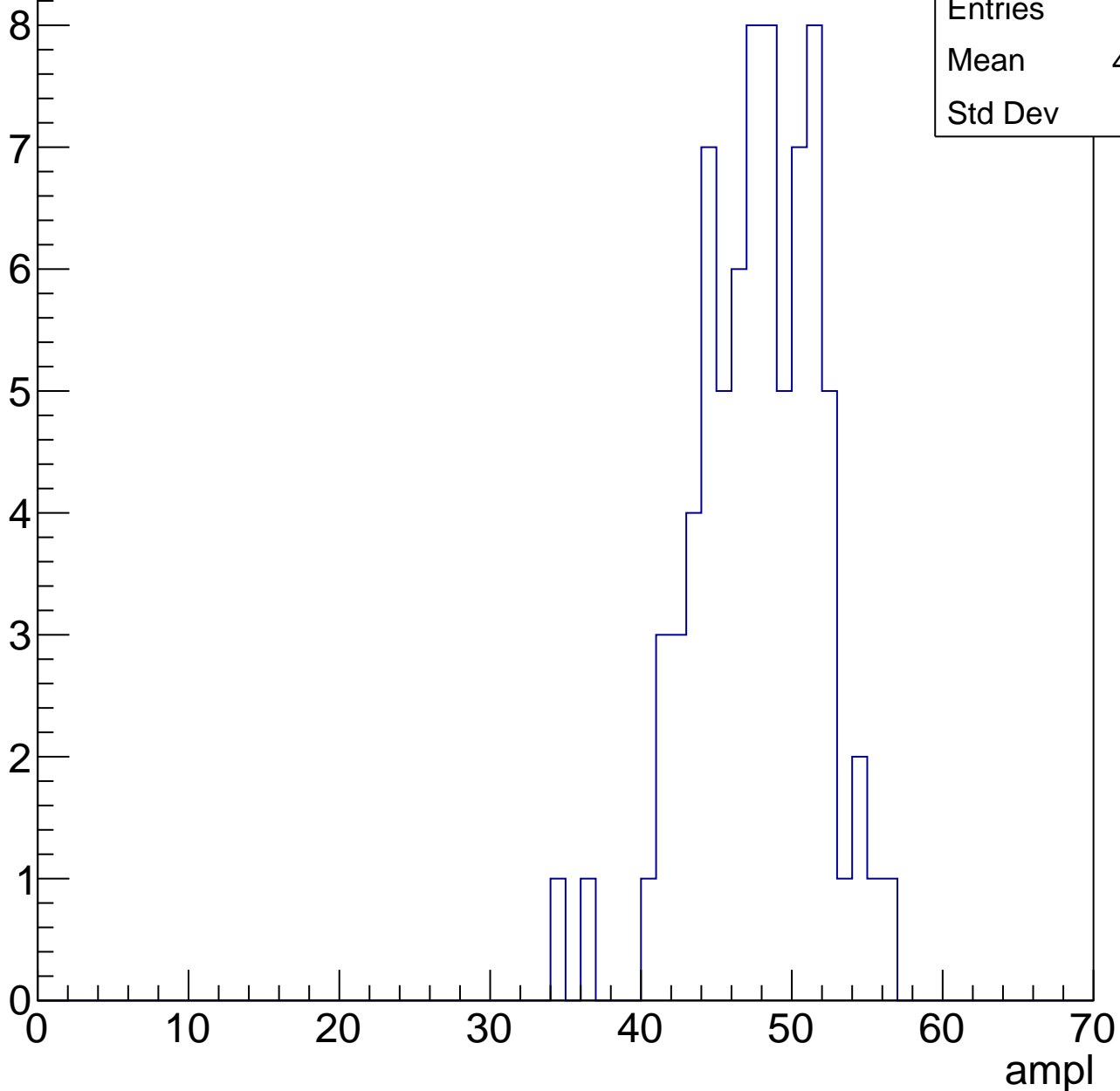


B1L102S, U2-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	47.21
Std Dev	4.1

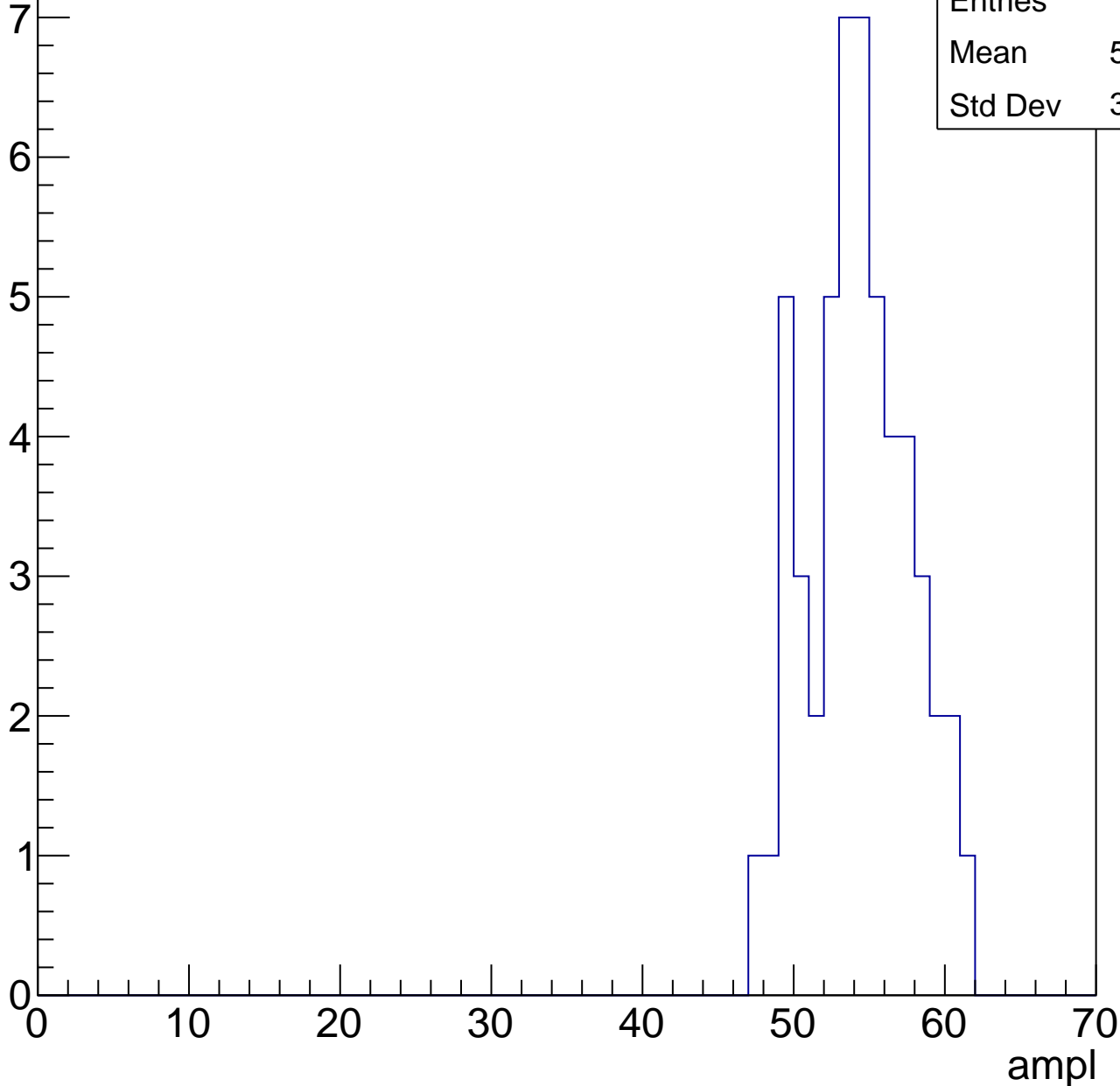


B1L102S, U2-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.87
Std Dev	3.334



B1L102S, U2-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

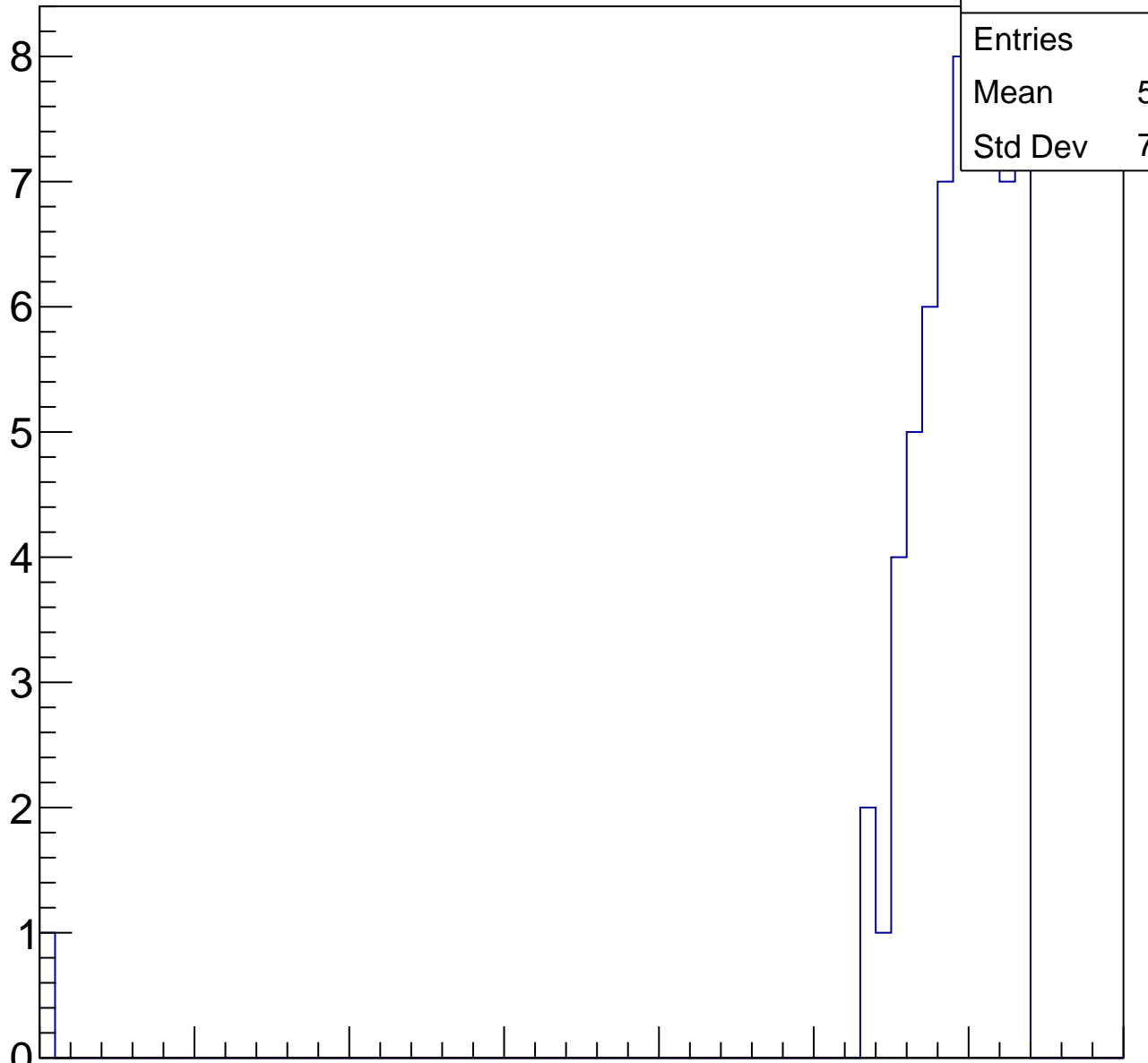
Entry

8
7
6
5
4
3
2
1
0

Entries	65
Mean	58.25
Std Dev	7.758

ampl

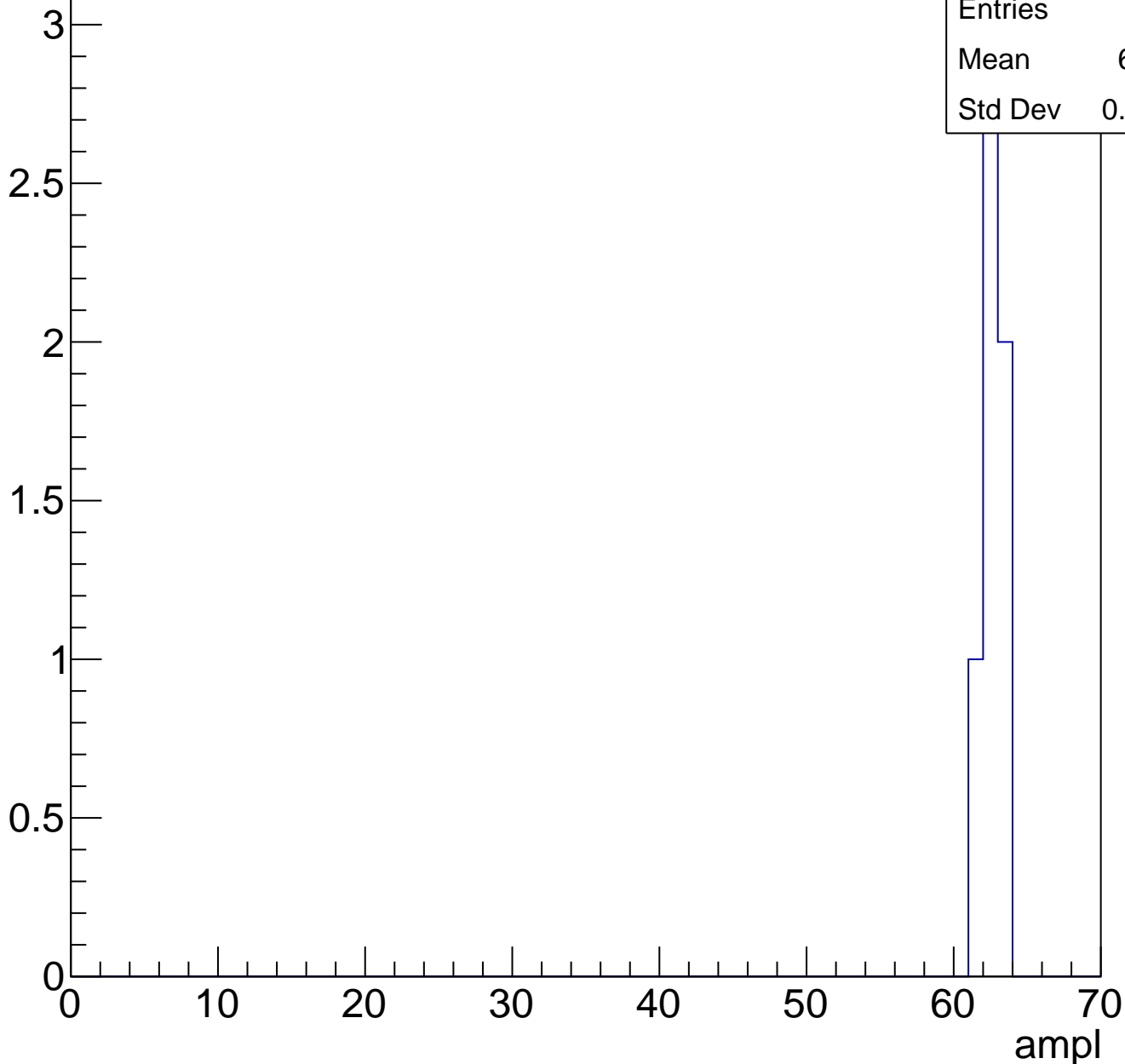
0 10 20 30 40 50 60 70



B1L102S, U2-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U2-ch109, adc0

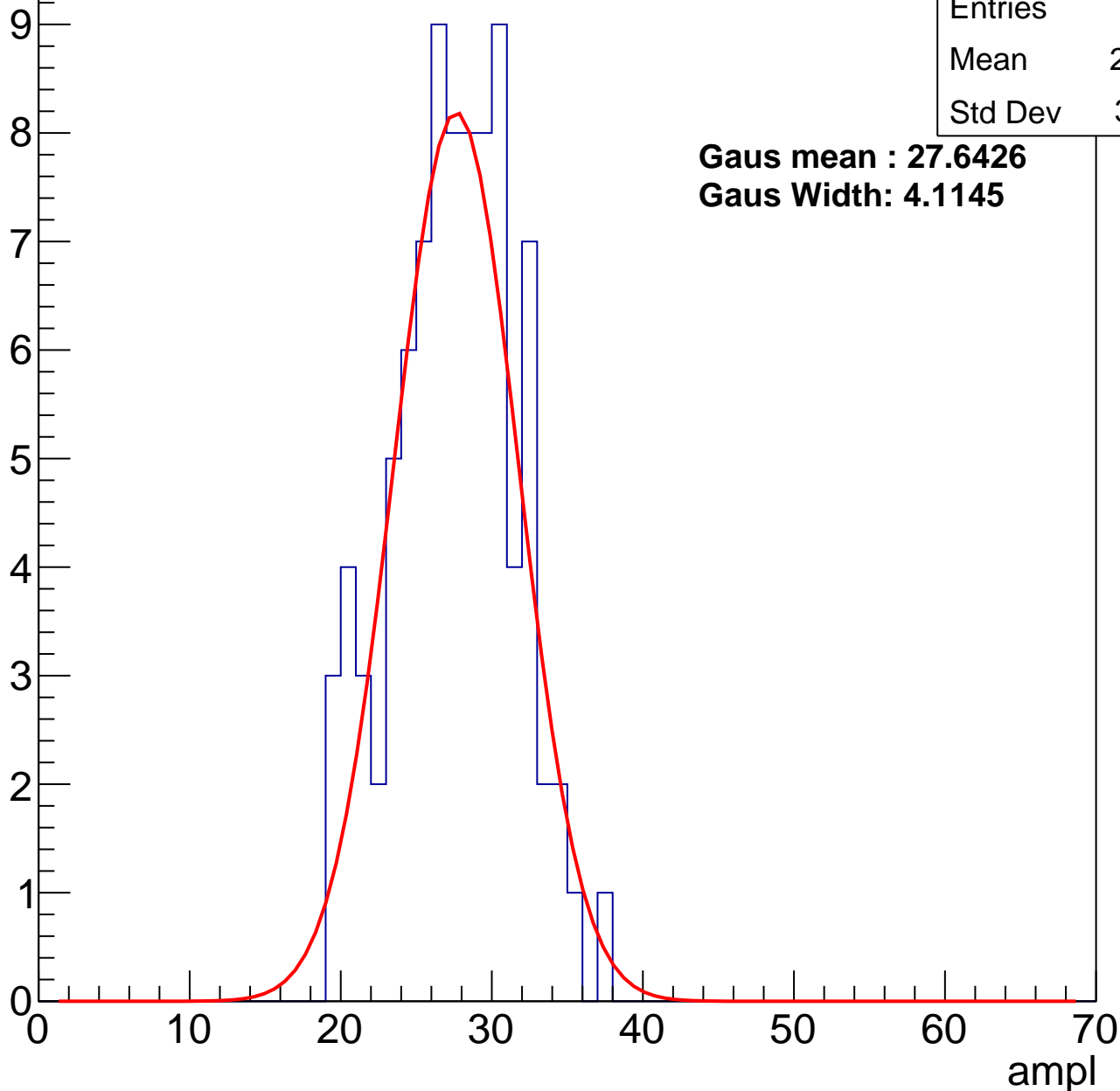
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	27.06
Std Dev	3.981

Gaus mean : 27.6426

Gaus Width: 4.1145



B1L102S, U2-ch109, adc1

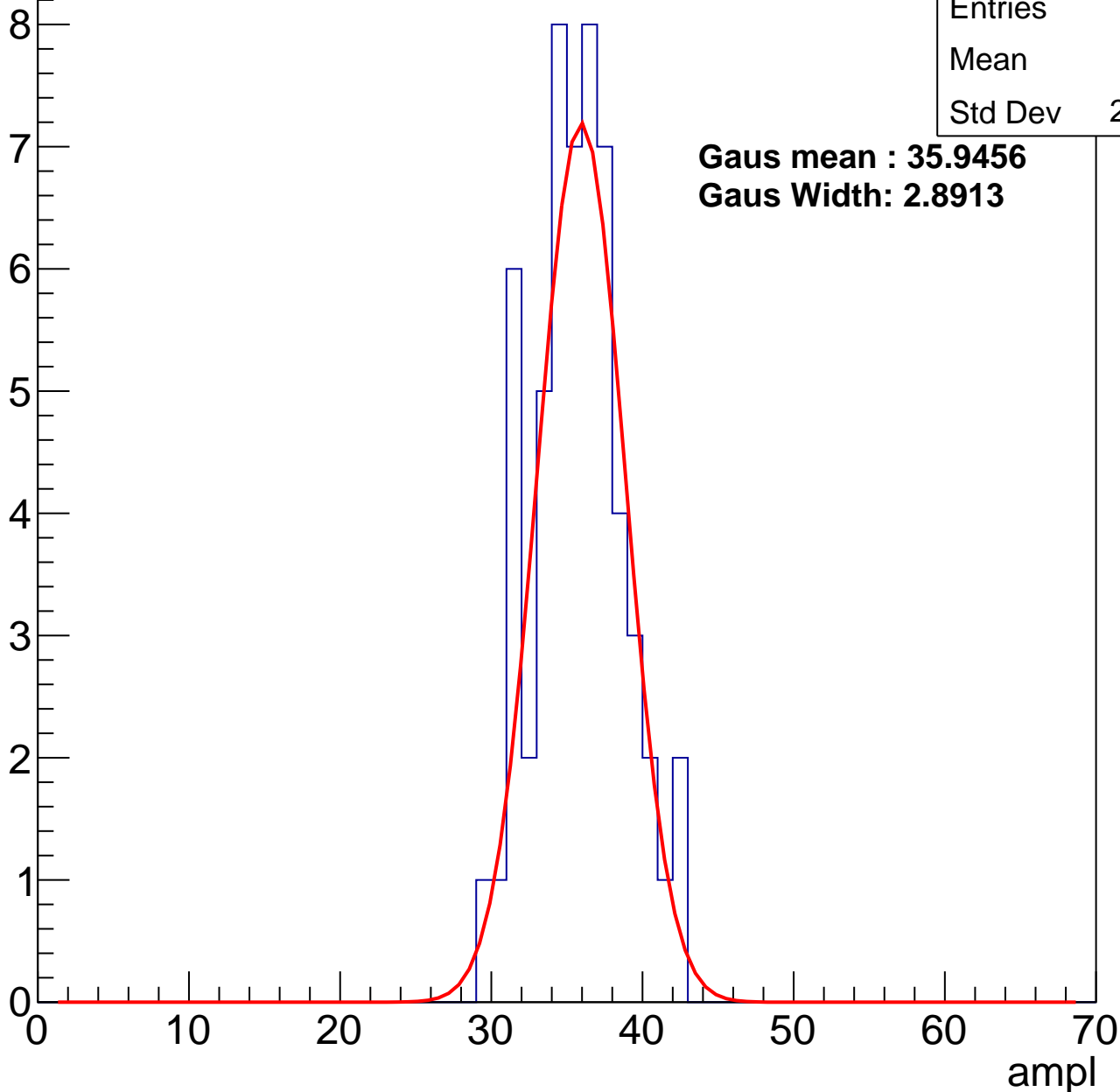
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.3
Std Dev	2.967

Gaus mean : 35.9456

Gaus Width: 2.8913



B1L102S, U2-ch109, adc2

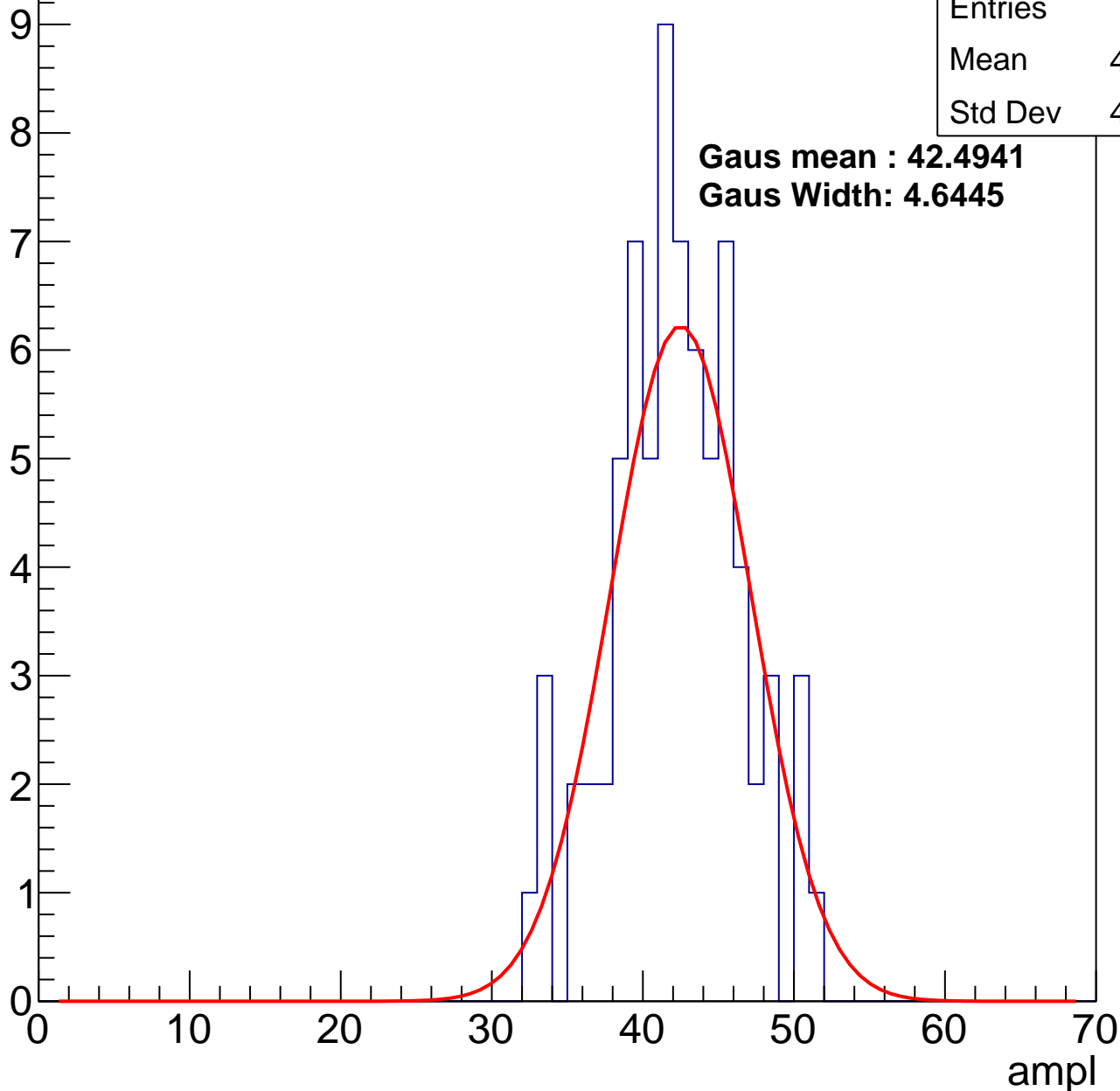
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.74
Std Dev	4.214

Gaus mean : 42.4941

Gaus Width: 4.6445

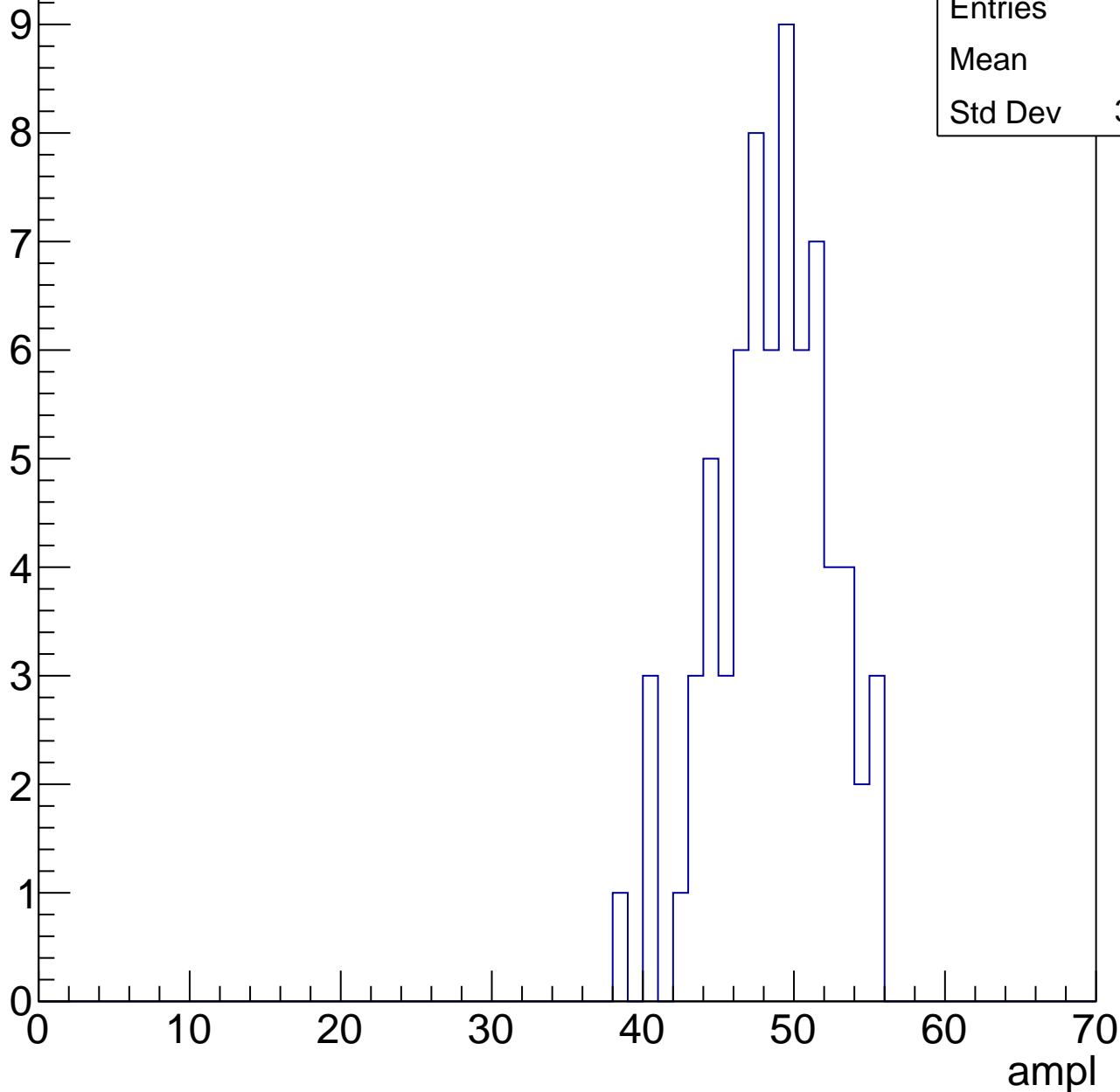


B1L102S, U2-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.1
Std Dev	3.791

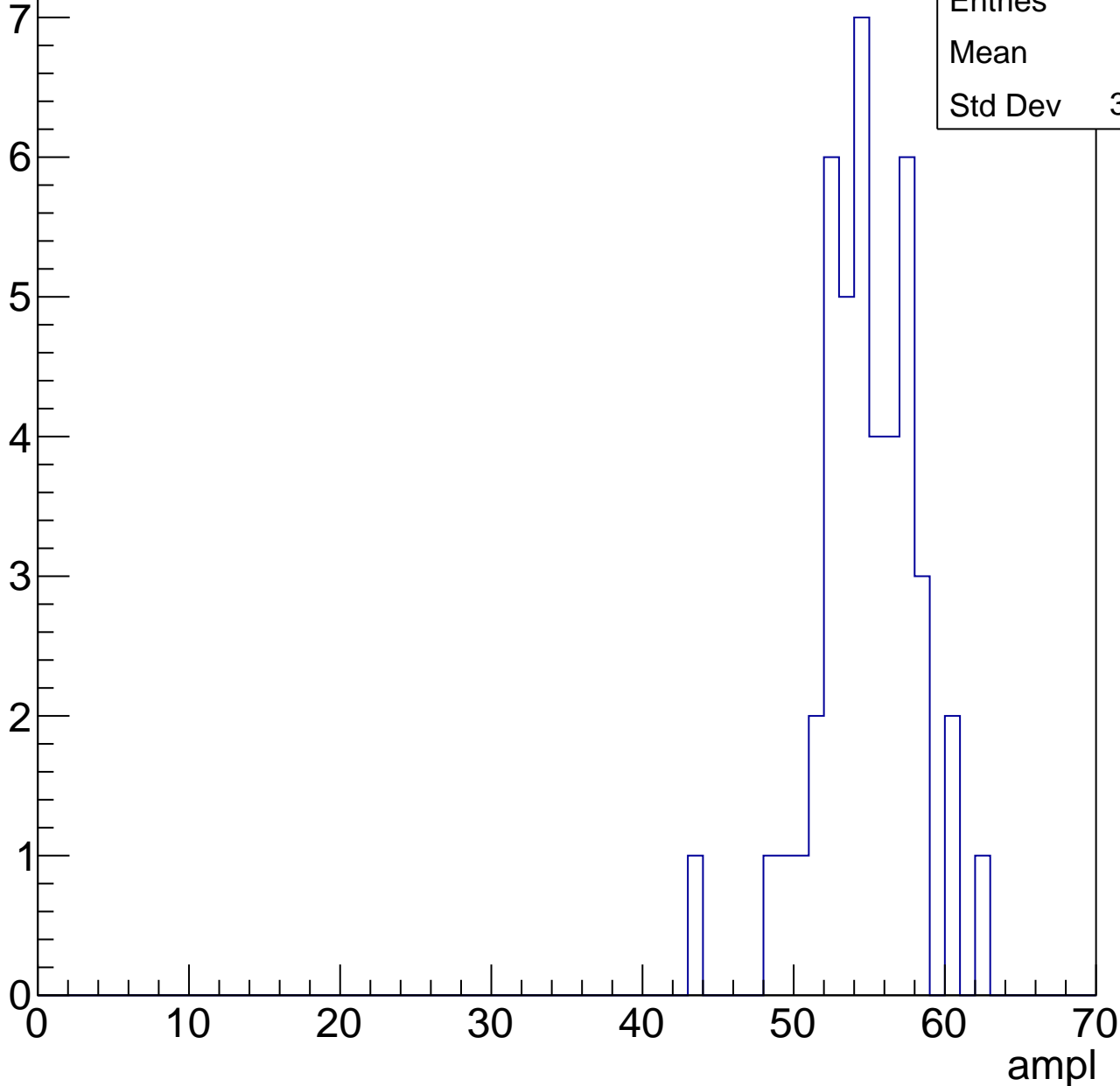


B1L102S, U2-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.3
Std Dev	3.368



B1L102S, U2-ch109, adc5

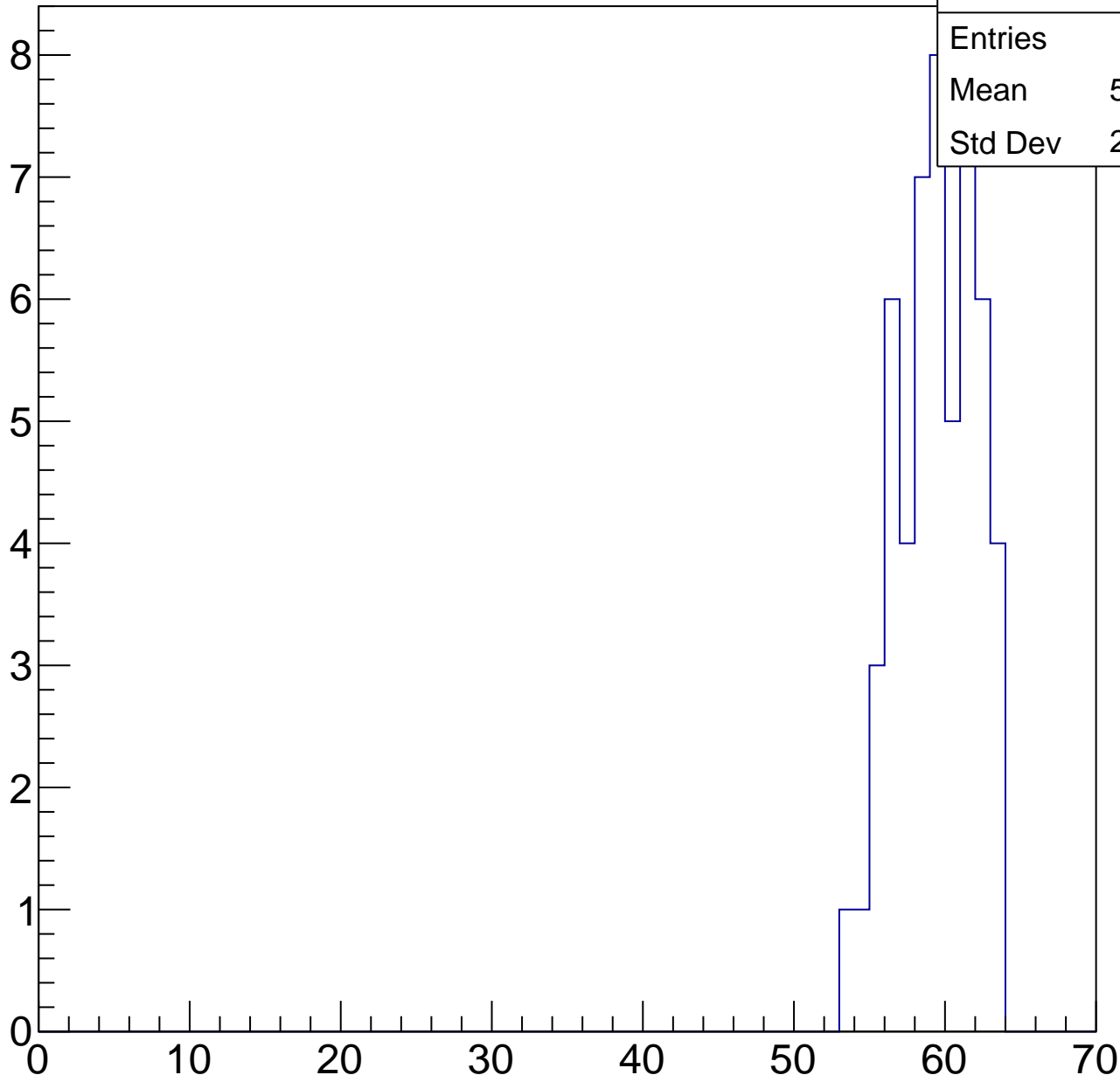
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.98
Std Dev	2.536

ampl

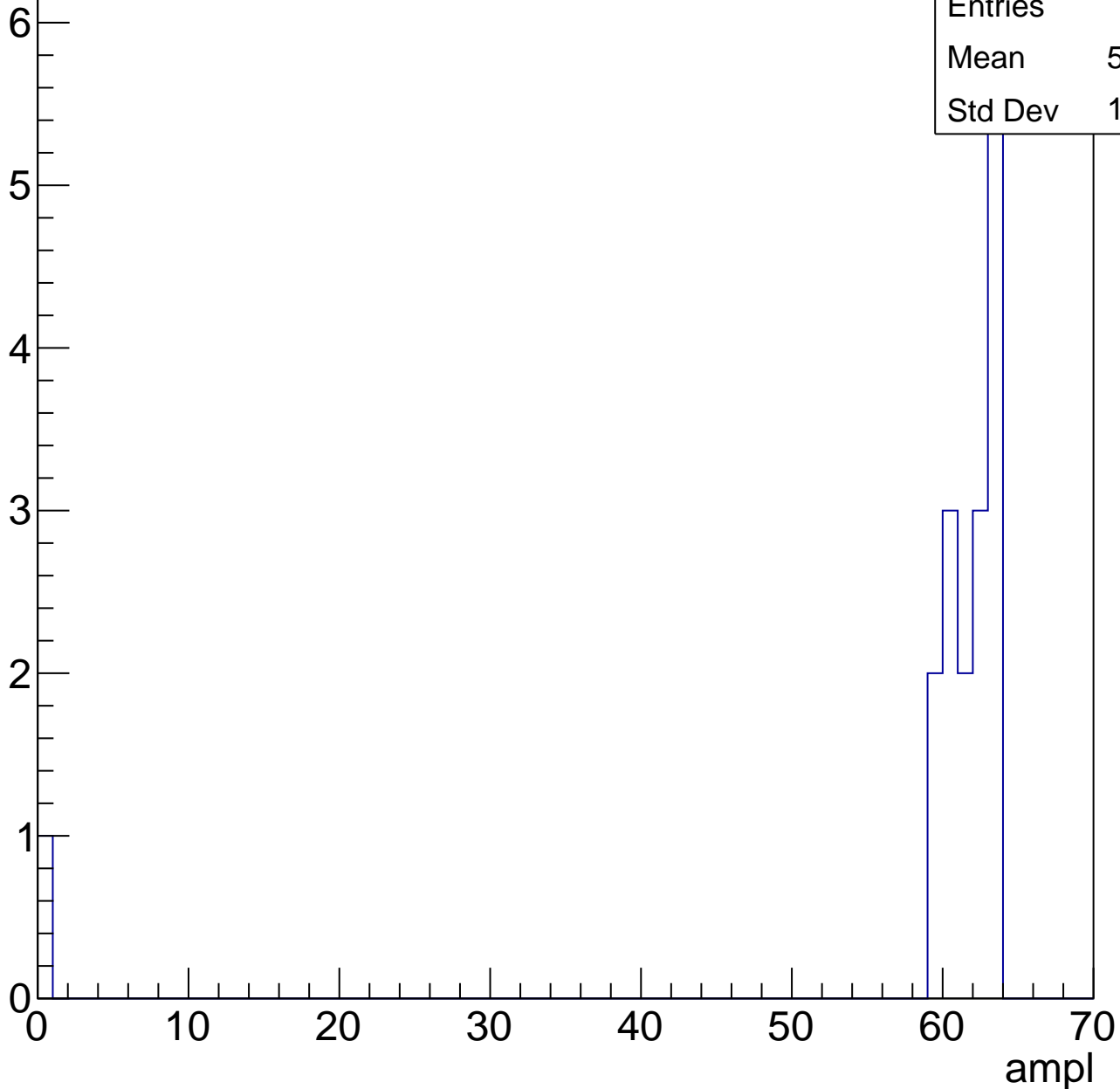


B1L102S, U2-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.88
Std Dev	14.54



B1L102S, U2-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch110, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	27.53
Std Dev	4.566

Gaus mean : 29.1136

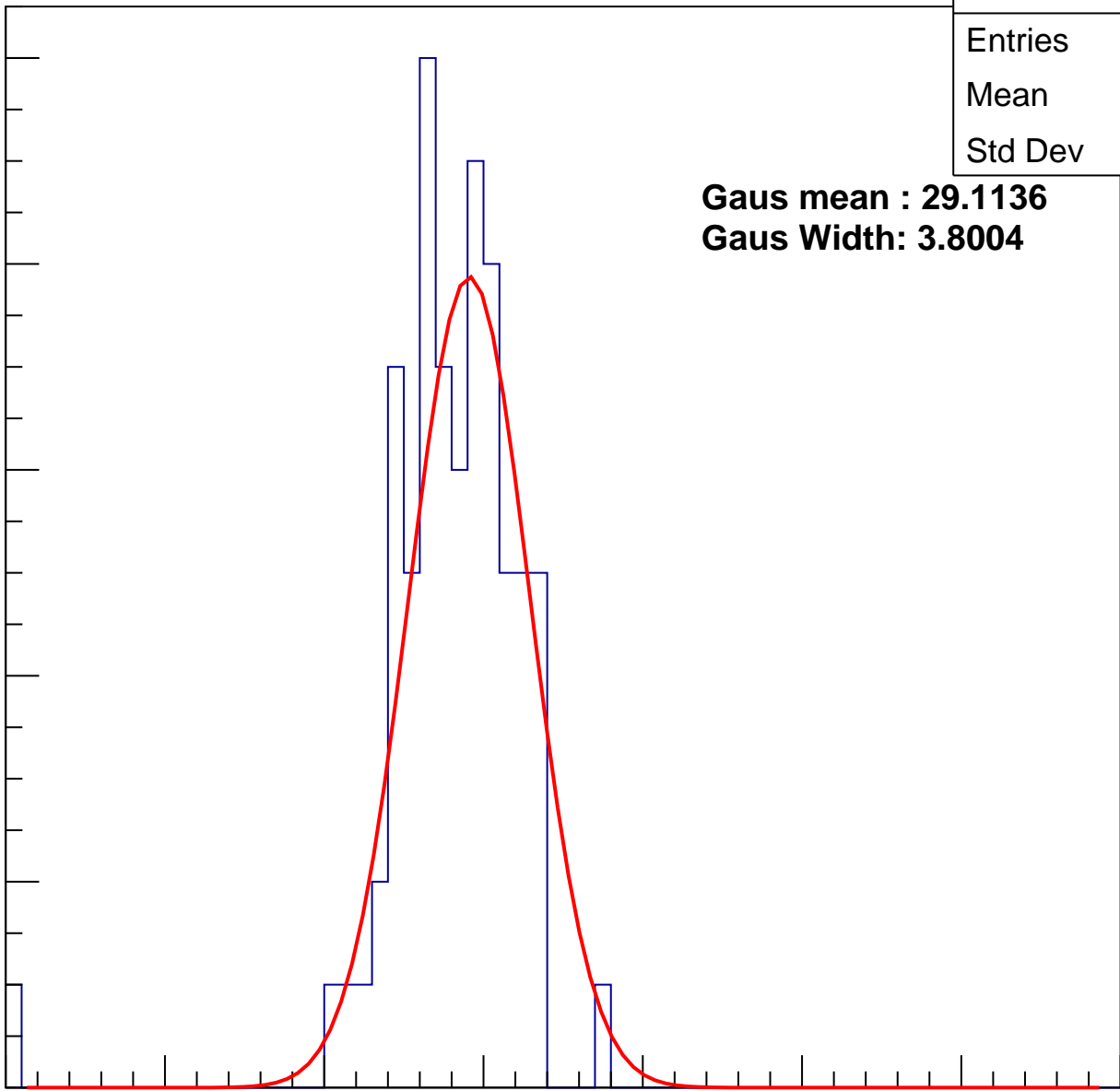
Gaus Width: 3.8004

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch110, adc1

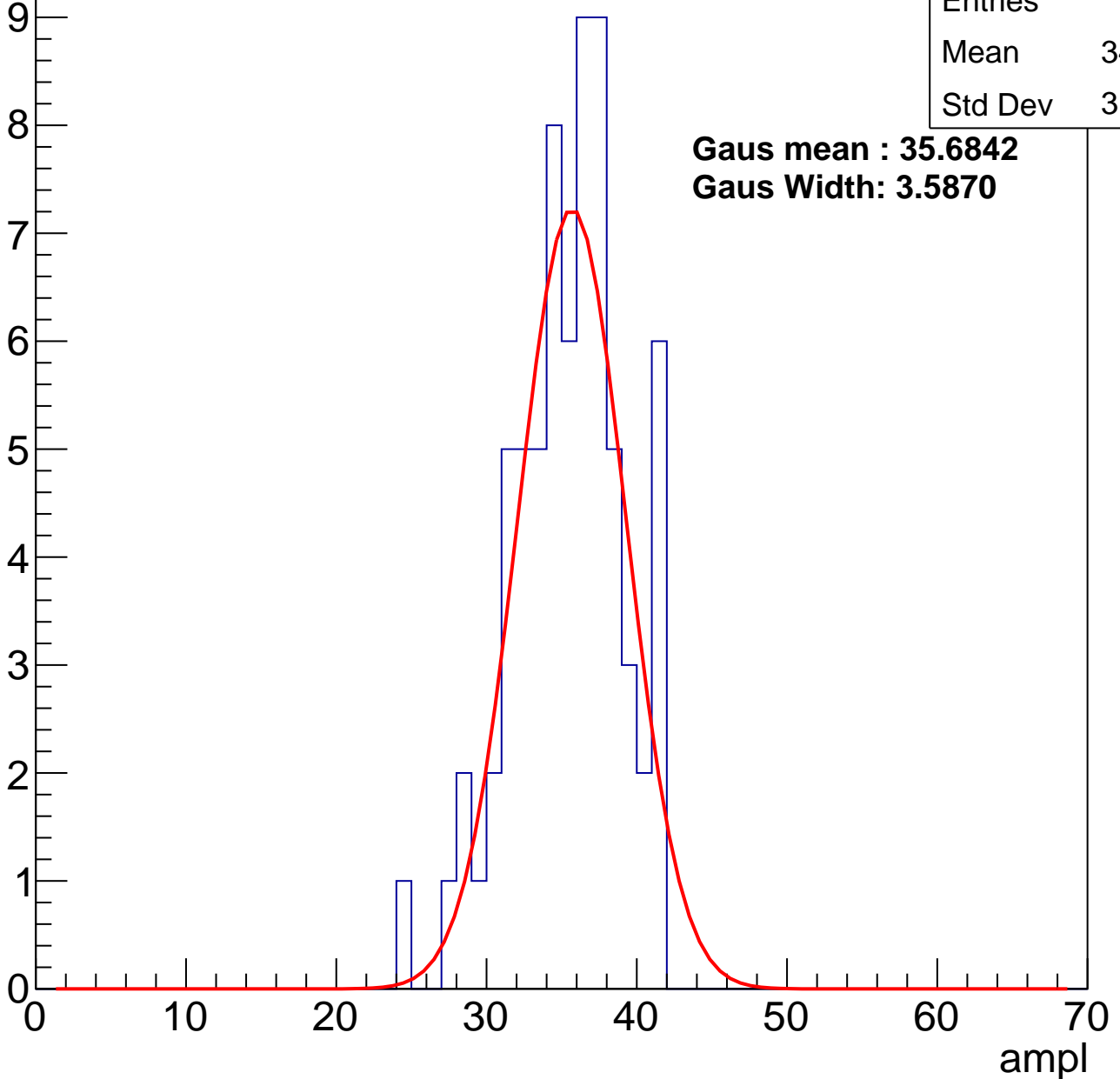
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.97
Std Dev	3.637

Gaus mean : 35.6842

Gaus Width: 3.5870



B1L102S, U2-ch110, adc2

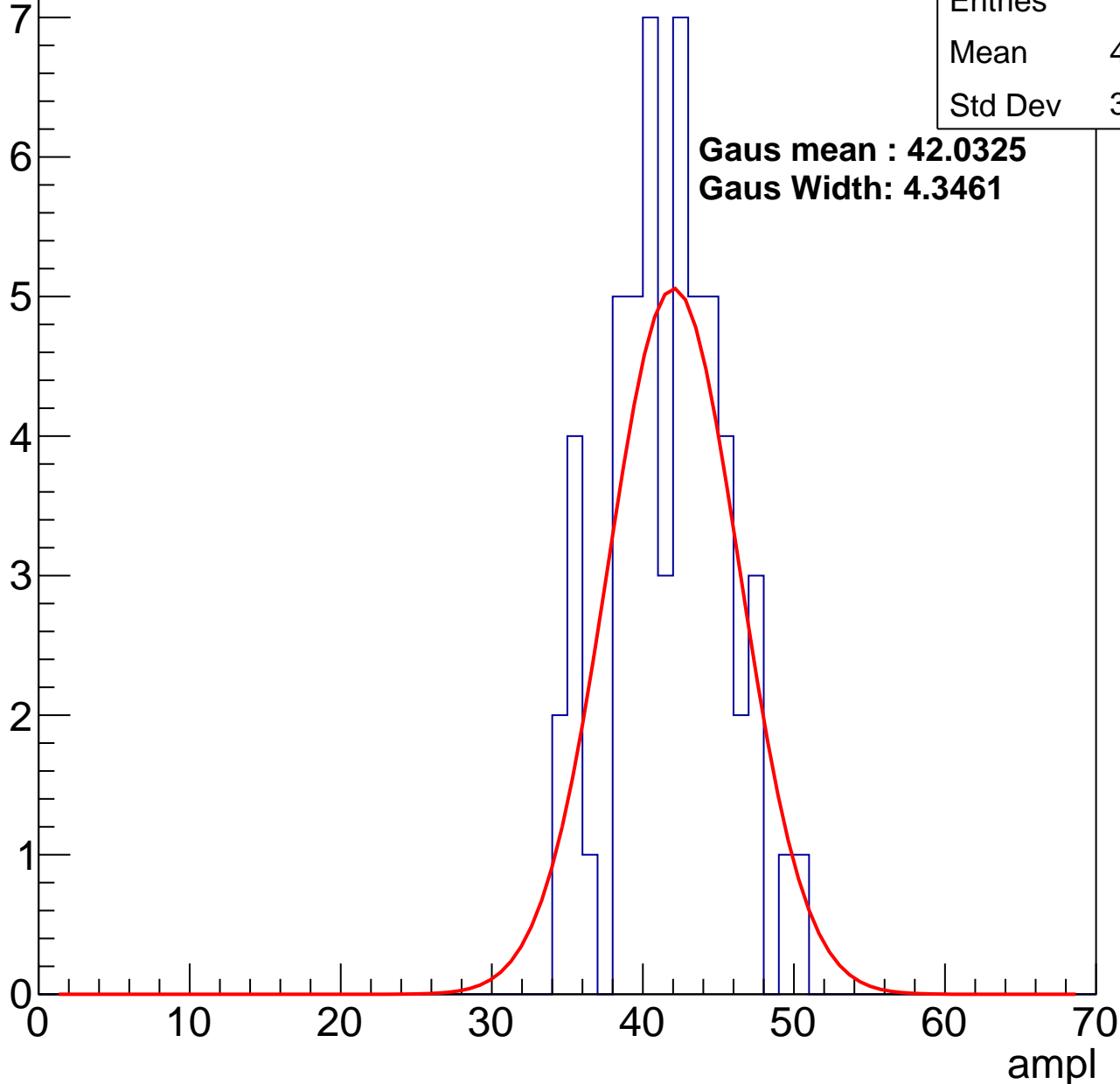
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.33
Std Dev	3.747

Gaus mean : 42.0325

Gaus Width: 4.3461

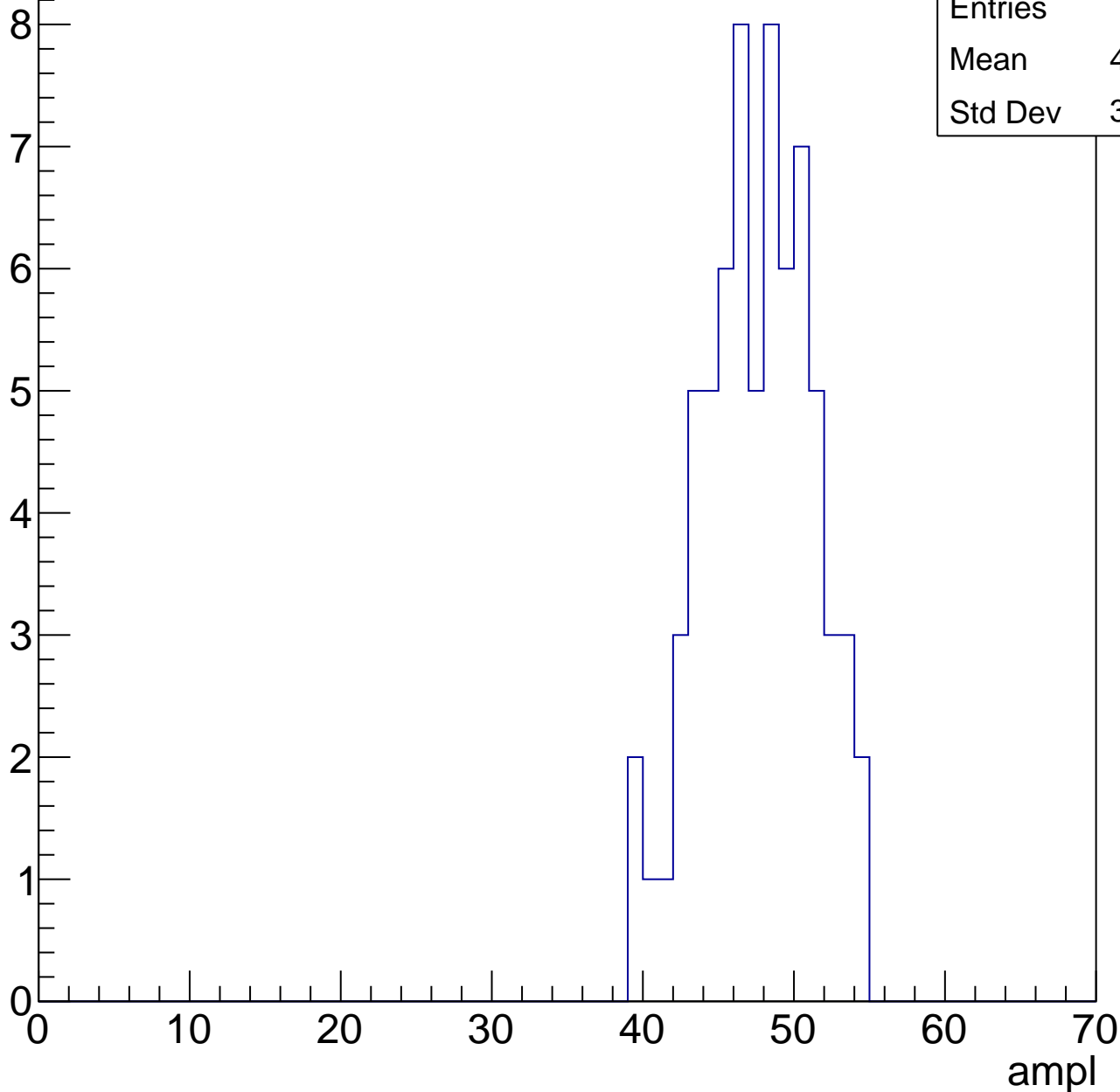


B1L102S, U2-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.13
Std Dev	3.585

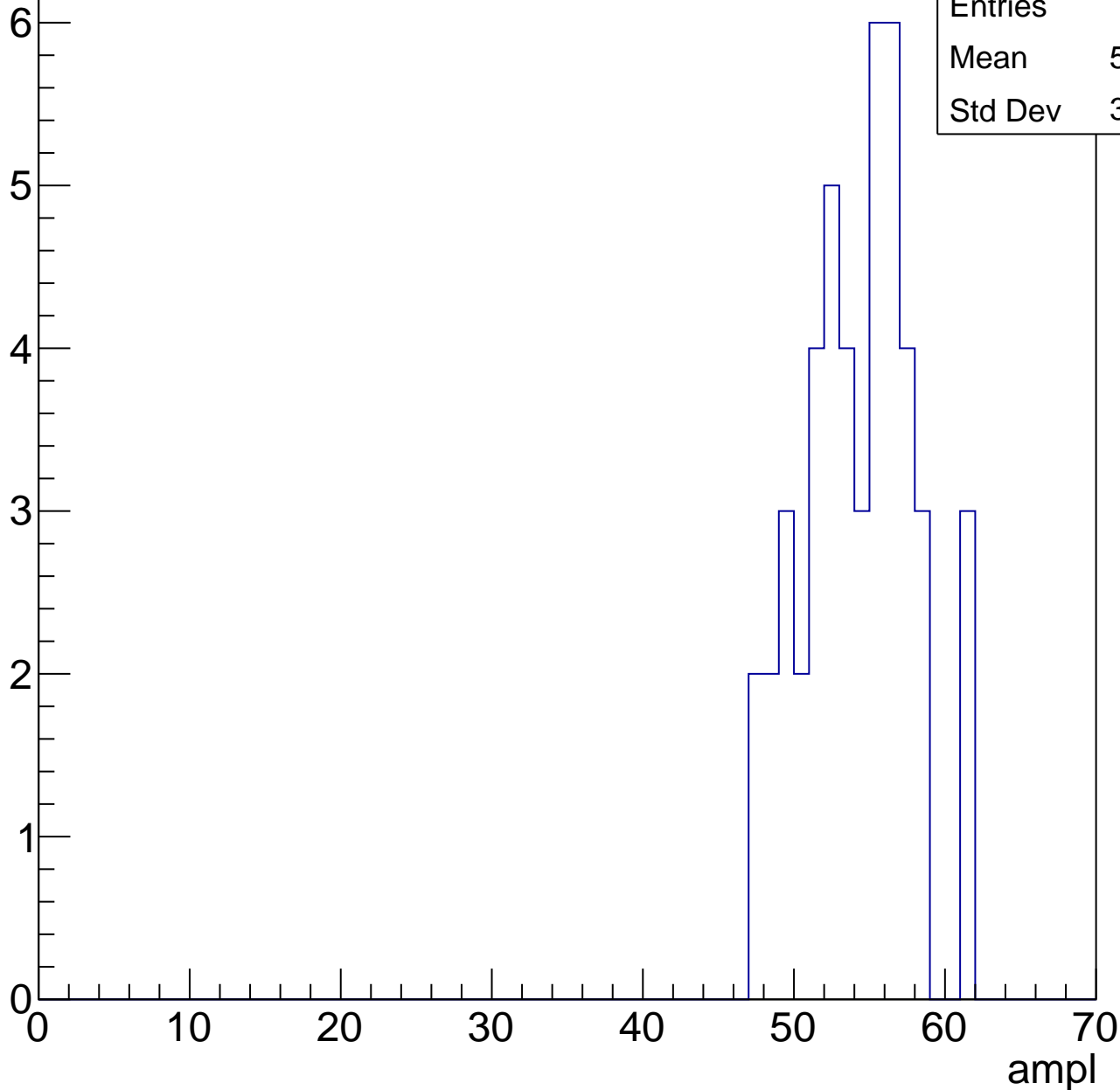


B1L102S, U2-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	53.74
Std Dev	3.558

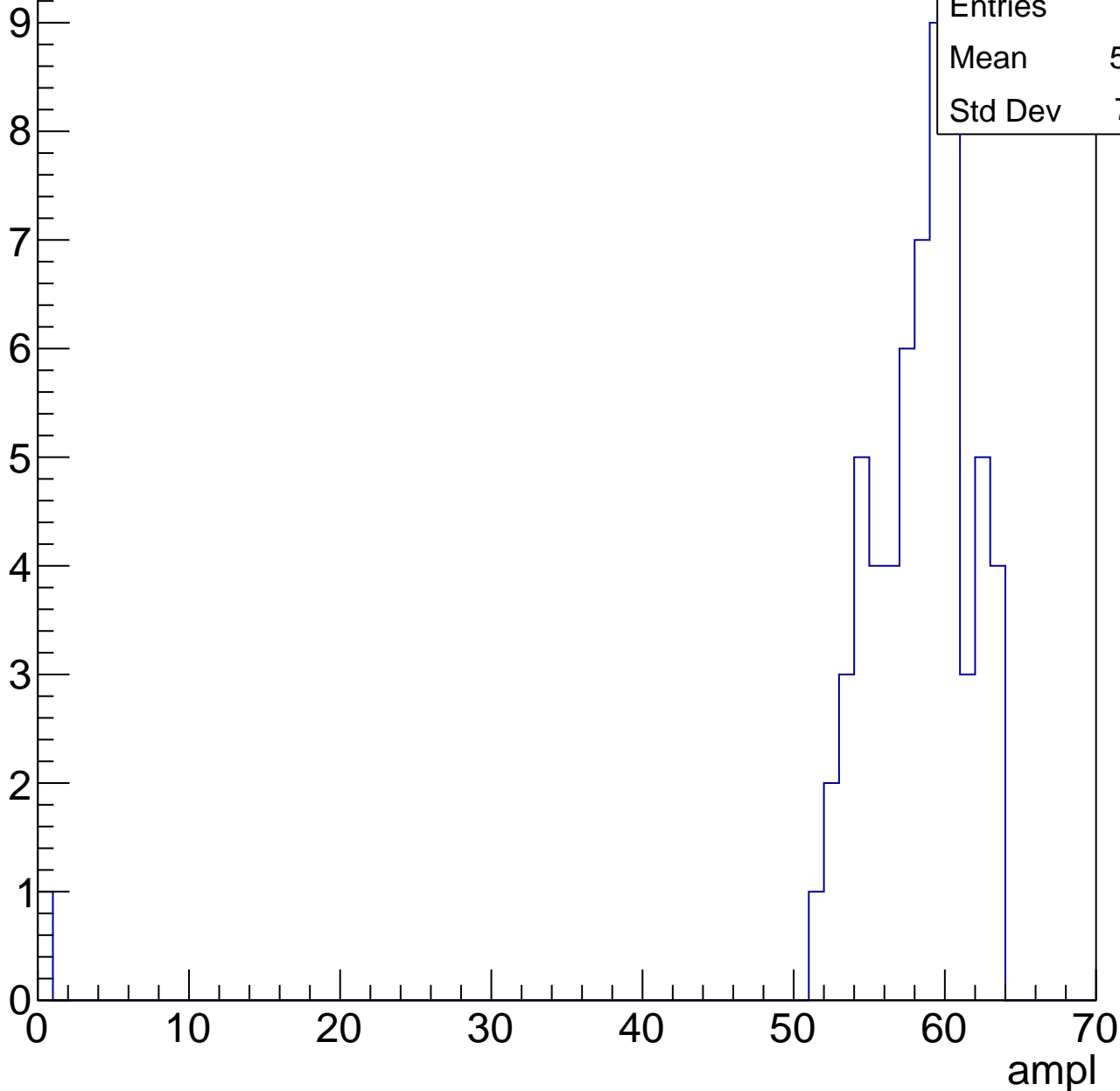


B1L102S, U2-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	56.97
Std Dev	7.911

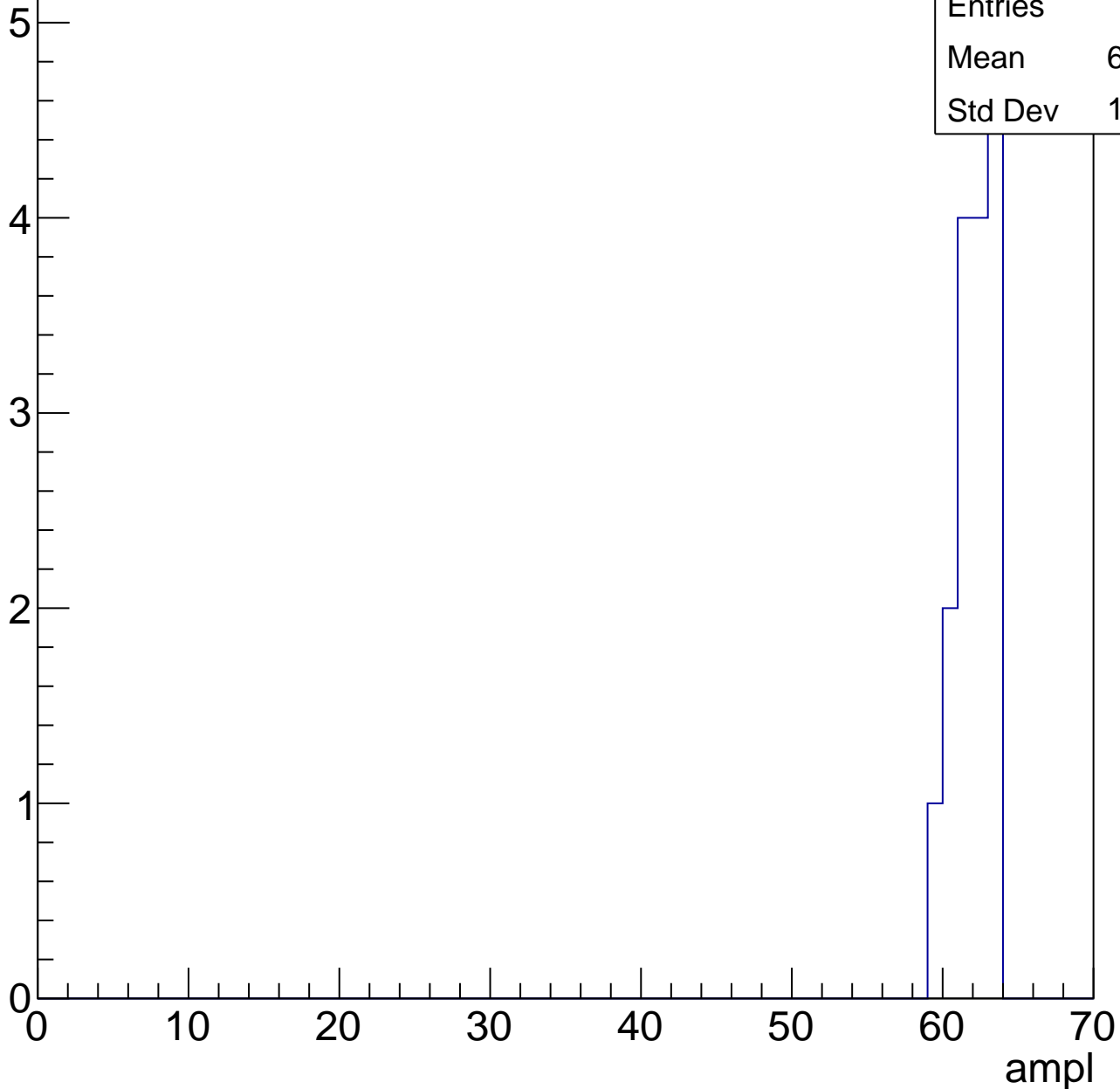


B1L102S, U2-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	61.62
Std Dev	1.218



B1L102S, U2-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L102S, U2-ch111, adc0

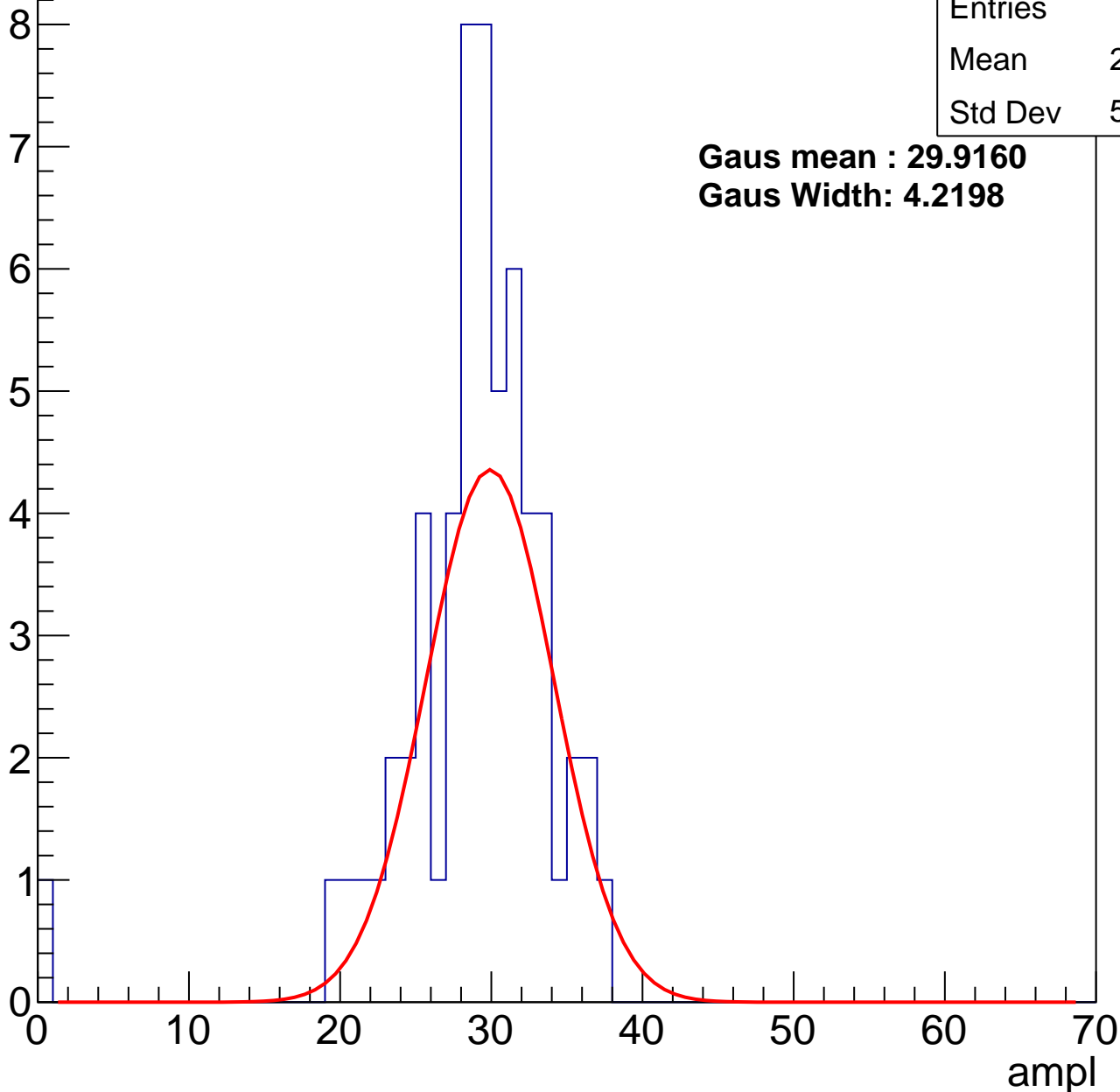
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.39
Std Dev	5.399

Gaus mean : 29.9160

Gaus Width: 4.2198



B1L102S, U2-ch111, adc1

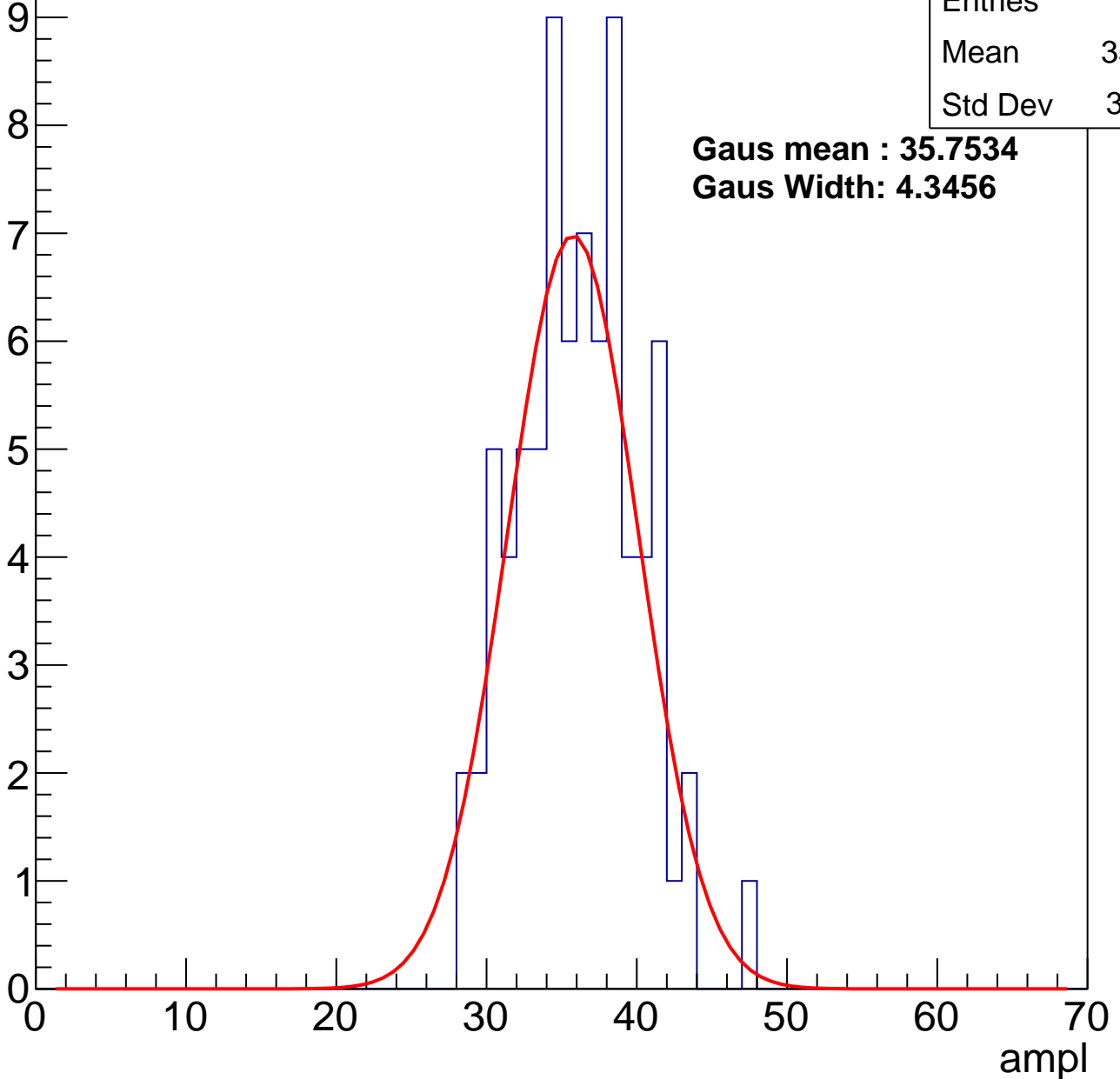
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.67
Std Dev	3.941

Gaus mean : 35.7534

Gaus Width: 4.3456



B1L102S, U2-ch111, adc2

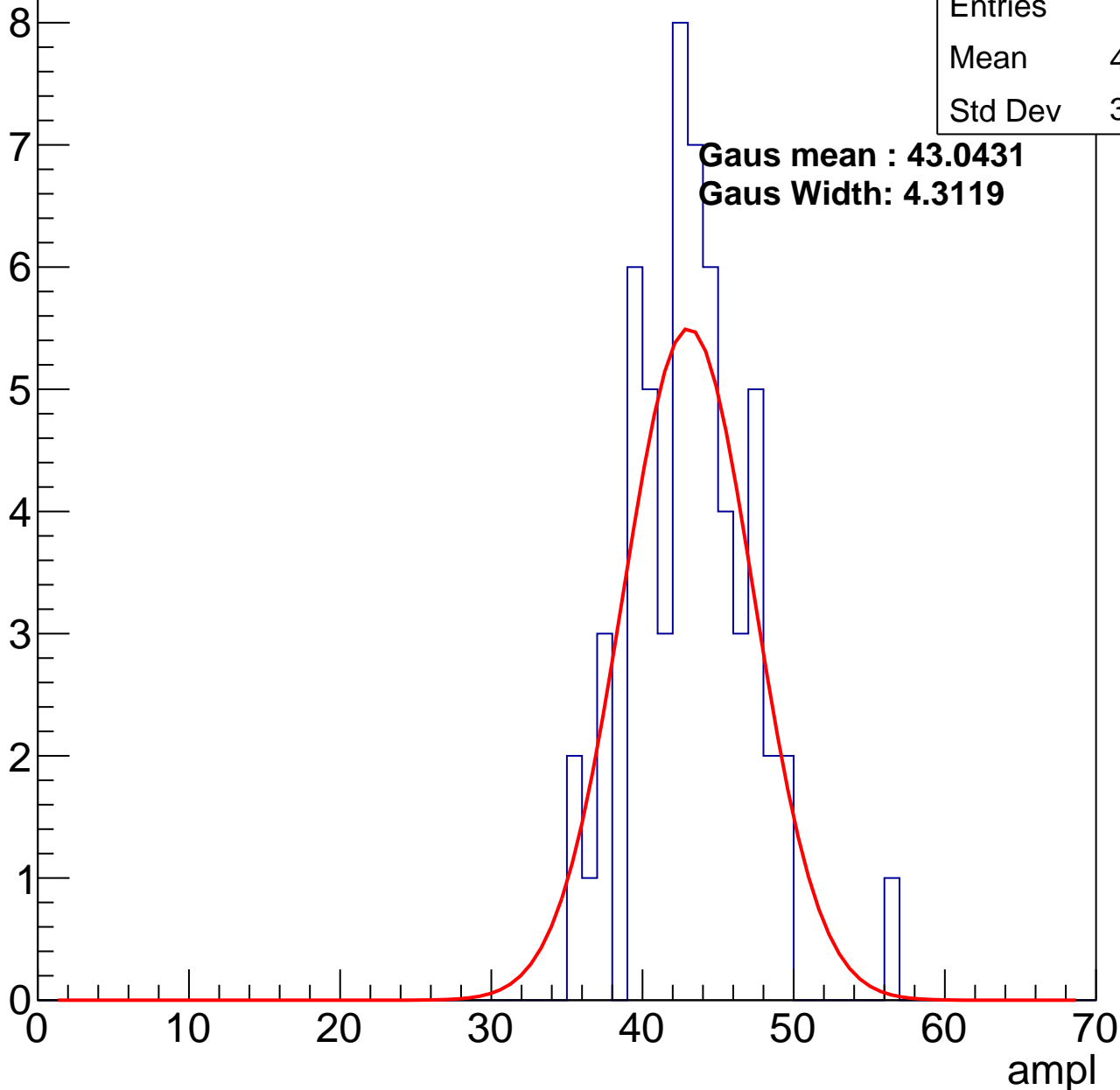
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.72
Std Dev	3.854

Gaus mean : 43.0431

Gaus Width: 4.3119

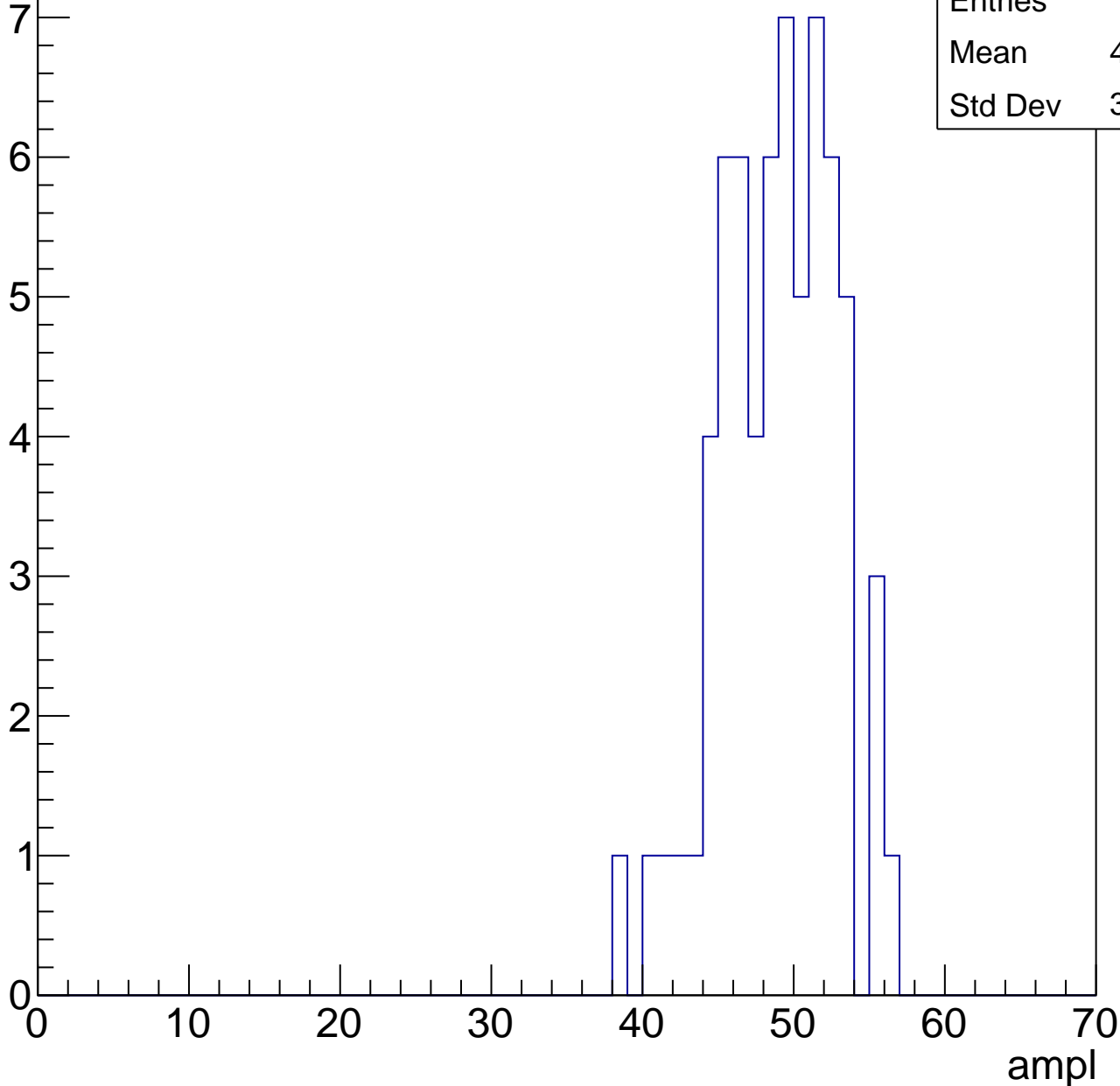


B1L102S, U2-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.46
Std Dev	3.779

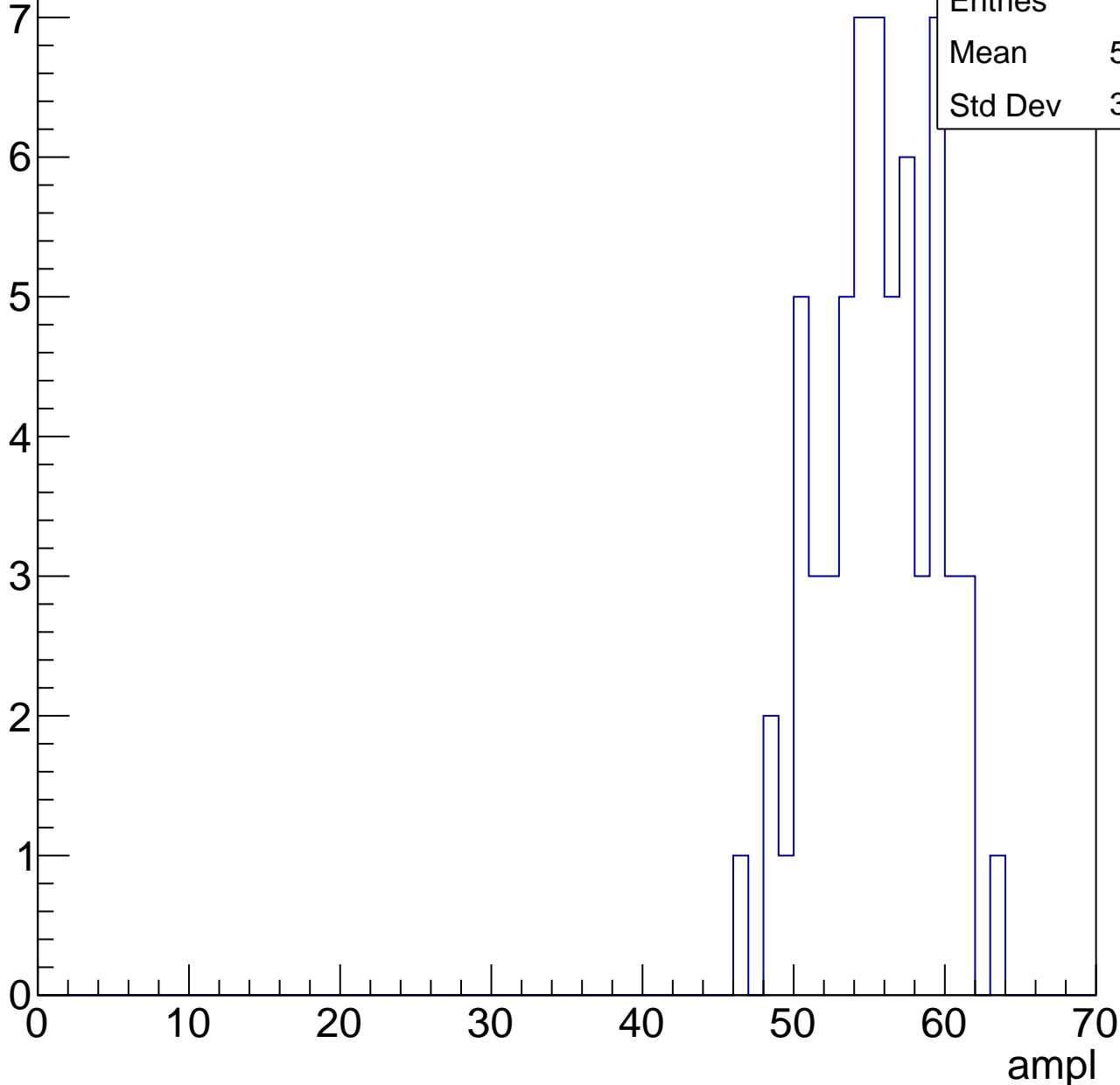


B1L102S, U2-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	55.05
Std Dev	3.718

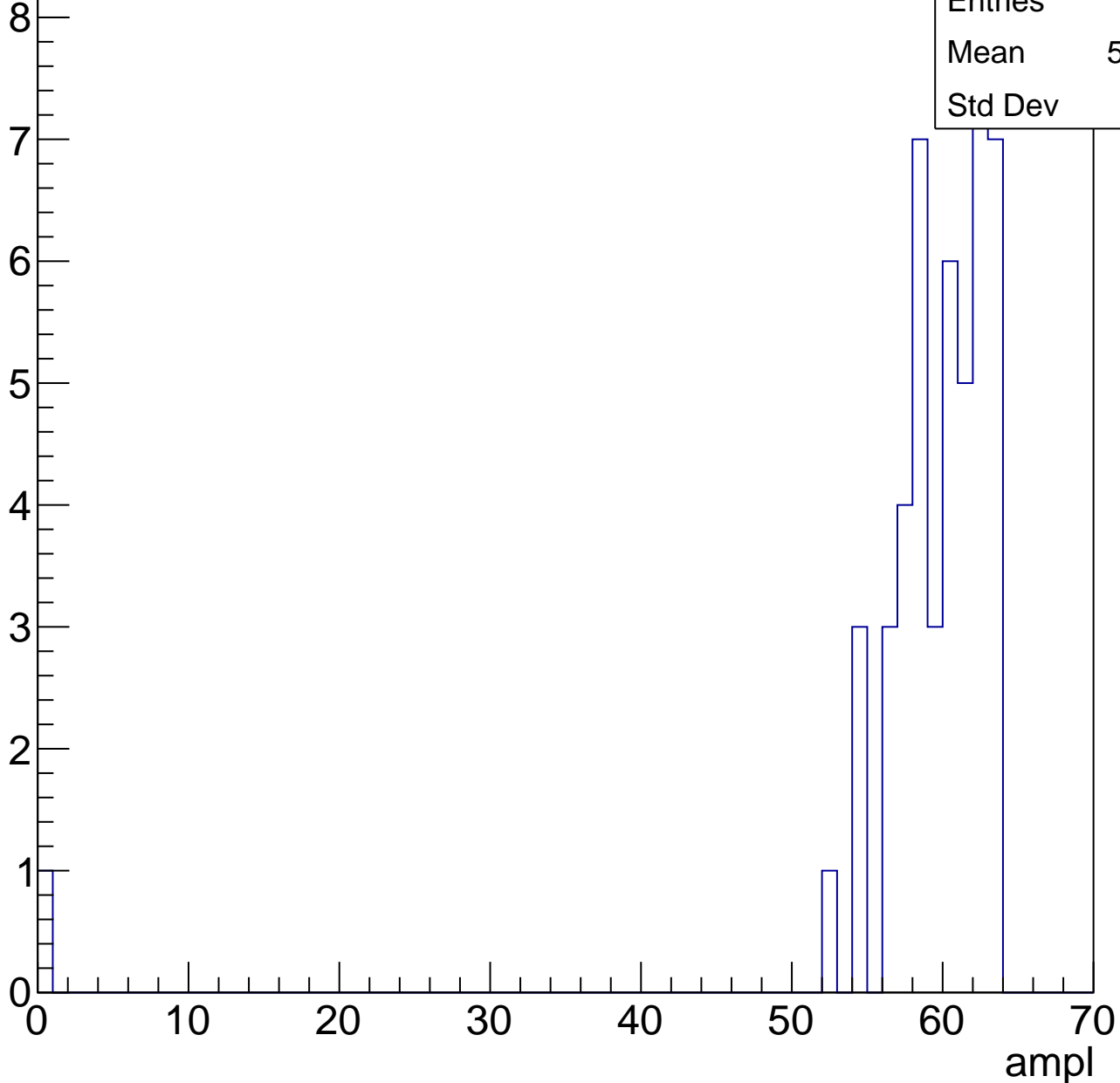


B1L102S, U2-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.23
Std Dev	8.94



B1L102S, U2-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.71
Std Dev	1.03

ampl

0 10 20 30 40 50 60 70

B1L102S, U2-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U2-ch112, adc0

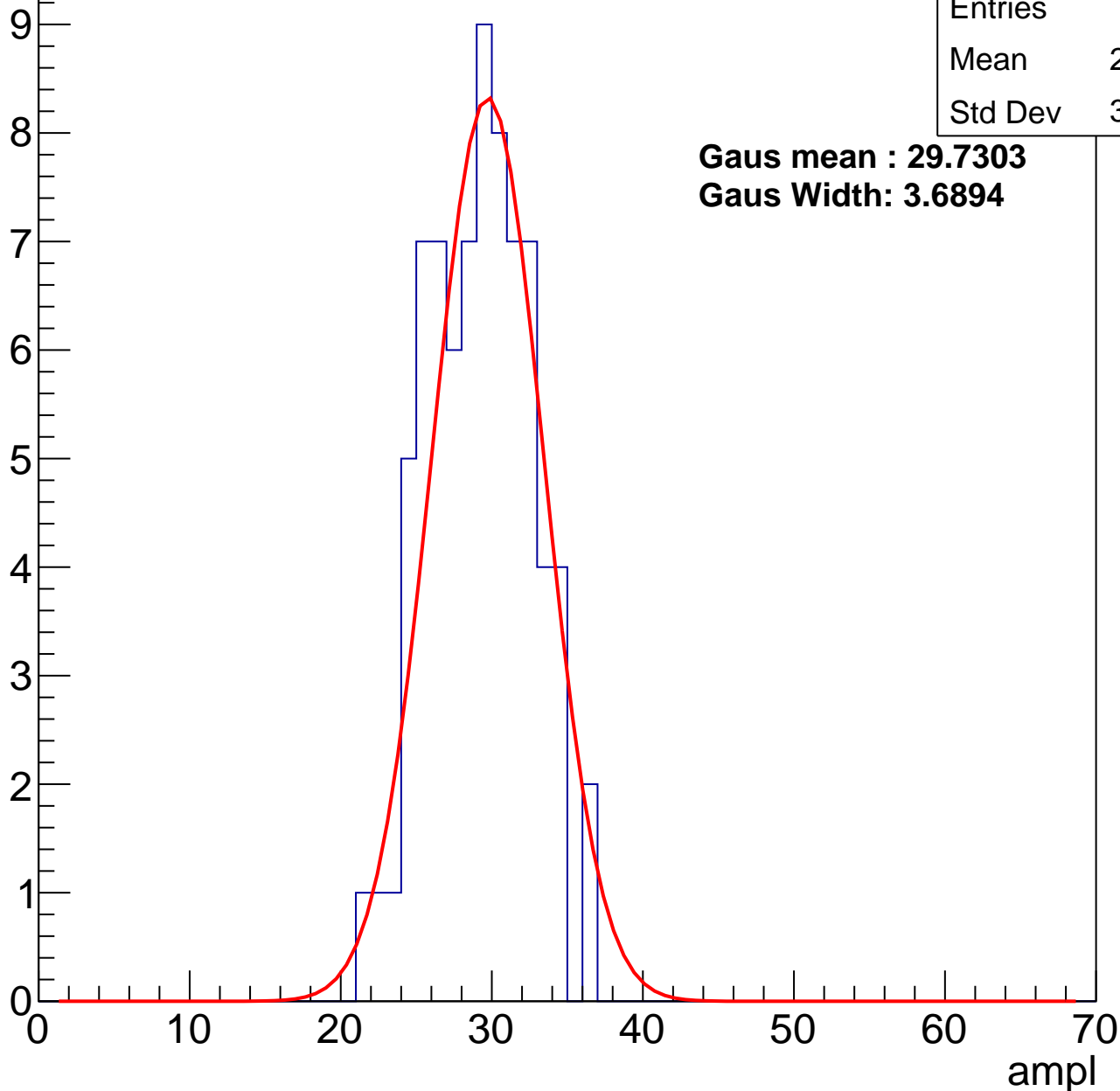
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.72
Std Dev	3.315

Gaus mean : 29.7303

Gaus Width: 3.6894



B1L102S, U2-ch112, adc1

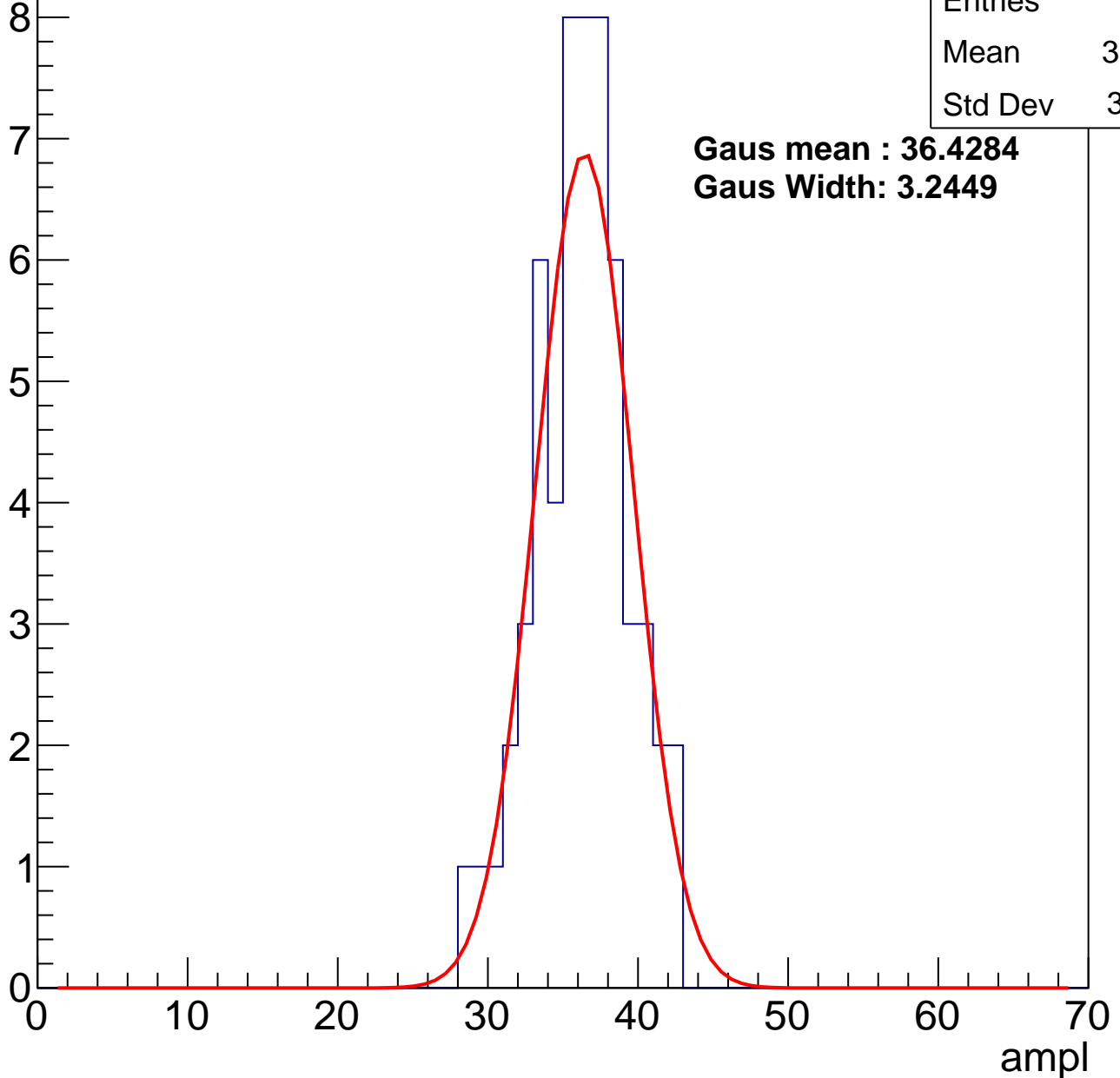
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.76
Std Dev	3.081

Gaus mean : 36.4284

Gaus Width: 3.2449



B1L102S, U2-ch112, adc2

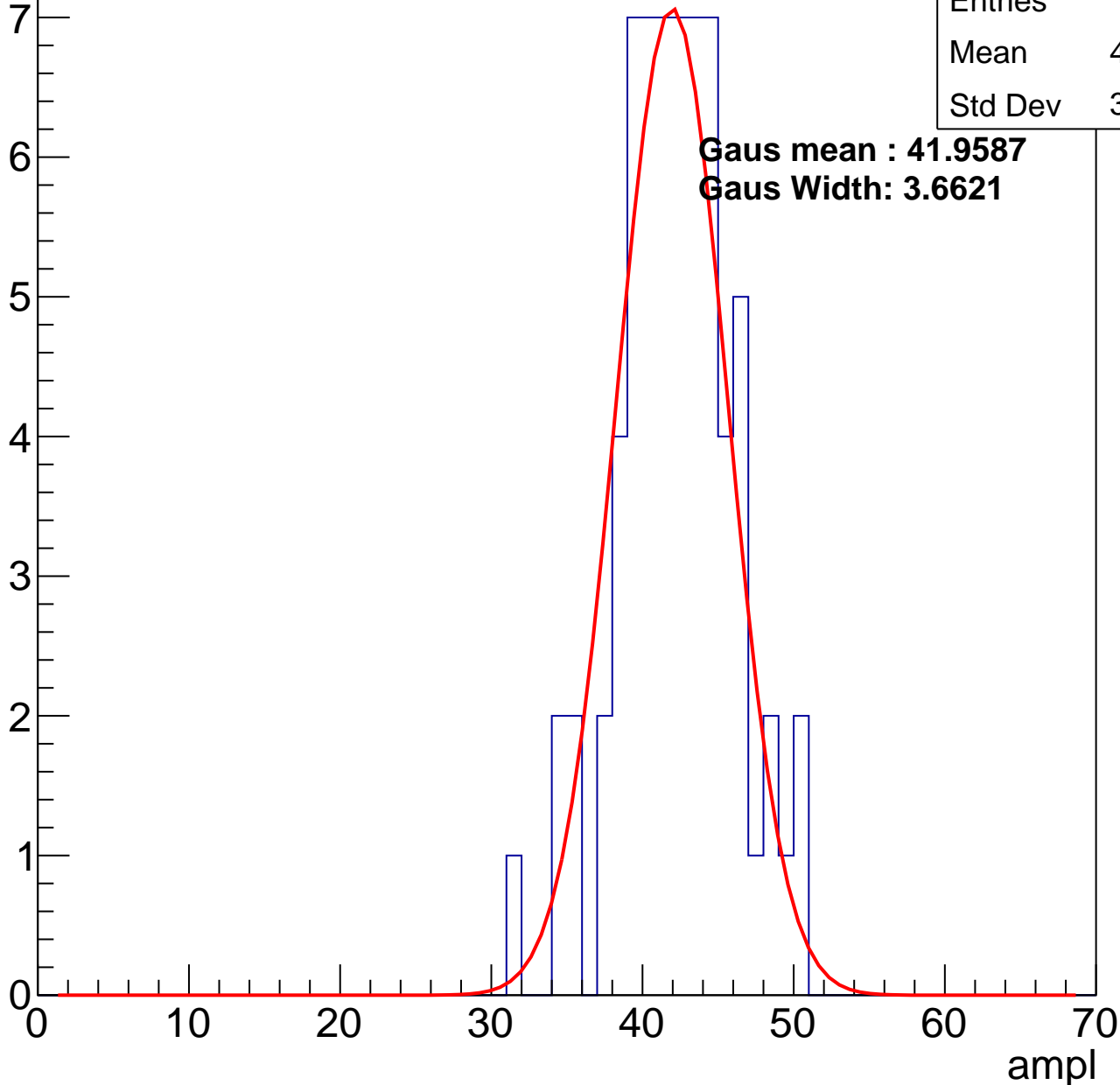
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.76
Std Dev	3.797

Gaus mean : 41.9587

Gaus Width: 3.6621

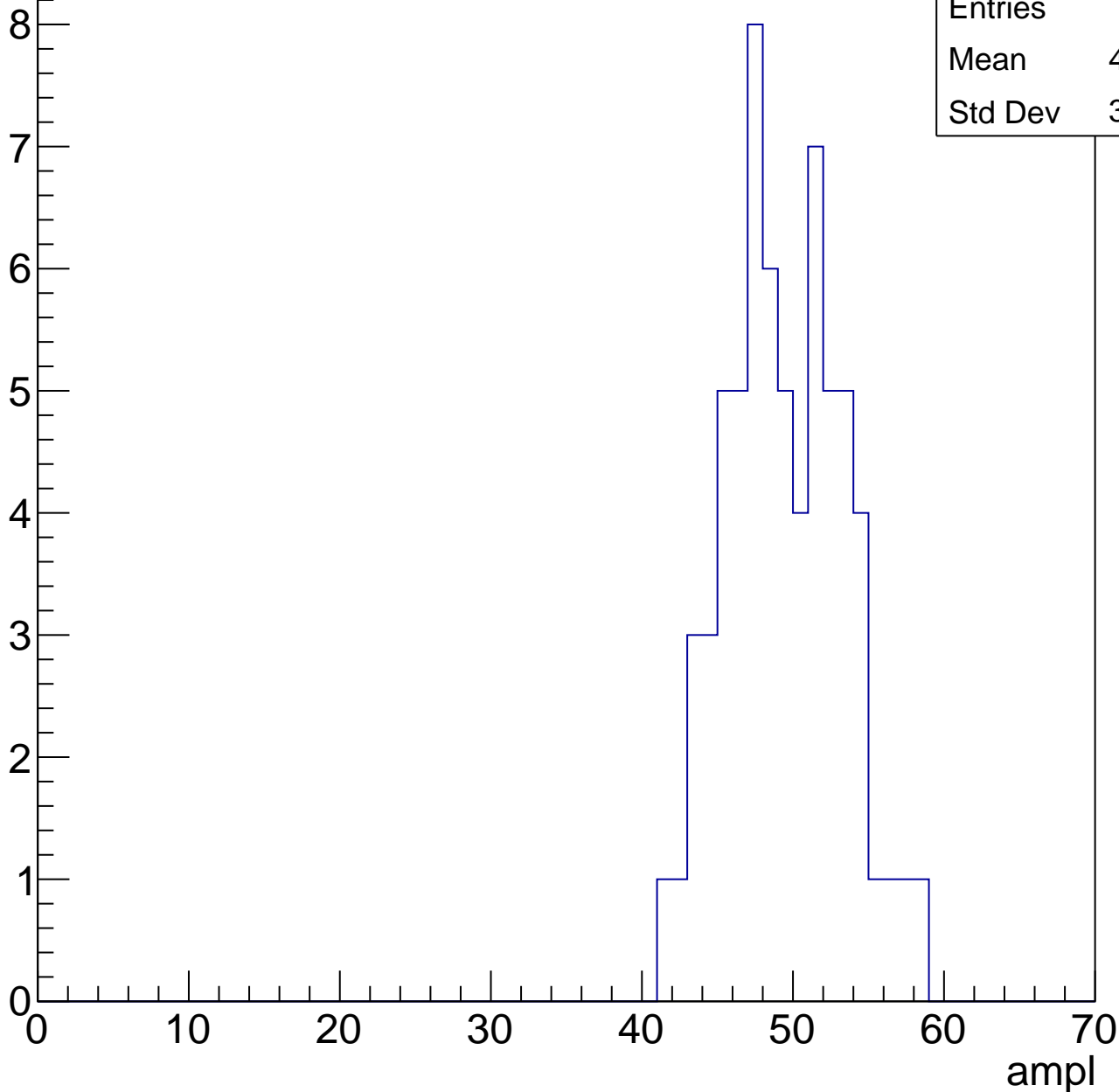


B1L102S, U2-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.97
Std Dev	3.786

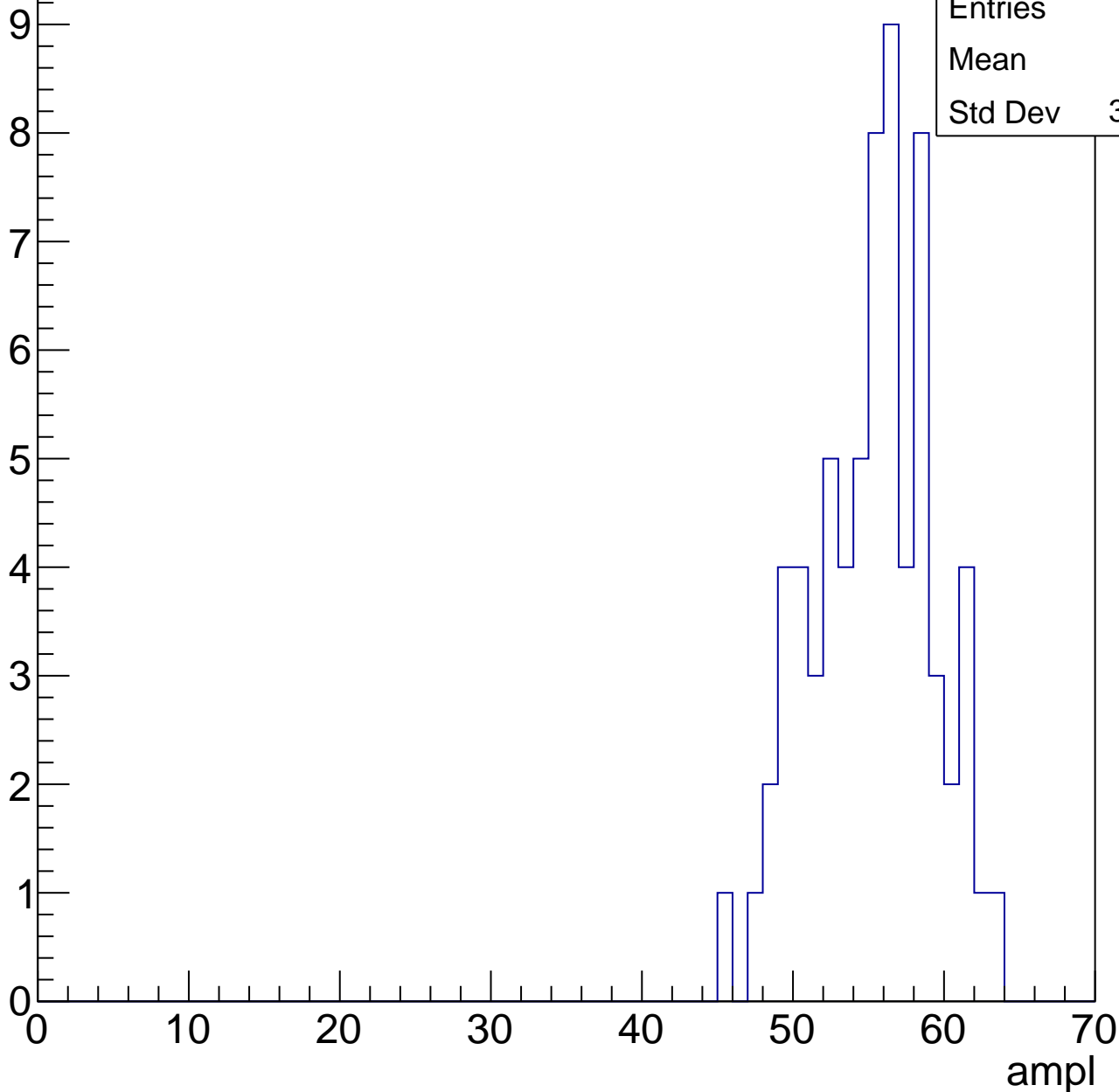


B1L102S, U2-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	54.8
Std Dev	3.936

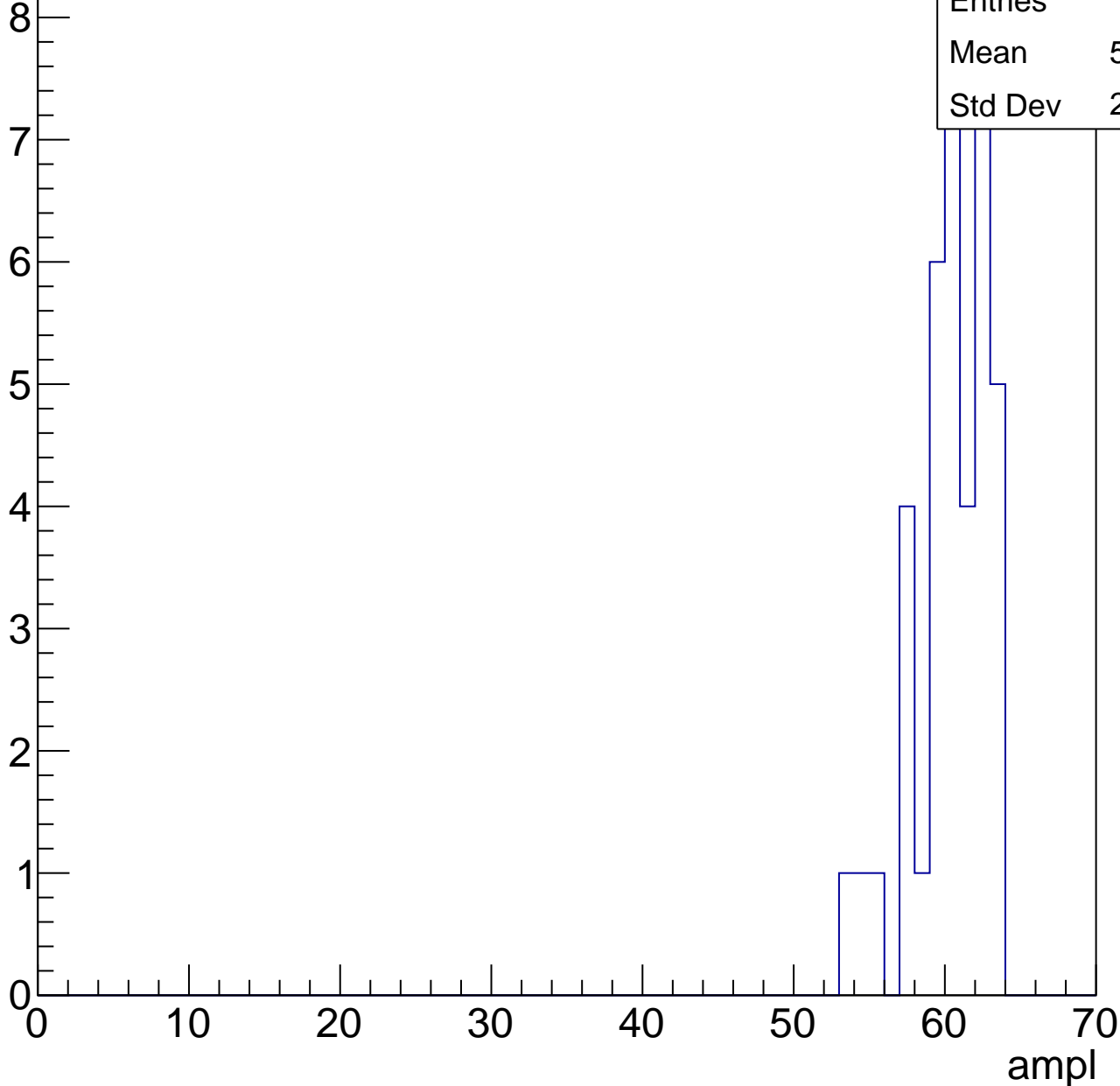


B1L102S, U2-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

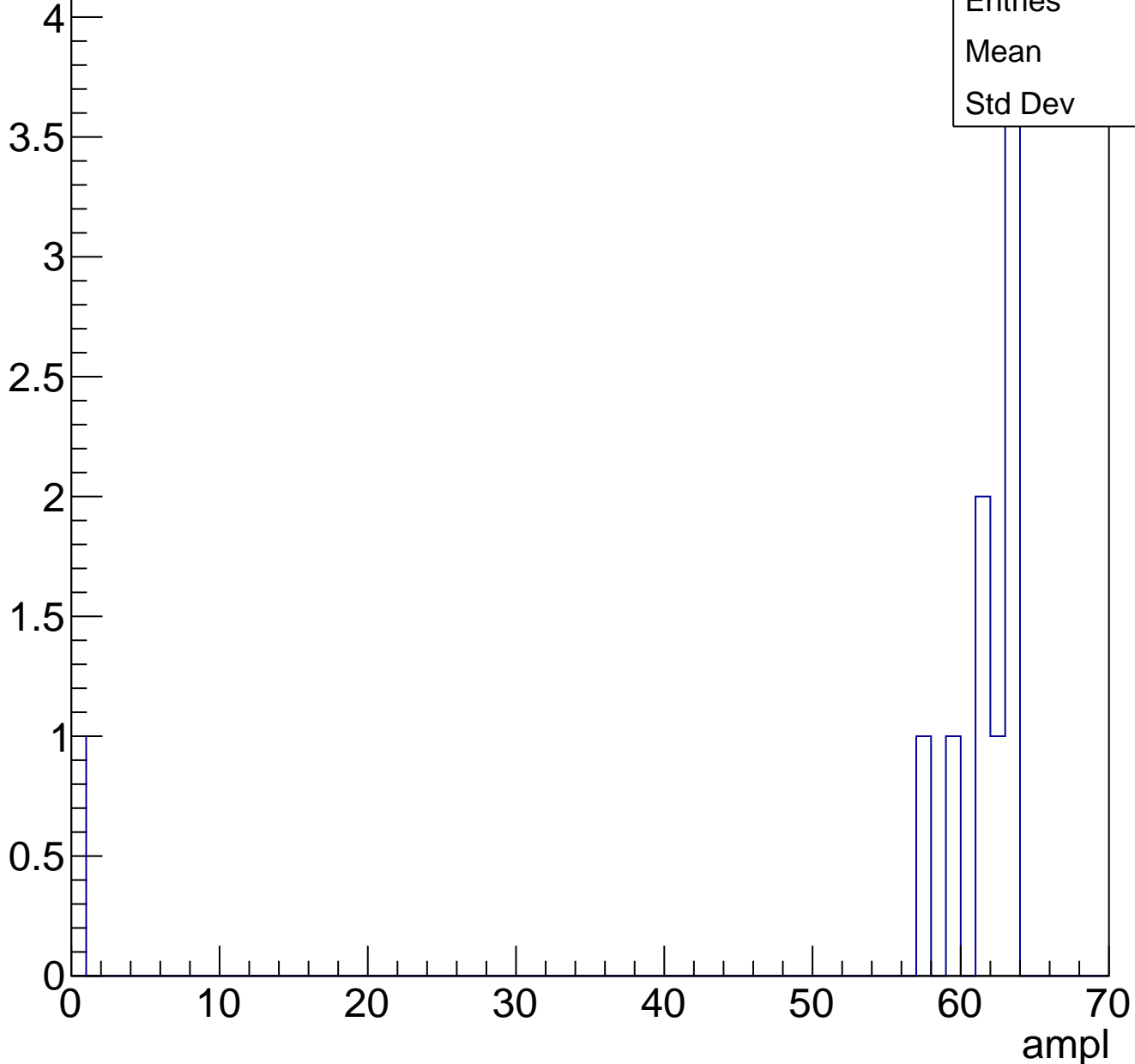
Entries	39
Mean	59.92
Std Dev	2.464



B1L102S, U2-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch113, adc0

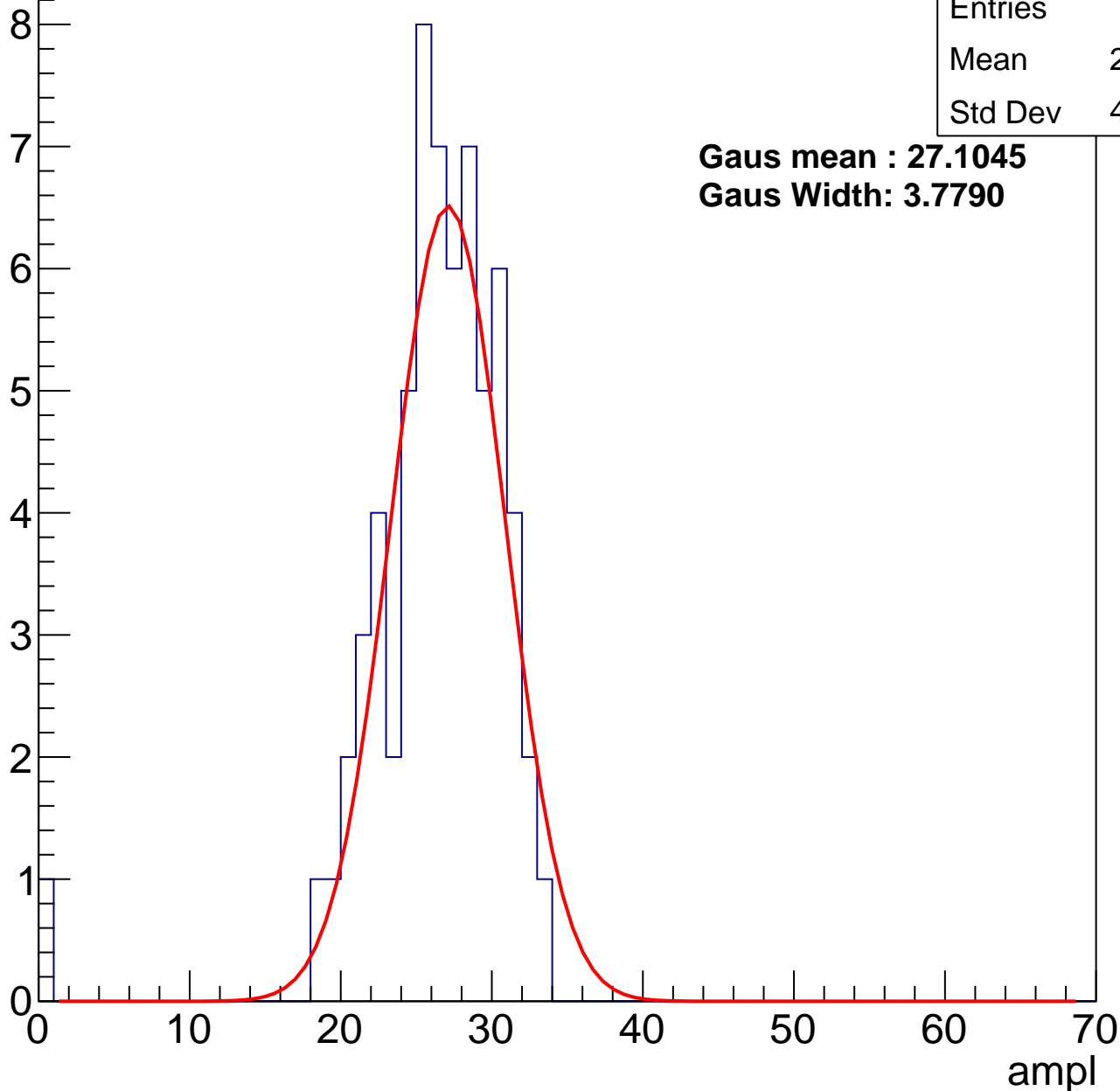
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	25.85
Std Dev	4.708

Gaus mean : 27.1045

Gaus Width: 3.7790



B1L102S, U2-ch113, adc1

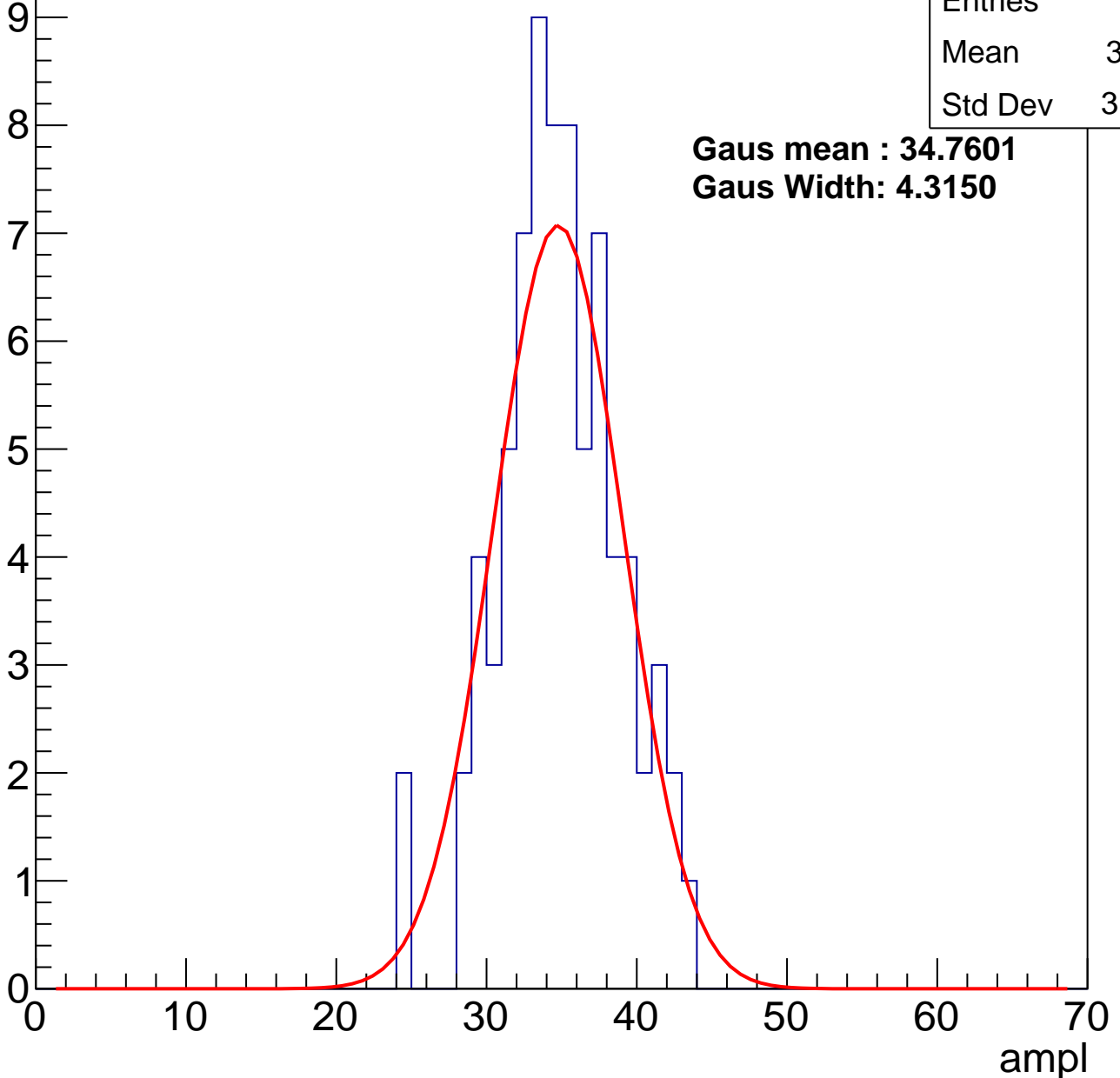
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.41
Std Dev	3.928

Gaus mean : 34.7601

Gaus Width: 4.3150



B1L102S, U2-ch113, adc2

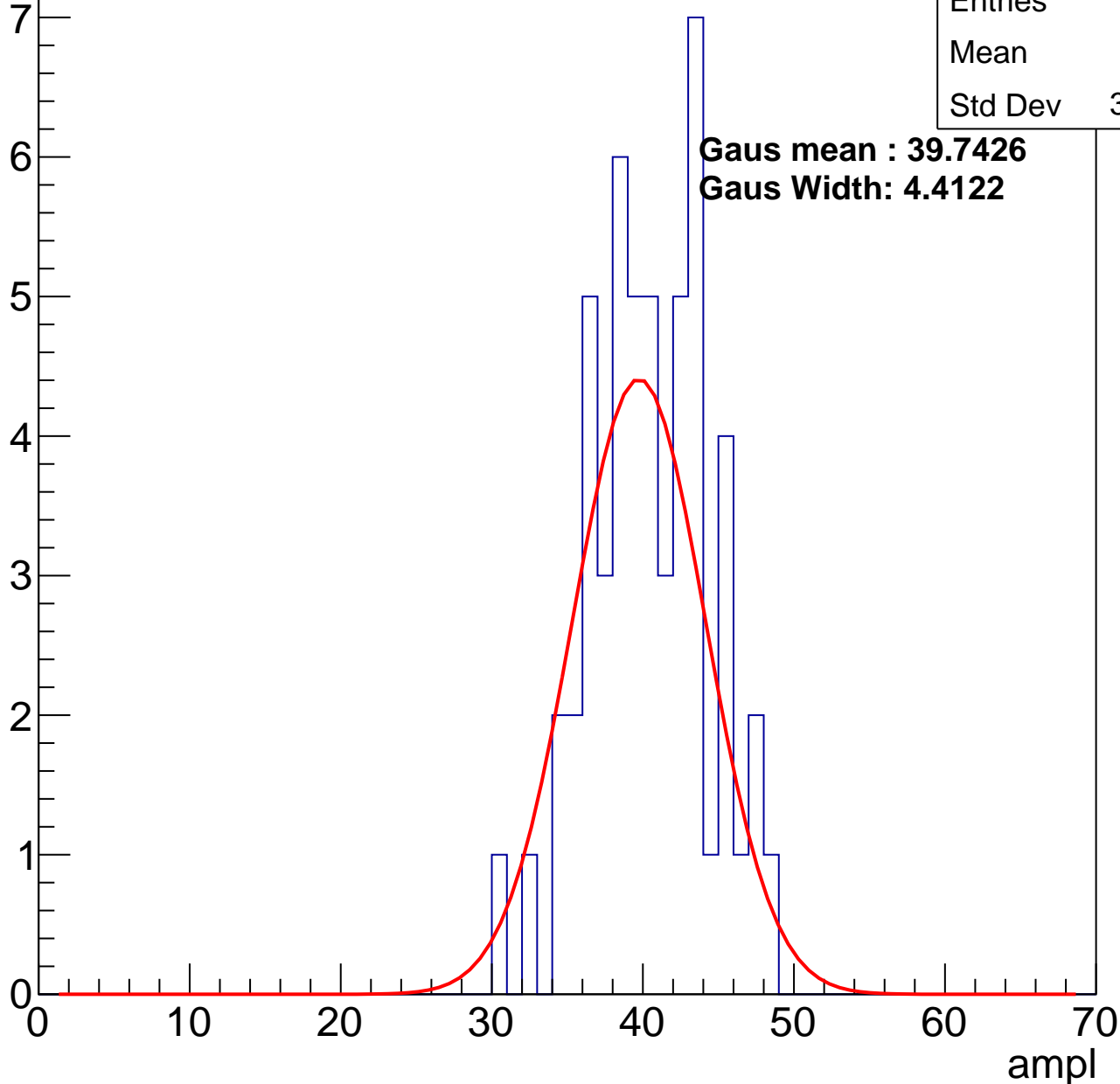
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40
Std Dev	3.902

Gaus mean : 39.7426

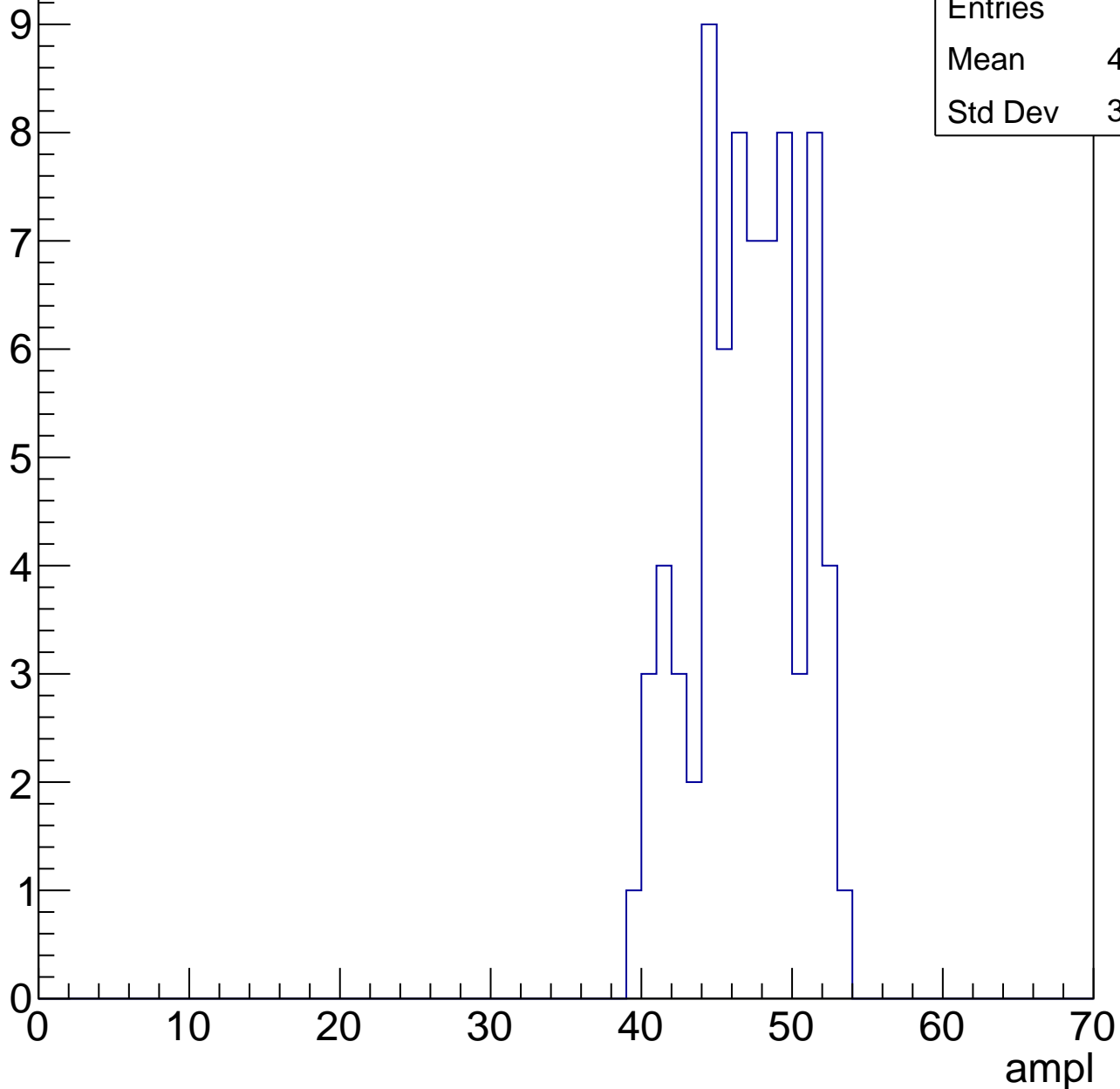
Gaus Width: 4.4122



B1L102S, U2-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



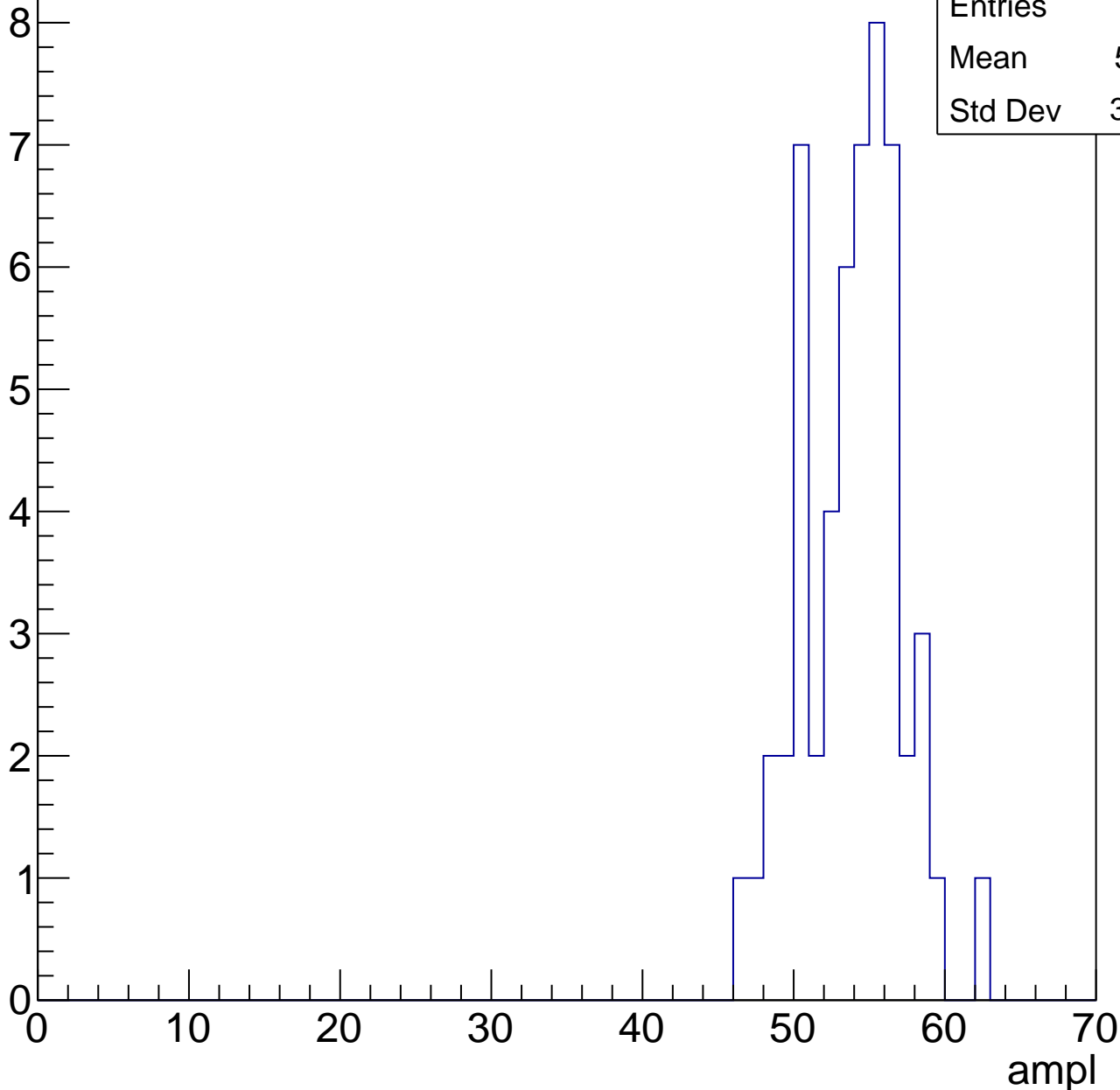
Entries	74
Mean	46.55
Std Dev	3.469

B1L102S, U2-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.41
Std Dev	3.218

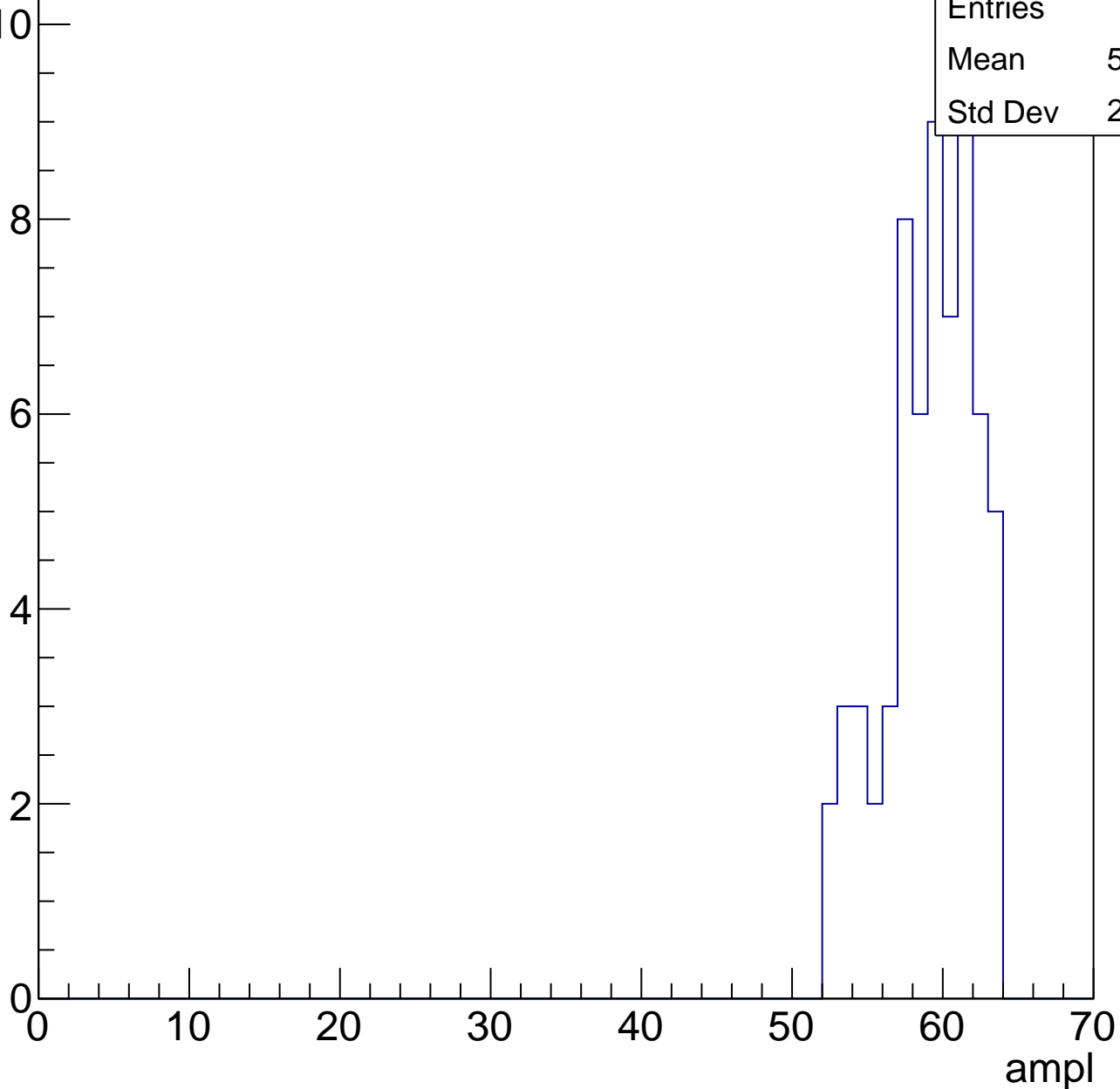


B1L102S, U2-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	58.67
Std Dev	2.937

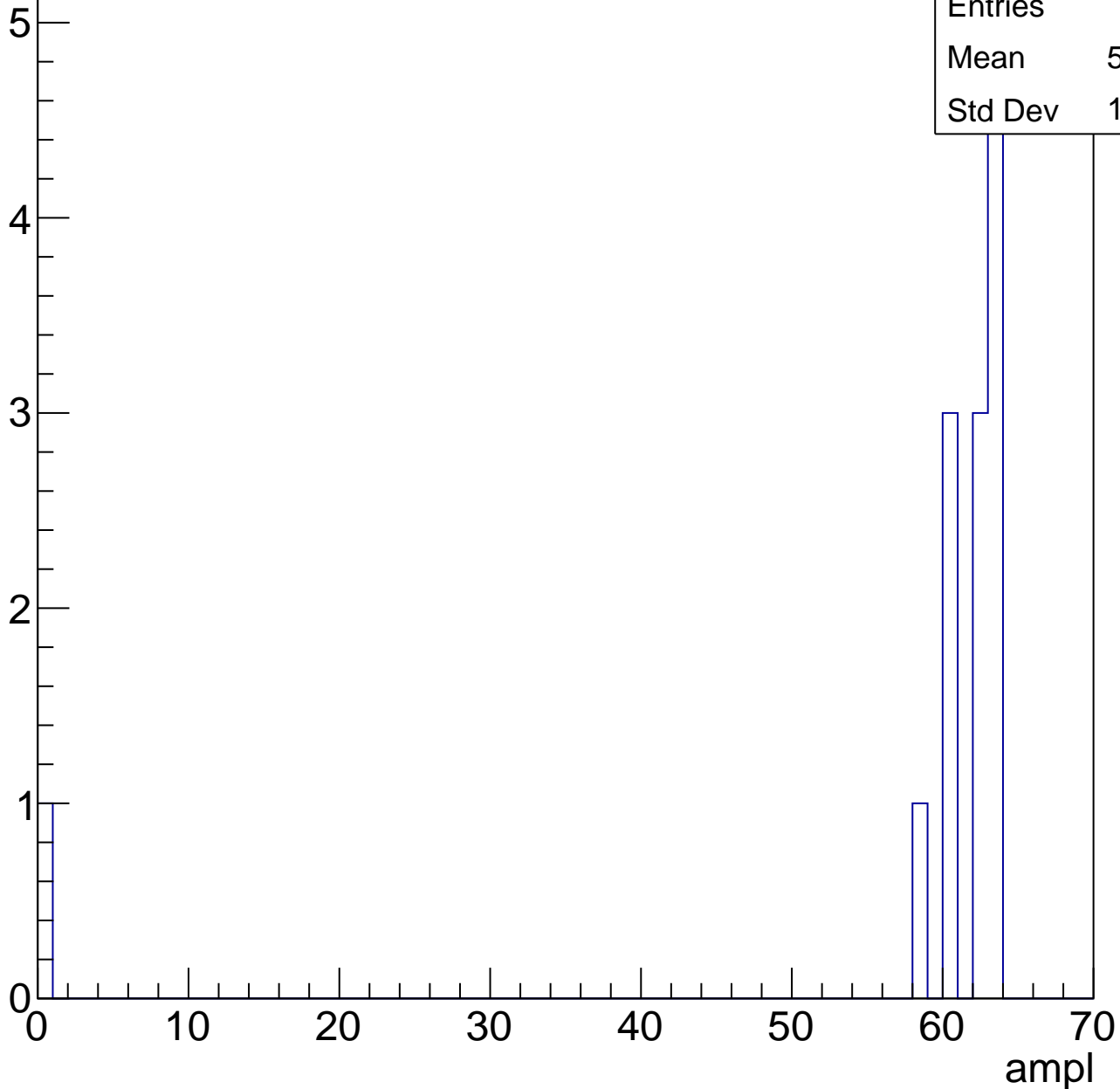


B1L102S, U2-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.85
Std Dev	16.48



B1L102S, U2-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch114, adc0

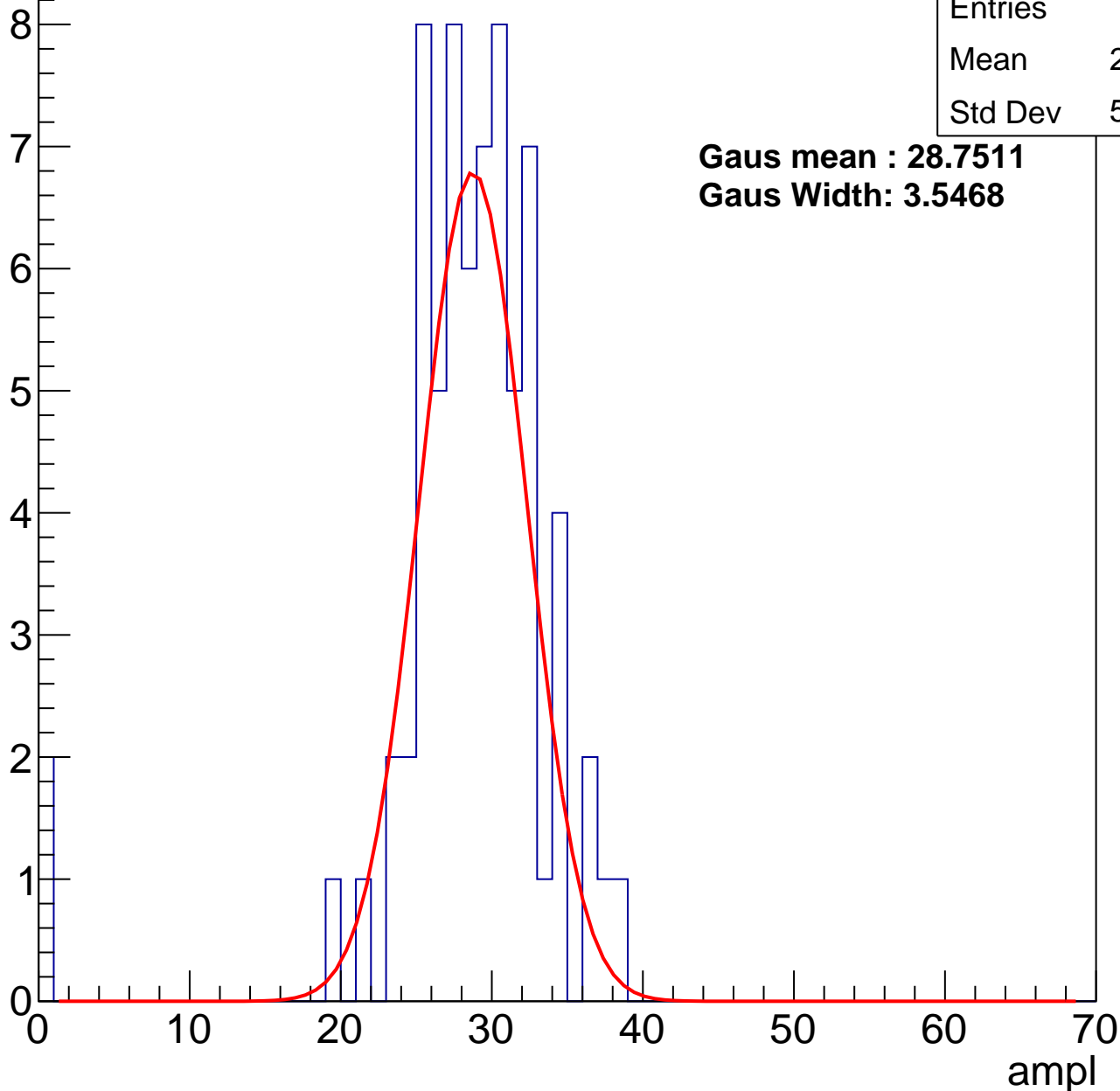
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.97
Std Dev	5.998

Gaus mean : 28.7511

Gaus Width: 3.5468



B1L102S, U2-ch114, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	35.07
Std Dev	4.24

Gaus mean : 34.9556

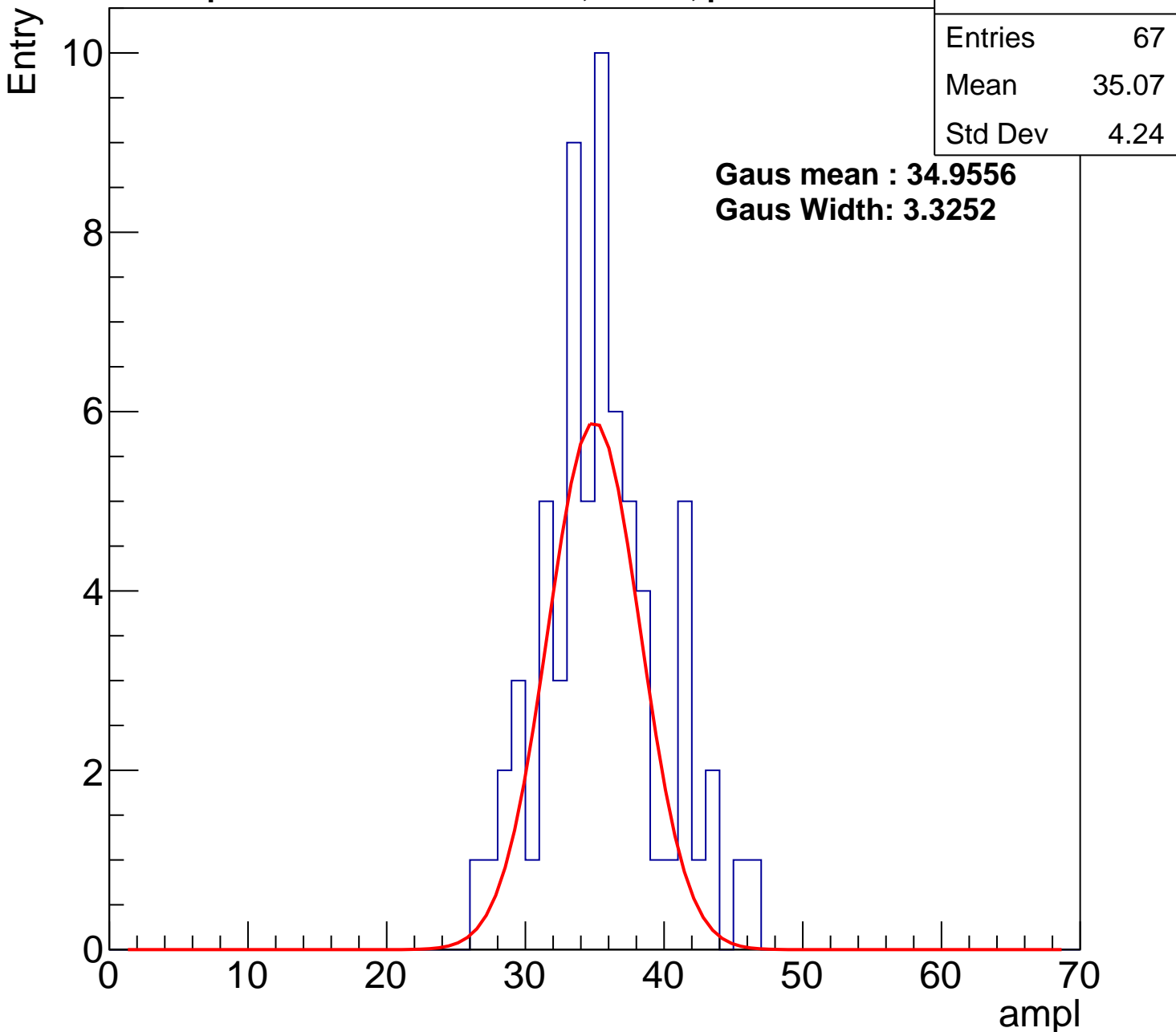
Gaus Width: 3.3252

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch114, adc2

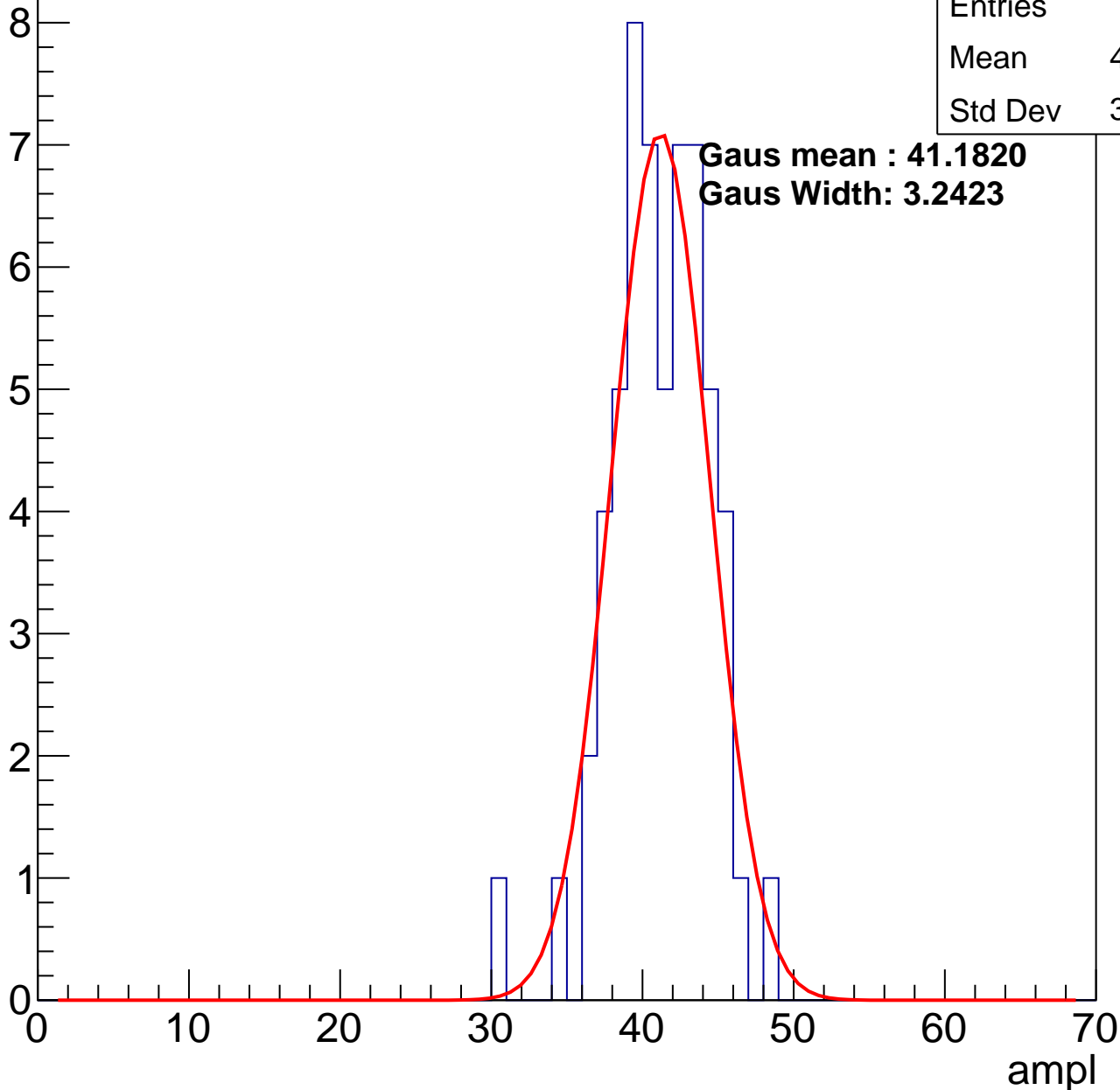
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.69
Std Dev	3.163

Gaus mean : 41.1820

Gaus Width: 3.2423

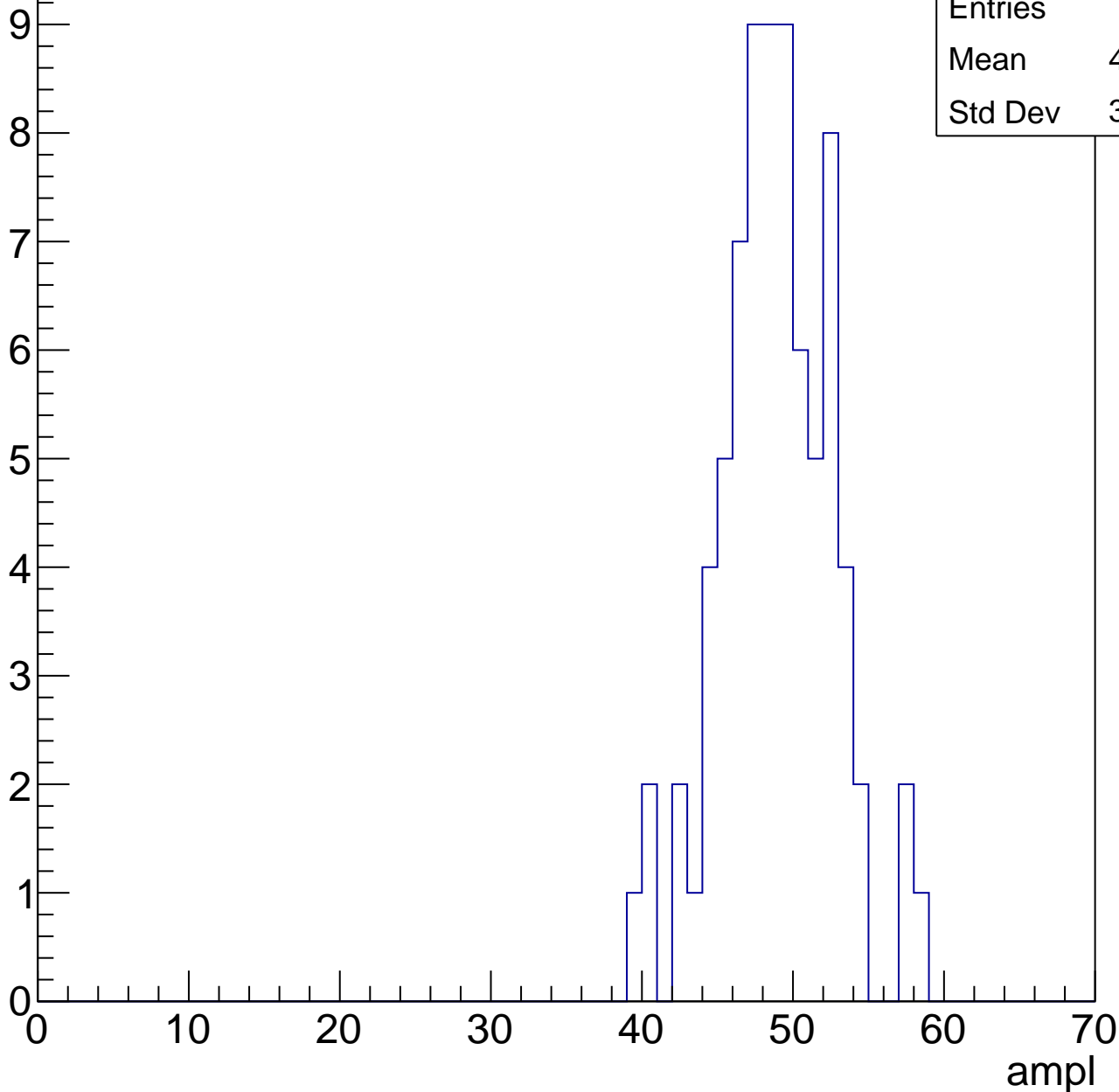


B1L102S, U2-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	48.42
Std Dev	3.743

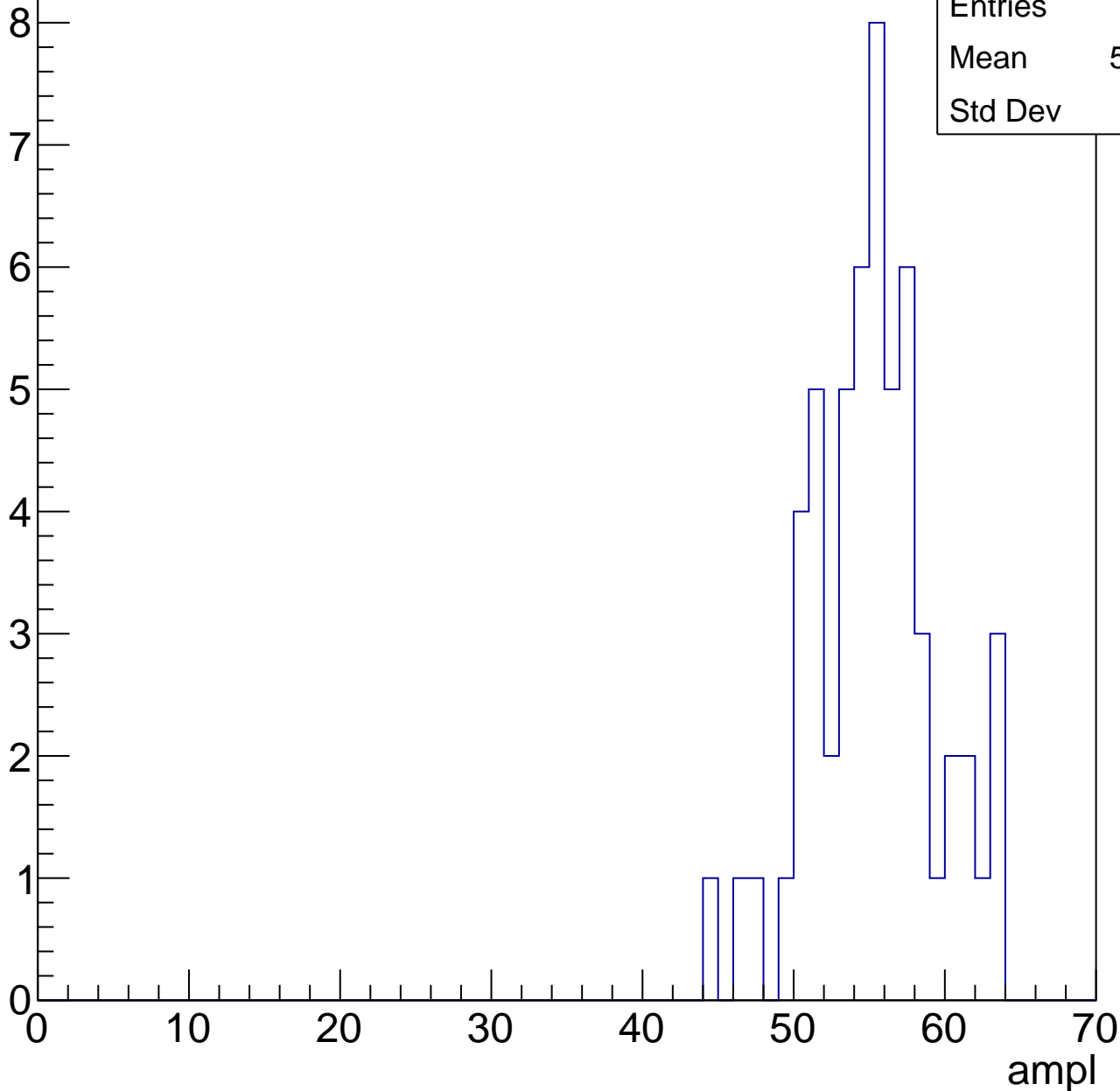


B1L102S, U2-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.77
Std Dev	4.1

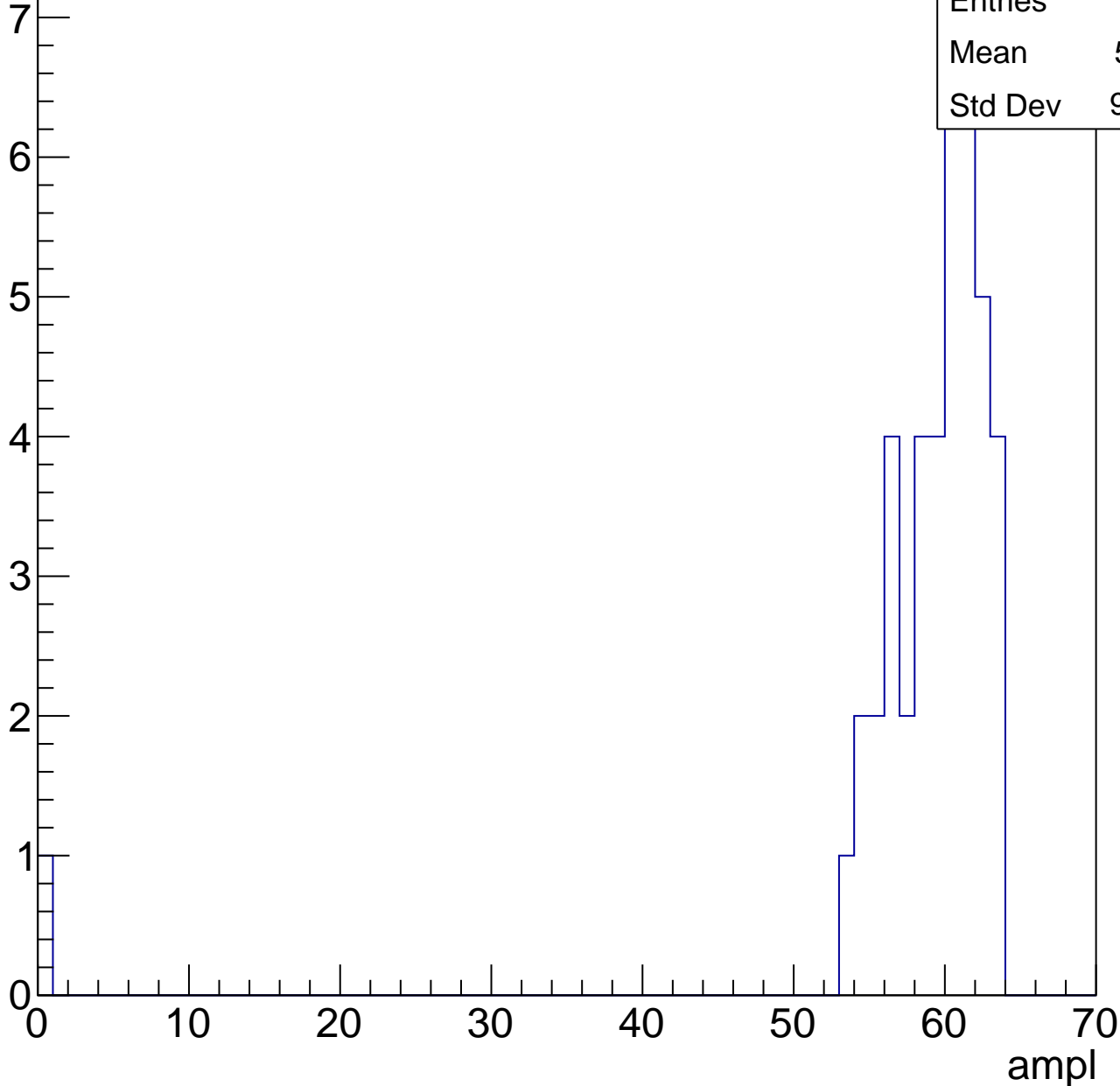


B1L102S, U2-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.81
Std Dev	9.314

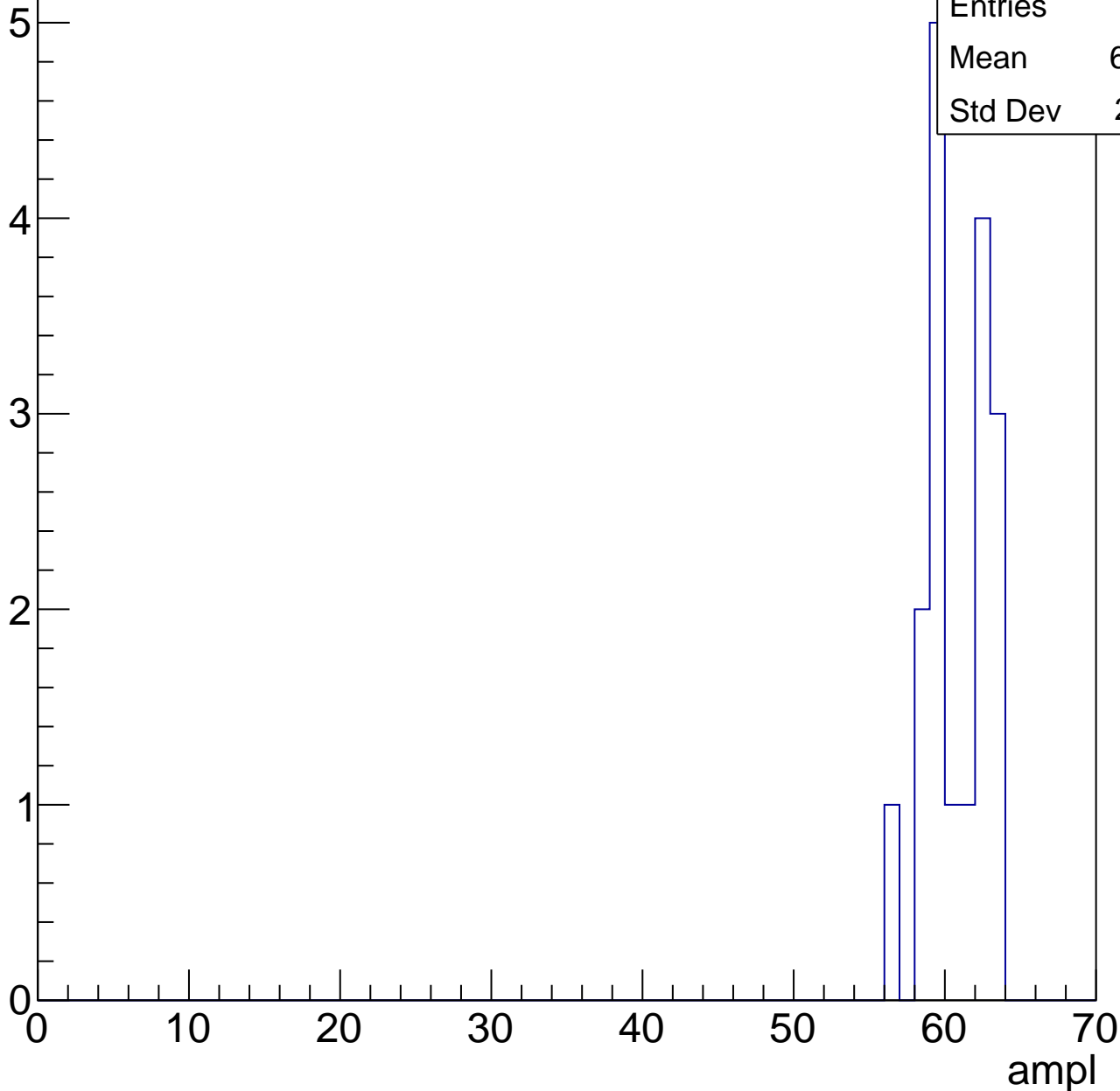


B1L102S, U2-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	60.29
Std Dev	2.051



B1L102S, U2-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch115, adc0

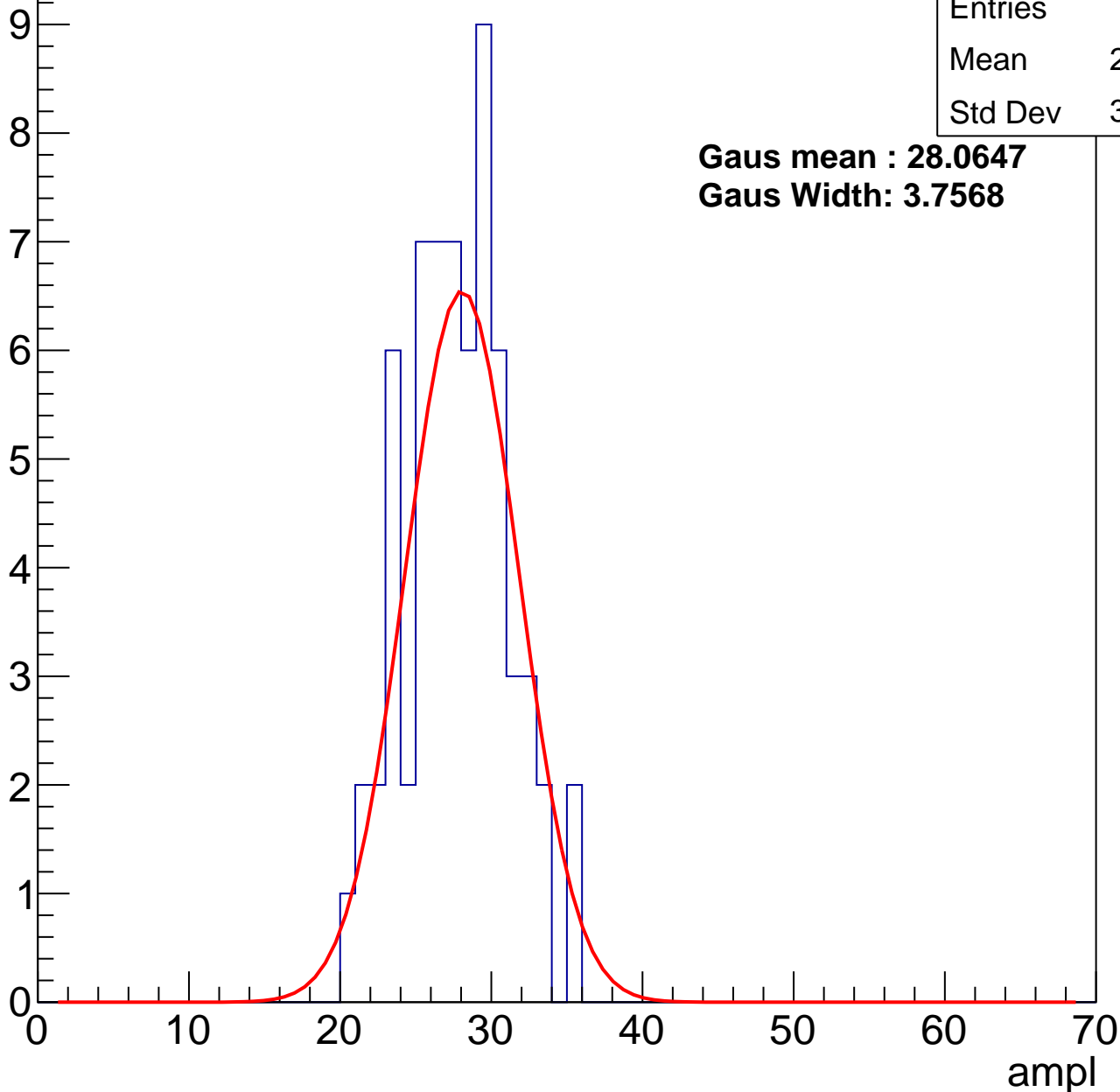
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.26
Std Dev	3.362

Gaus mean : 28.0647

Gaus Width: 3.7568



B1L102S, U2-ch115, adc1

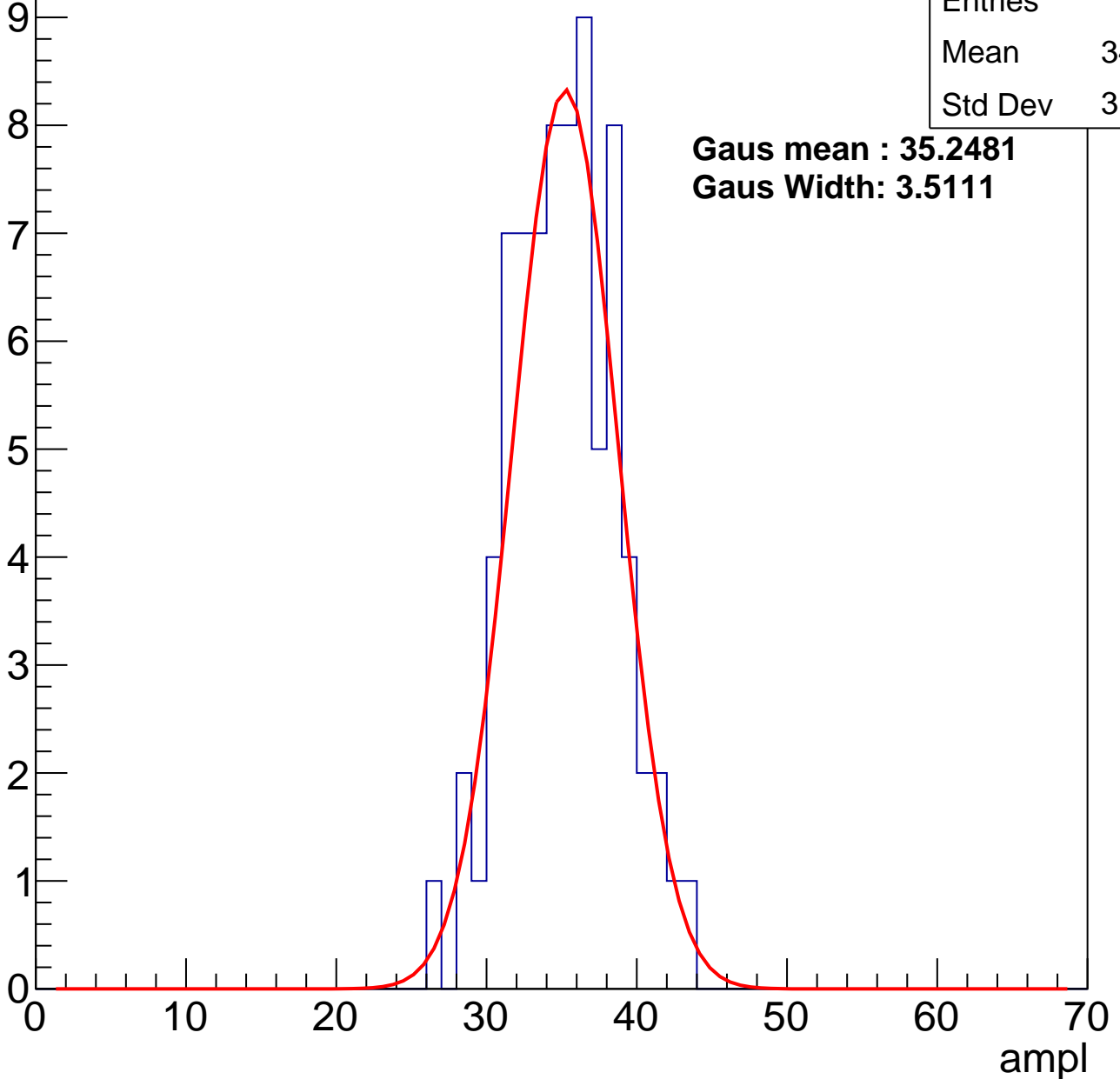
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.69
Std Dev	3.454

Gaus mean : 35.2481

Gaus Width: 3.5111

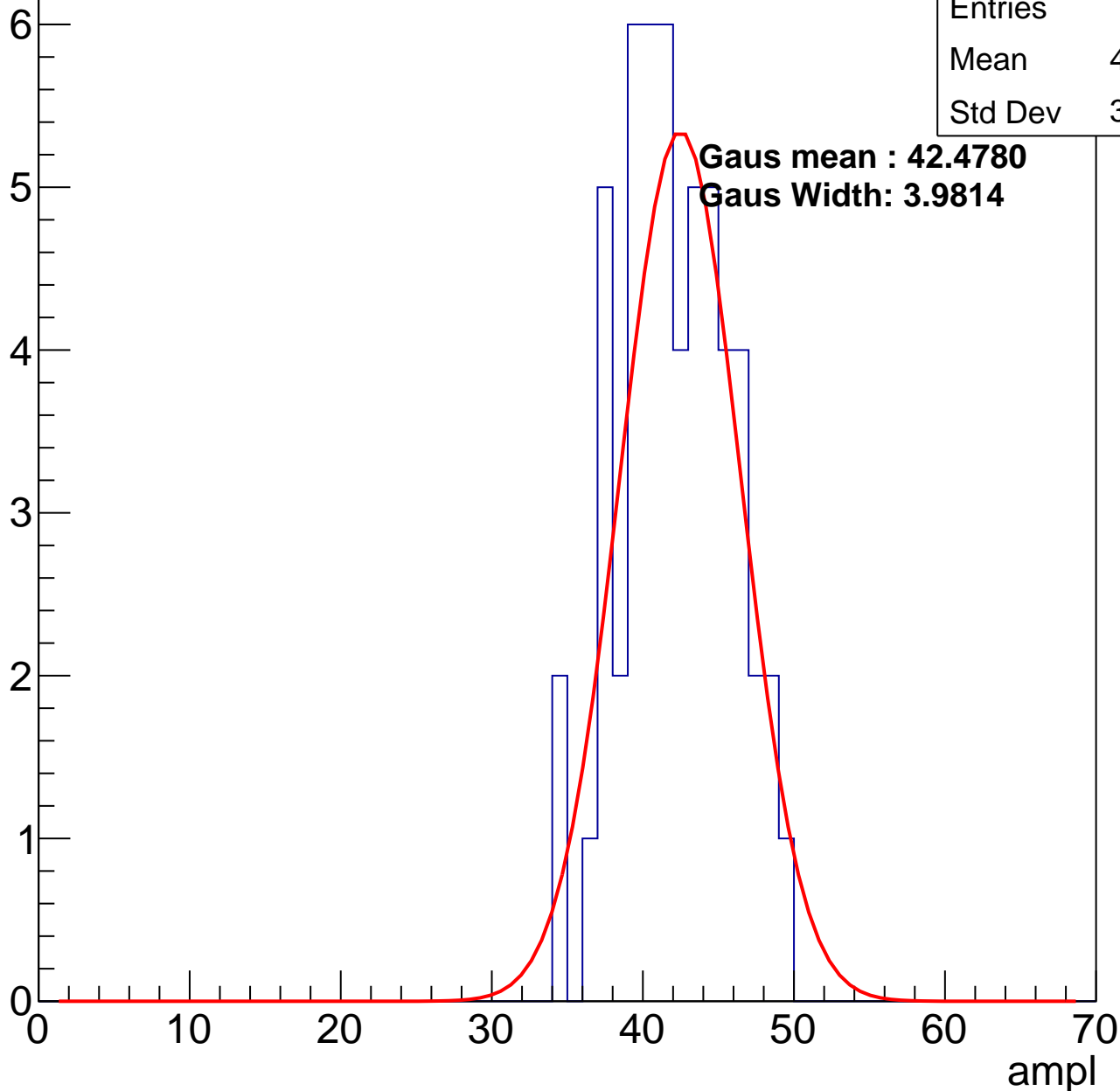


B1L102S, U2-ch115, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.65
Std Dev	3.553

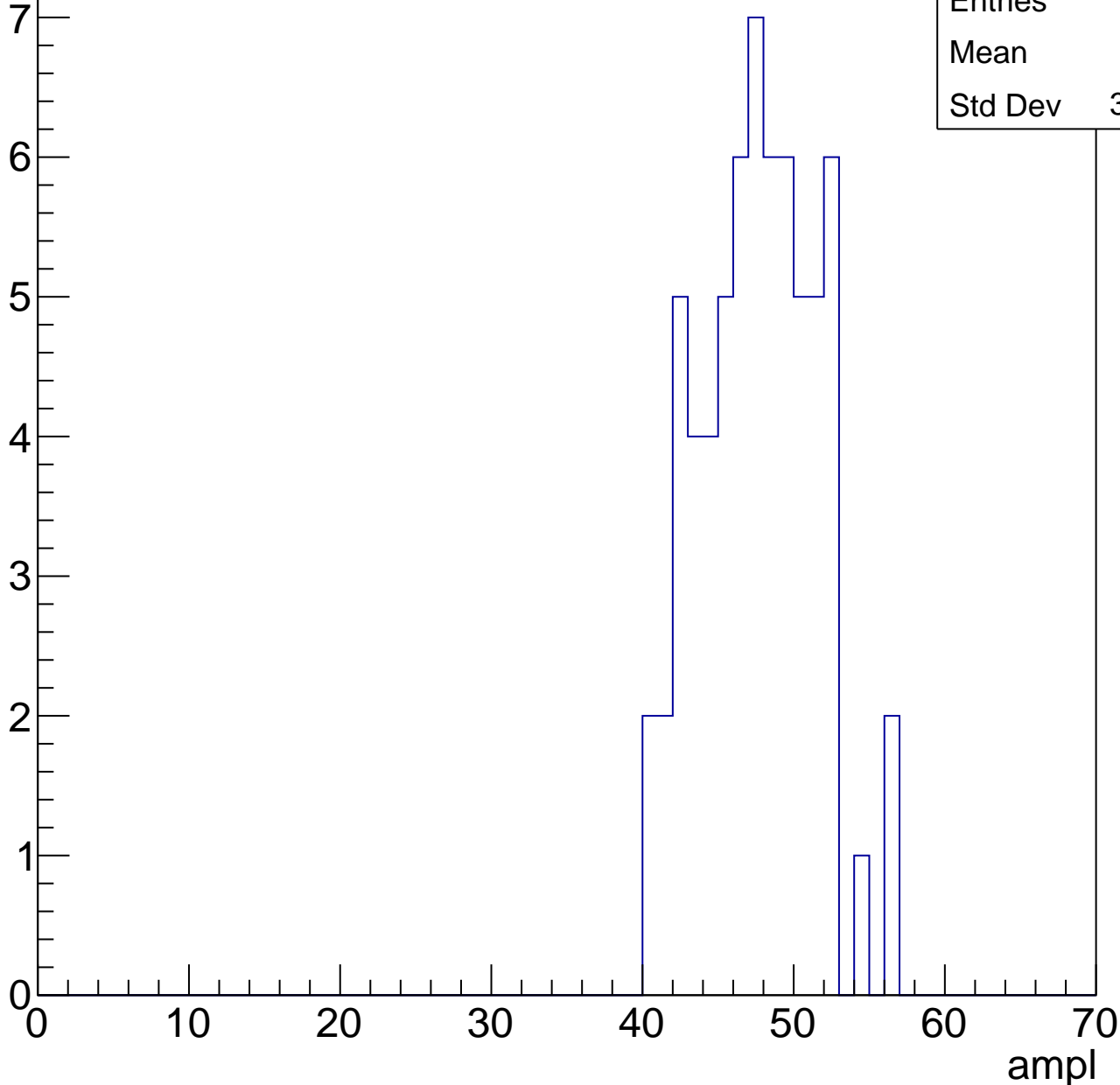


B1L102S, U2-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.2
Std Dev	3.763

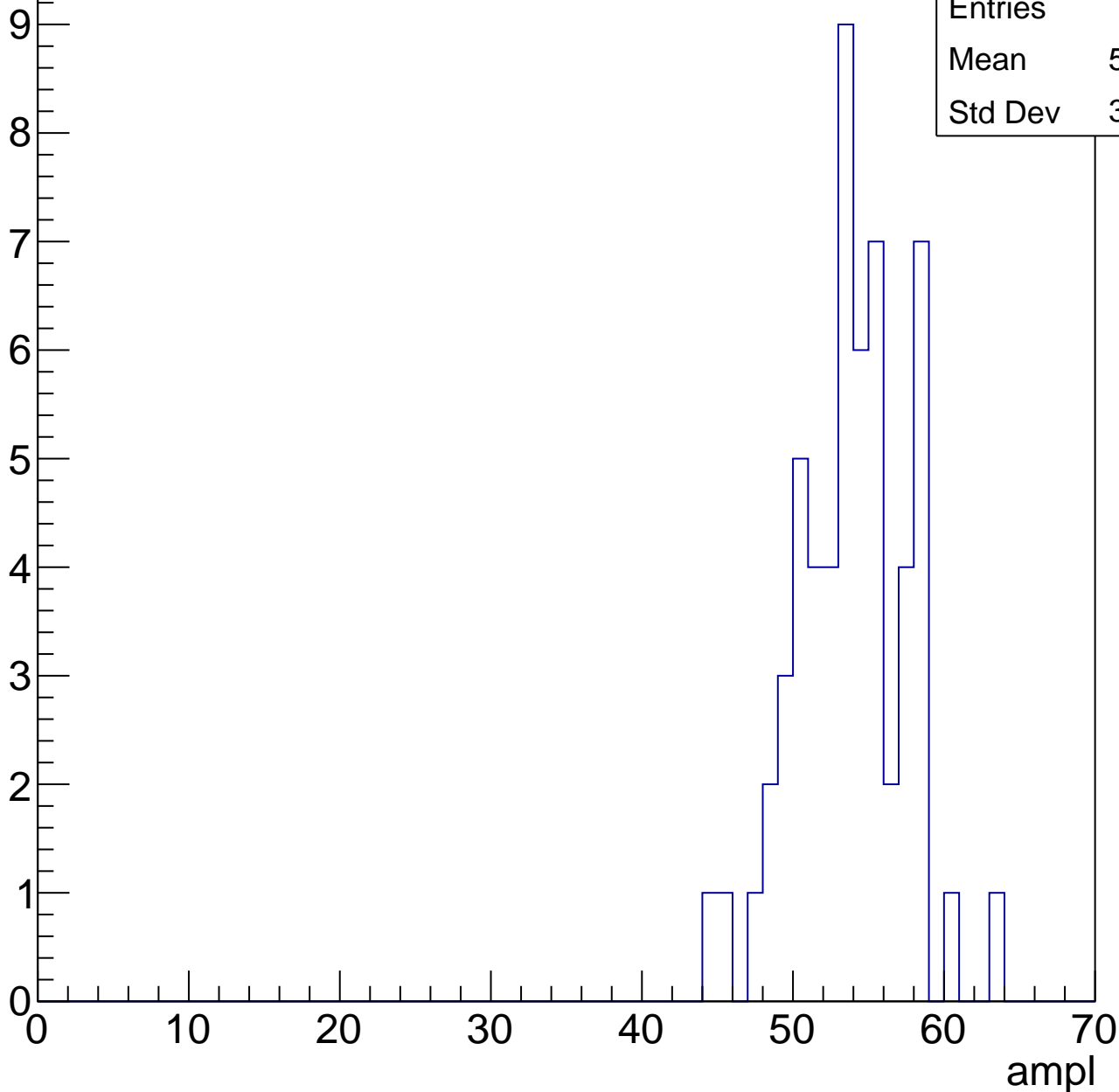


B1L102S, U2-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.38
Std Dev	3.657

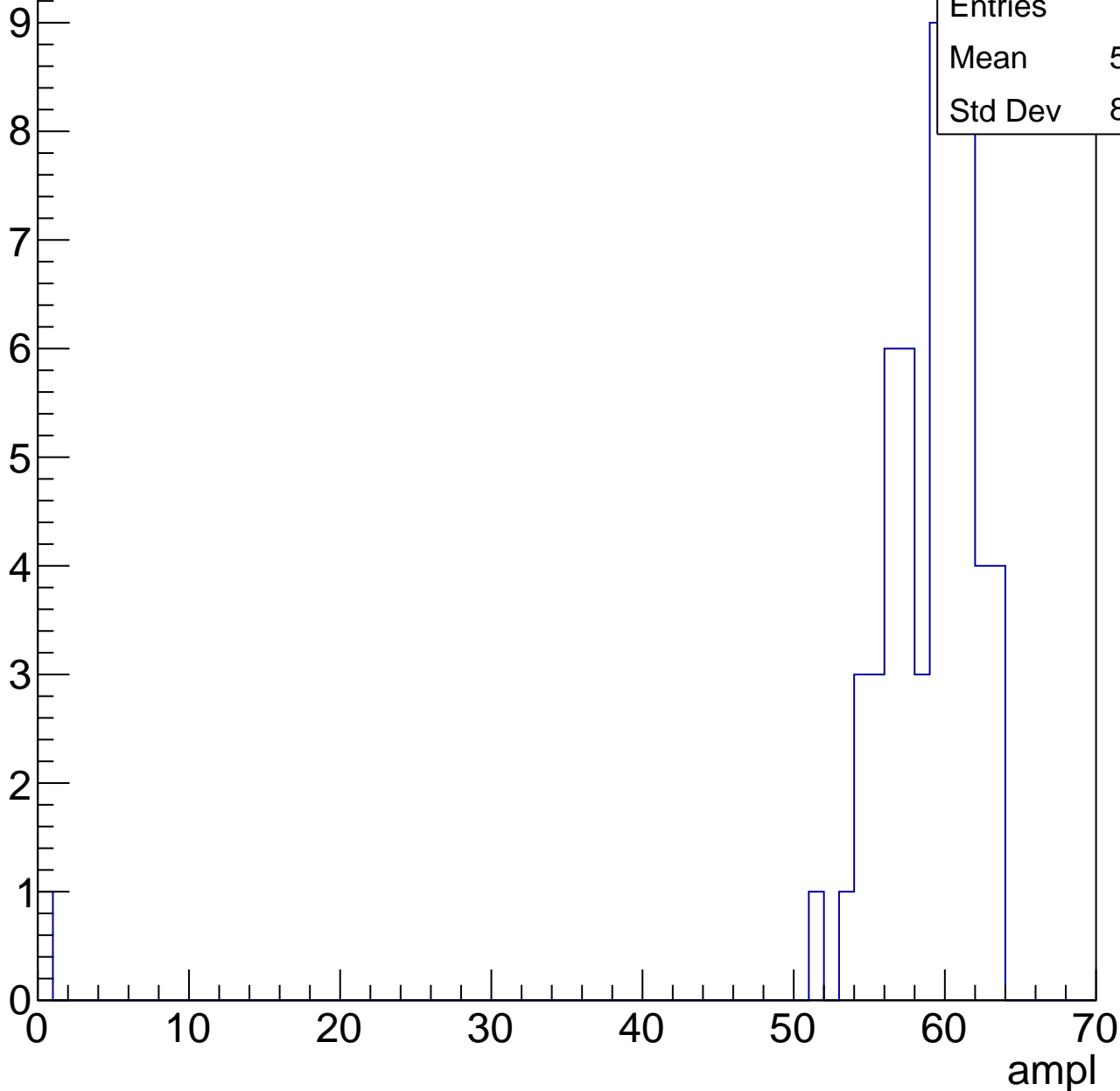


B1L102S, U2-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.64
Std Dev	8.117

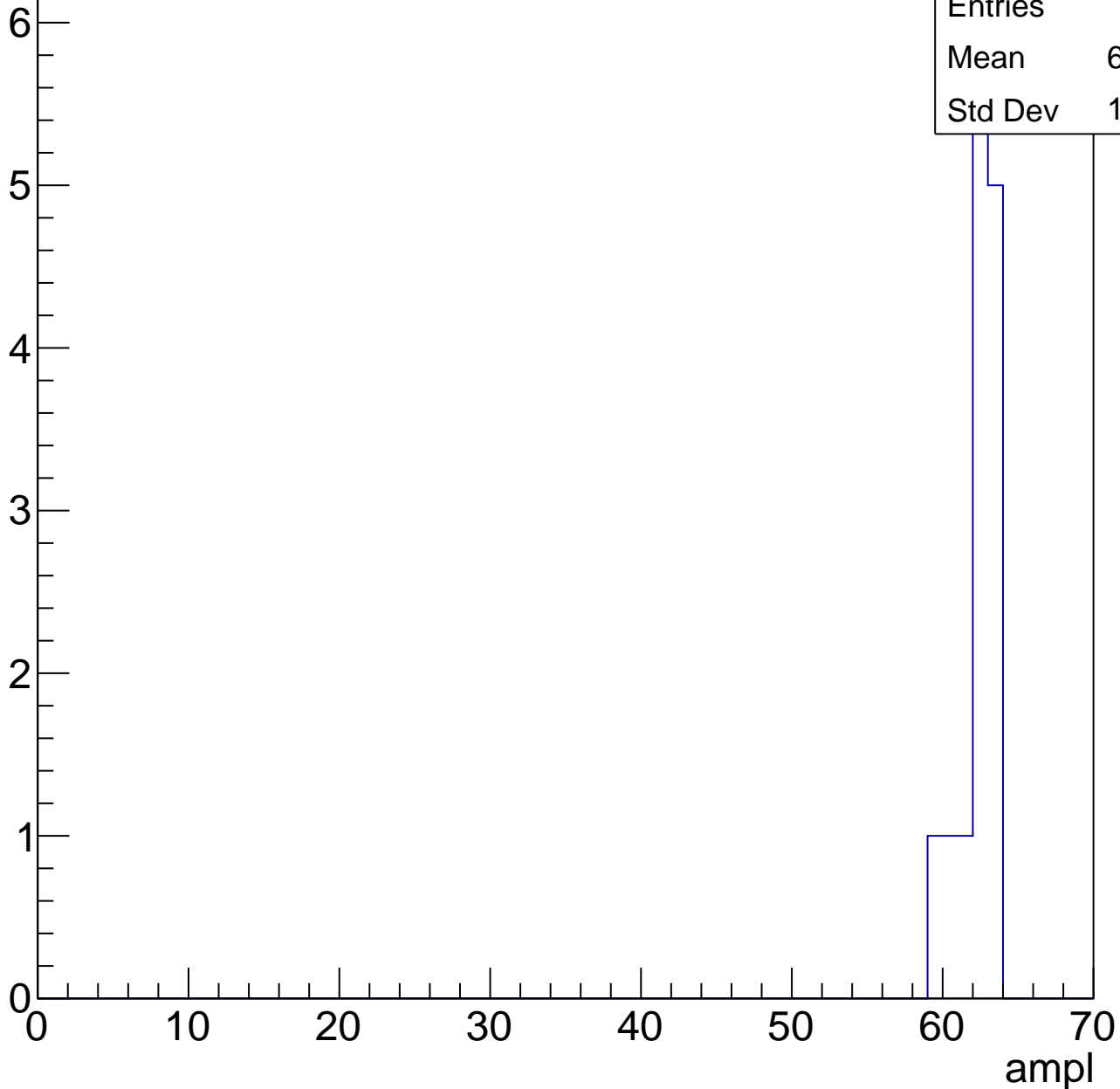


B1L102S, U2-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.93
Std Dev	1.163



B1L102S, U2-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch116, adc0

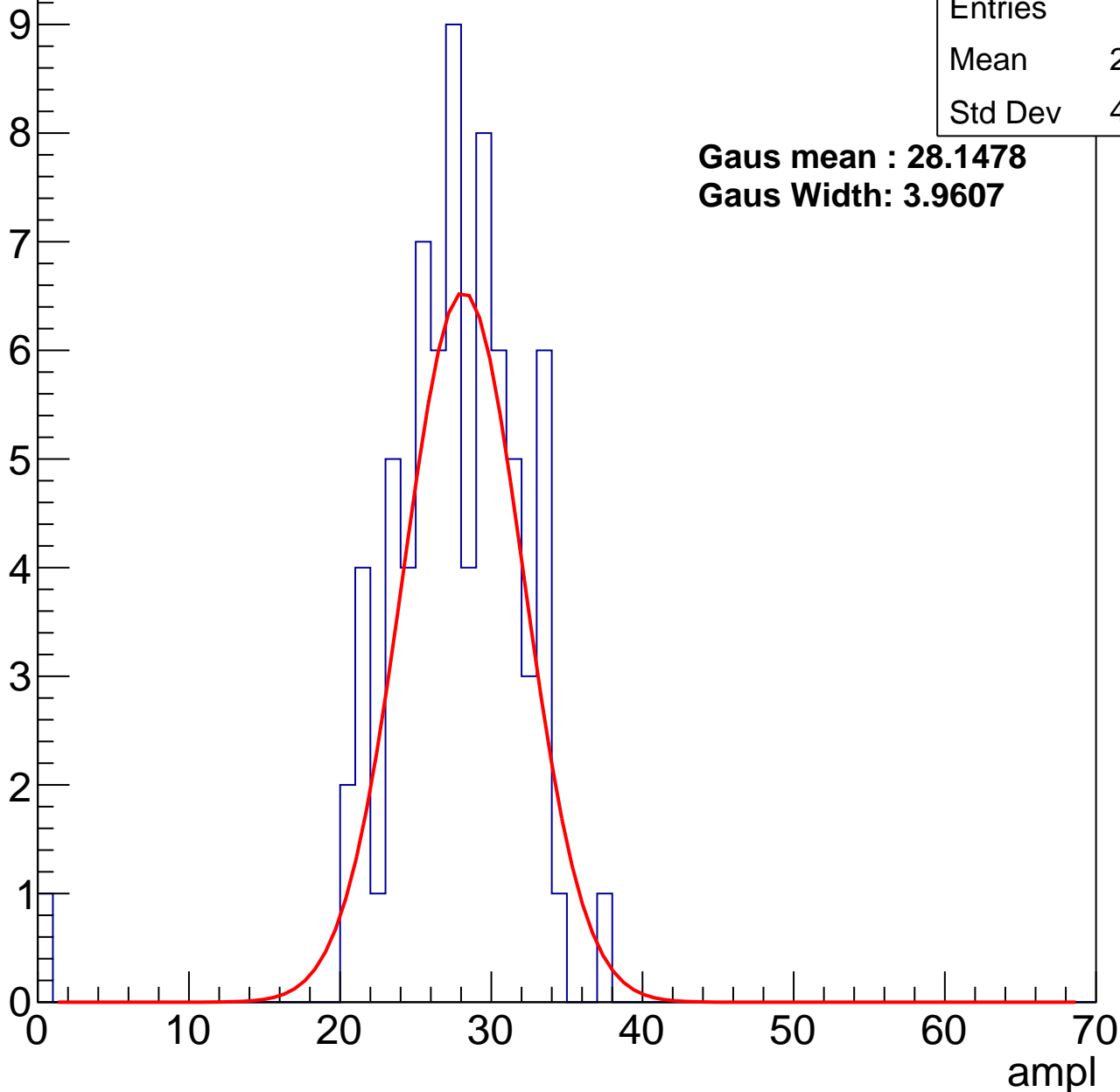
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.05
Std Dev	4.904

Gaus mean : 28.1478

Gaus Width: 3.9607



B1L102S, U2-ch116, adc1

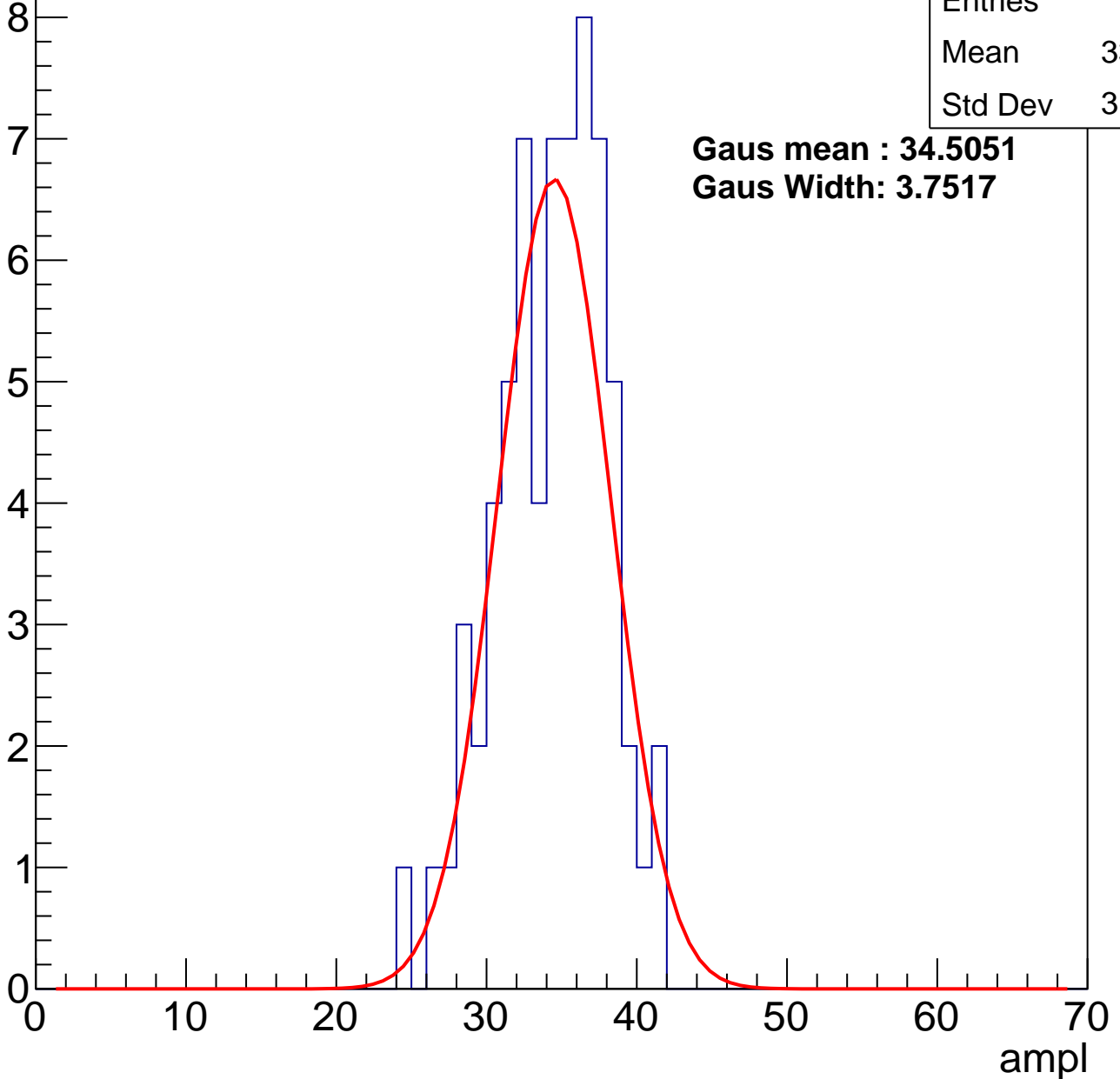
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.88
Std Dev	3.618

Gaus mean : 34.5051

Gaus Width: 3.7517



B1L102S, U2-ch116, adc2

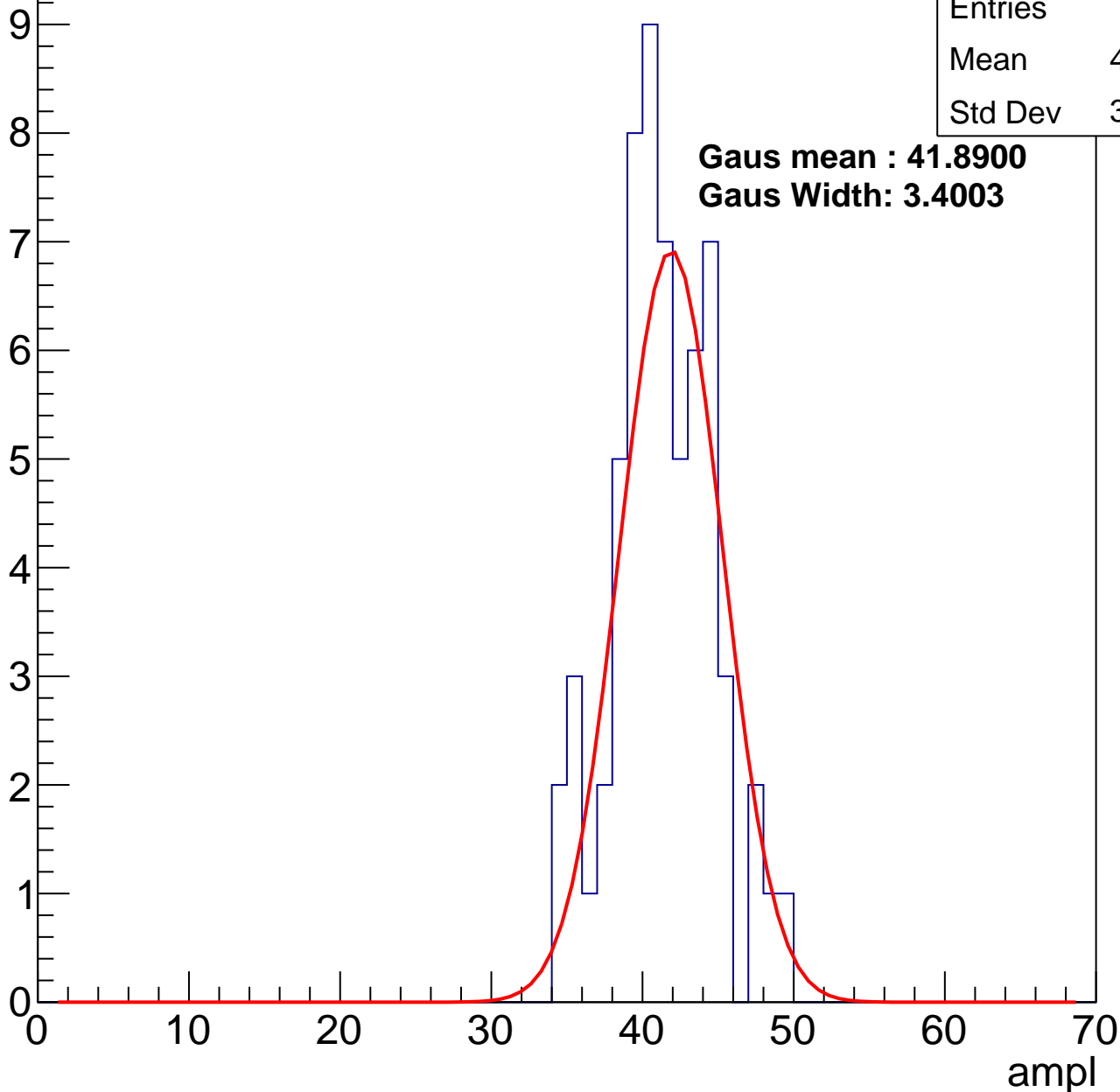
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.87
Std Dev	3.295

Gaus mean : 41.8900

Gaus Width: 3.4003

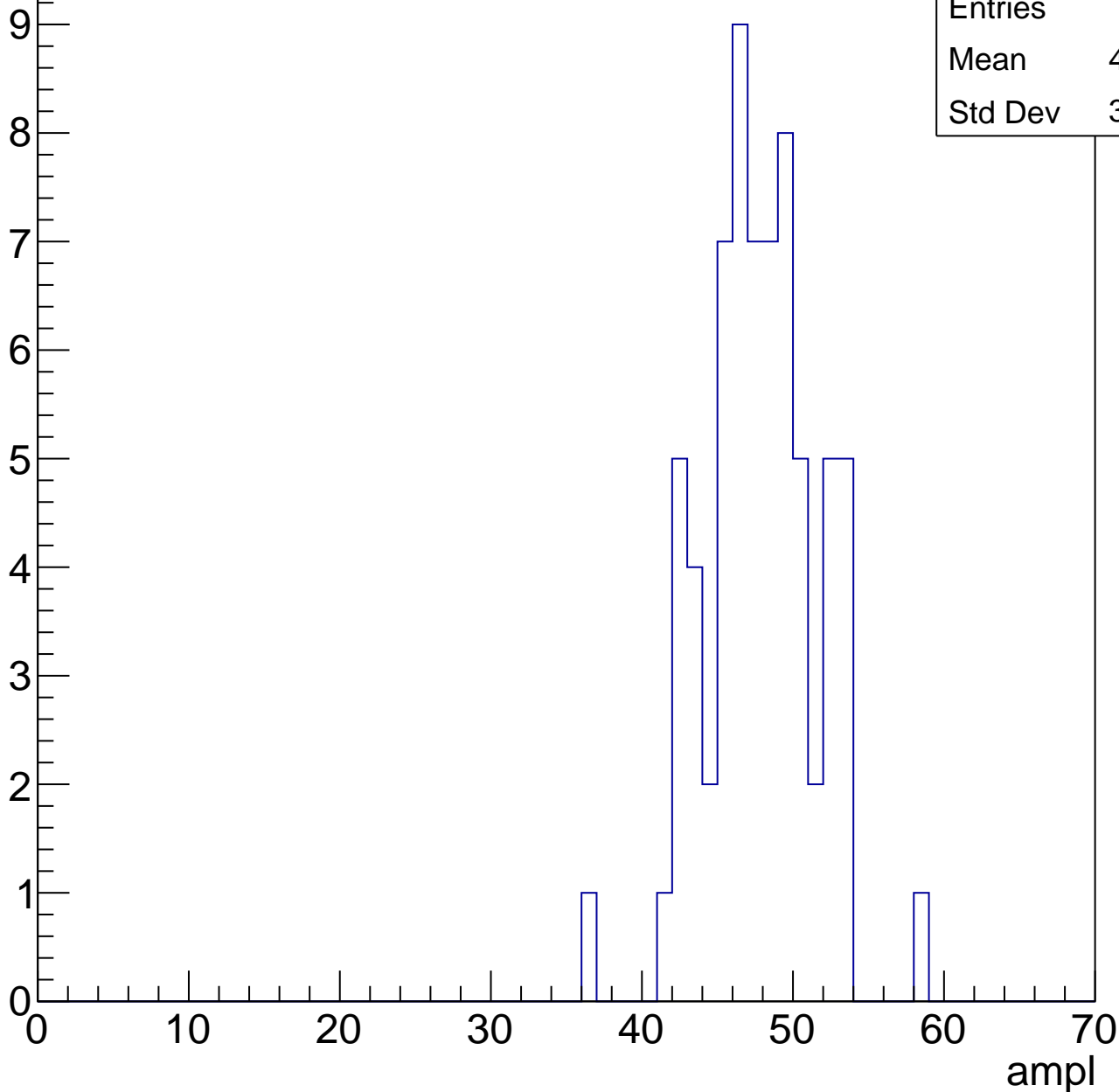


B1L102S, U2-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.36
Std Dev	3.679

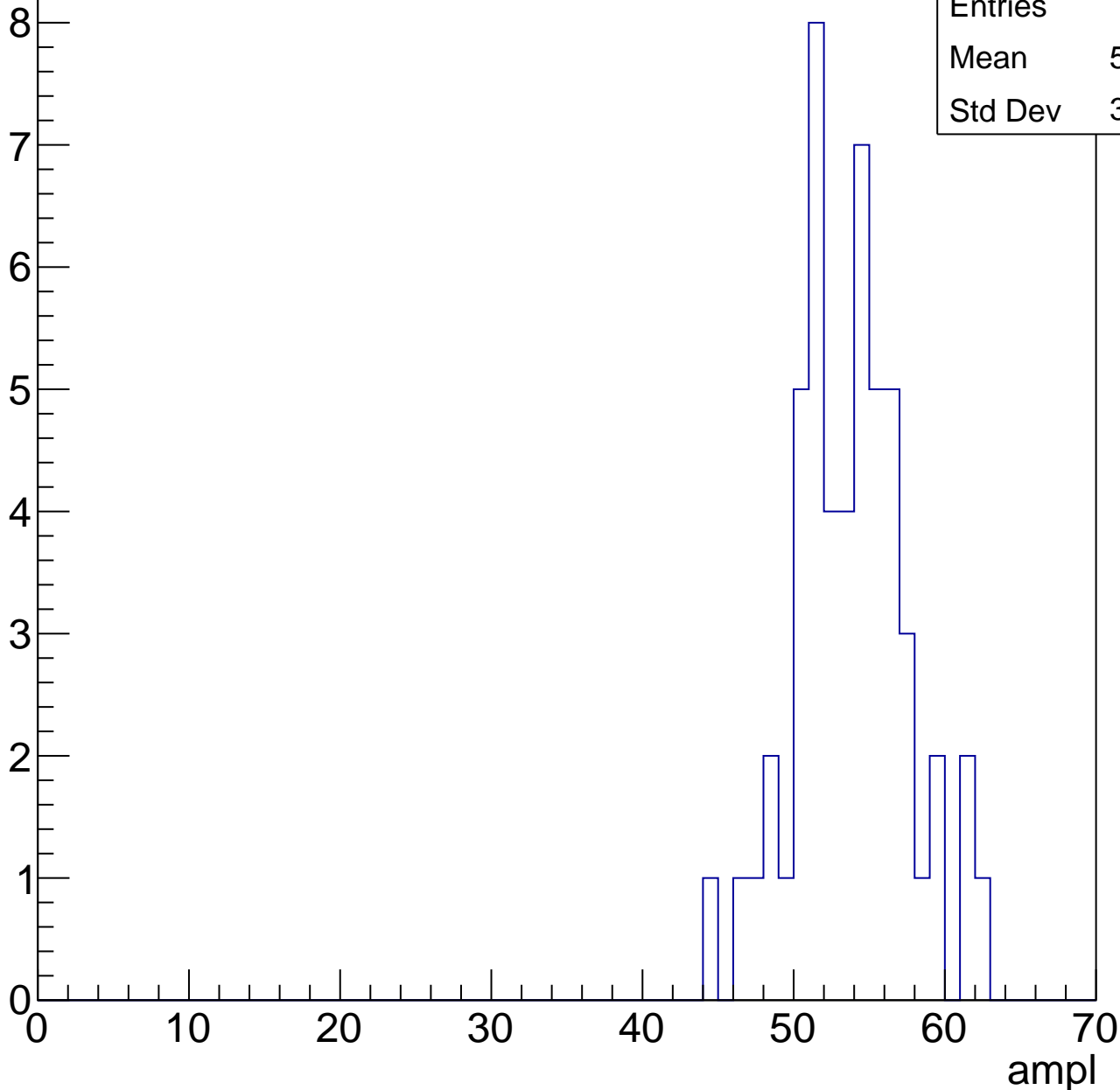


B1L102S, U2-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	53.28
Std Dev	3.723

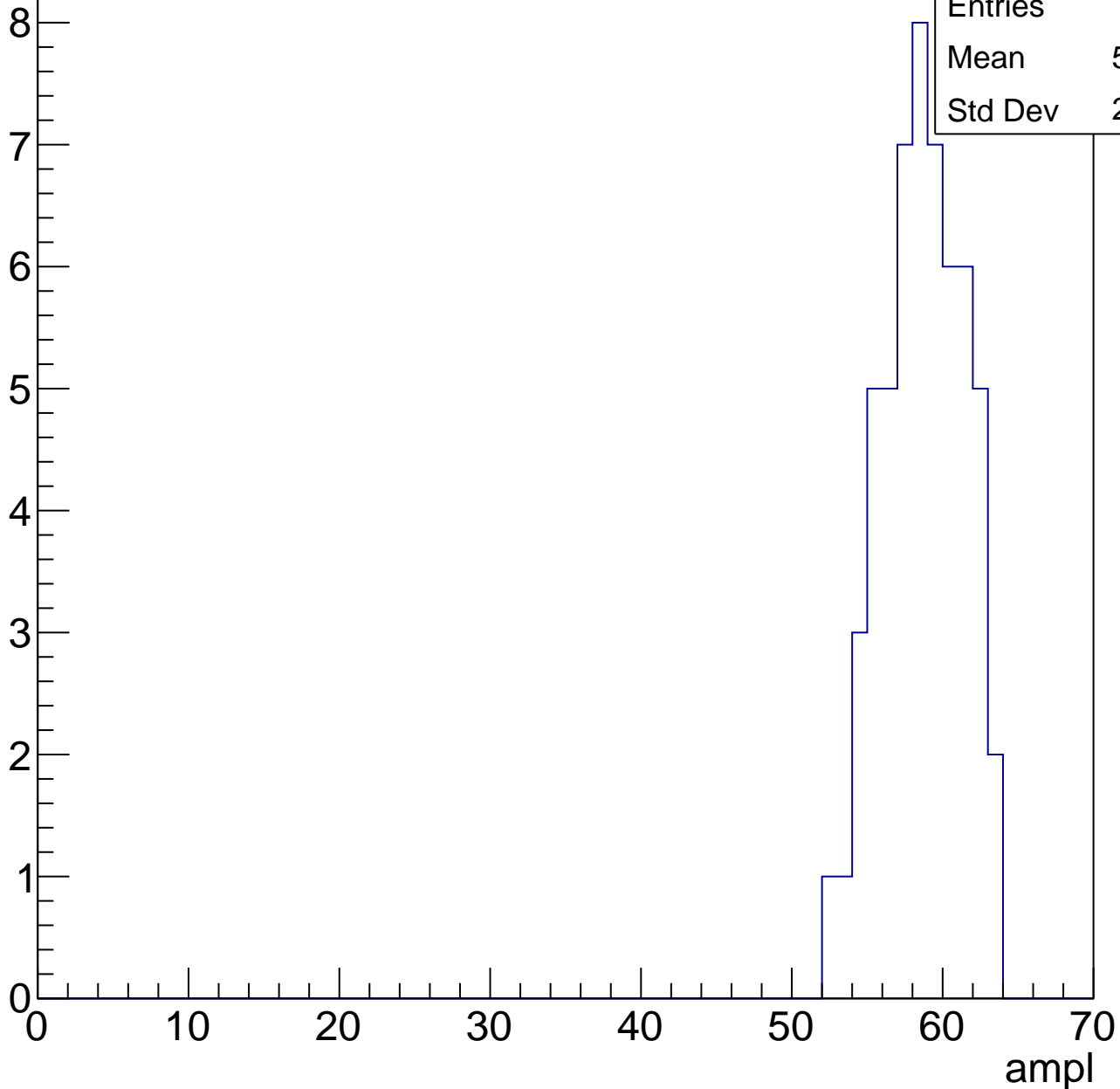


B1L102S, U2-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.21
Std Dev	2.651

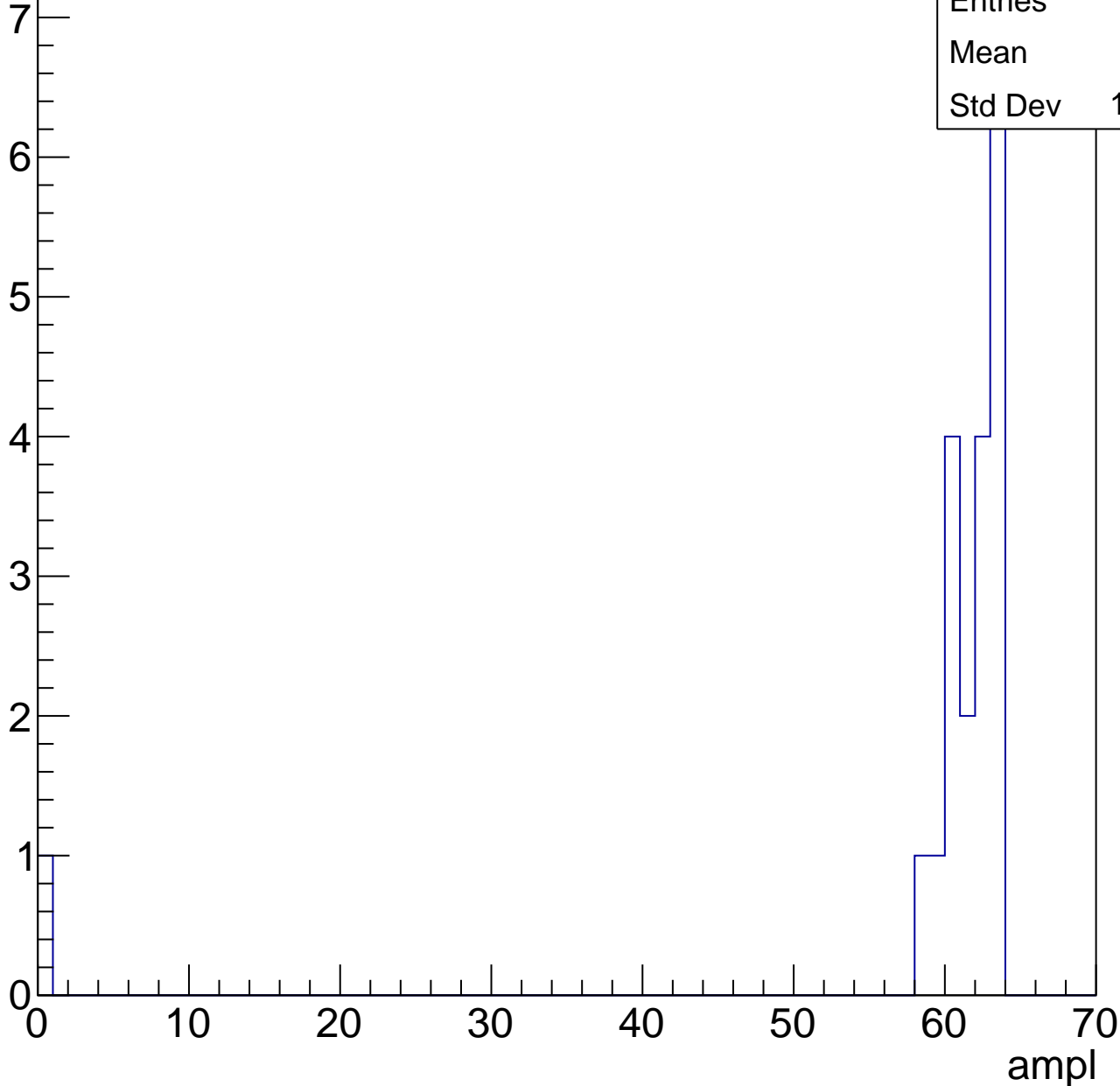


B1L102S, U2-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	58.4
Std Dev	13.48



B1L102S, U2-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch117, adc0

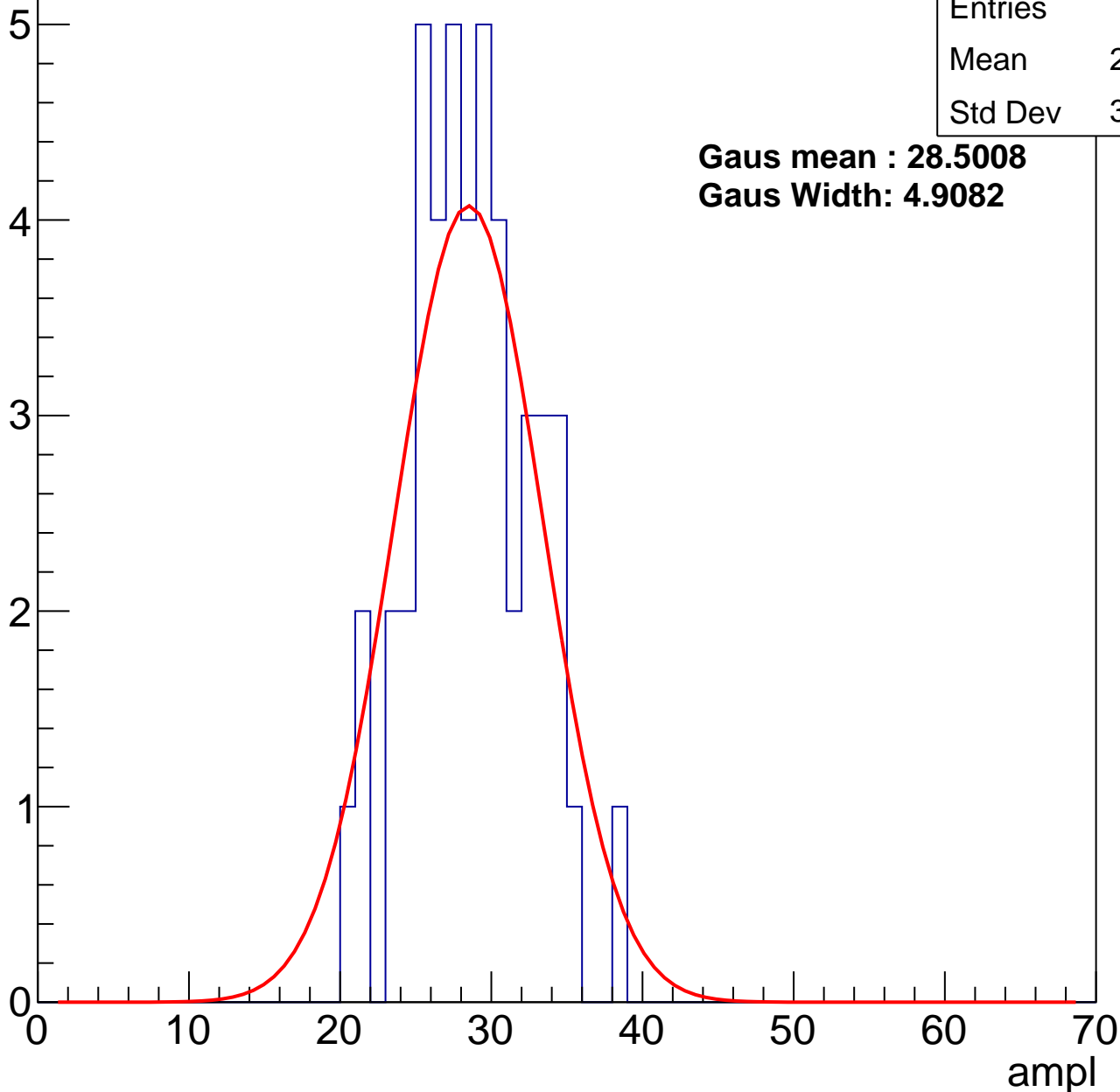
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	28.28
Std Dev	3.929

Gaus mean : 28.5008

Gaus Width: 4.9082



B1L102S, U2-ch117, adc1

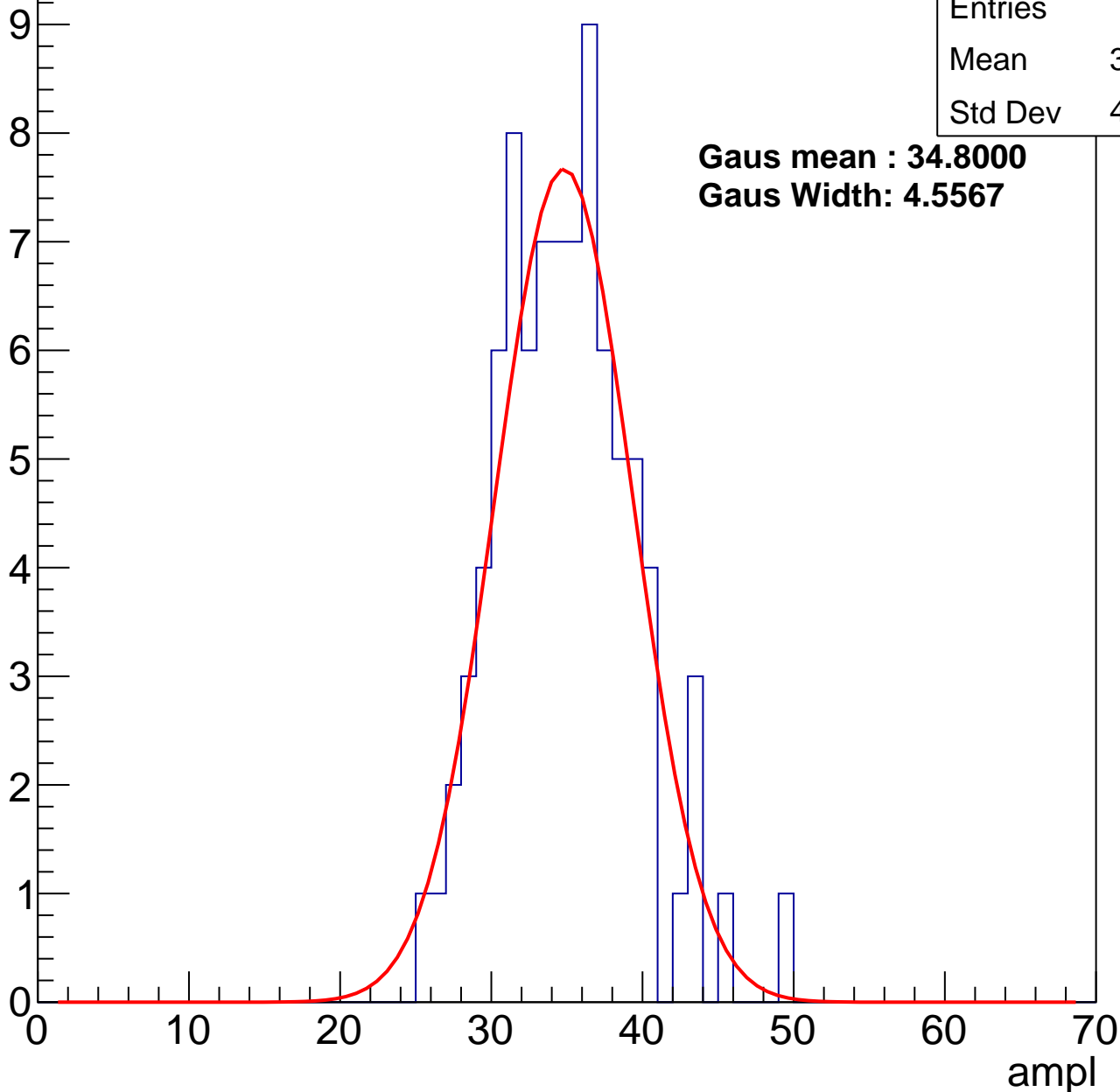
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	34.43
Std Dev	4.463

Gaus mean : 34.8000

Gaus Width: 4.5567



B1L102S, U2-ch117, adc2

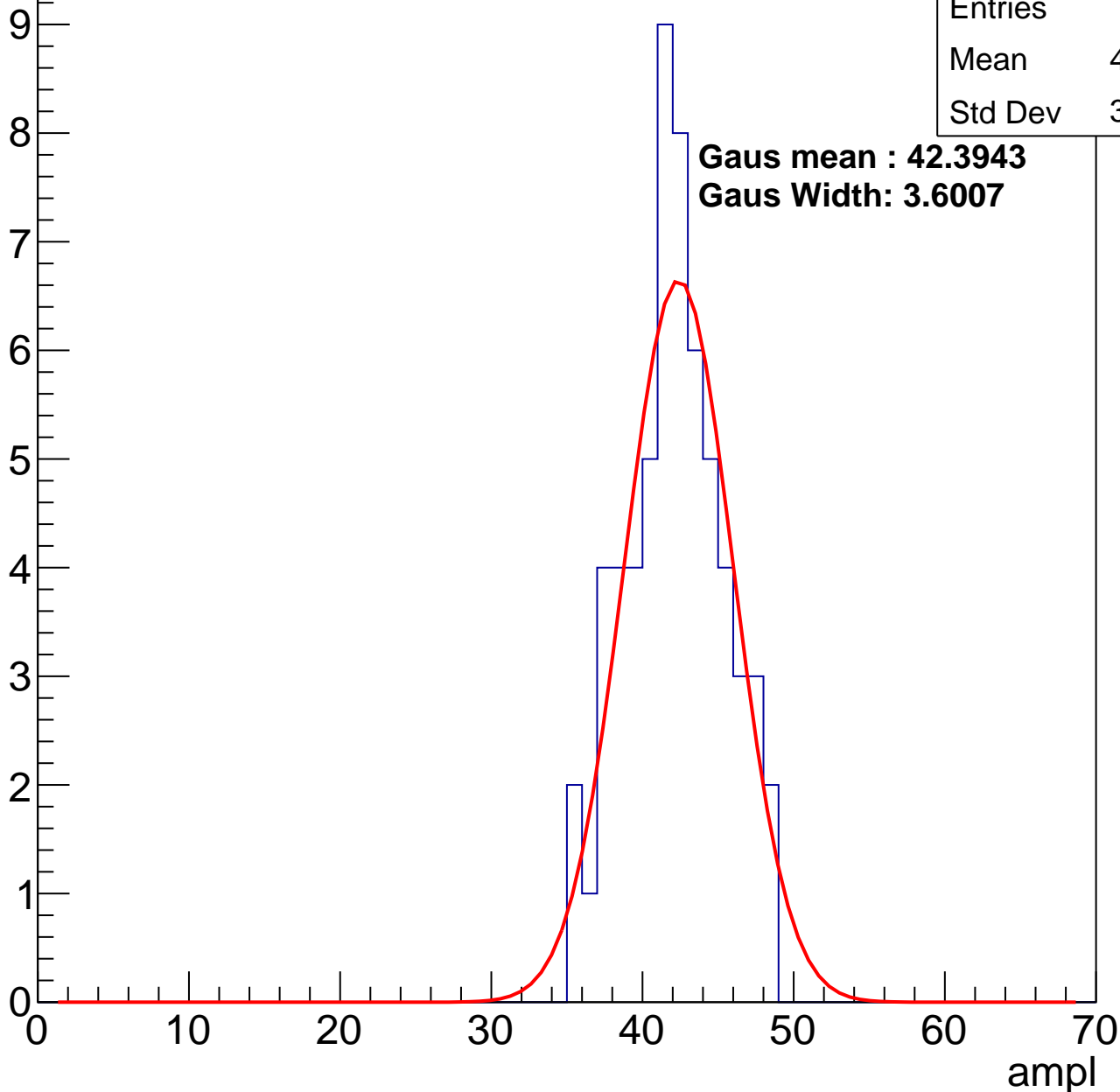
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.67
Std Dev	3.197

Gaus mean : 42.3943

Gaus Width: 3.6007

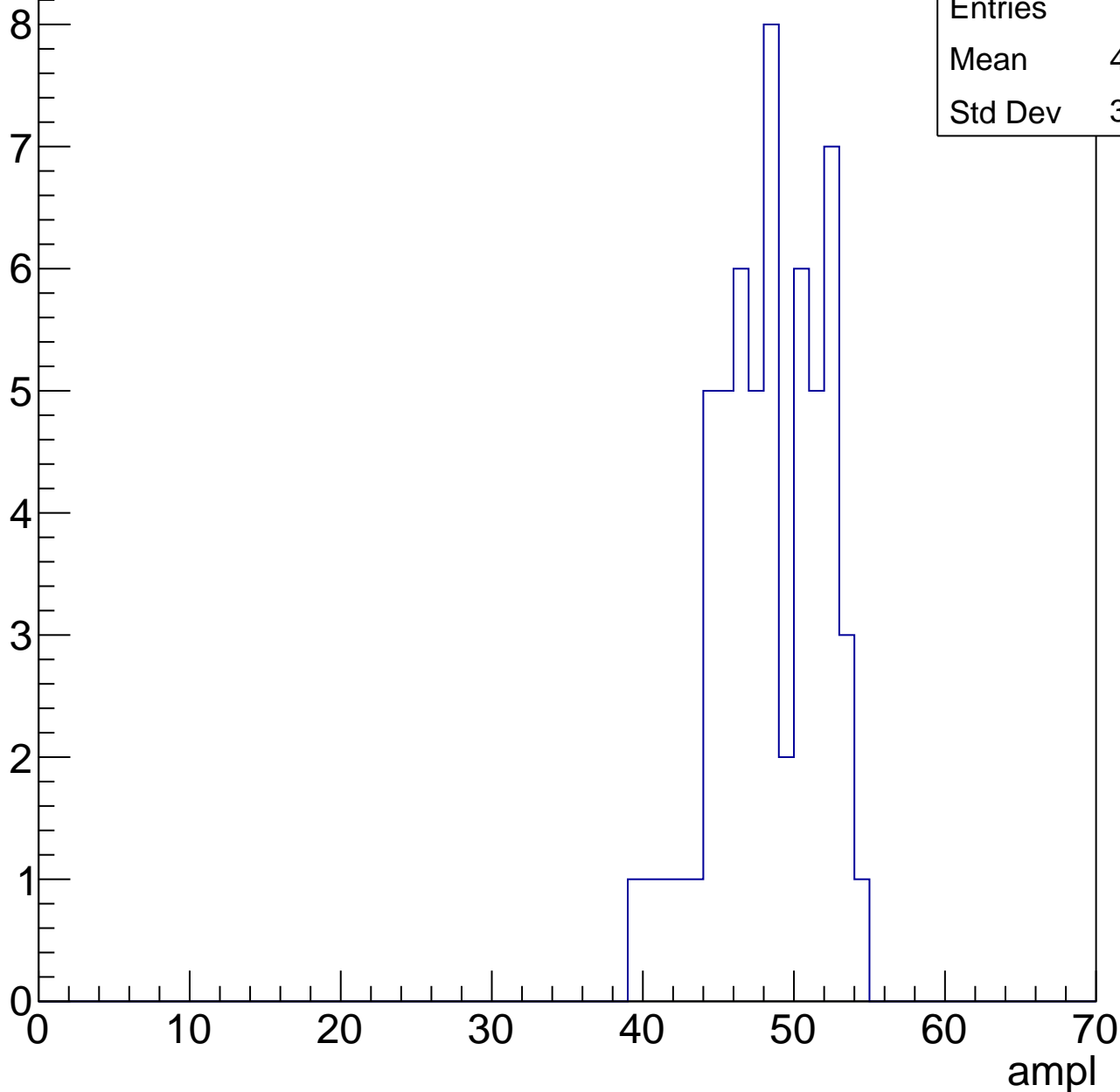


B1L102S, U2-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.84
Std Dev	3.478

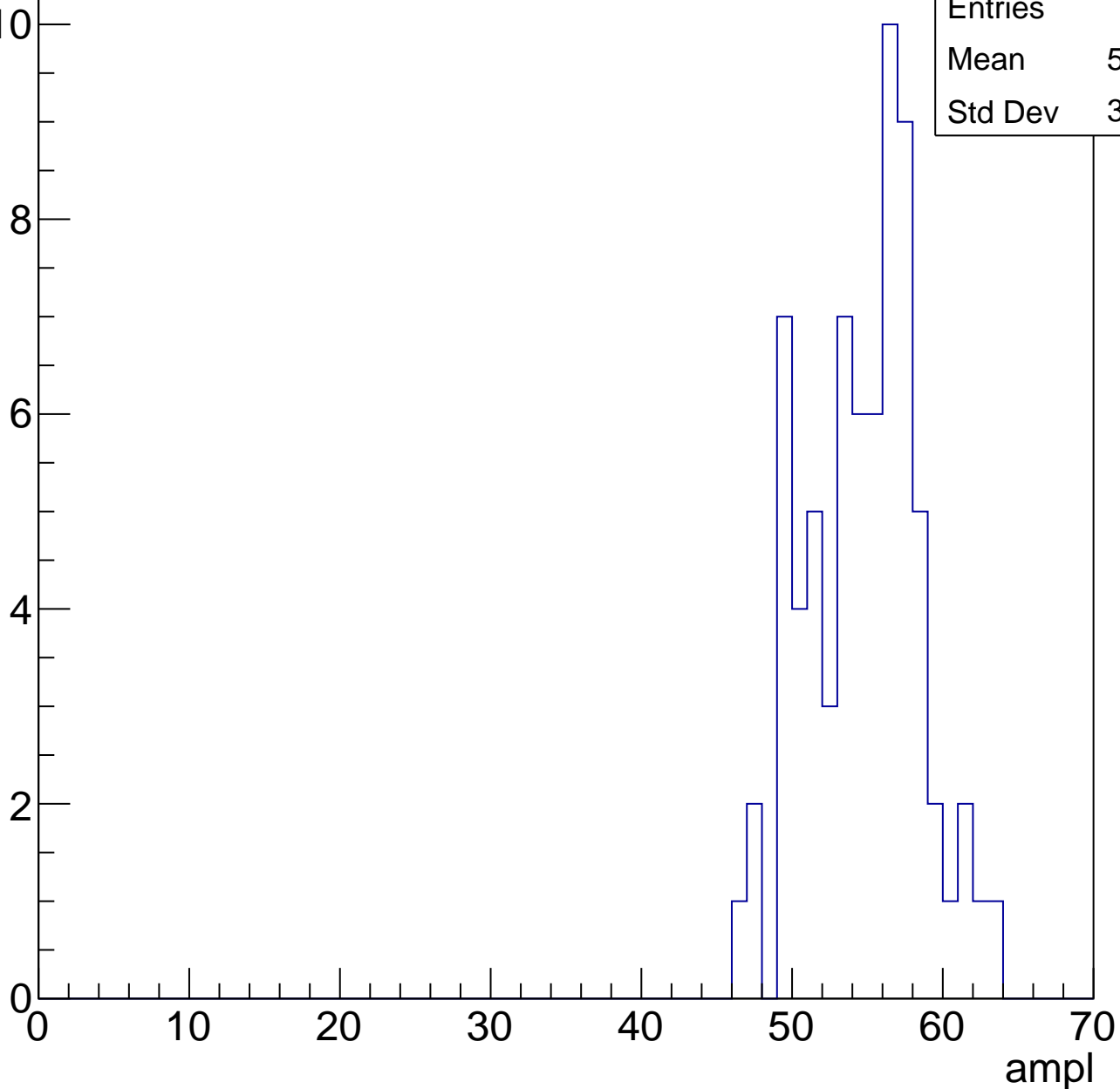


B1L102S, U2-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	54.26
Std Dev	3.719

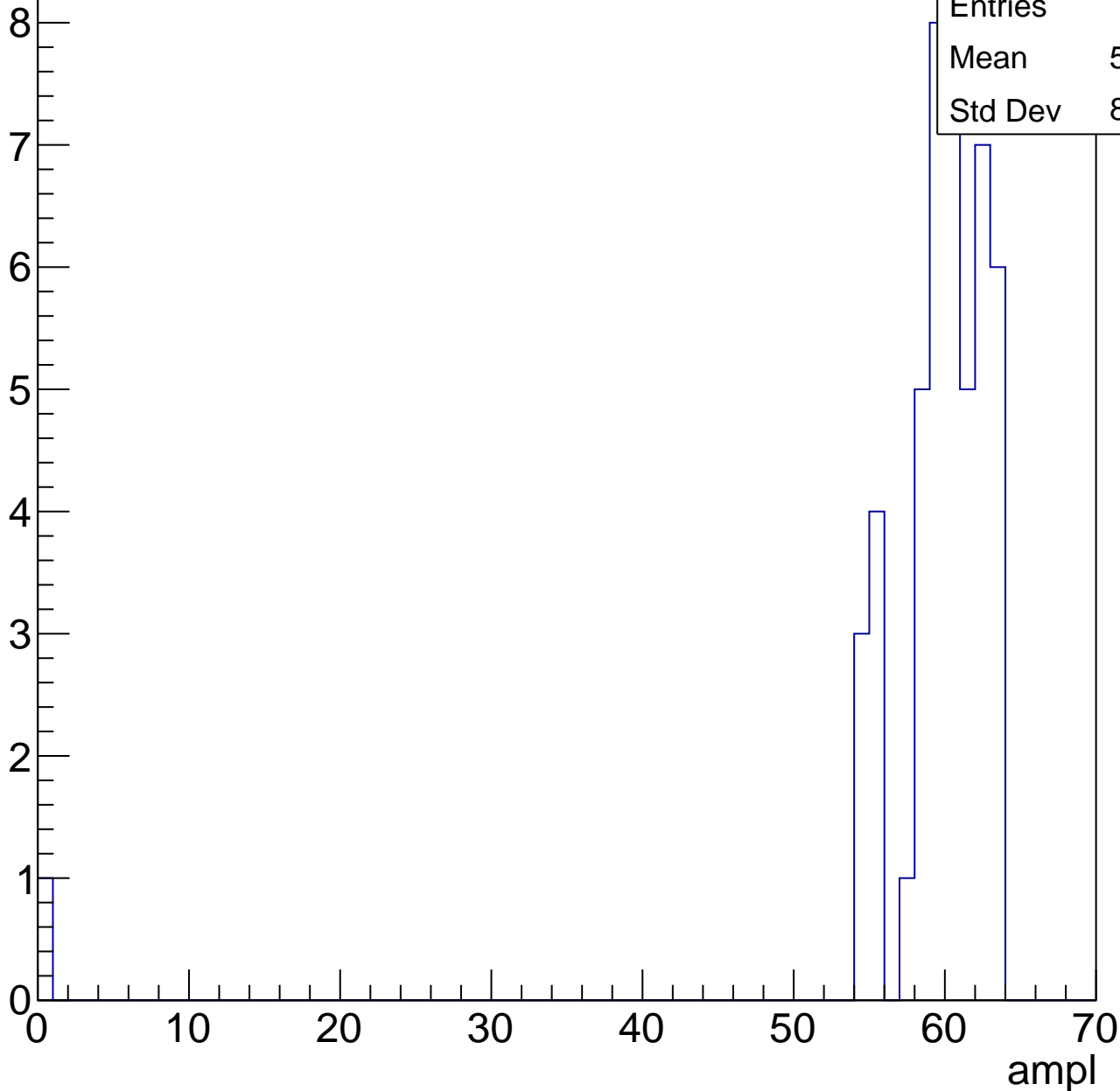


B1L102S, U2-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

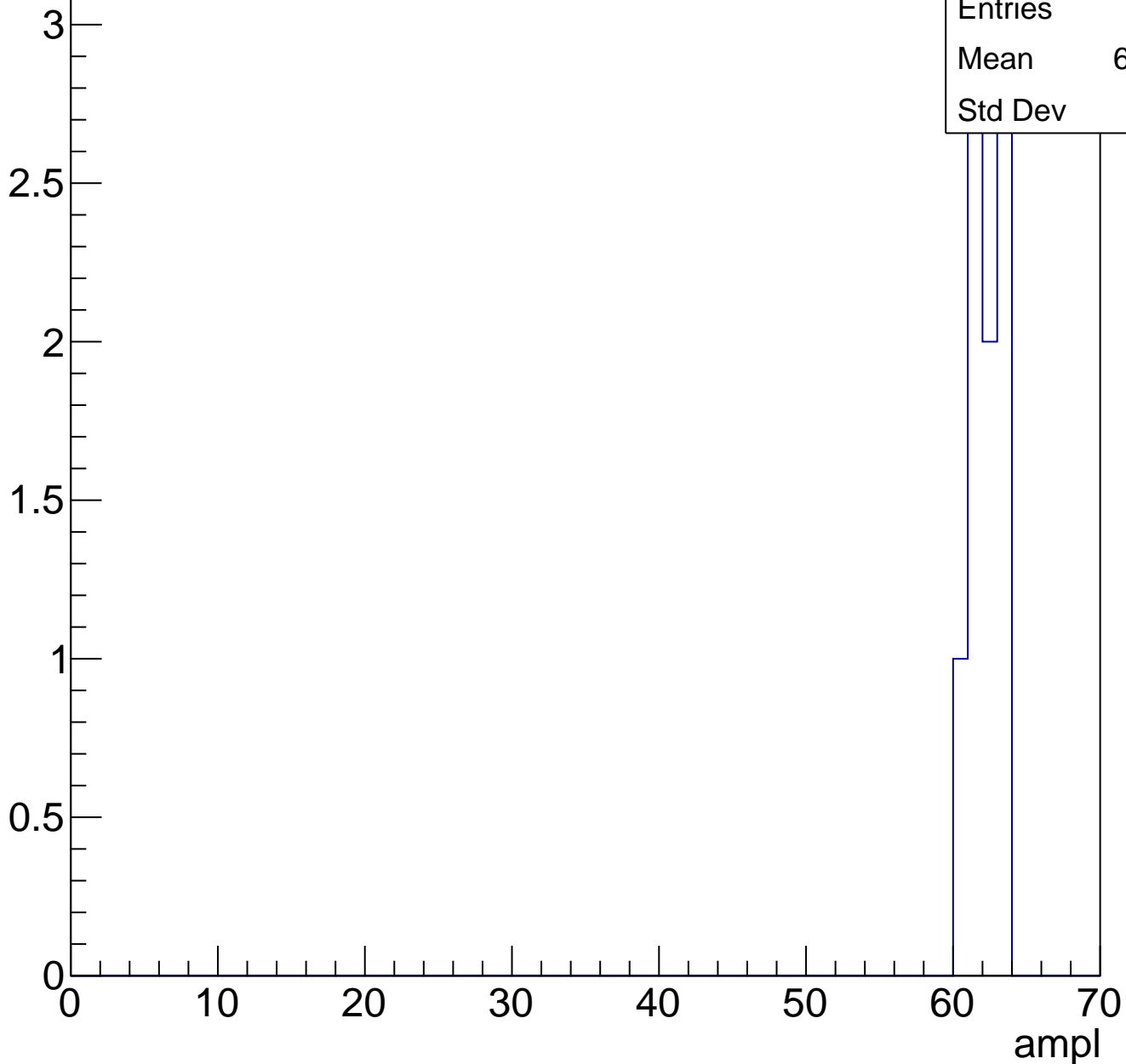
Entries	48
Mean	58.29
Std Dev	8.888



B1L102S, U2-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	9
Mean	61.78
Std Dev	1.03

B1L102S, U2-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch118, adc0

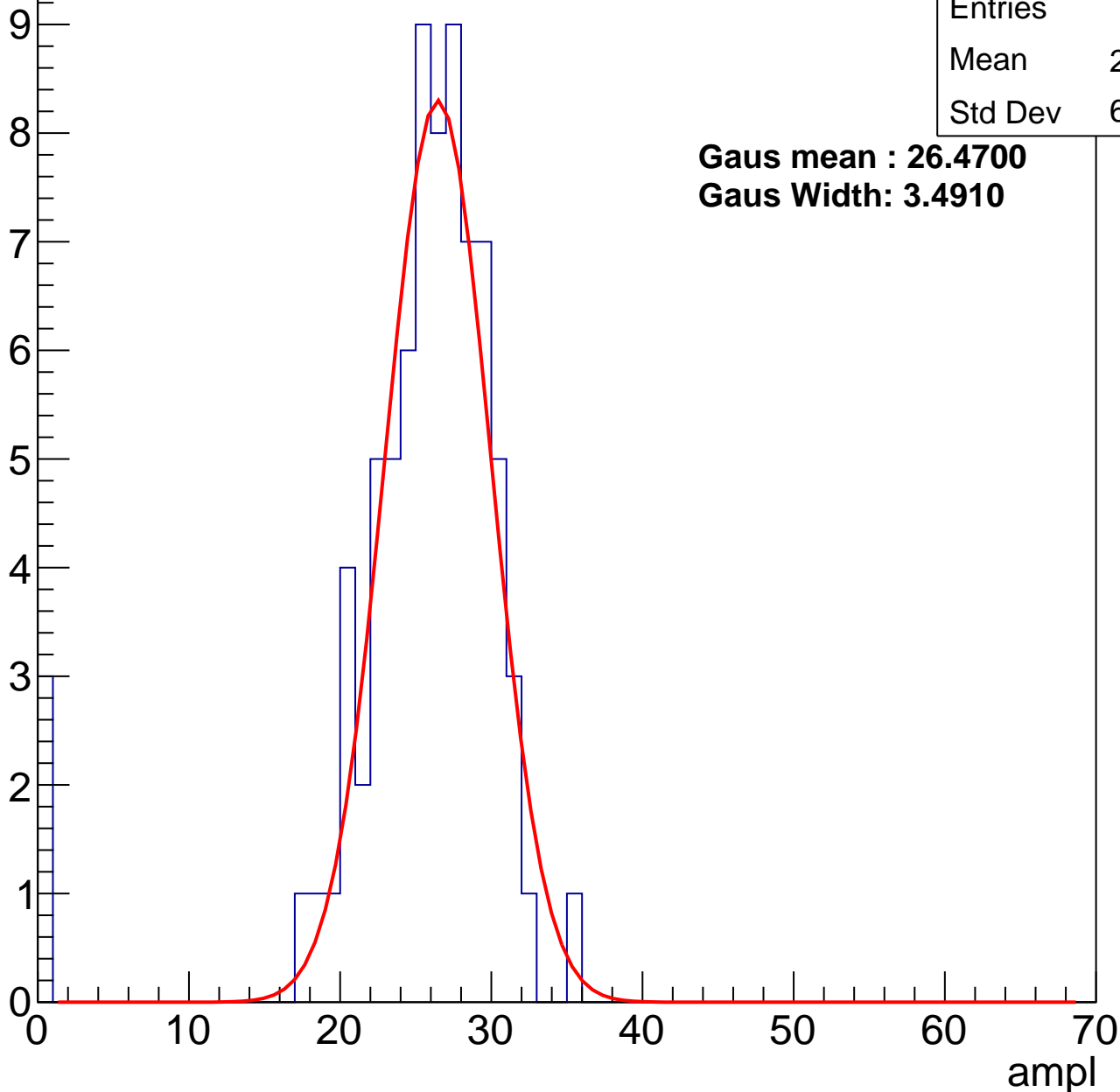
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	24.74
Std Dev	6.016

Gaus mean : 26.4700

Gaus Width: 3.4910



B1L102S, U2-ch118, adc1

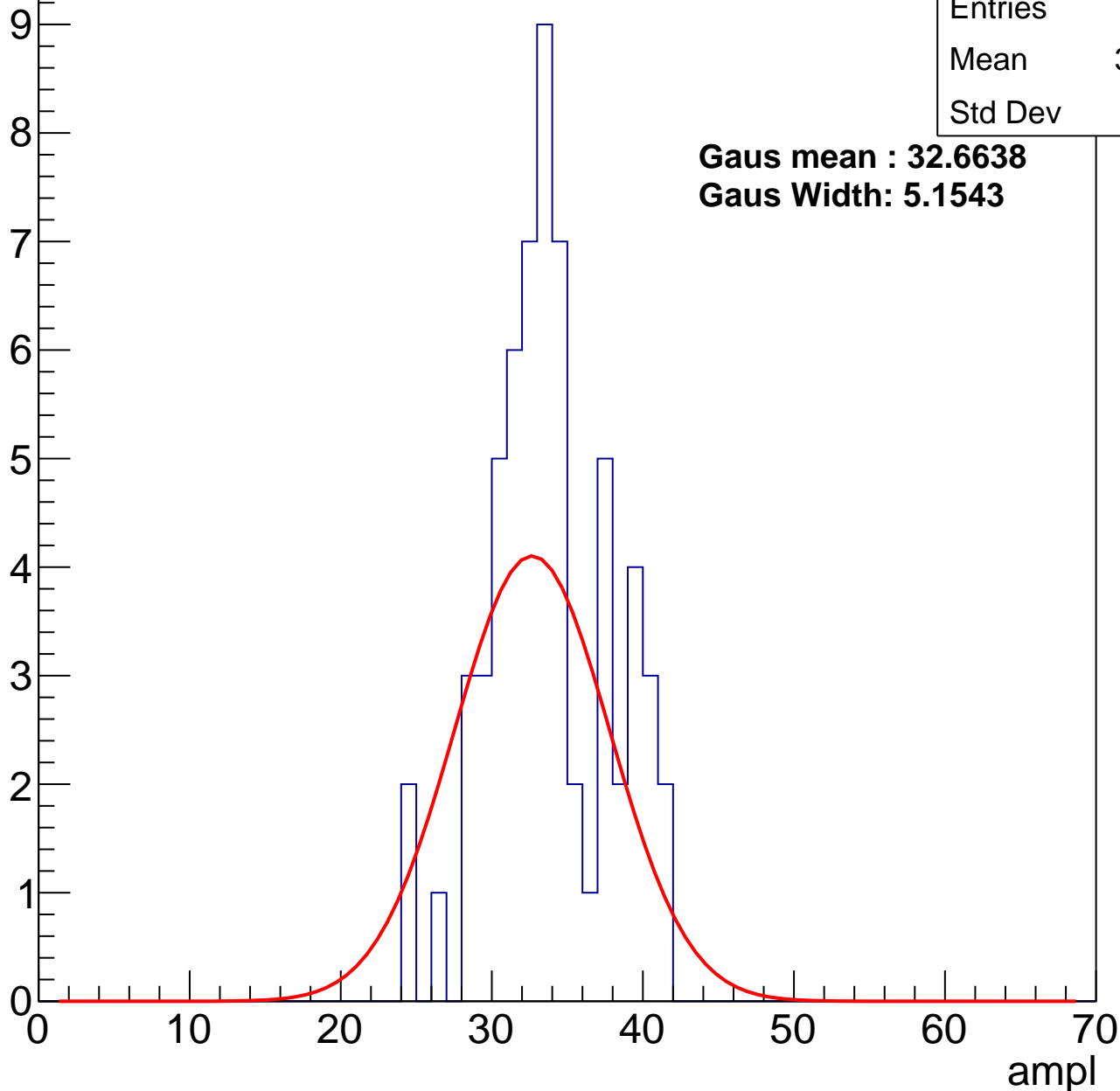
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.31
Std Dev	3.97

Gaus mean : 32.6638

Gaus Width: 5.1543



B1L102S, U2-ch118, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	39.3
Std Dev	3.712

Gaus mean : 39.8430

Gaus Width: 3.9753

10

8

6

4

2

0

0

10

20

30

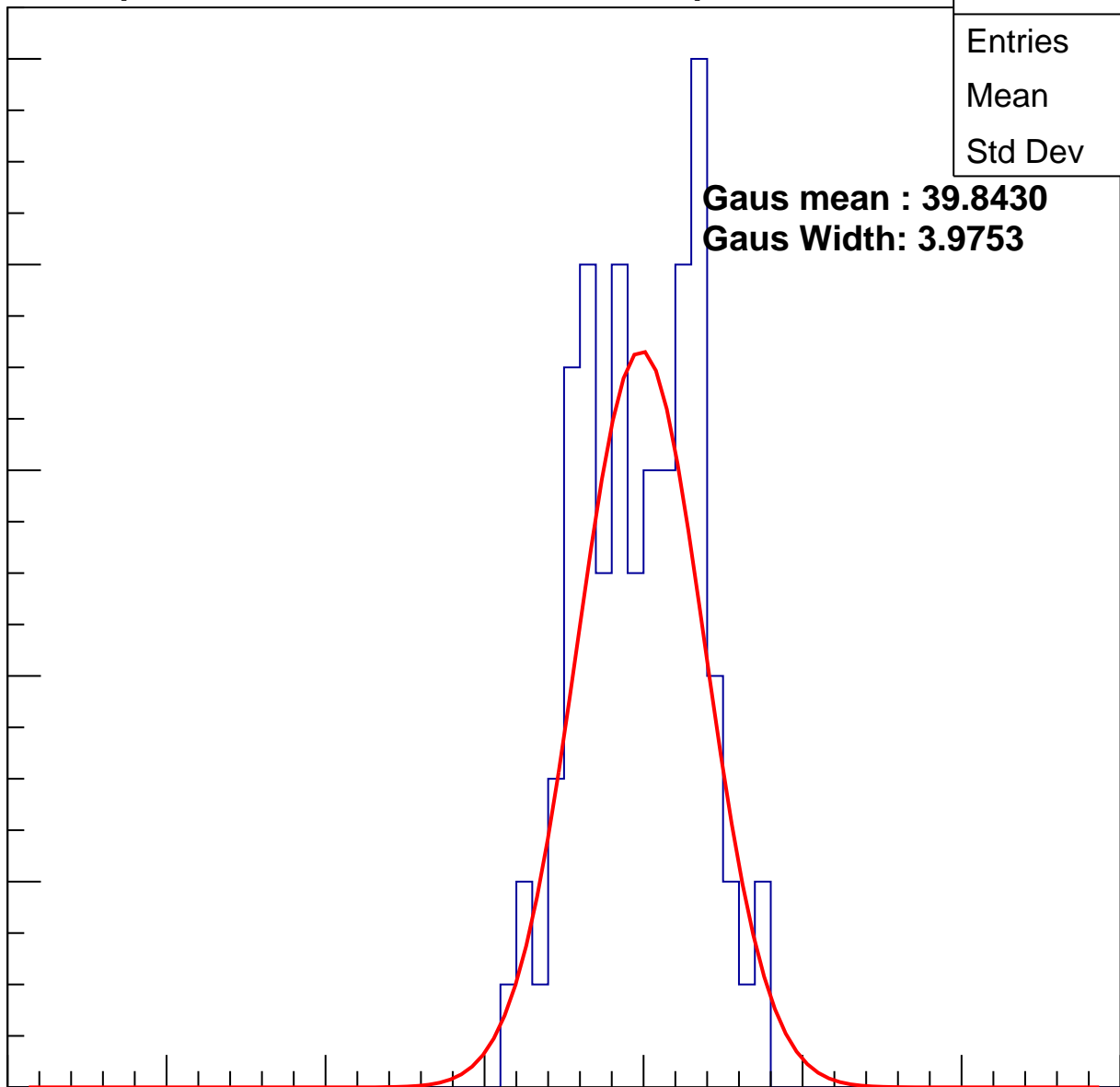
40

50

60

70

ampl

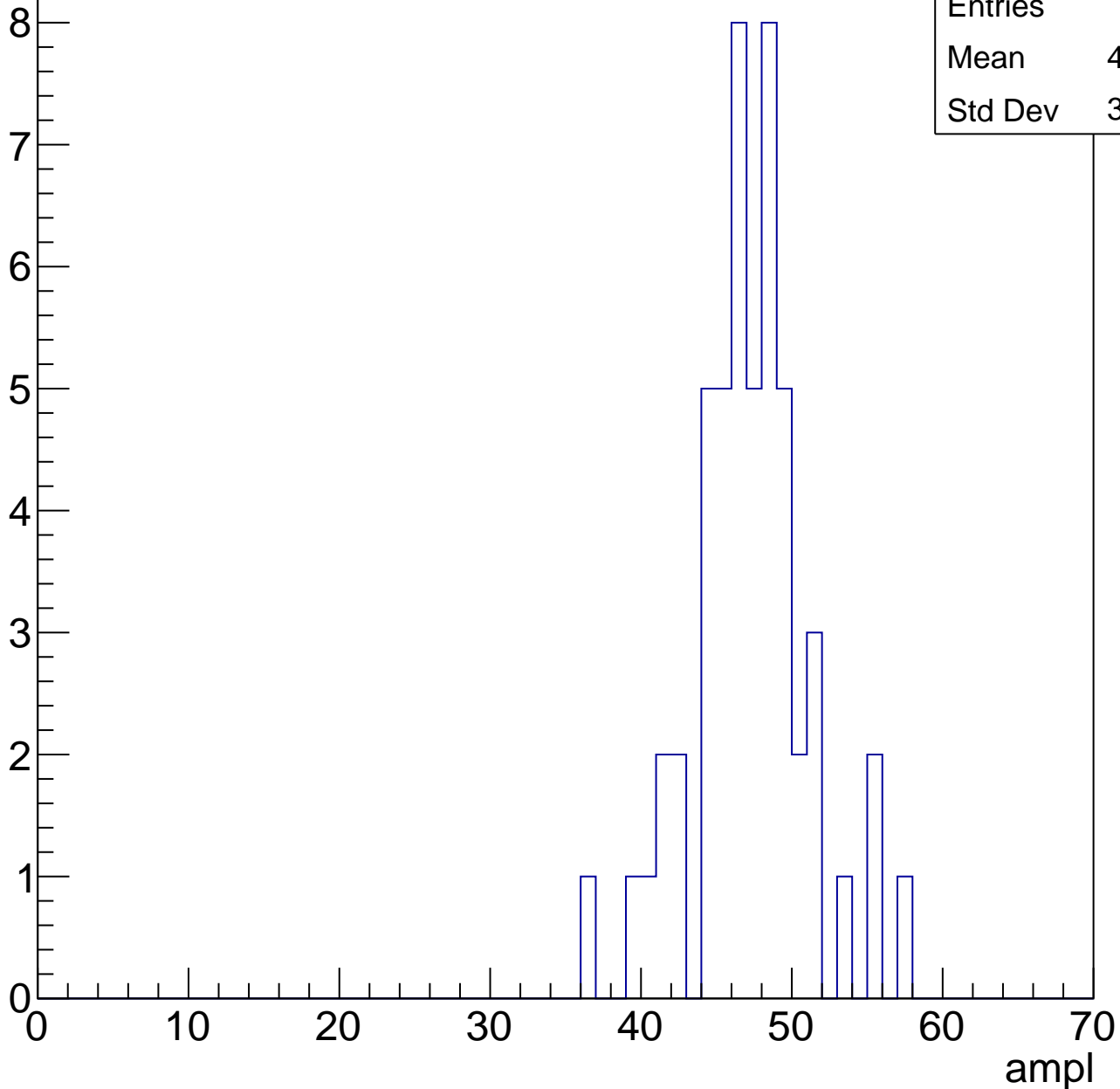


B1L102S, U2-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	46.75
Std Dev	3.882

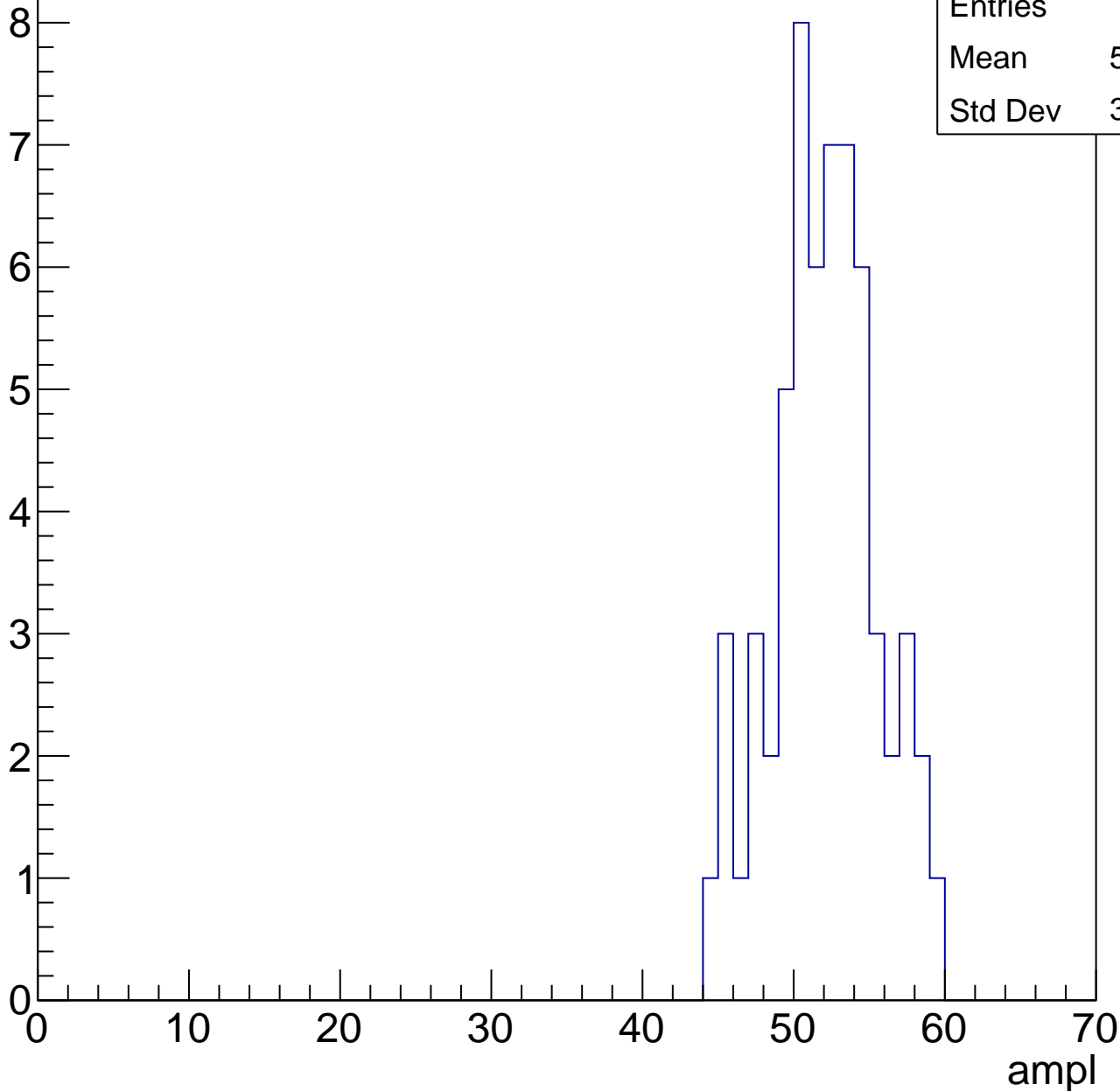


B1L102S, U2-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	51.58
Std Dev	3.446

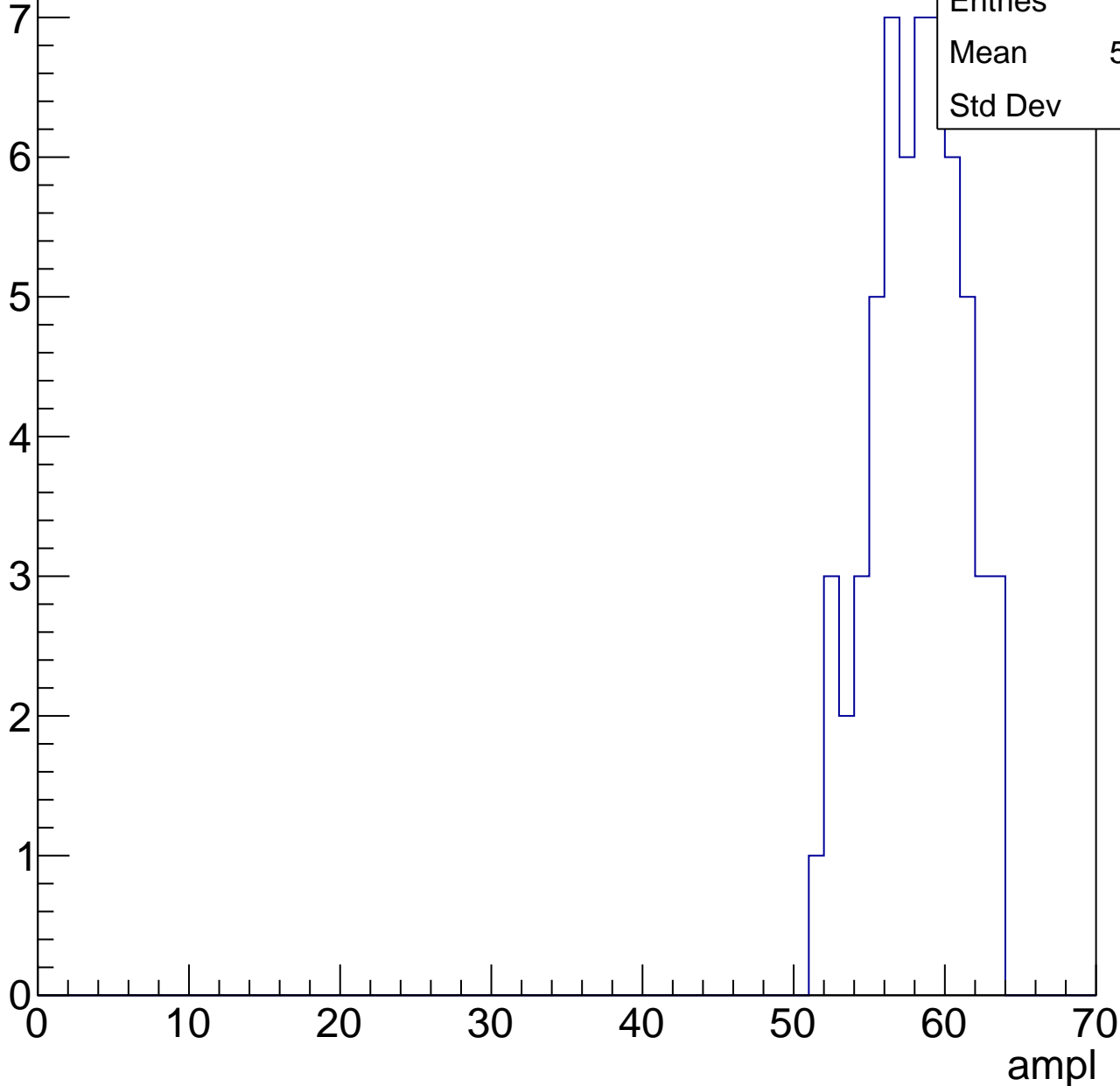


B1L102S, U2-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.64
Std Dev	3.01

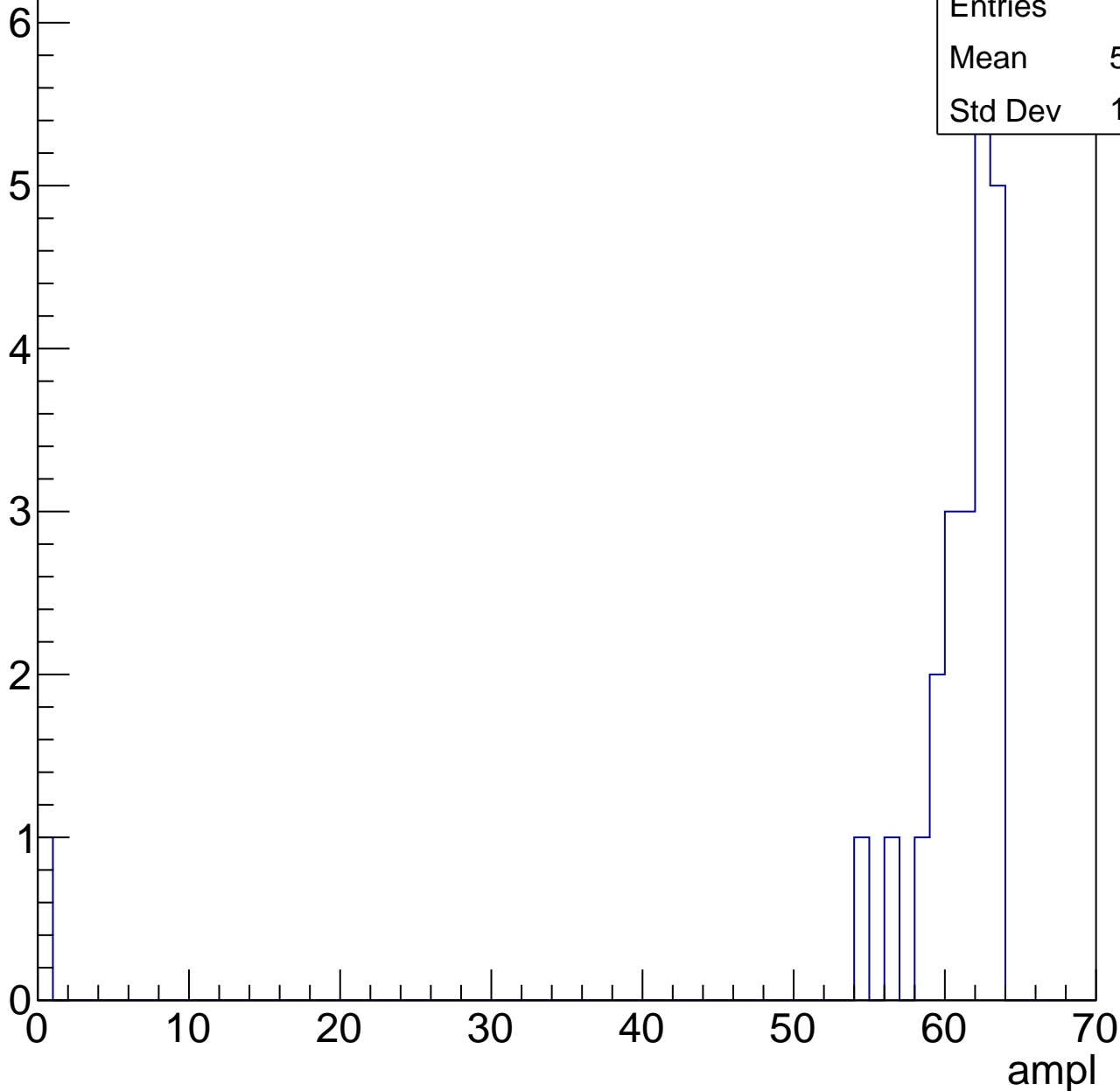


B1L102S, U2-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	58.09
Std Dev	12.59



B1L102S, U2-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

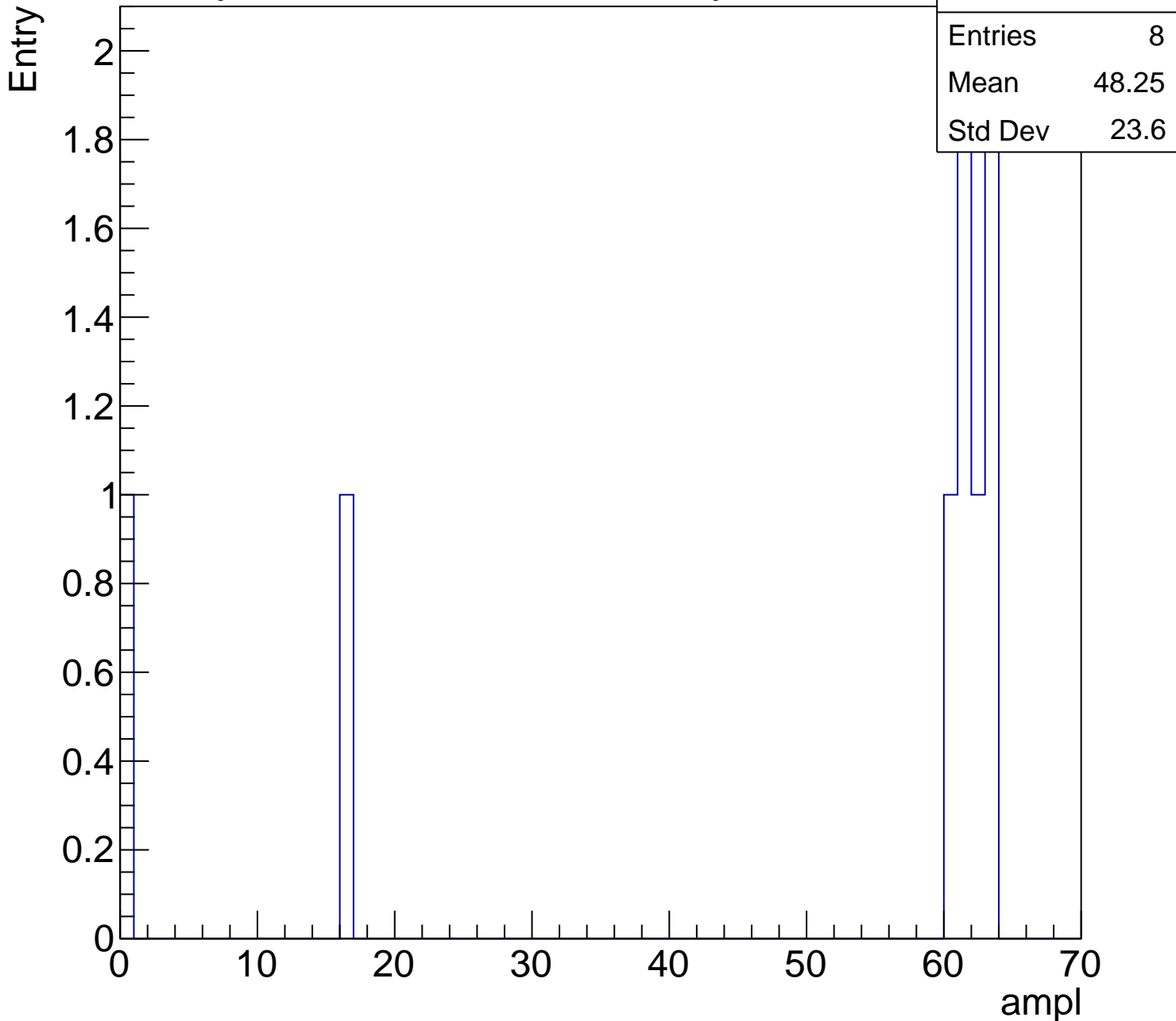
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	48.25
Std Dev	23.6

0 10 20 30 40 50 60 70

ampl



B1L102S, U2-ch119, adc0

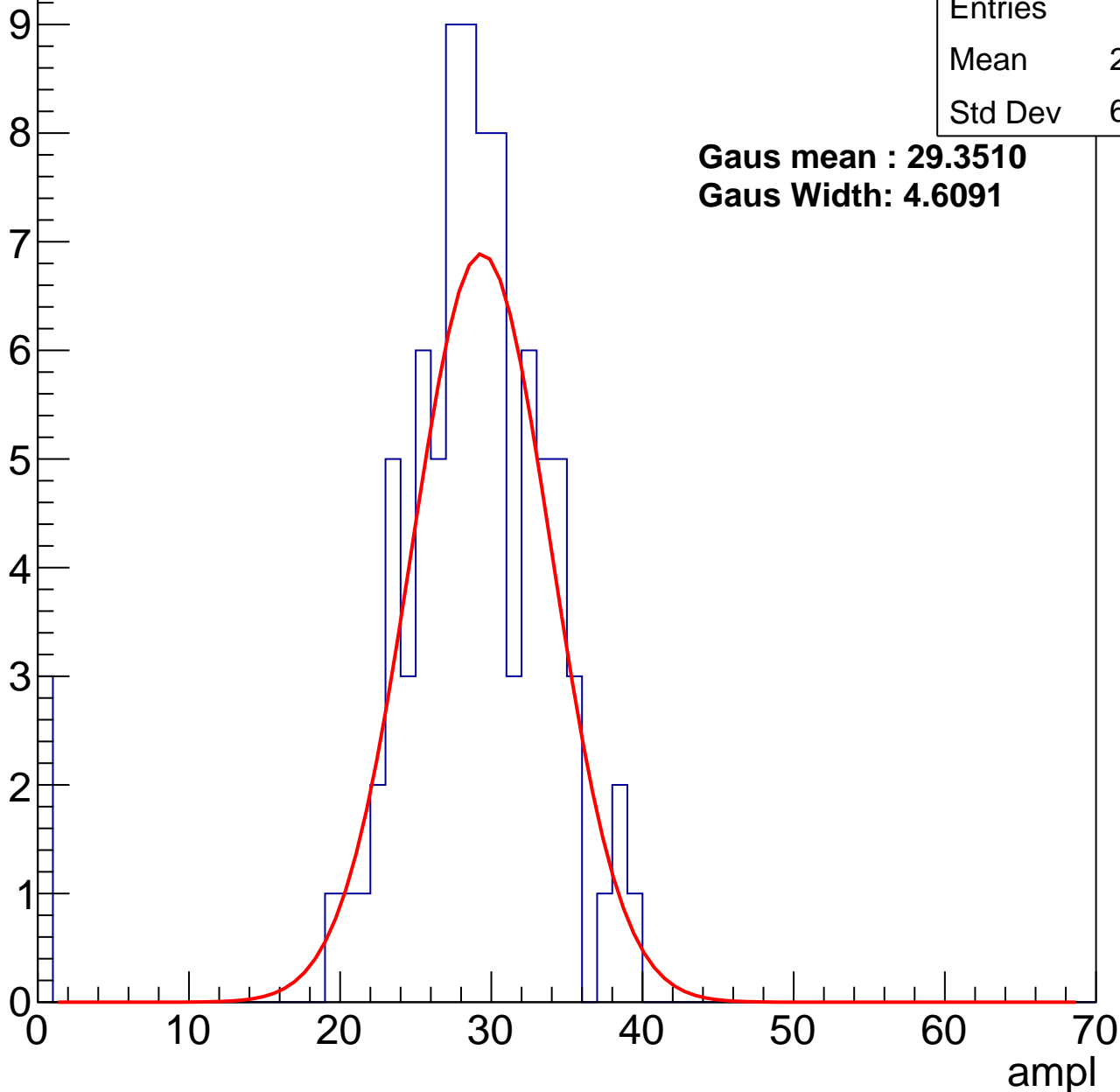
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	27.76
Std Dev	6.683

Gaus mean : 29.3510

Gaus Width: 4.6091



B1L102S, U2-ch119, adc1

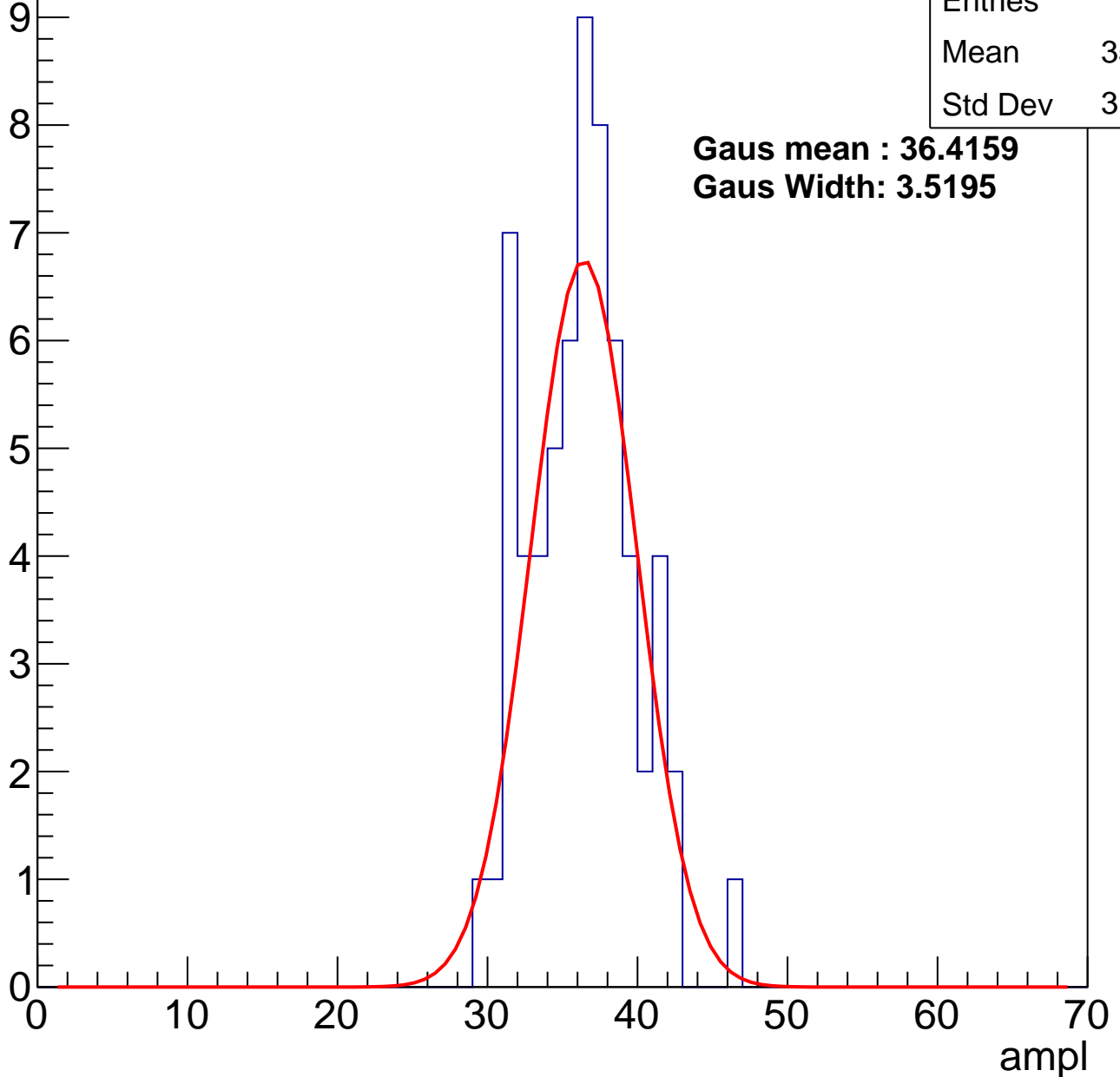
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.84
Std Dev	3.438

Gaus mean : 36.4159

Gaus Width: 3.5195



B1L102S, U2-ch119, adc2

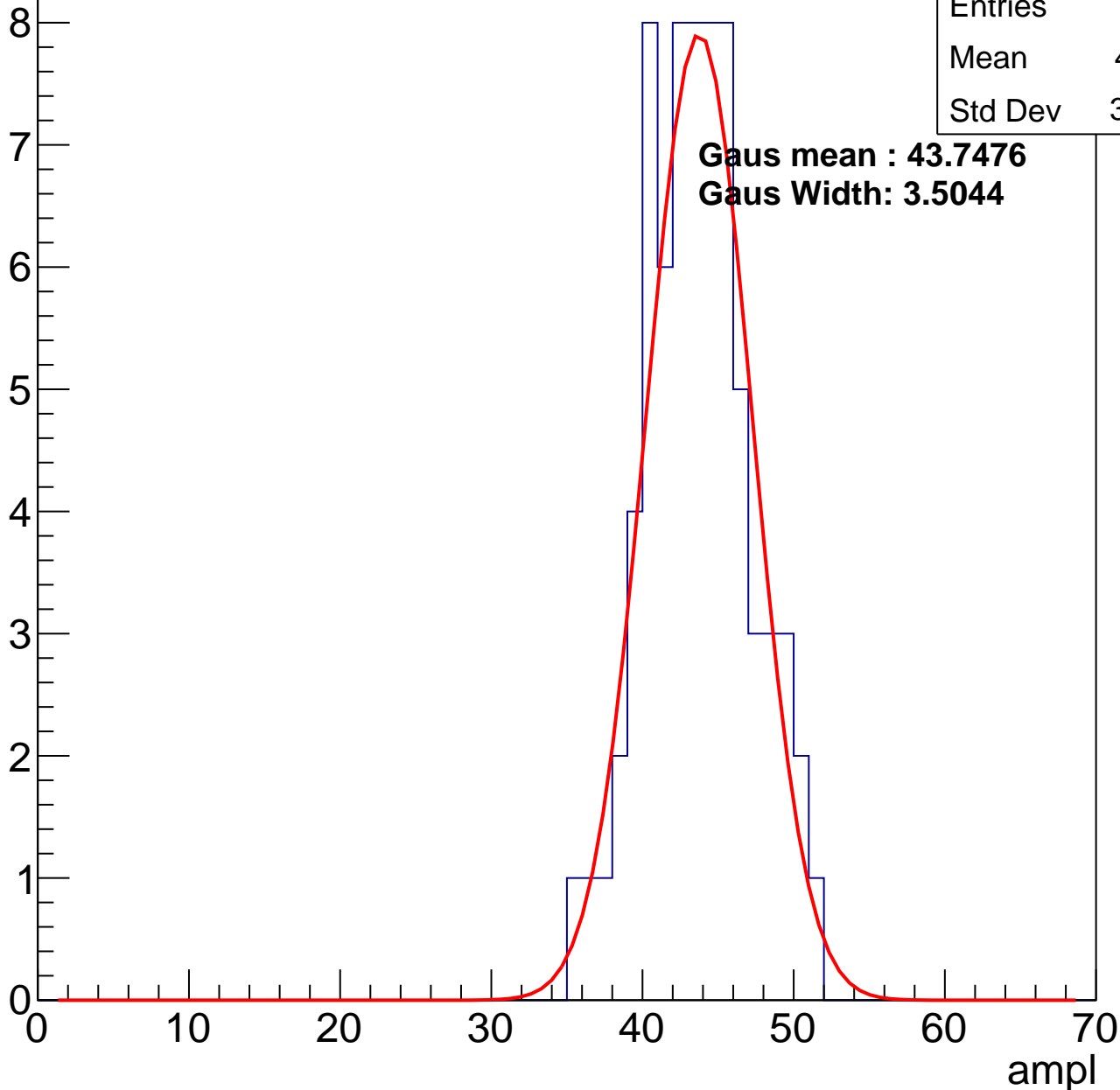
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	43.21
Std Dev	3.419

Gaus mean : 43.7476

Gaus Width: 3.5044

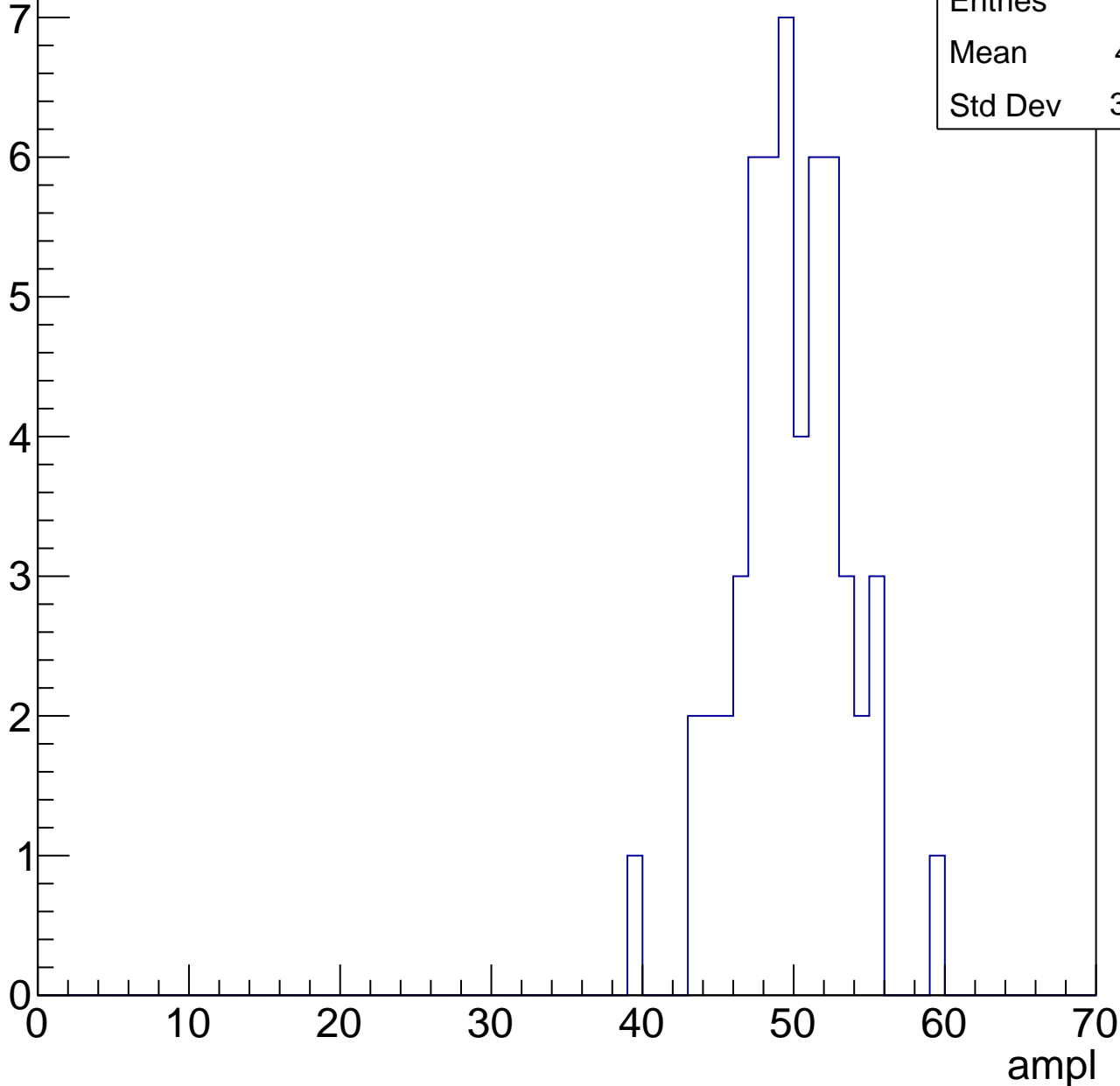


B1L102S, U2-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	49.31
Std Dev	3.584

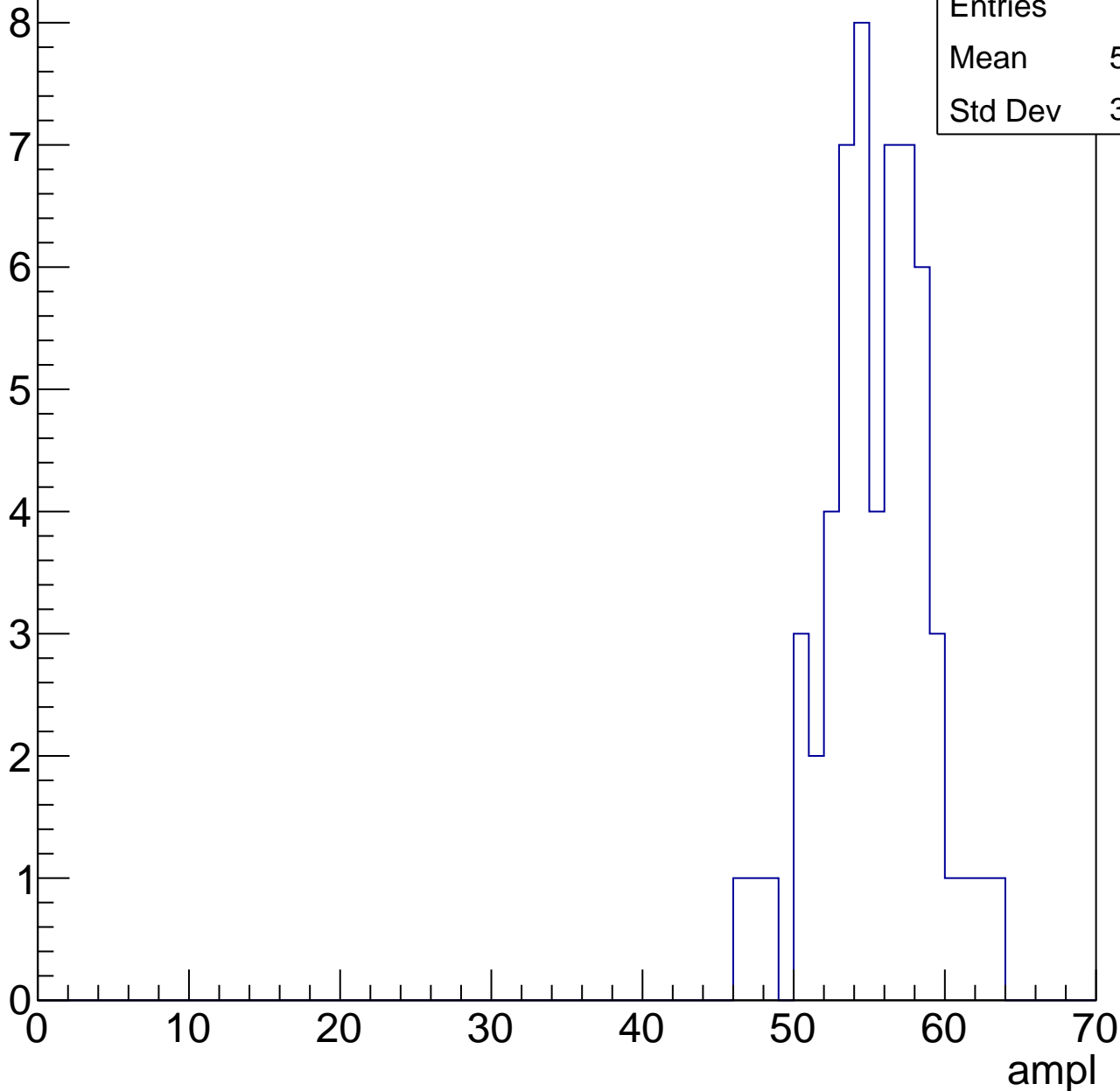


B1L102S, U2-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.93
Std Dev	3.433



B1L102S, U2-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

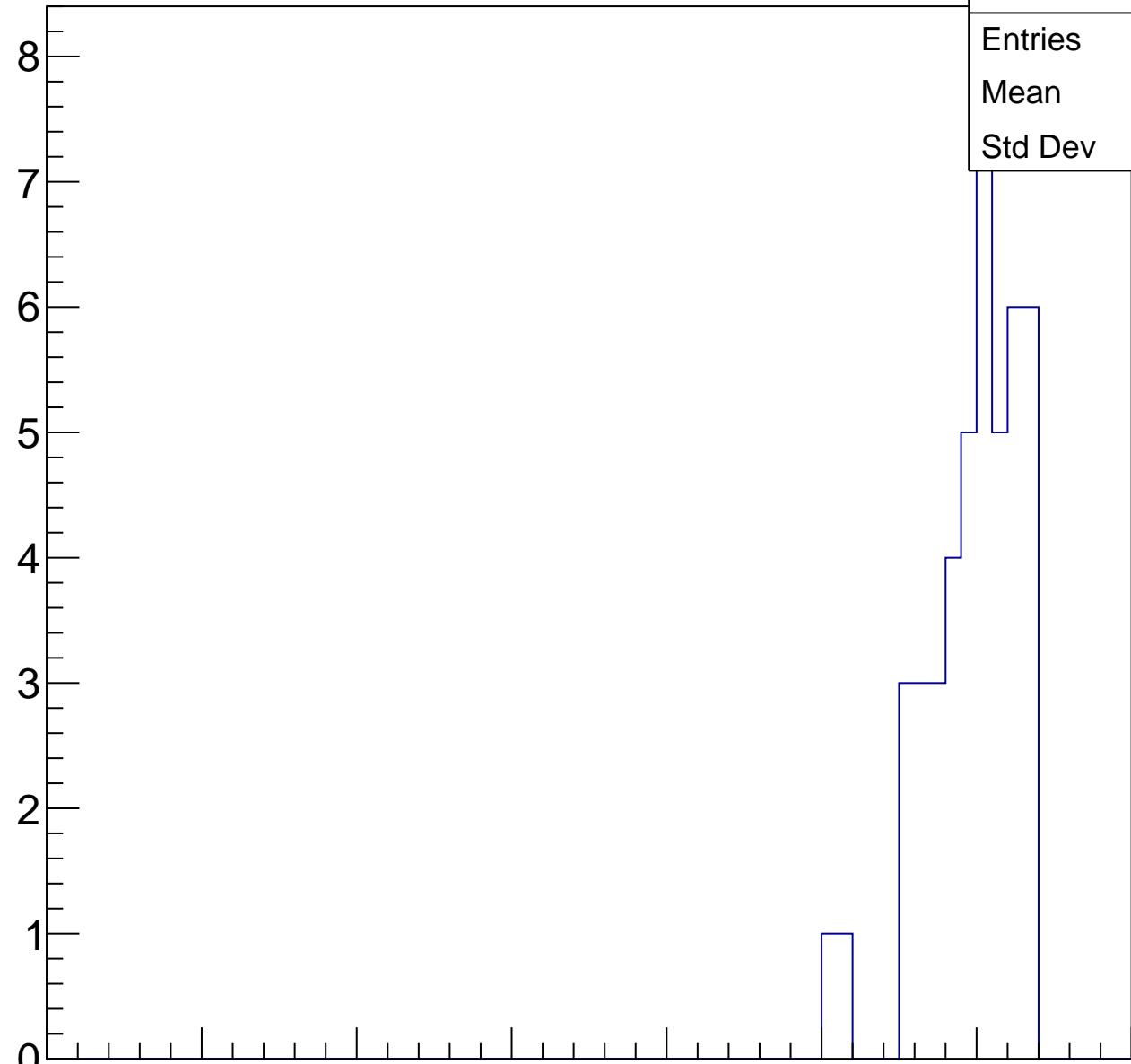
Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	59.27
Std Dev	3.021

ampl

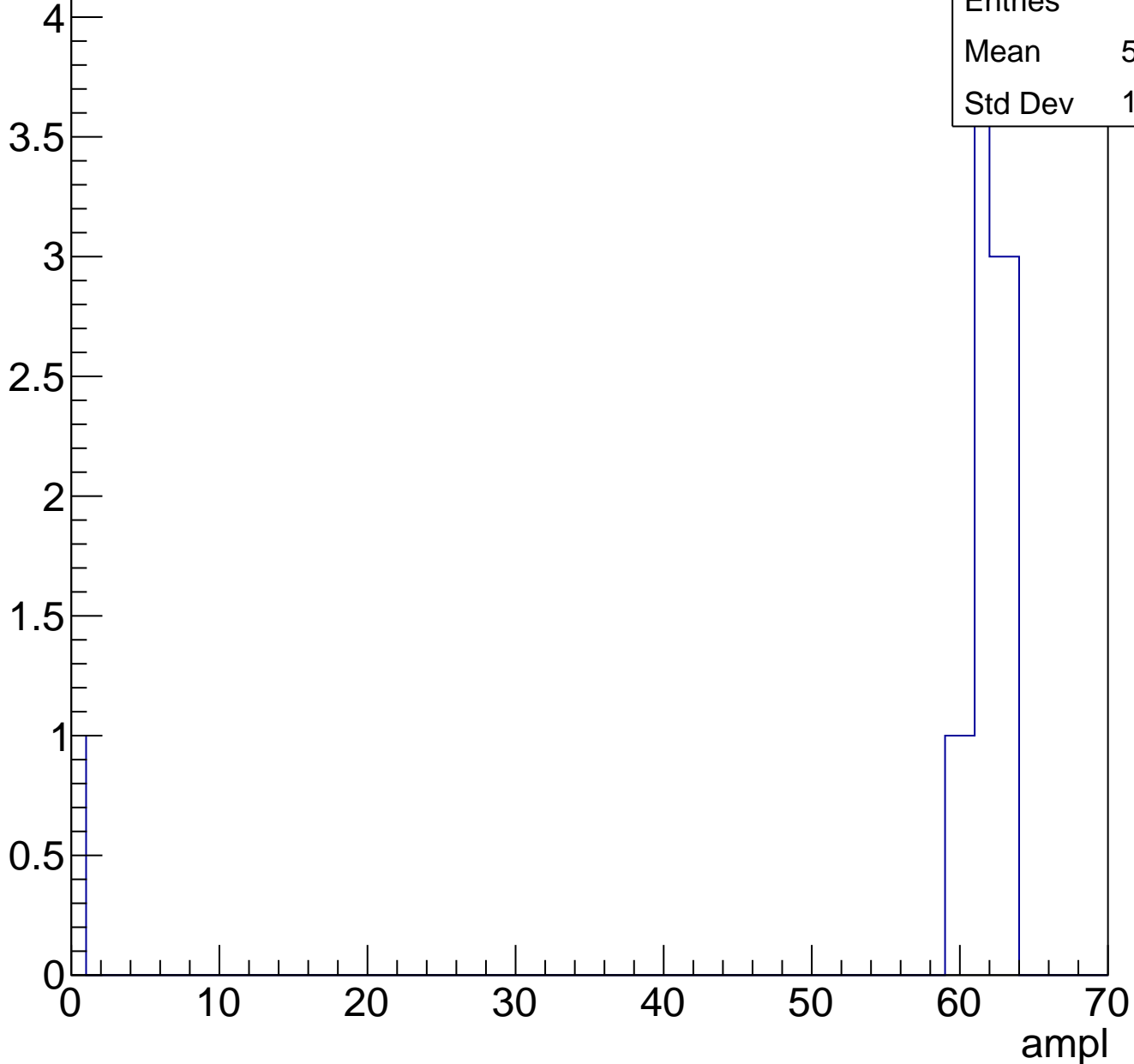
0 10 20 30 40 50 60 70



B1L102S, U2-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

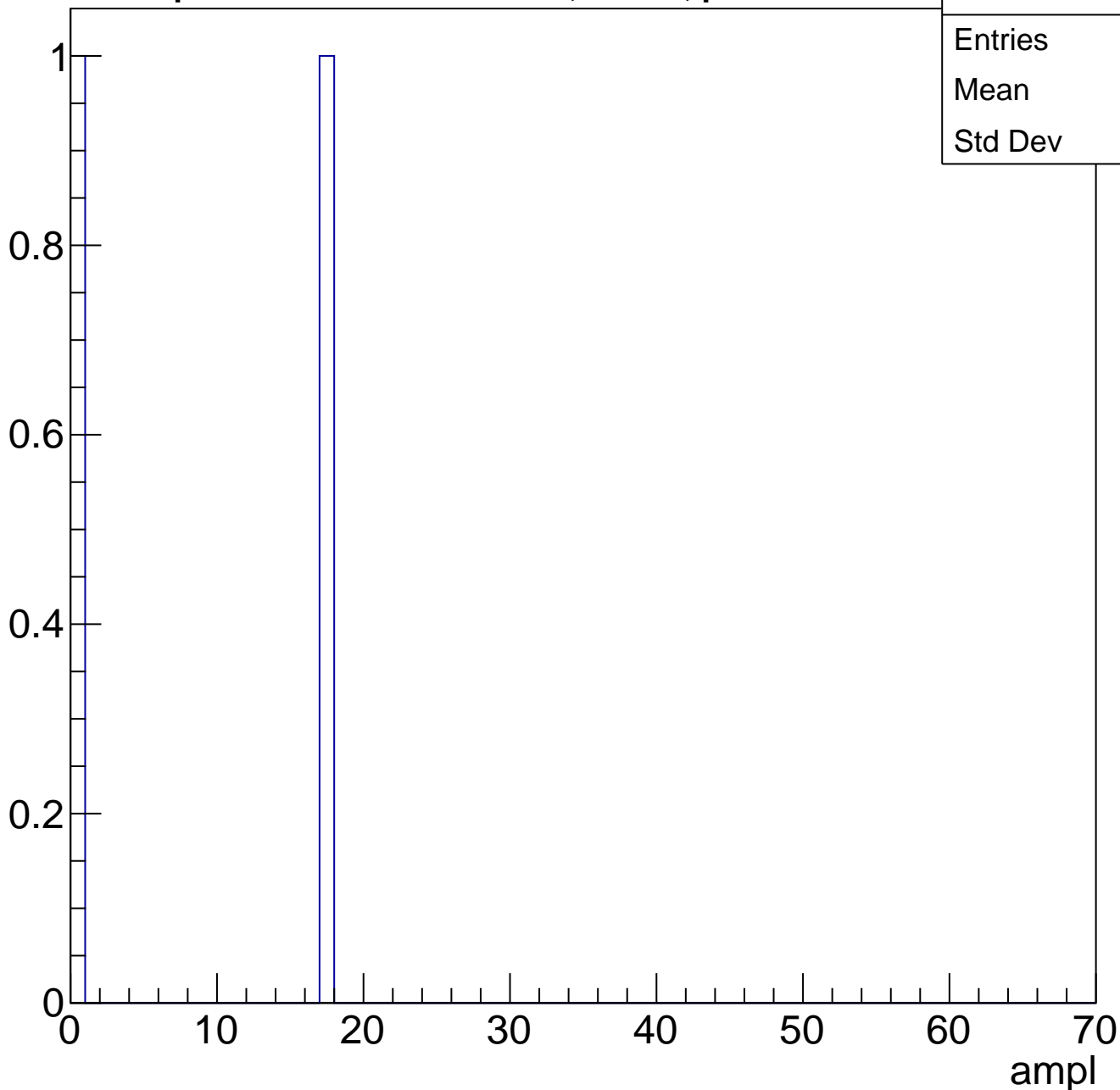
Entry



B1L102S, U2-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	8.5
Std Dev	8.5

B1L102S, U2-ch120, adc0

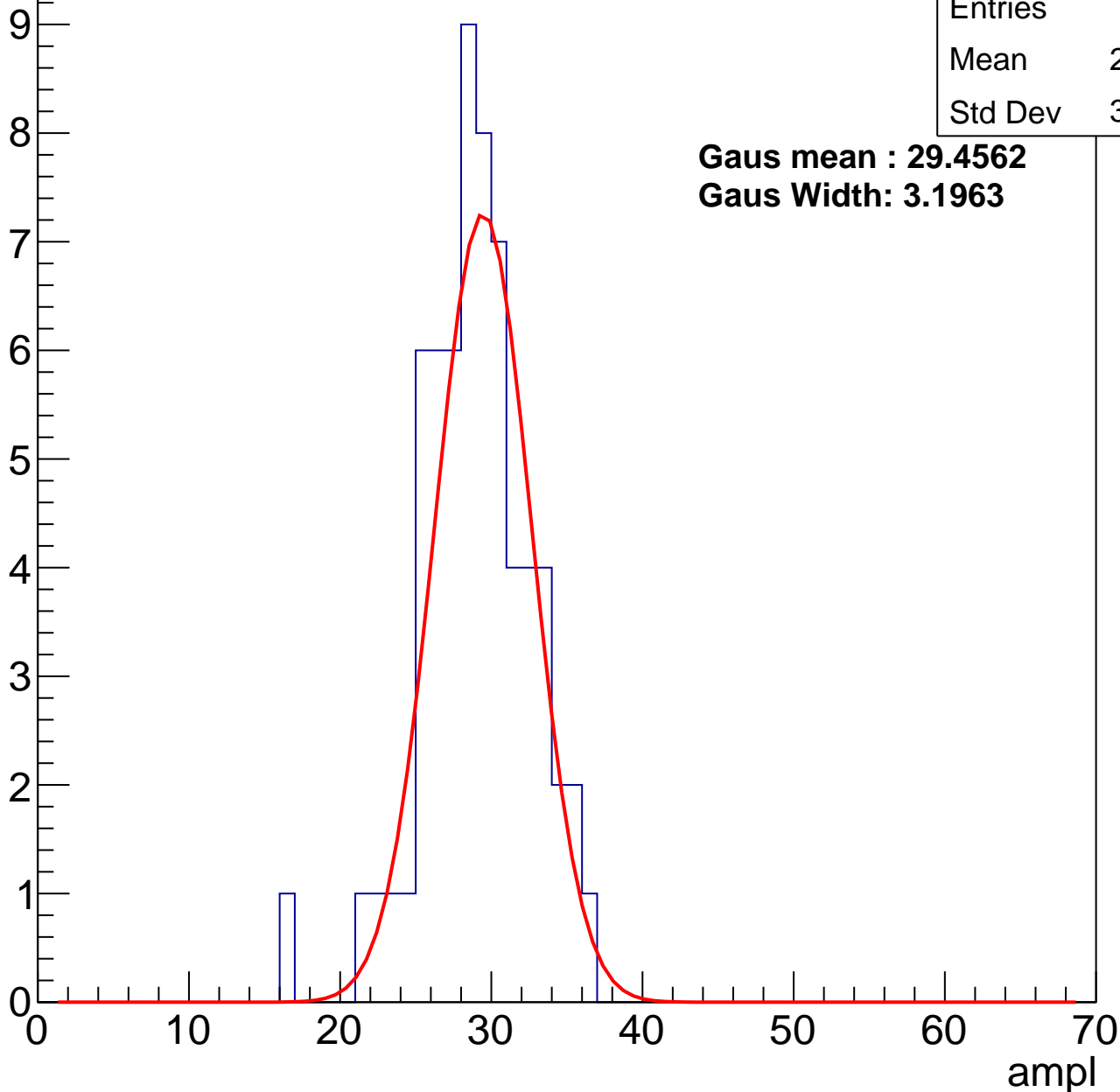
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.53
Std Dev	3.549

Gaus mean : 29.4562

Gaus Width: 3.1963



B1L102S, U2-ch120, adc1

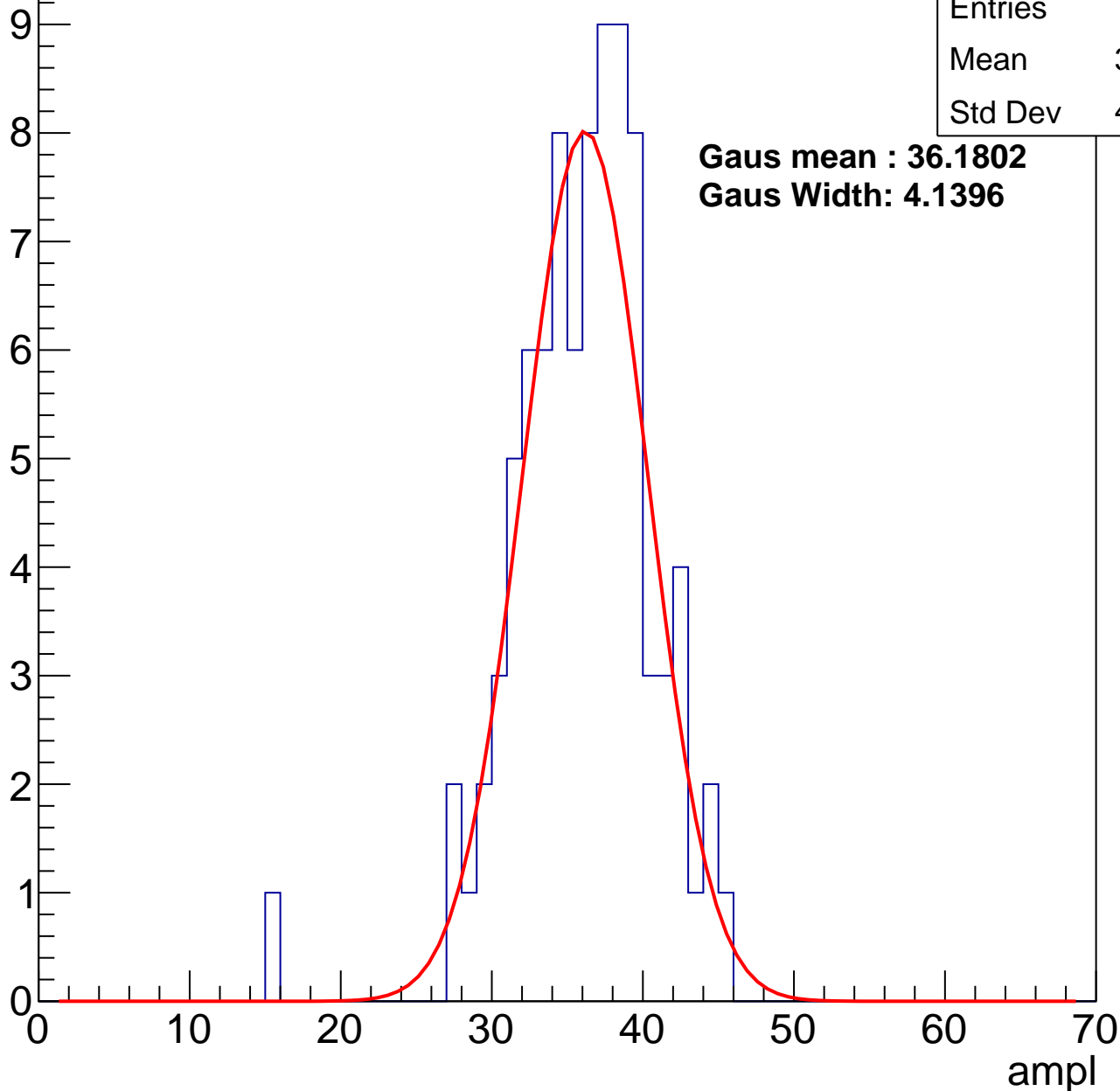
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	35.61
Std Dev	4.541

Gaus mean : 36.1802

Gaus Width: 4.1396



B1L102S, U2-ch120, adc2

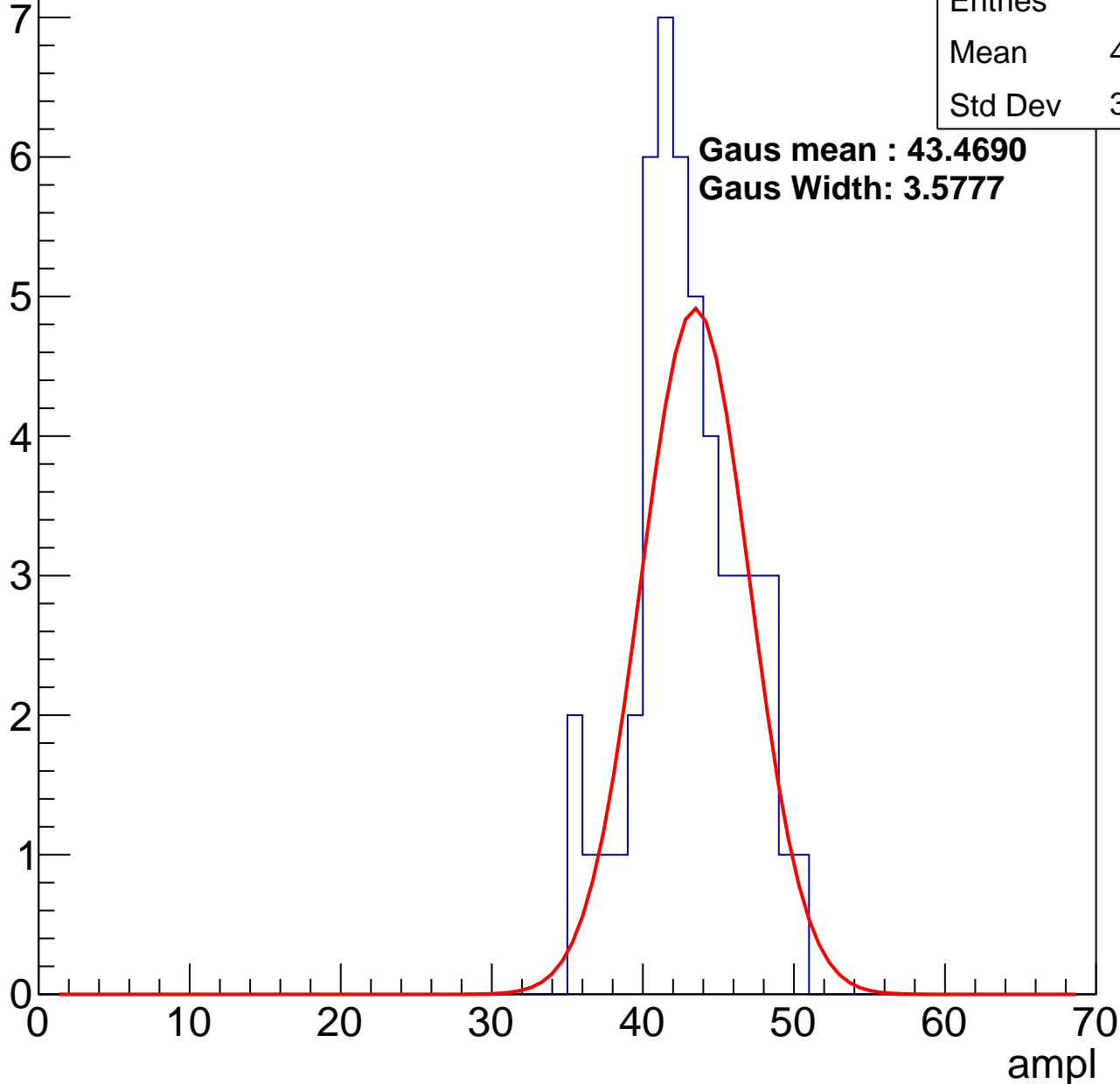
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.57
Std Dev	3.499

Gaus mean : 43.4690

Gaus Width: 3.5777

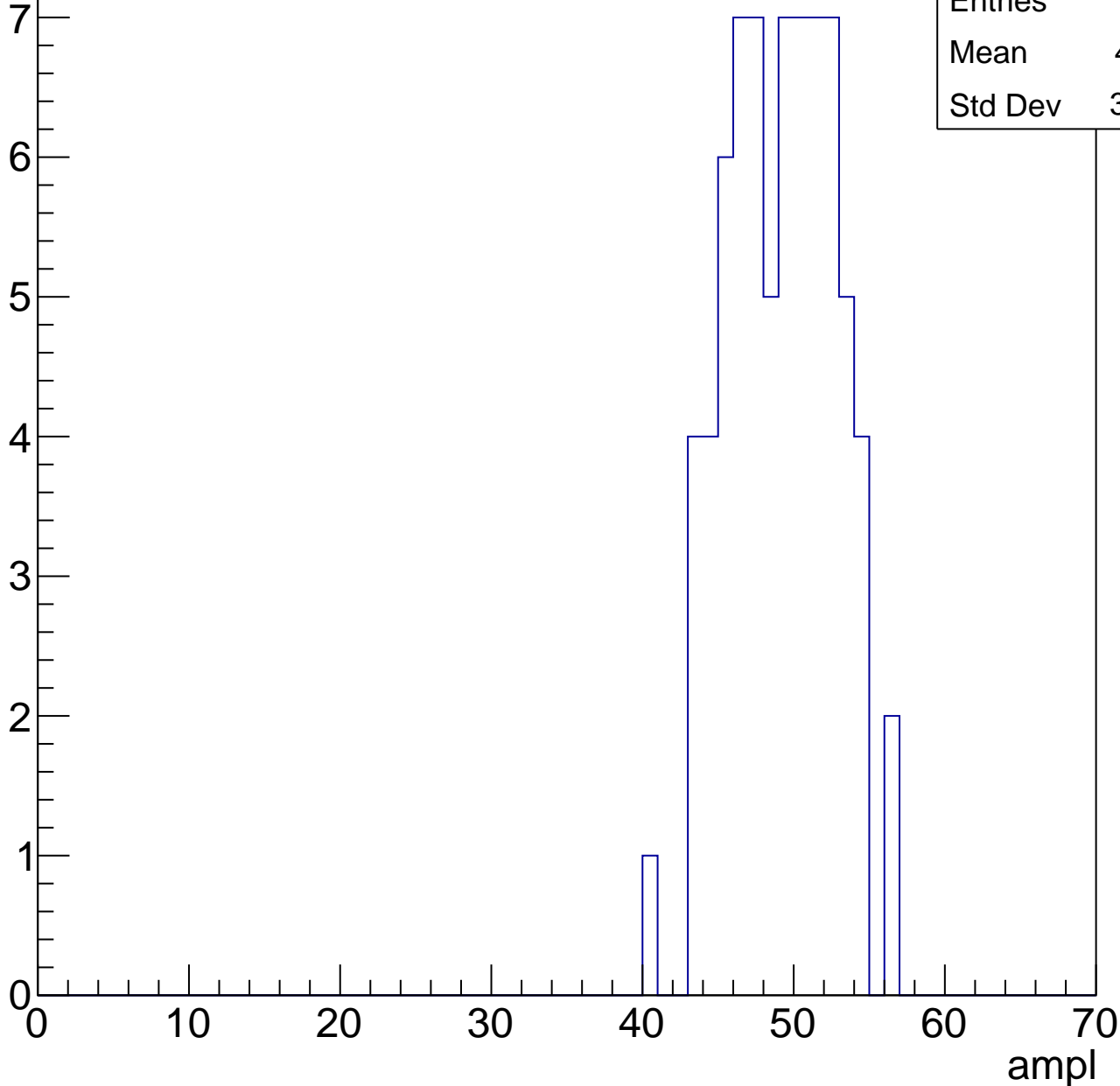


B1L102S, U2-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	48.71
Std Dev	3.486

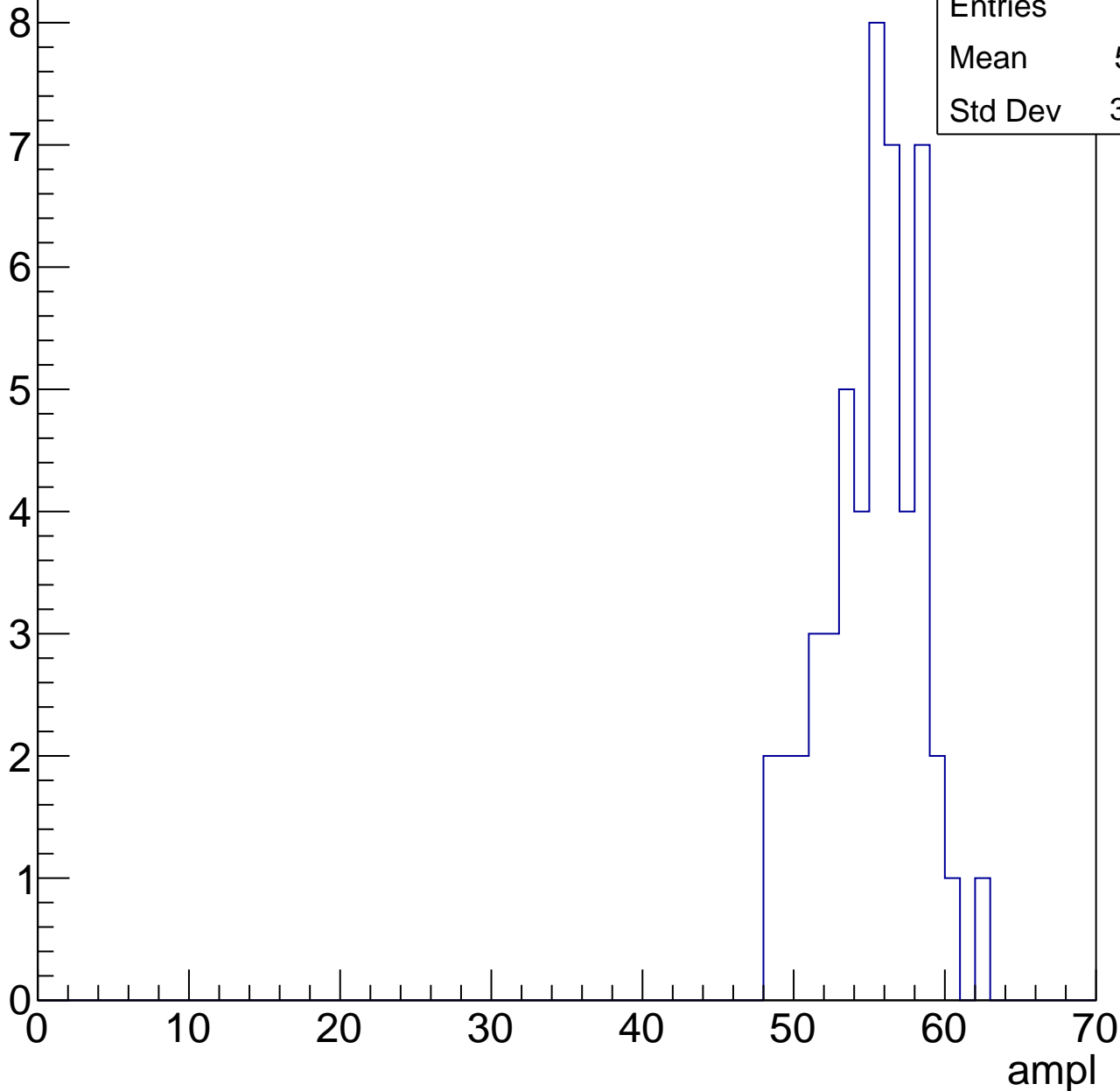


B1L102S, U2-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.71
Std Dev	3.152

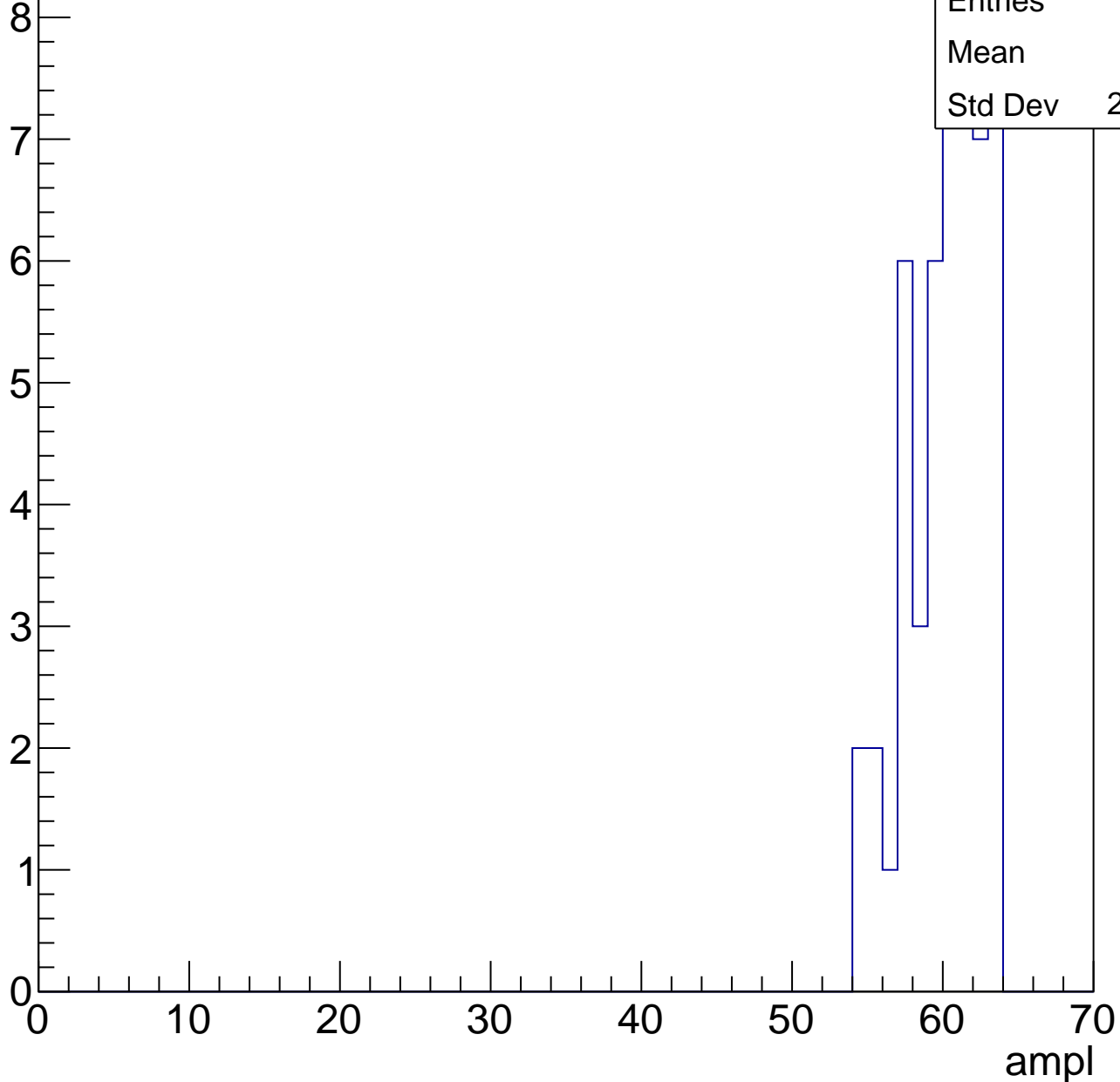


B1L102S, U2-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.8
Std Dev	2.489



B1L102S, U2-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	10
Mean	55
Std Dev	18.38

B1L102S, U2-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch121, adc0

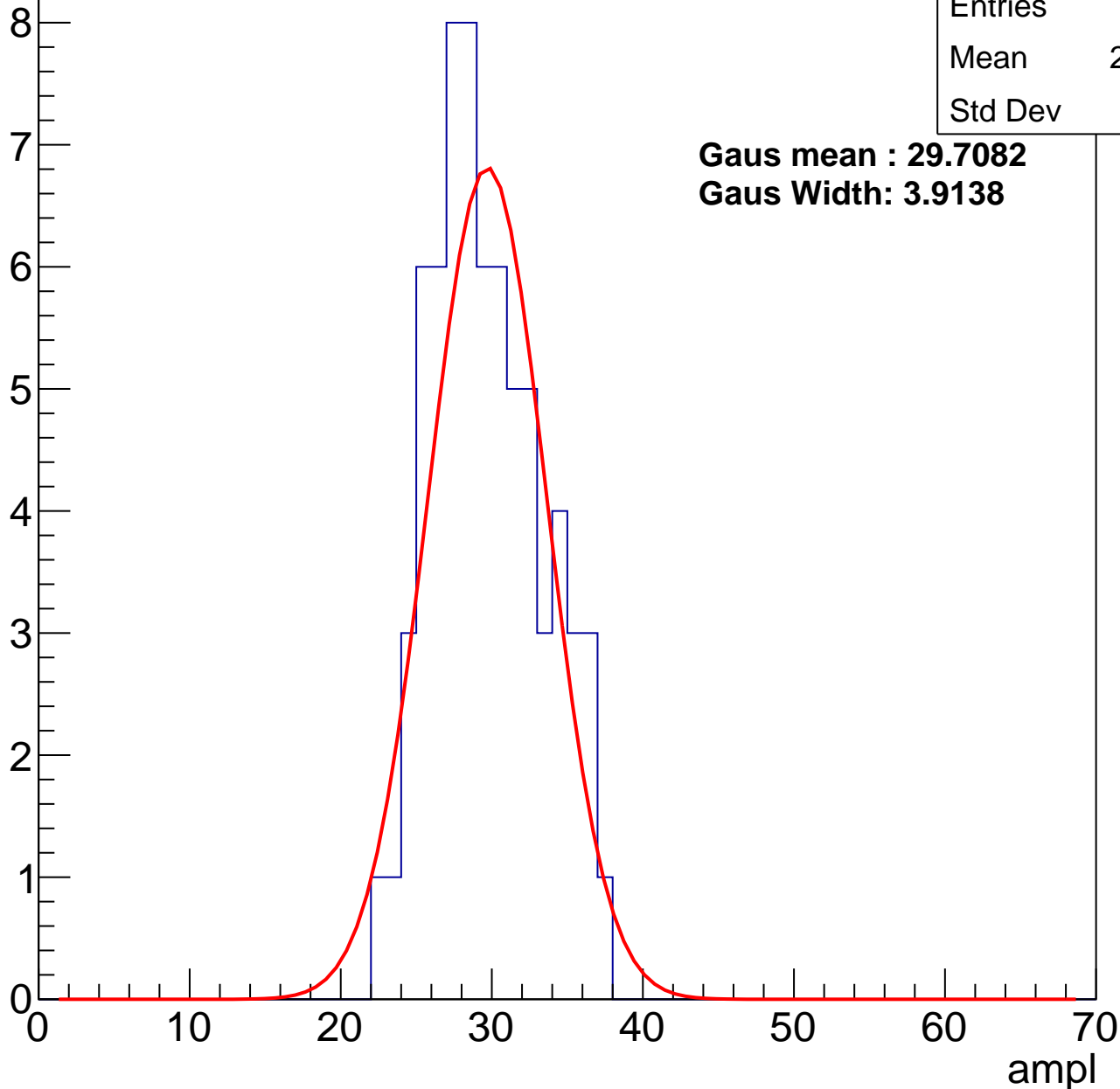
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.23
Std Dev	3.58

Gaus mean : 29.7082

Gaus Width: 3.9138



B1L102S, U2-ch121, adc1

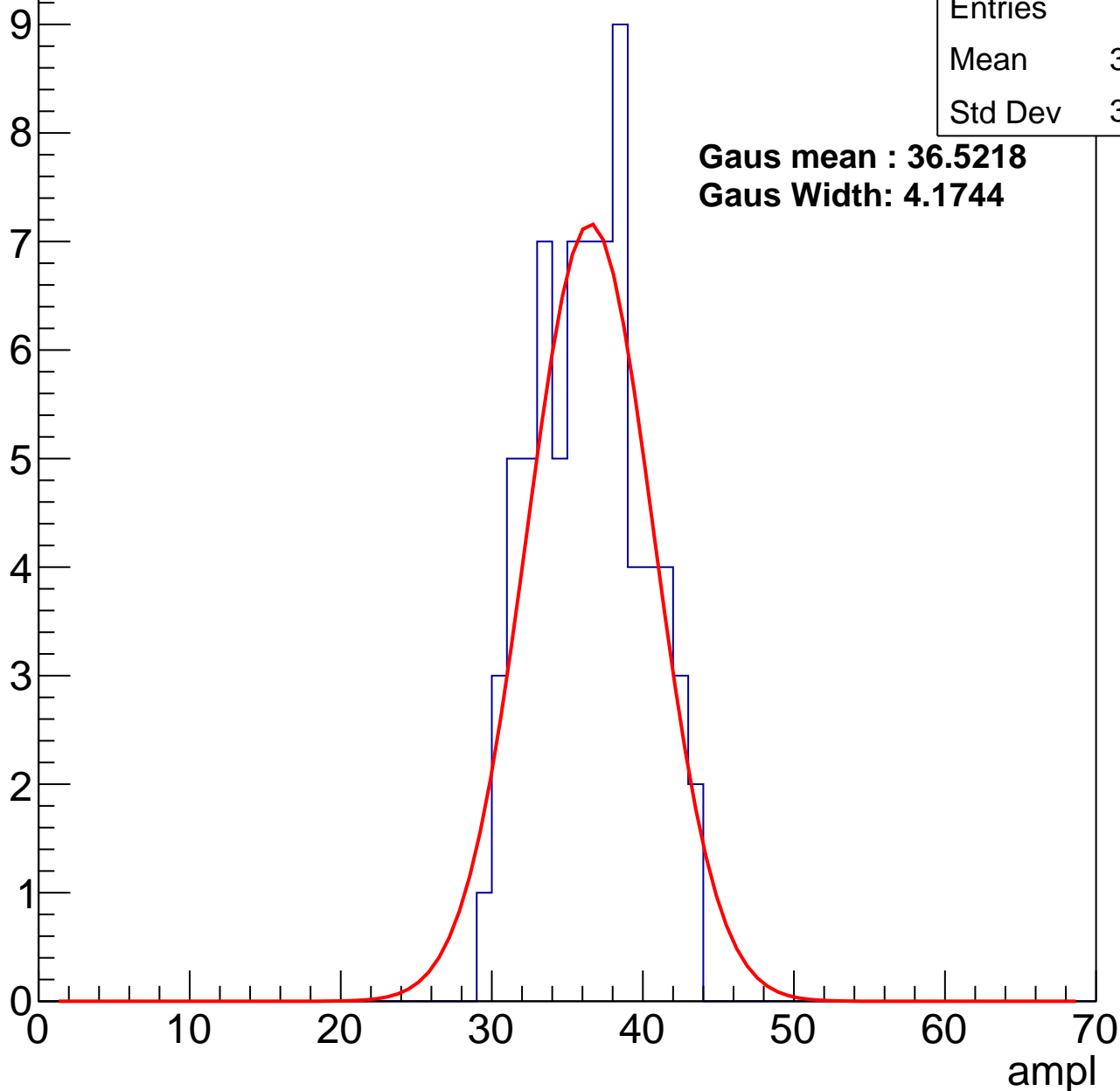
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	35.96
Std Dev	3.513

Gaus mean : 36.5218

Gaus Width: 4.1744



B1L102S, U2-ch121, adc2

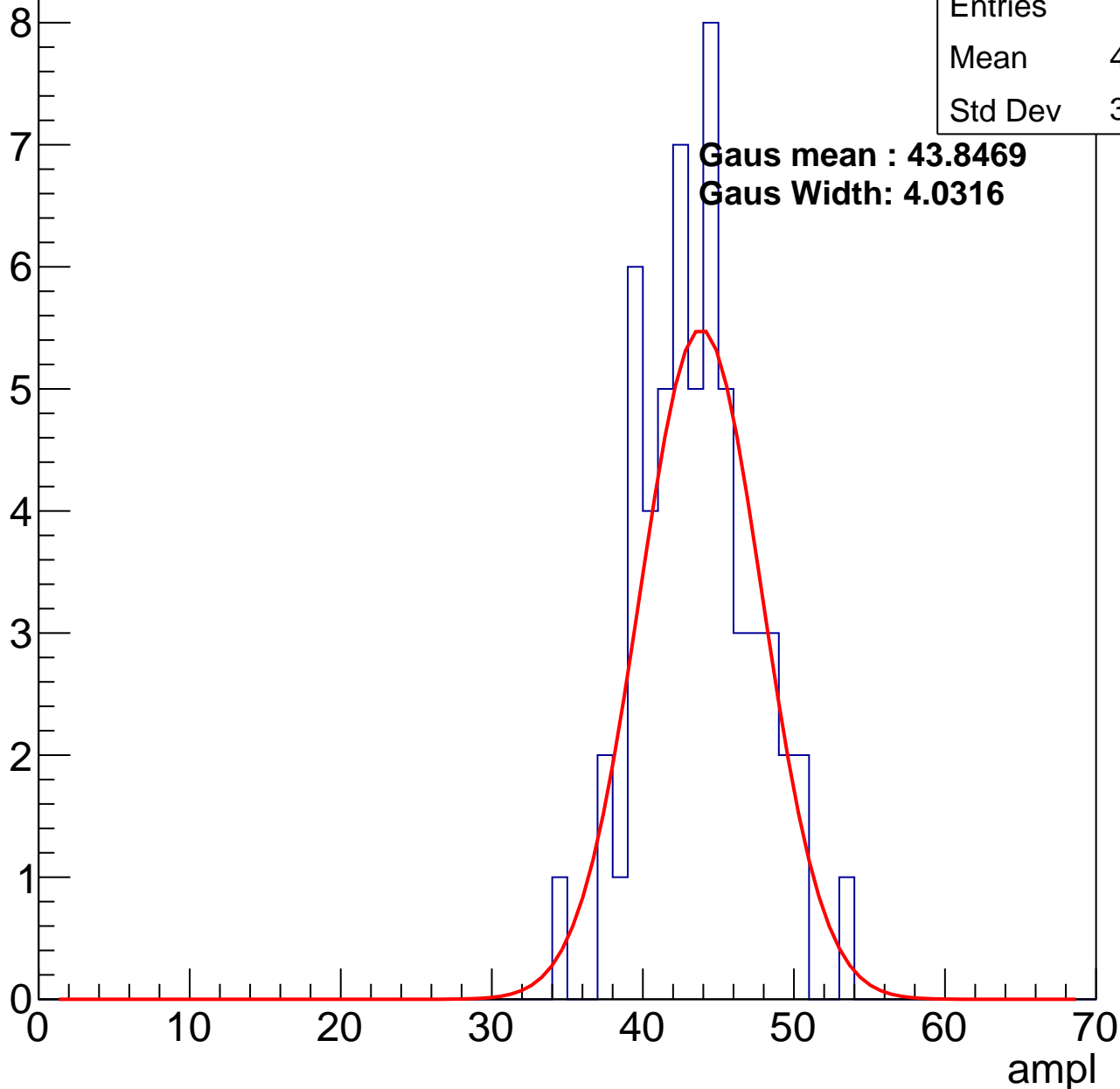
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.19
Std Dev	3.674

Gaus mean : 43.8469

Gaus Width: 4.0316

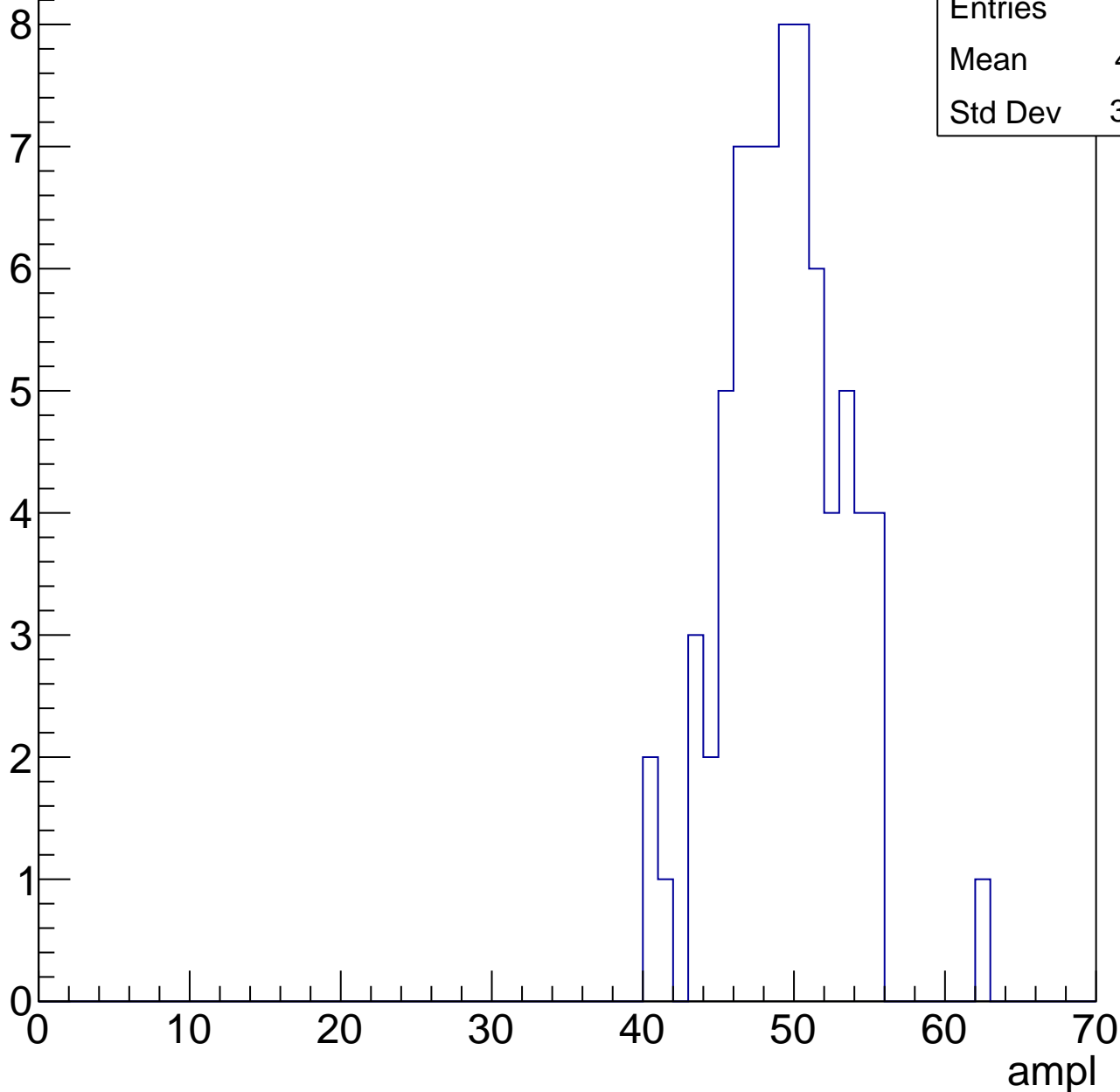


B1L102S, U2-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.91
Std Dev	3.894

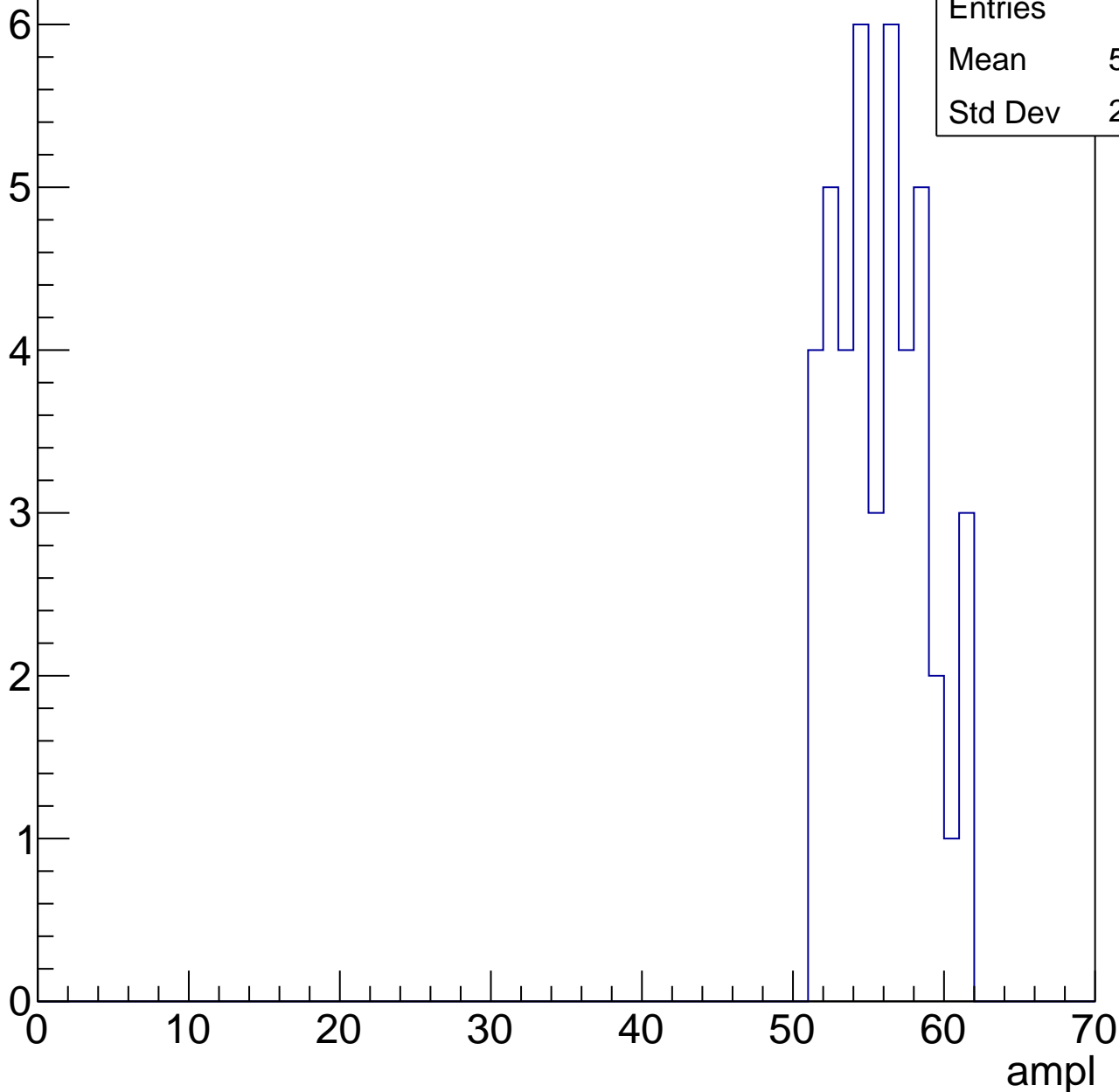


B1L102S, U2-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.35
Std Dev	2.884

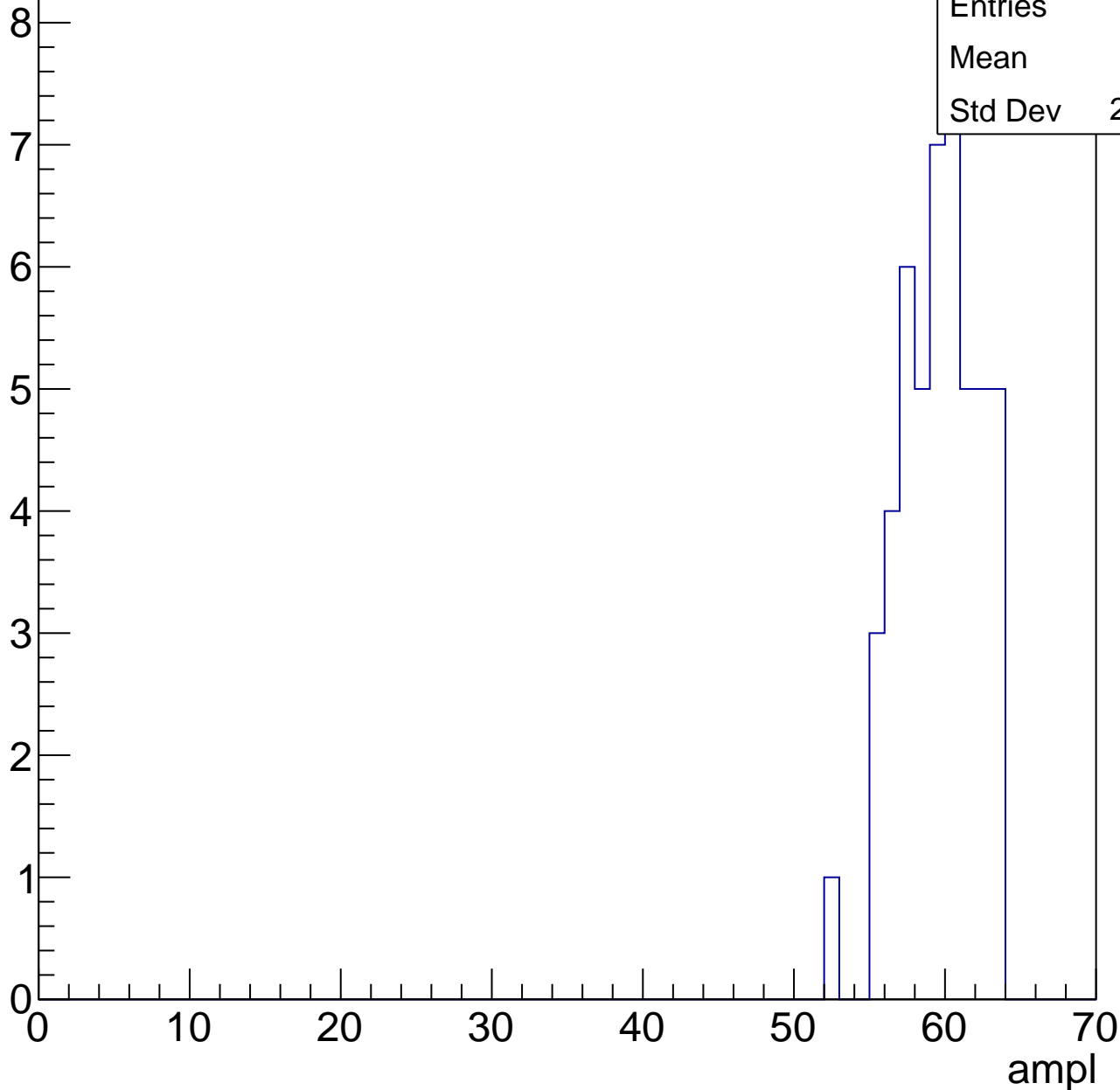


B1L102S, U2-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.1
Std Dev	2.533

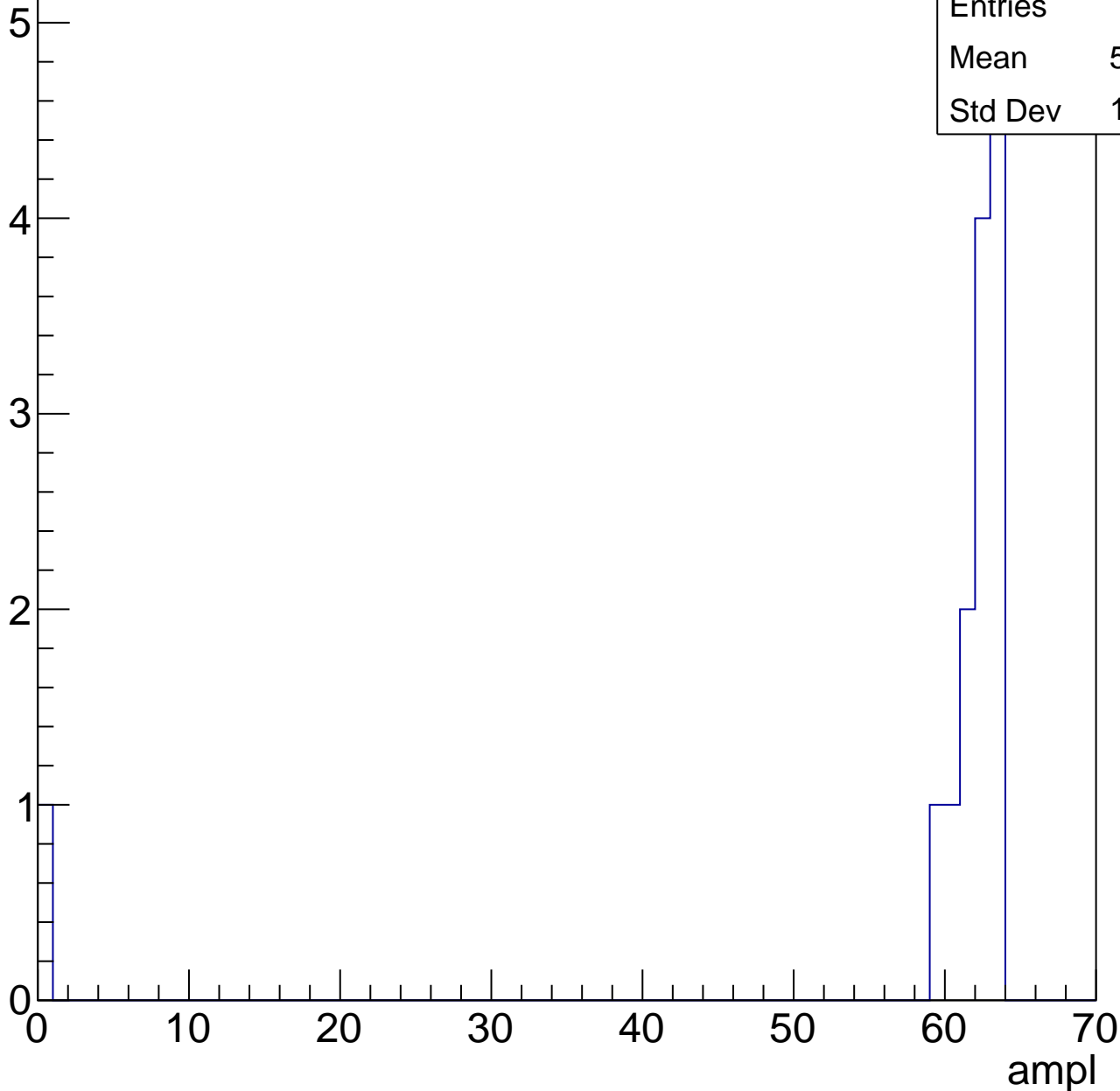


B1L102S, U2-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.43
Std Dev	15.97



B1L102S, U2-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch122, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.13
Std Dev	5.011

Gaus mean : 29.1436

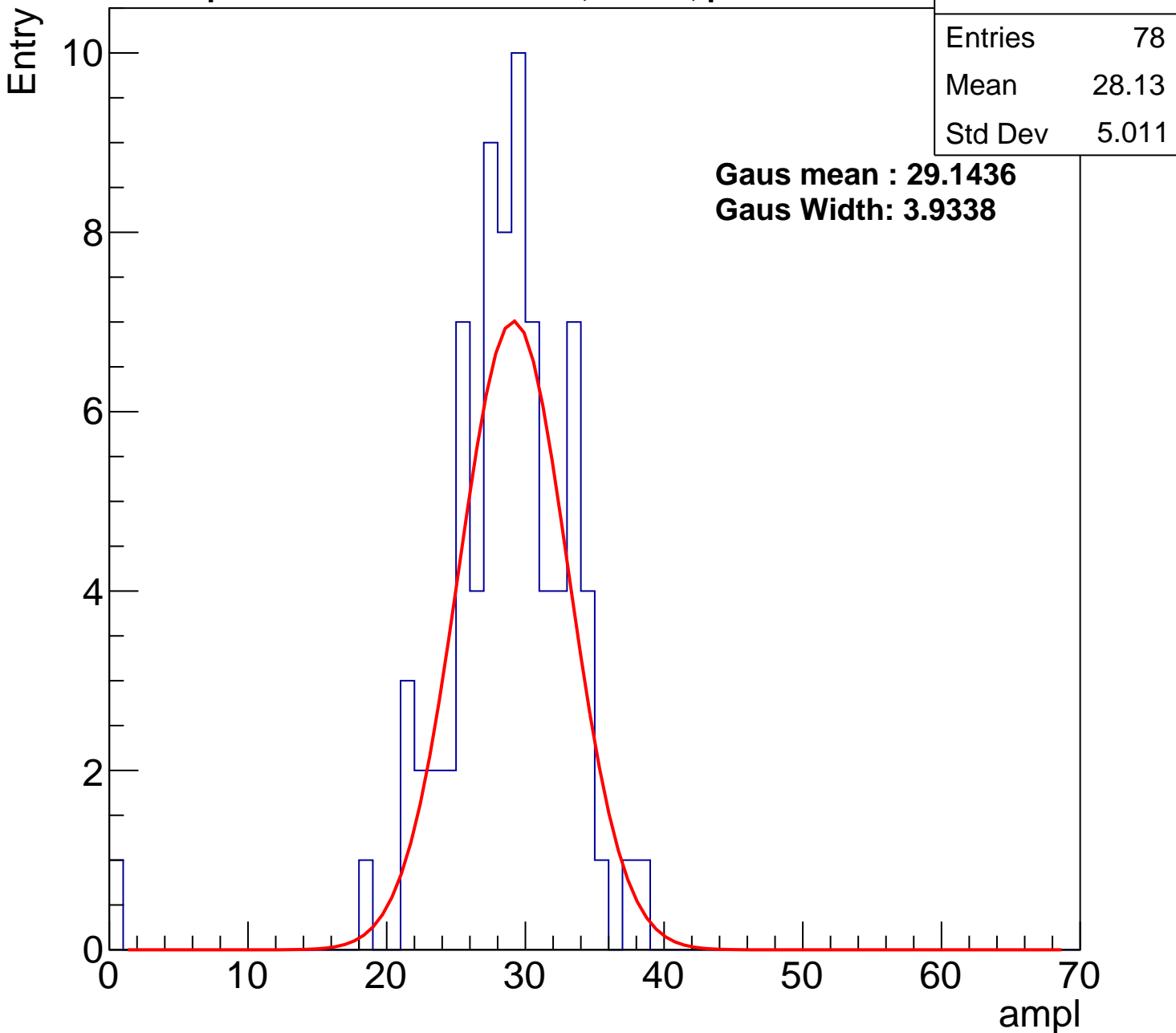
Gaus Width: 3.9338

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch122, adc1

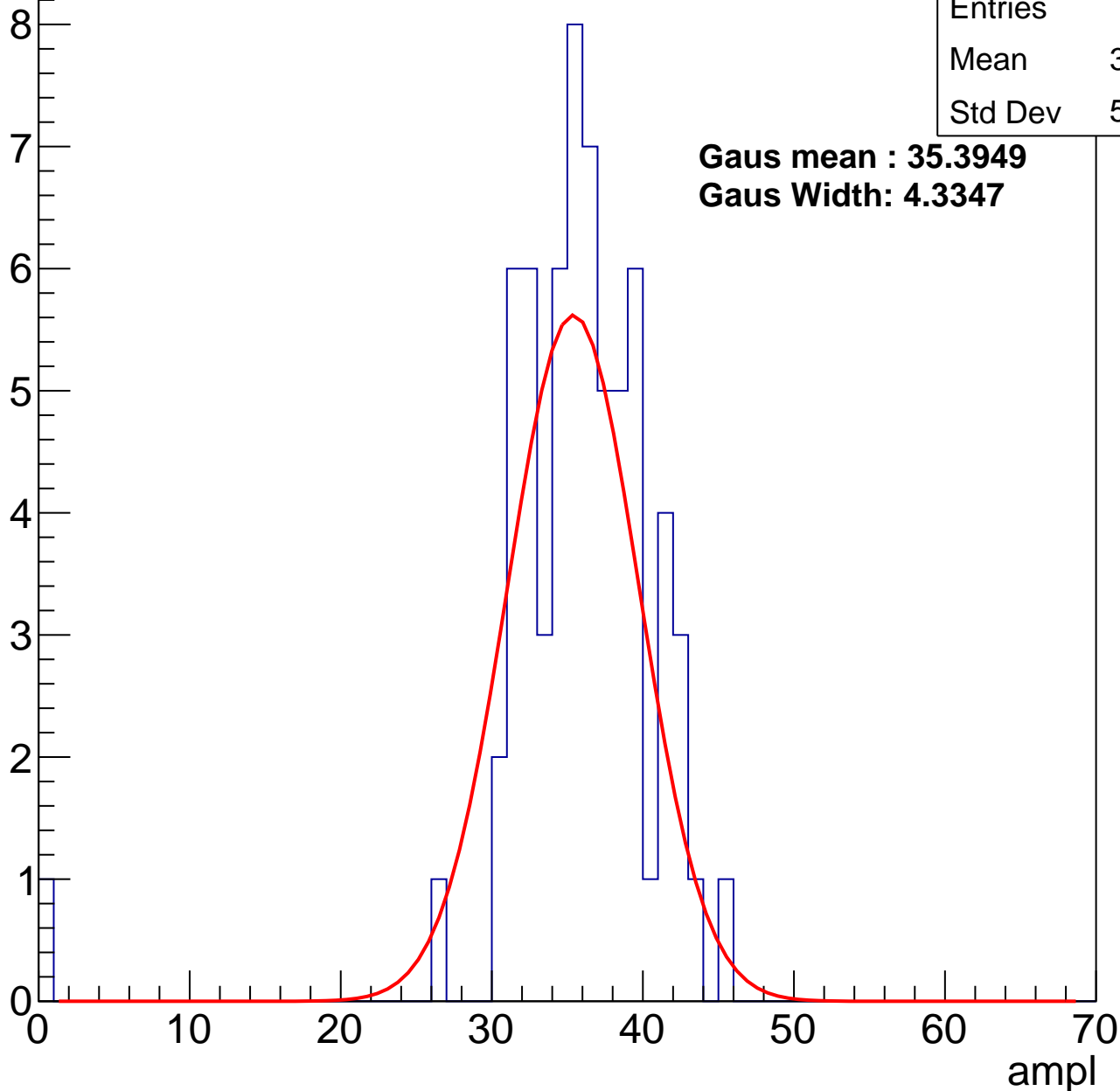
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.24
Std Dev	5.732

Gaus mean : 35.3949

Gaus Width: 4.3347



B1L102S, U2-ch122, adc2

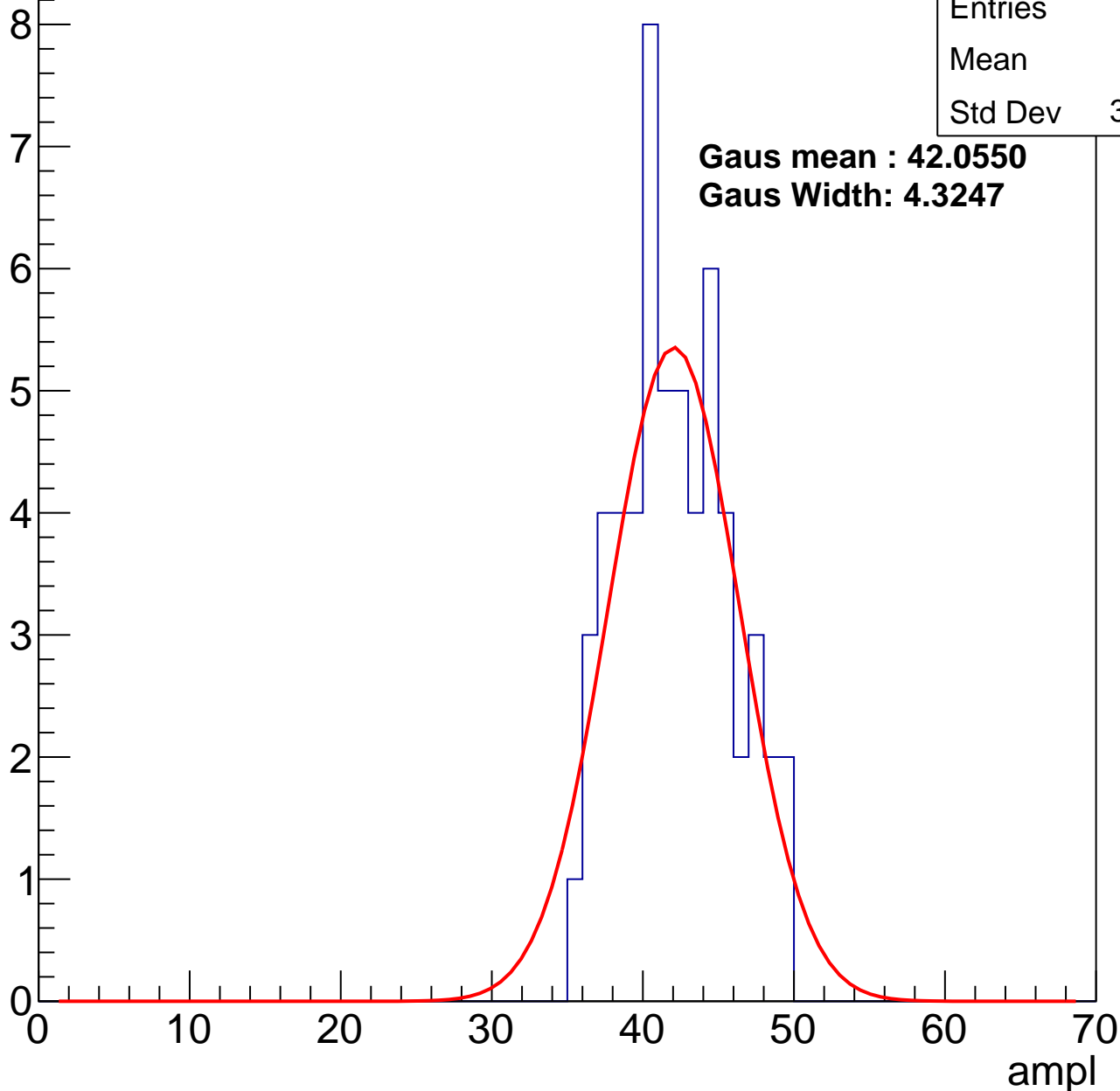
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.7
Std Dev	3.579

Gaus mean : 42.0550

Gaus Width: 4.3247

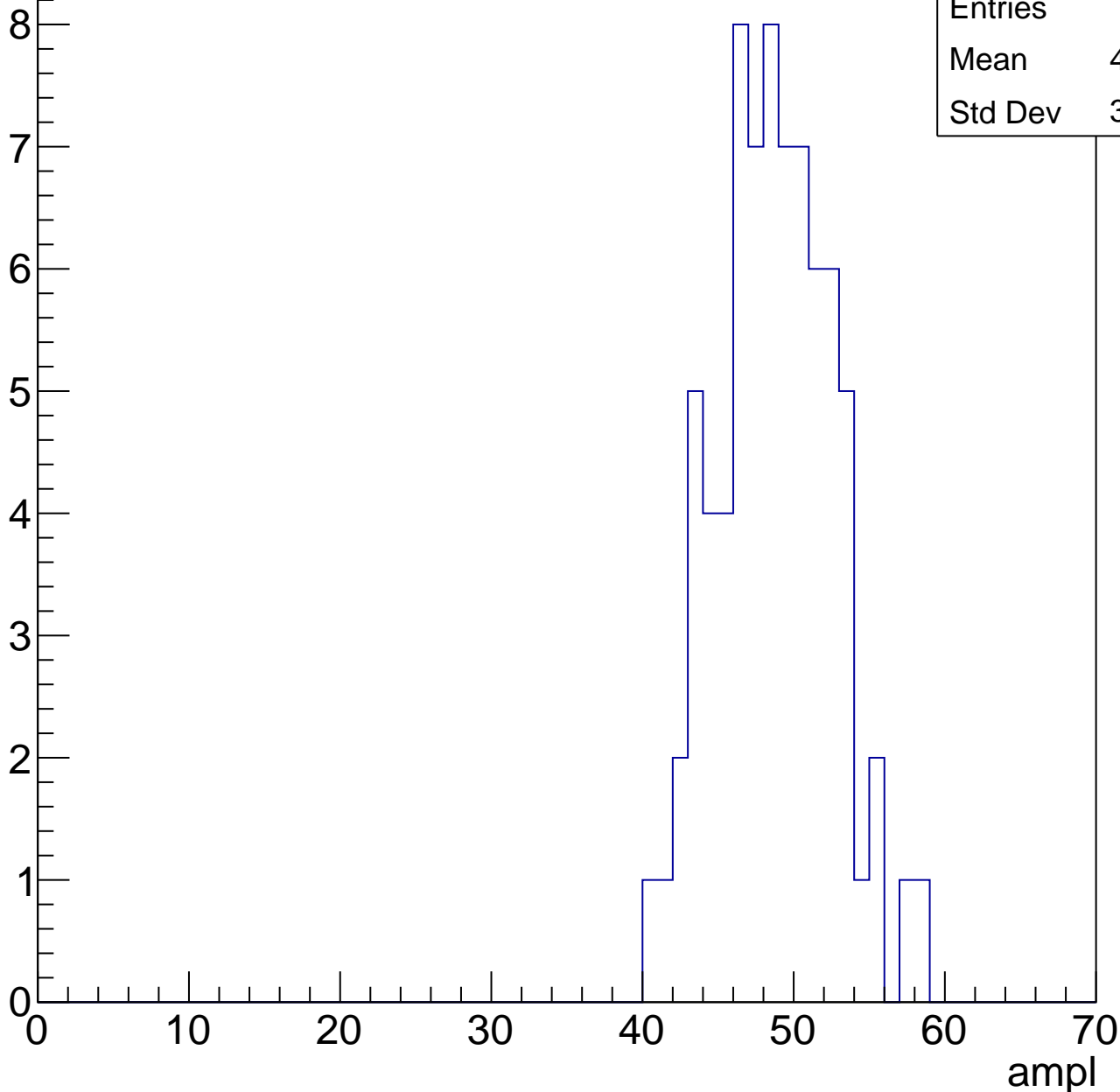


B1L102S, U2-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

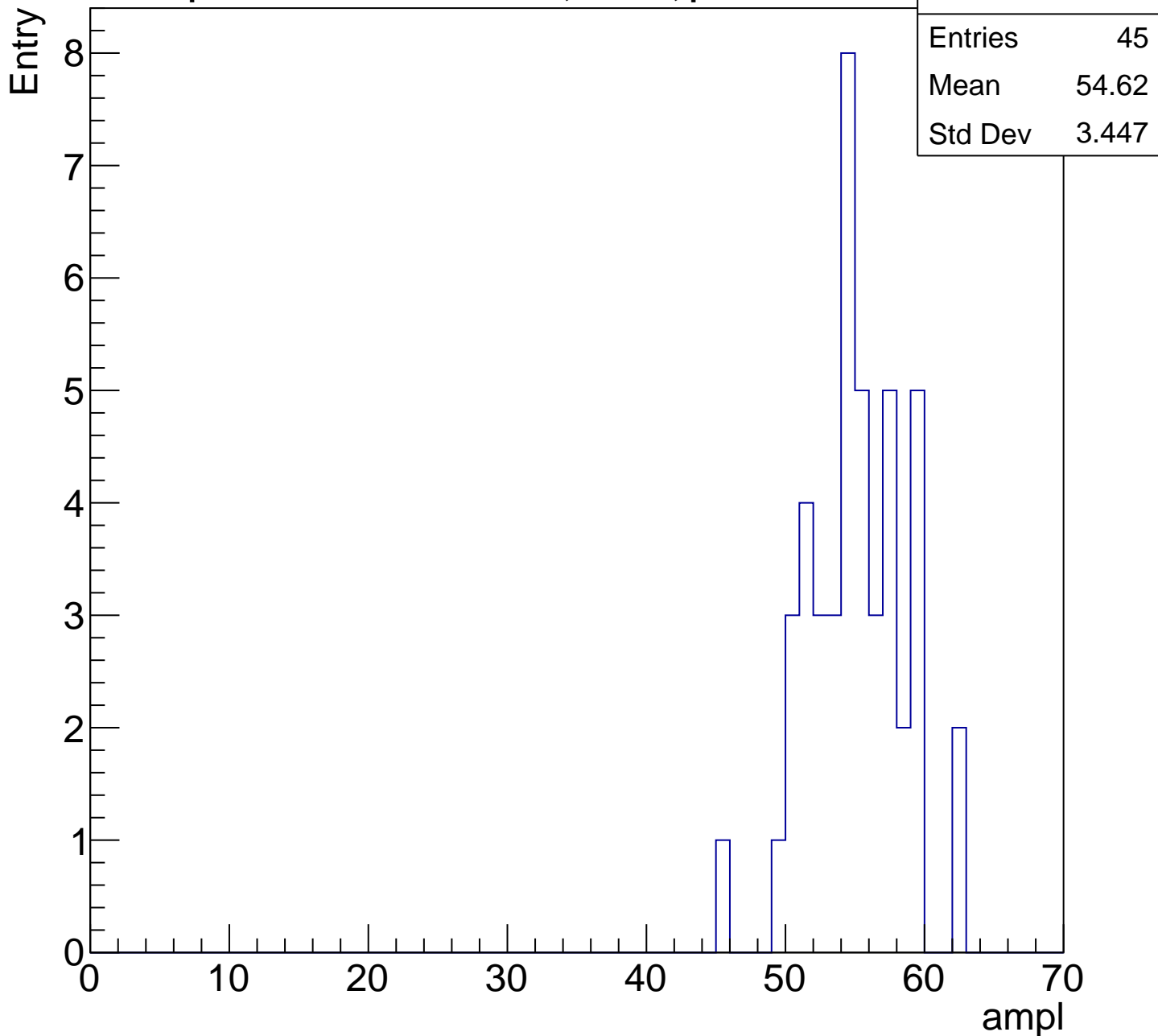
Entry

Entries	76
Mean	48.32
Std Dev	3.746



B1L102S, U2-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

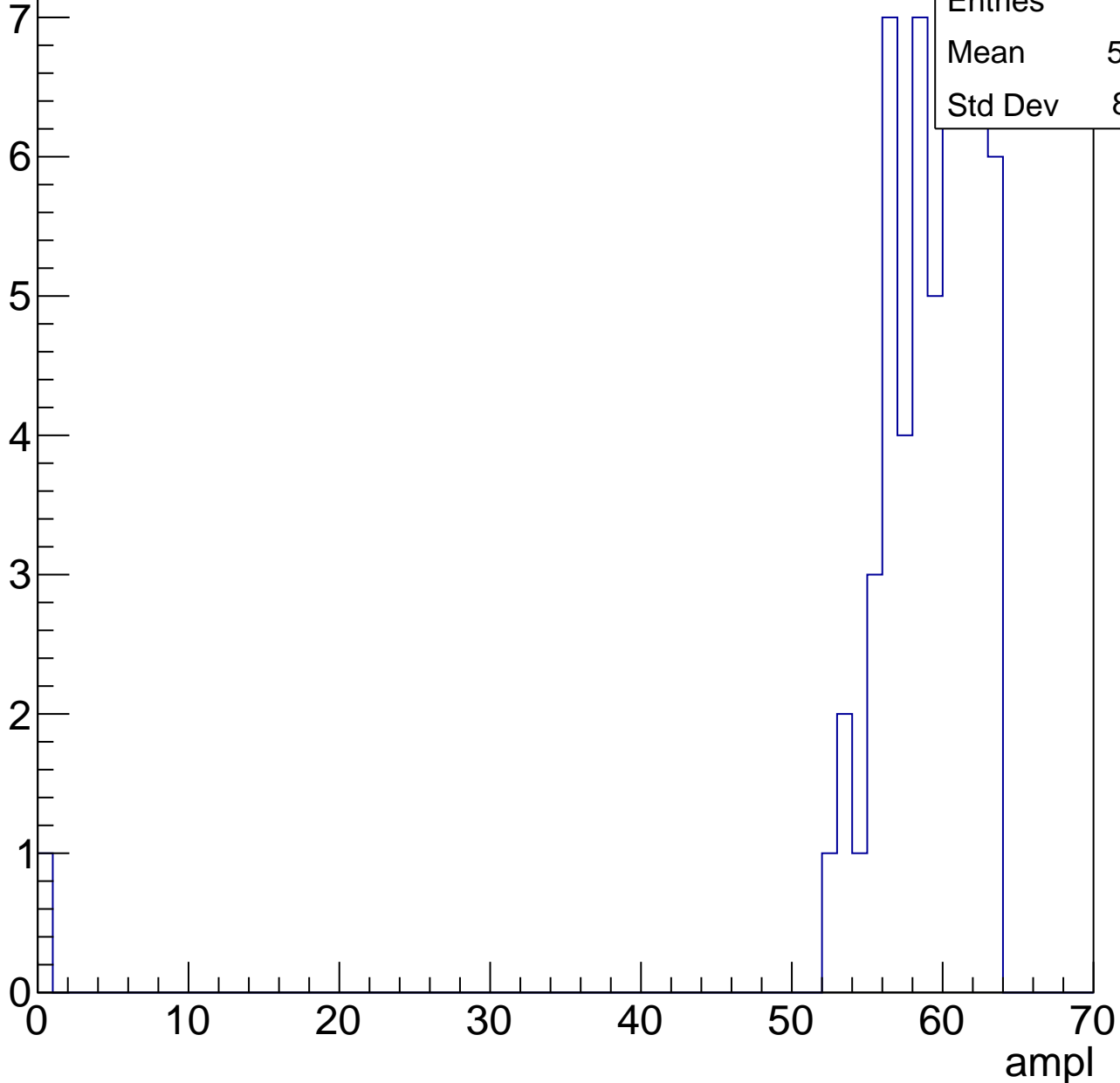


B1L102S, U2-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

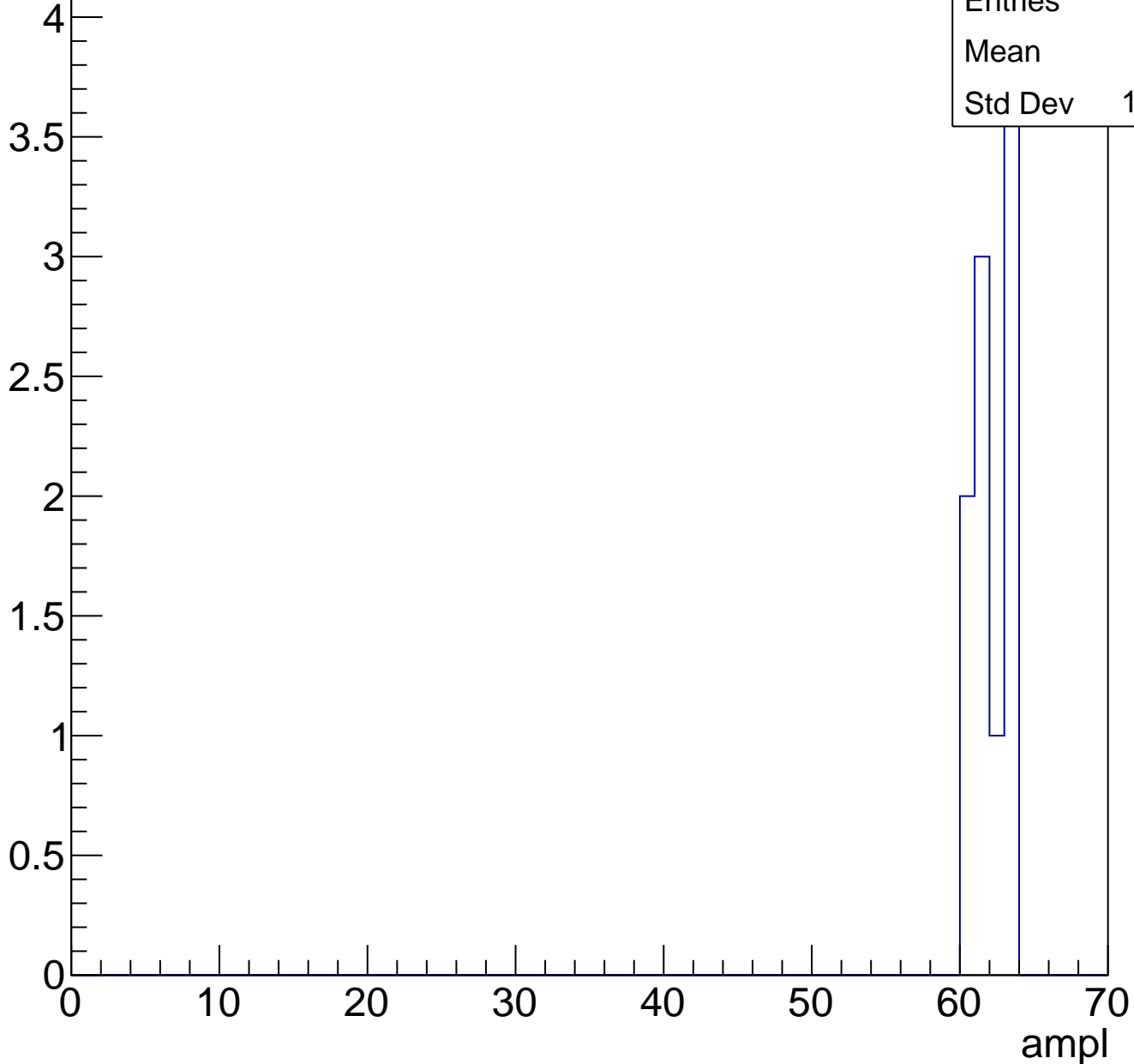
Entries	58
Mean	57.88
Std Dev	8.181



B1L102S, U2-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch123, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	27.1
Std Dev	6.633

Gaus mean : 28.5741

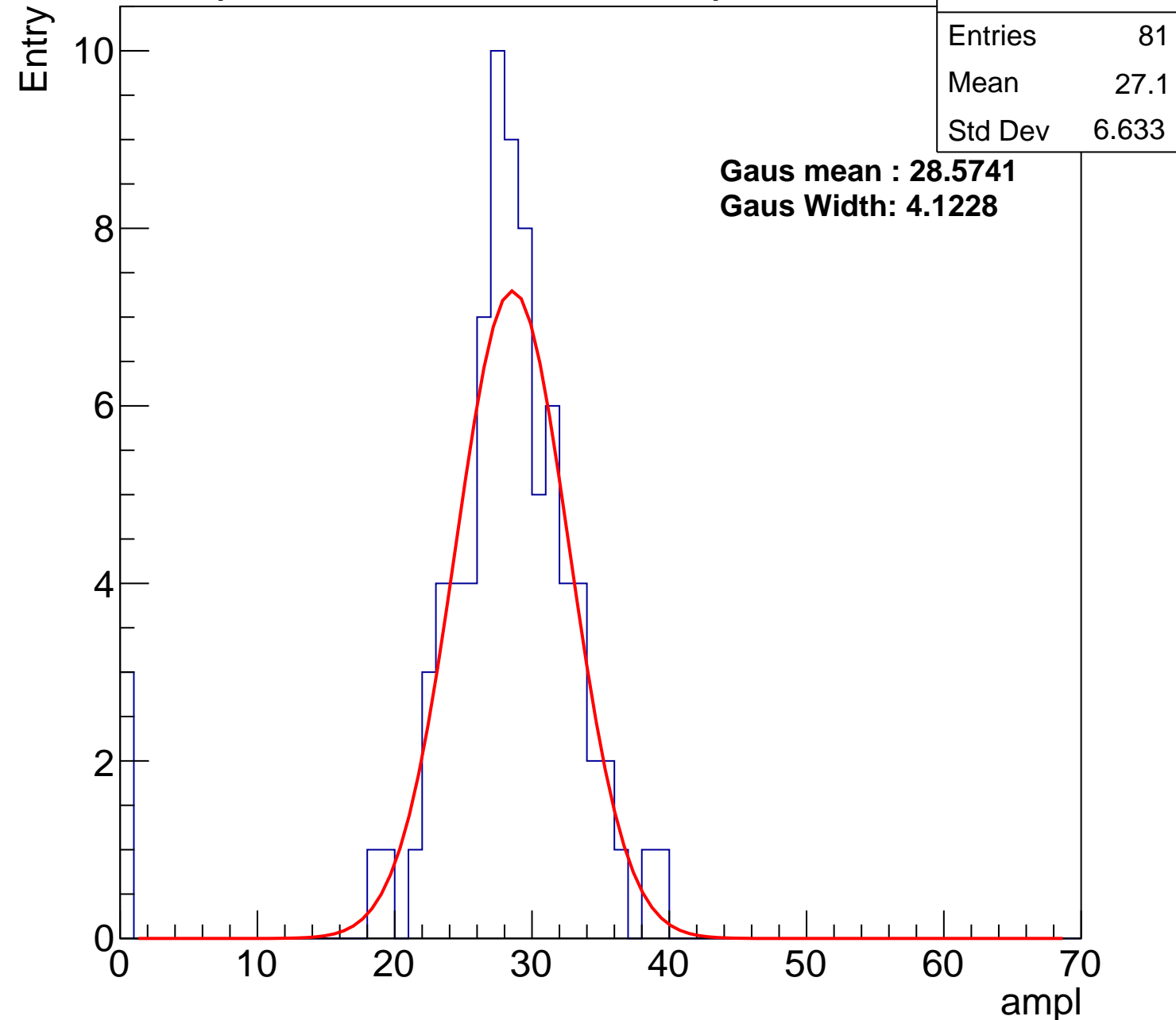
Gaus Width: 4.1228

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U2-ch123, adc1

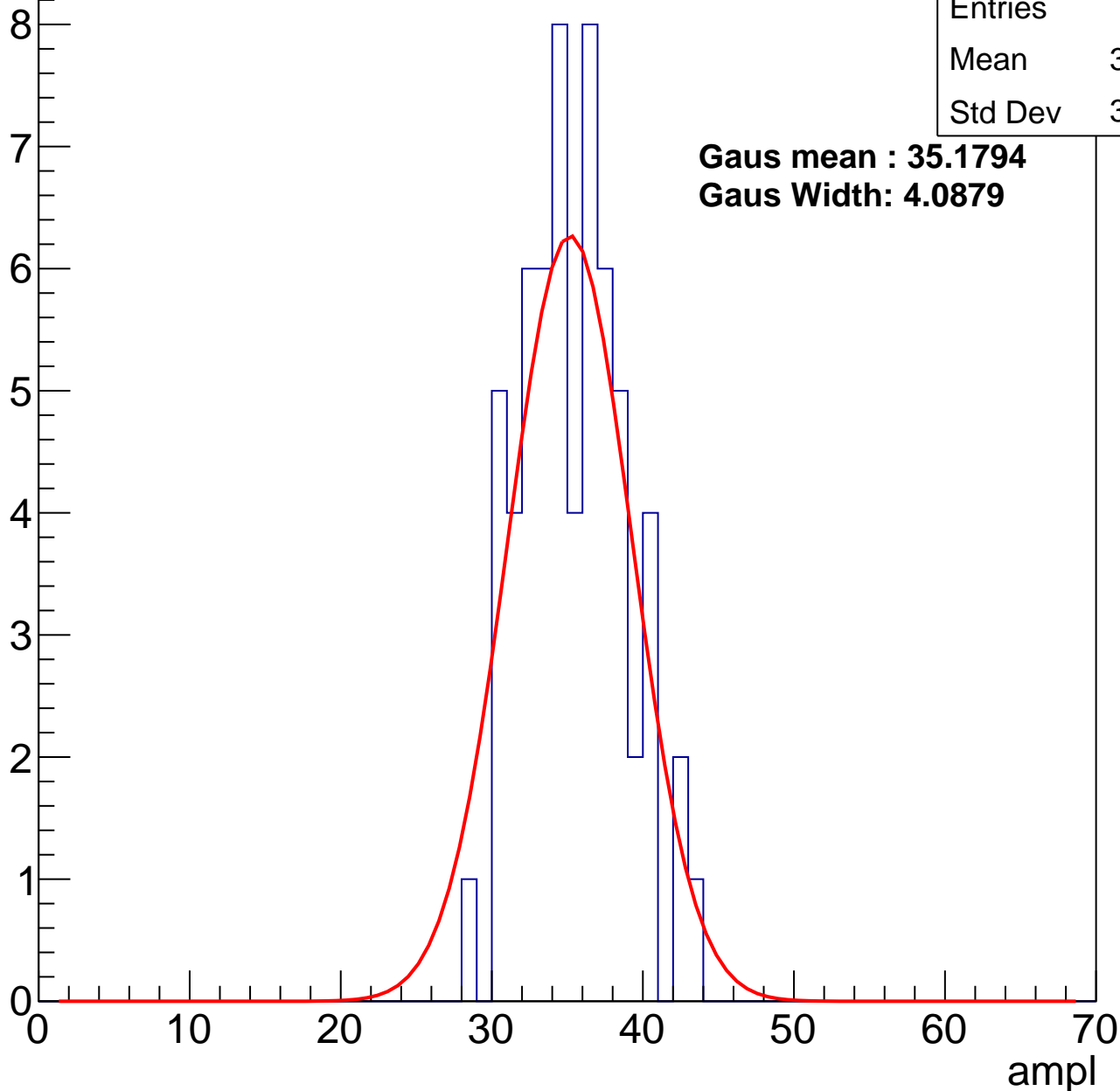
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.98
Std Dev	3.348

Gaus mean : 35.1794

Gaus Width: 4.0879



B1L102S, U2-ch123, adc2

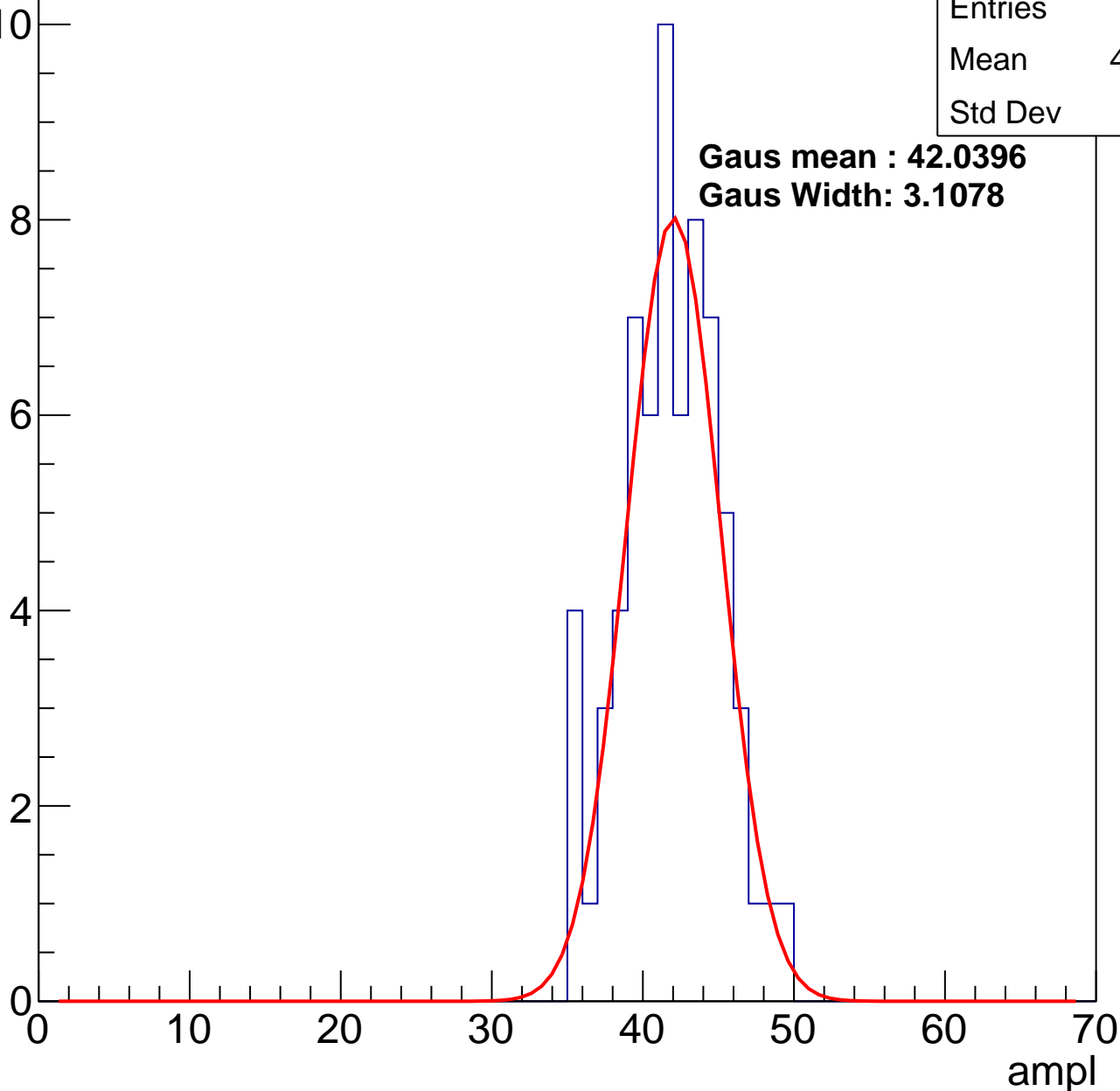
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.39
Std Dev	3.19

Gaus mean : 42.0396

Gaus Width: 3.1078

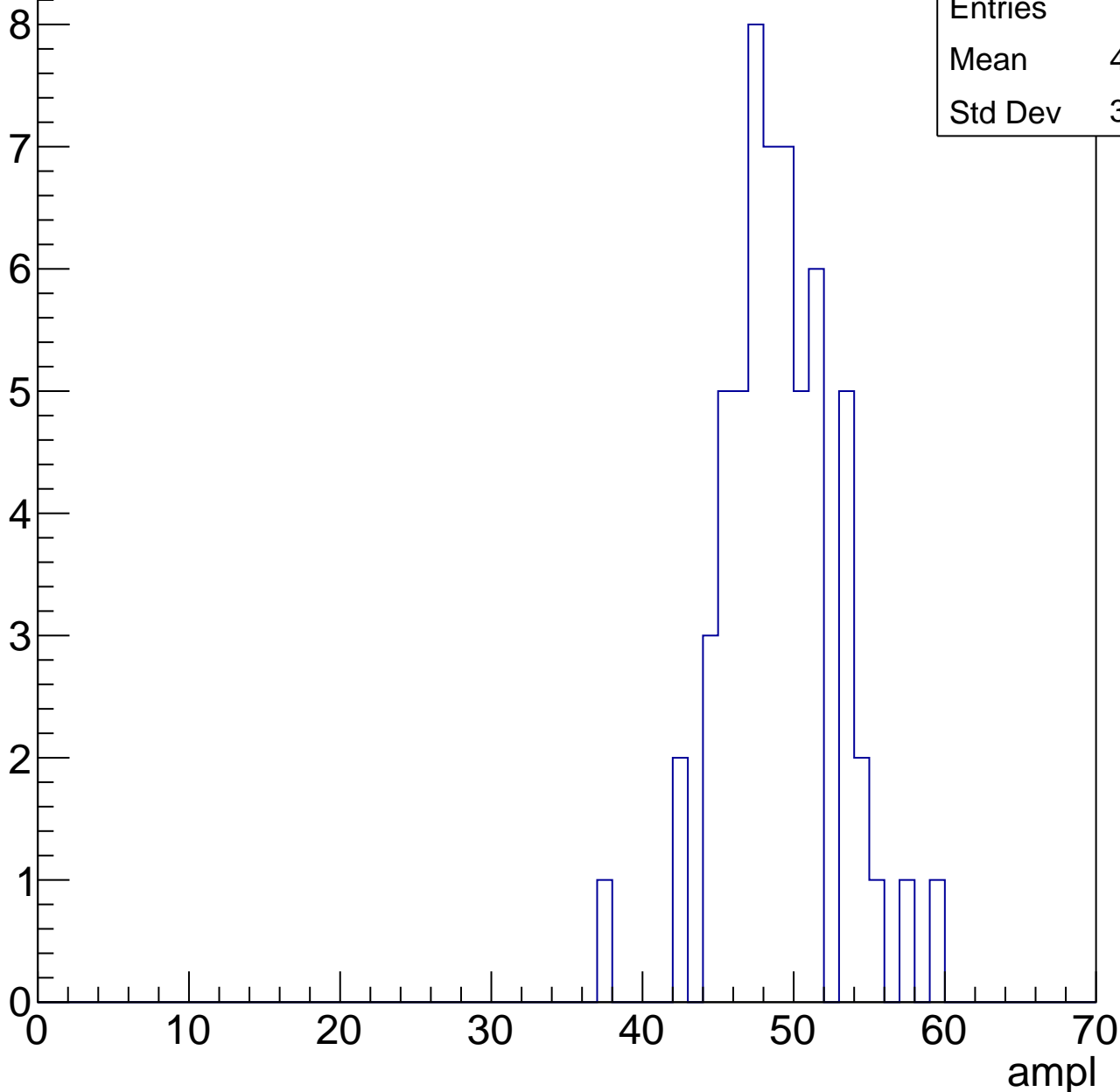


B1L102S, U2-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	48.53
Std Dev	3.743



B1L102S, U2-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

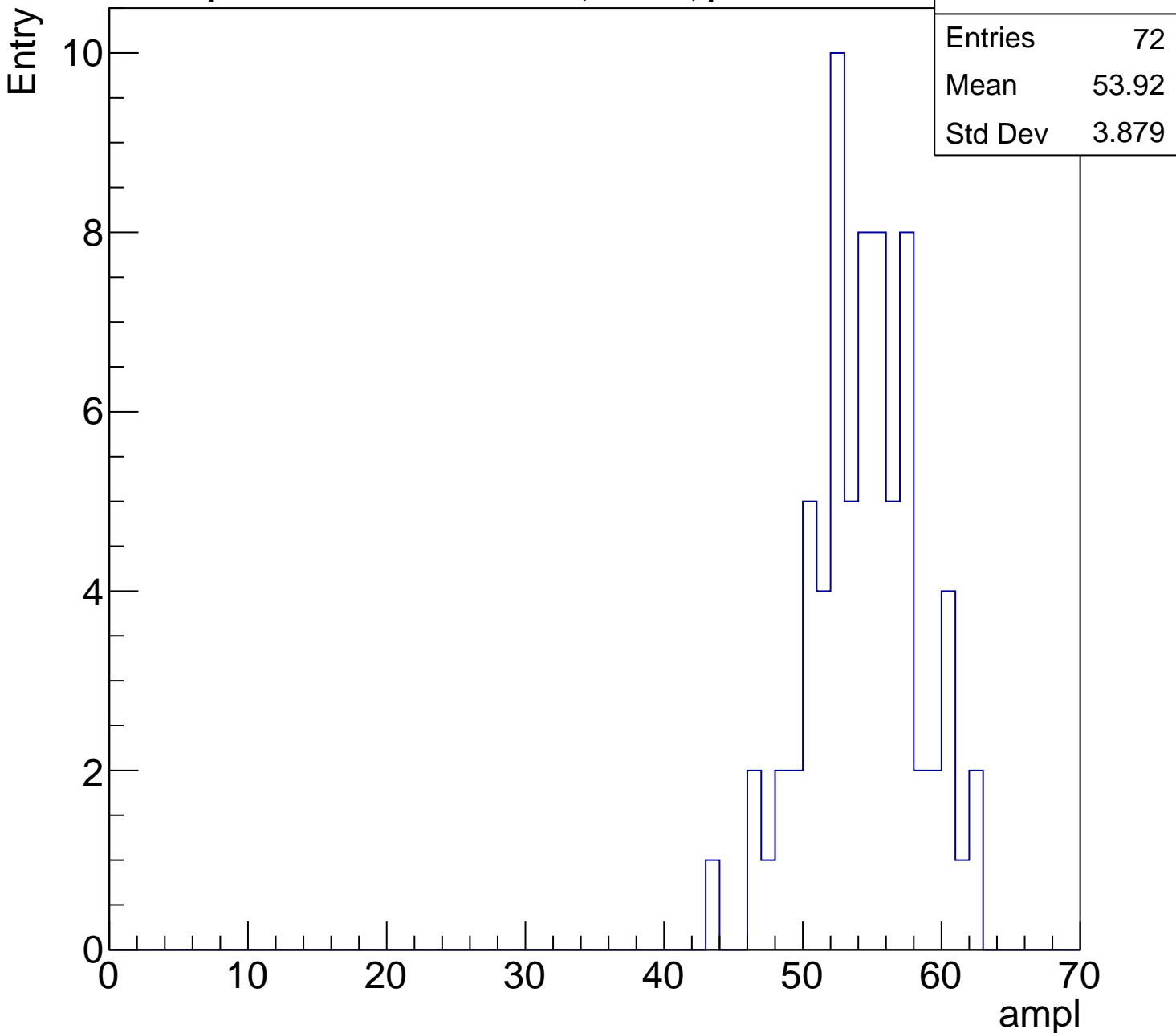
Entries	72
Mean	53.92
Std Dev	3.879

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

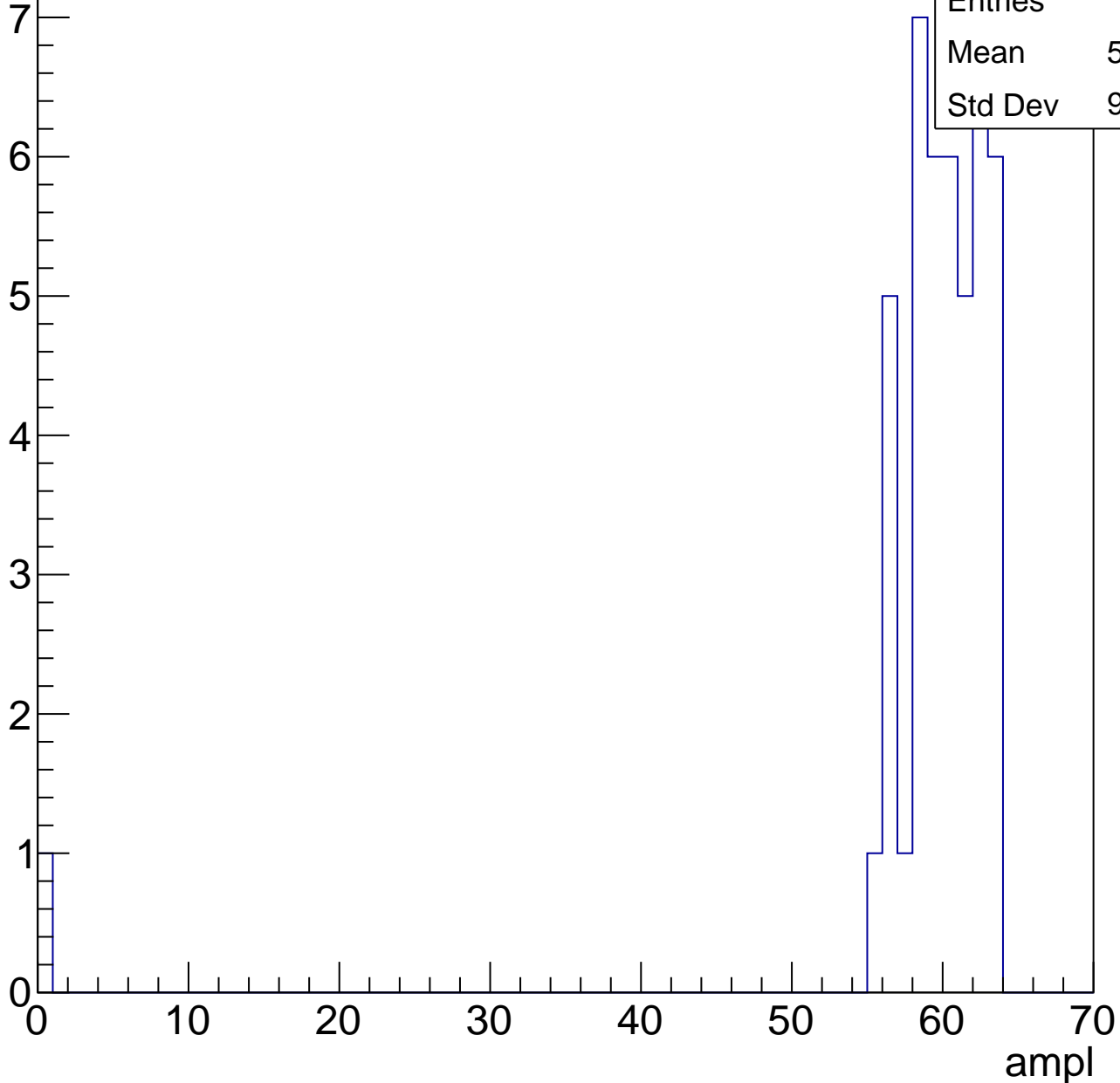


B1L102S, U2-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

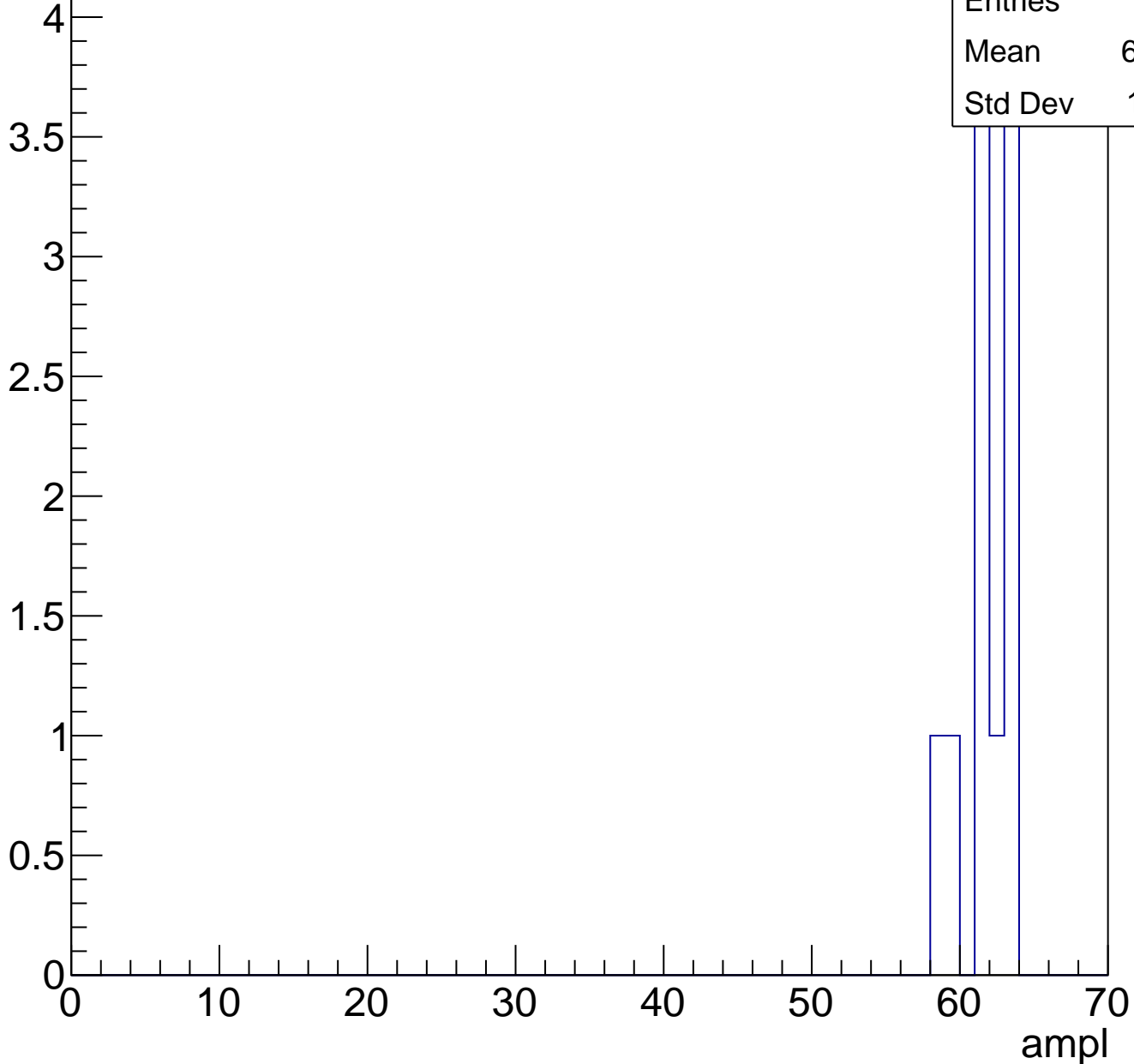
Entries	45
Mean	58.42
Std Dev	9.096



B1L102S, U2-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U2-ch124, adc0

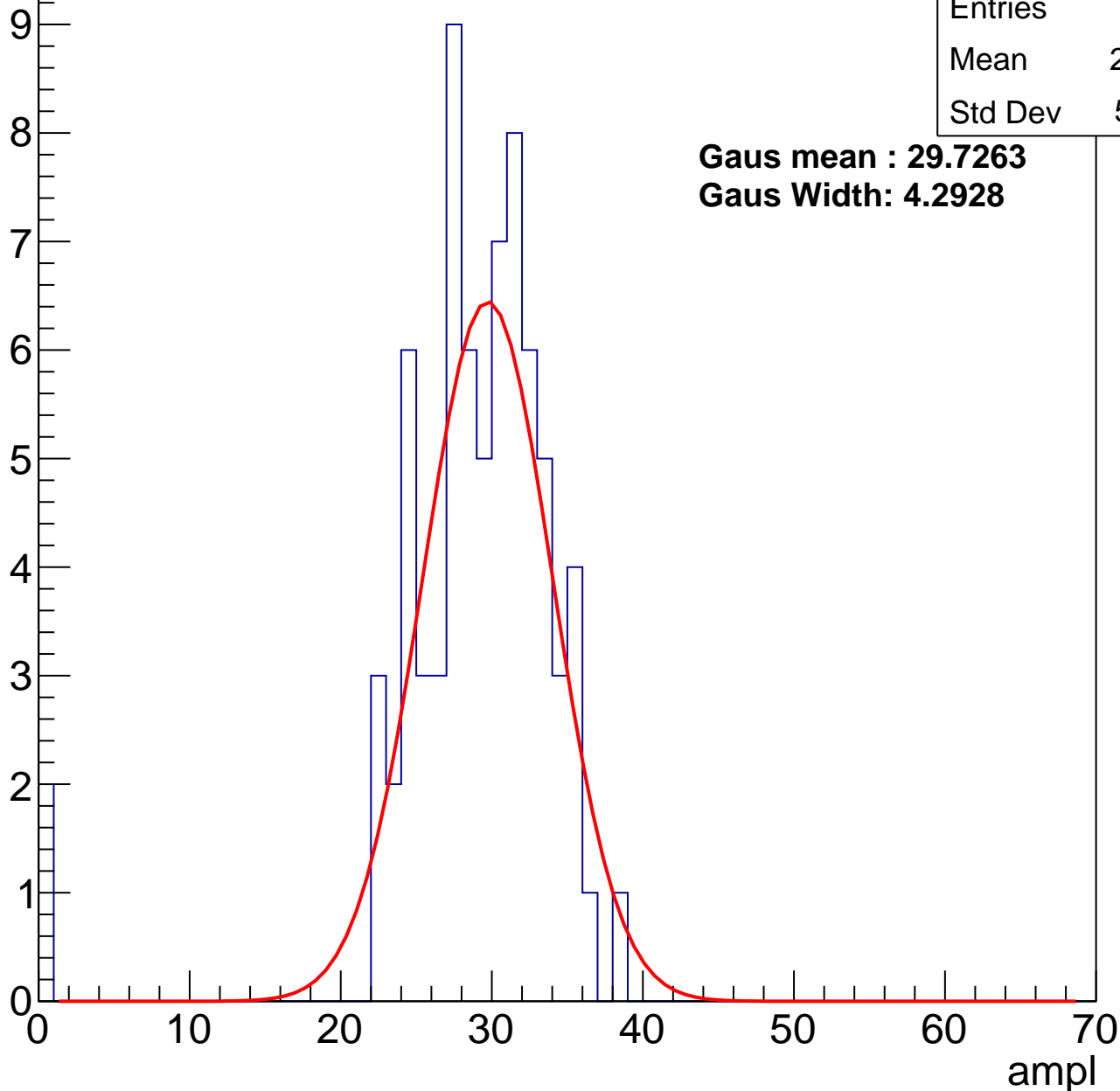
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.32
Std Dev	5.991

Gaus mean : 29.7263

Gaus Width: 4.2928



B1L102S, U2-ch124, adc1

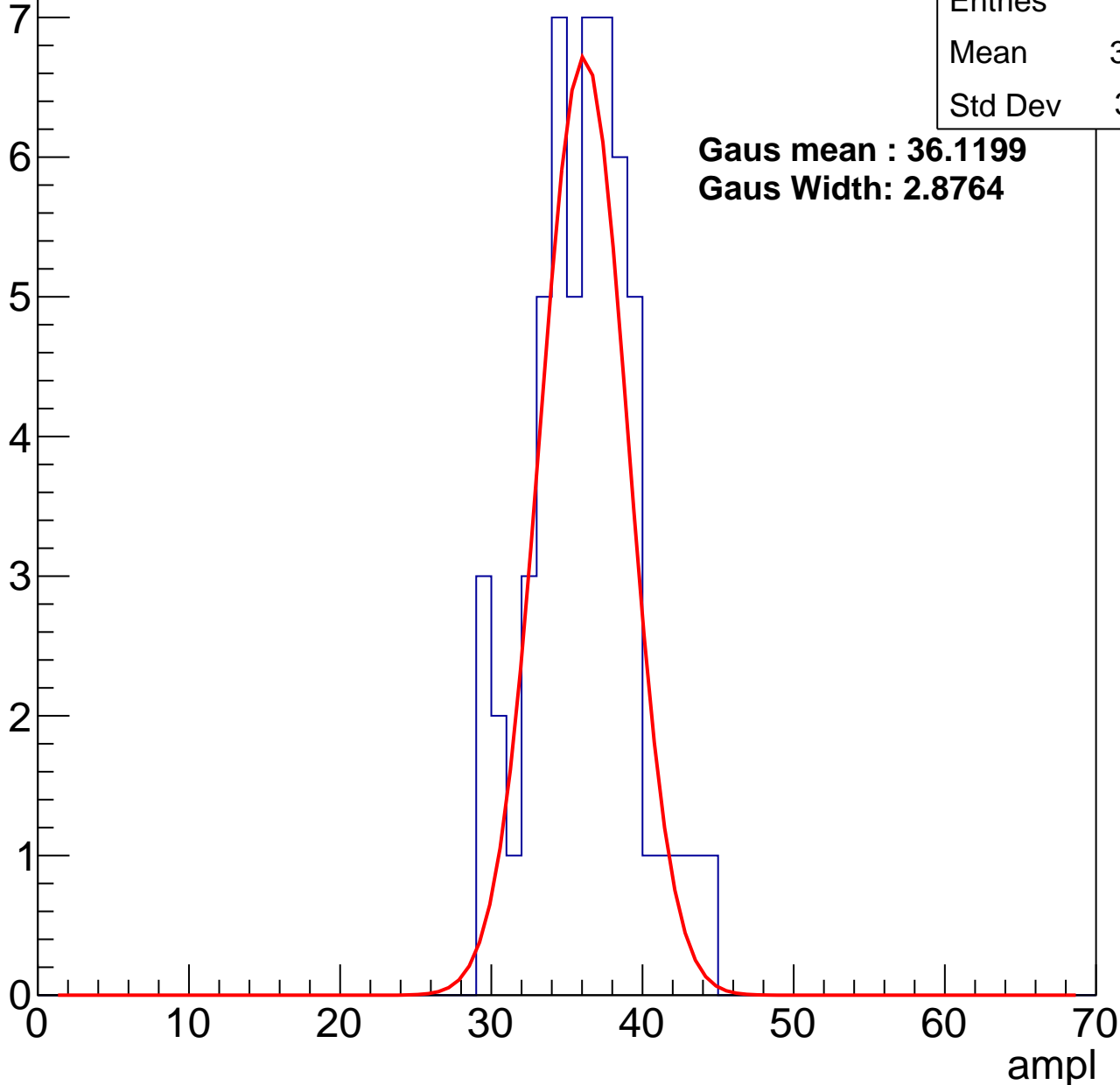
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.64
Std Dev	3.351

Gaus mean : 36.1199

Gaus Width: 2.8764



B1L102S, U2-ch124, adc2

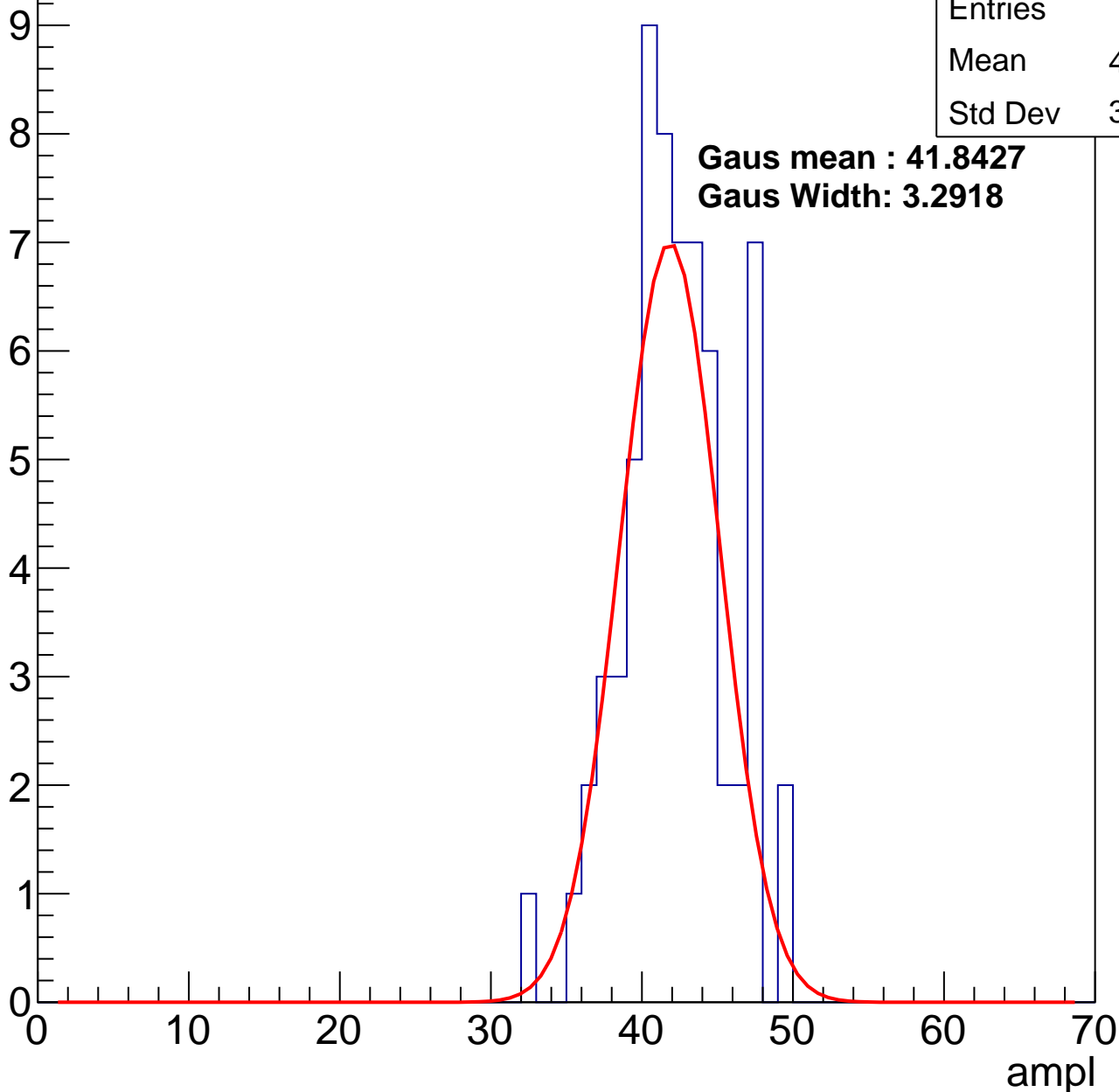
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.77
Std Dev	3.472

Gaus mean : 41.8427

Gaus Width: 3.2918

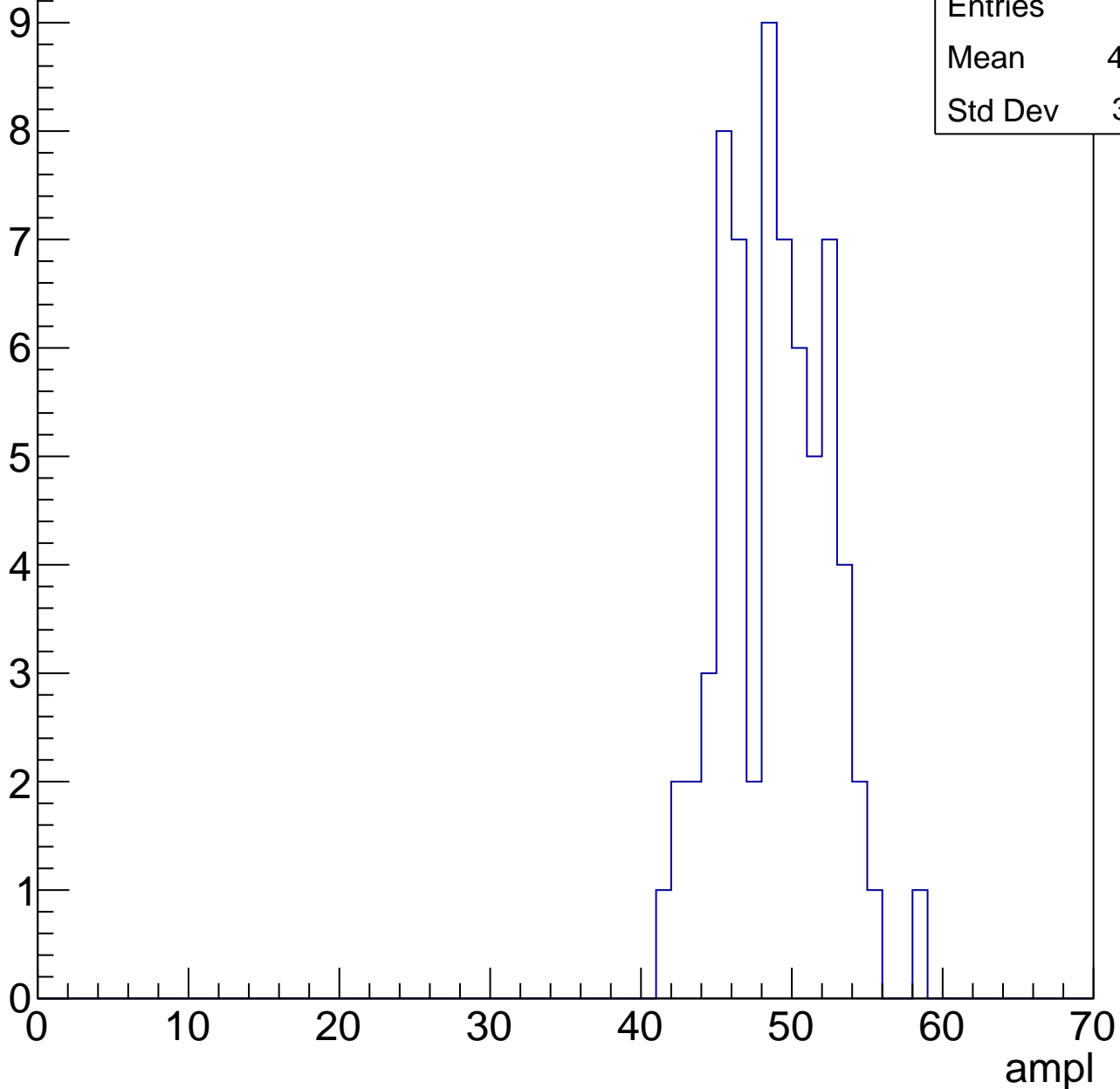


B1L102S, U2-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

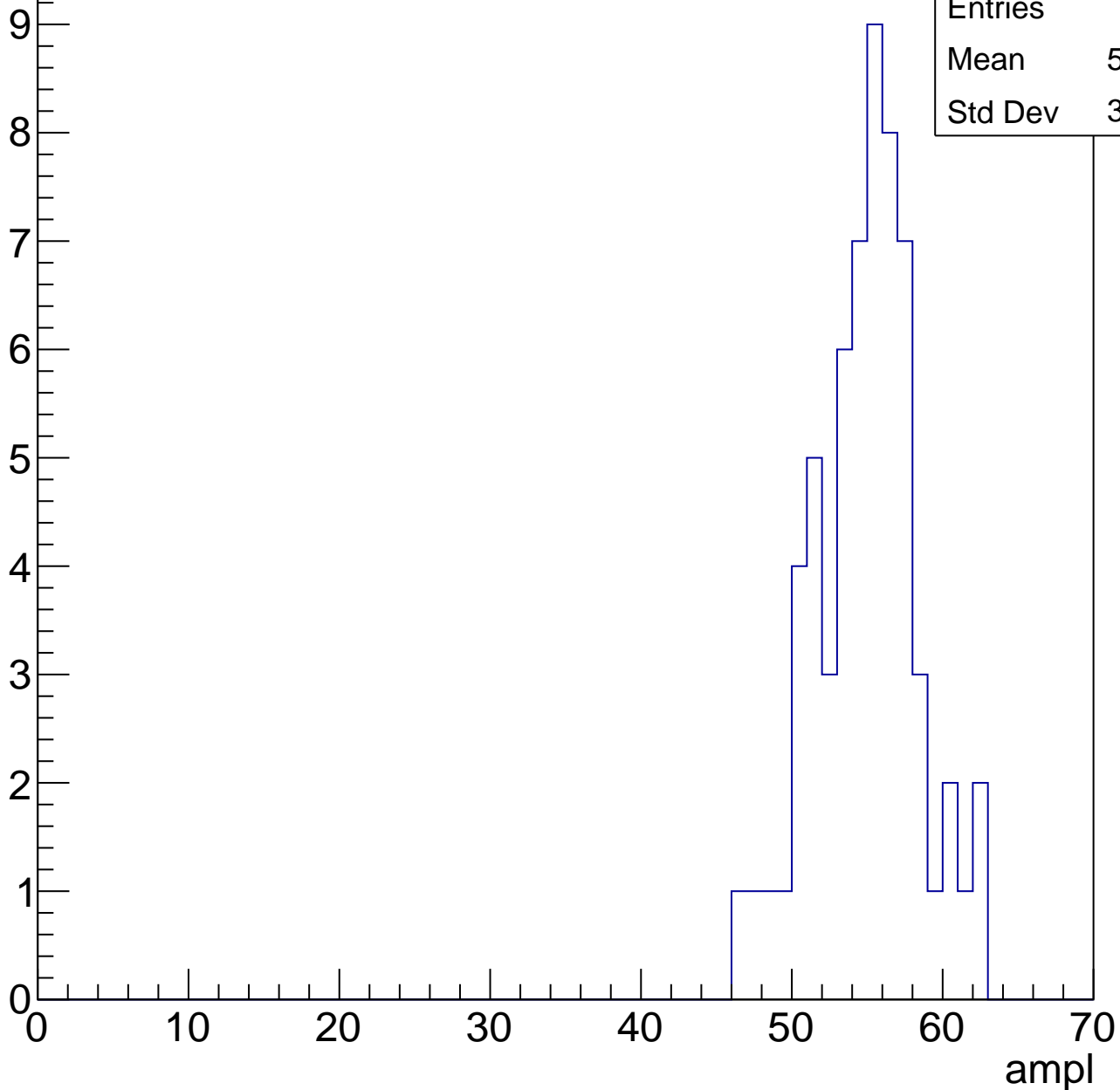
Entries	67
Mean	48.45
Std Dev	3.491



B1L102S, U2-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

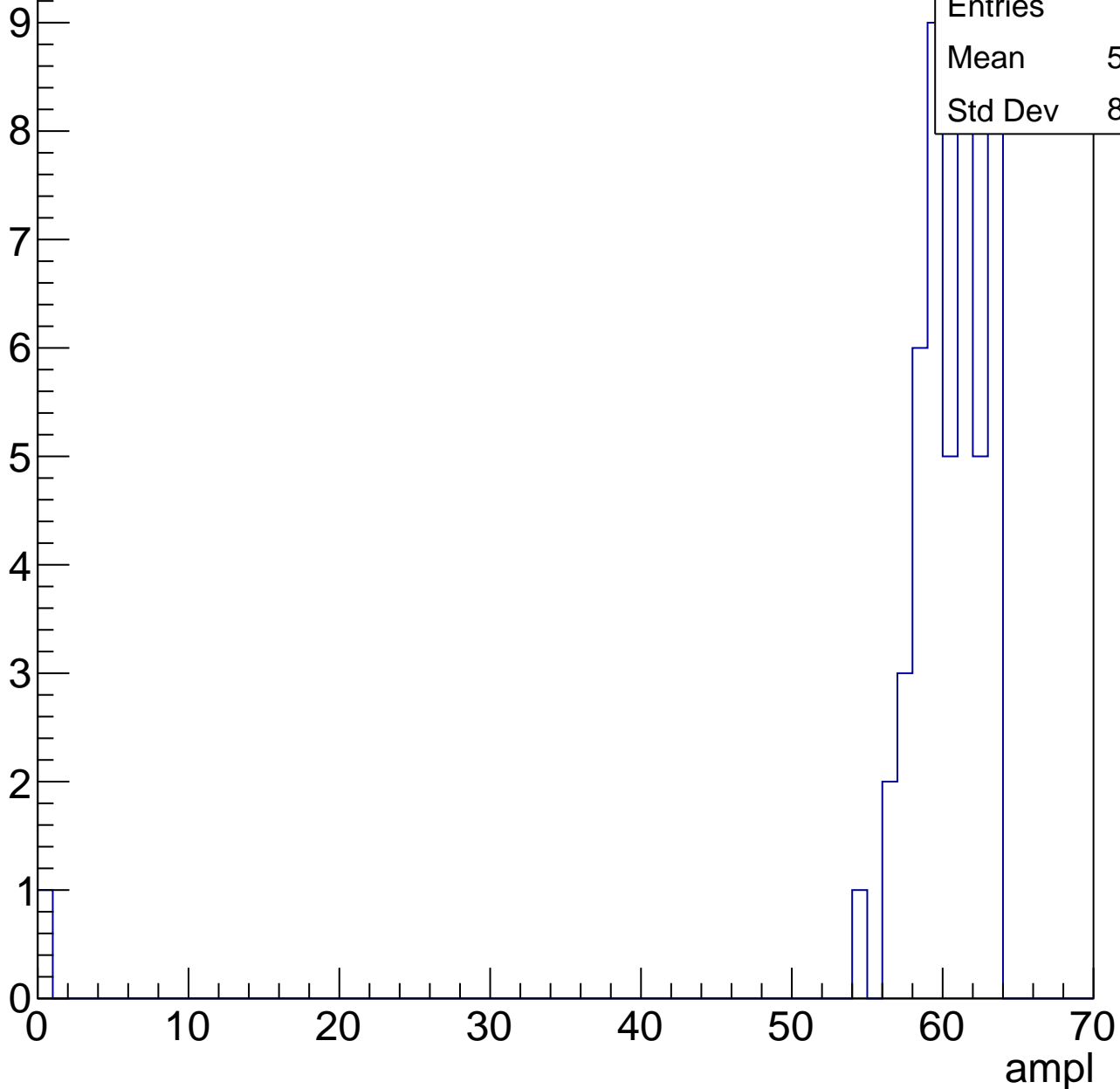


Entries	62
Mean	54.47
Std Dev	3.392

B1L102S, U2-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	8
Mean	61.5
Std Dev	1.225

B1L102S, U2-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U2-ch125, adc0

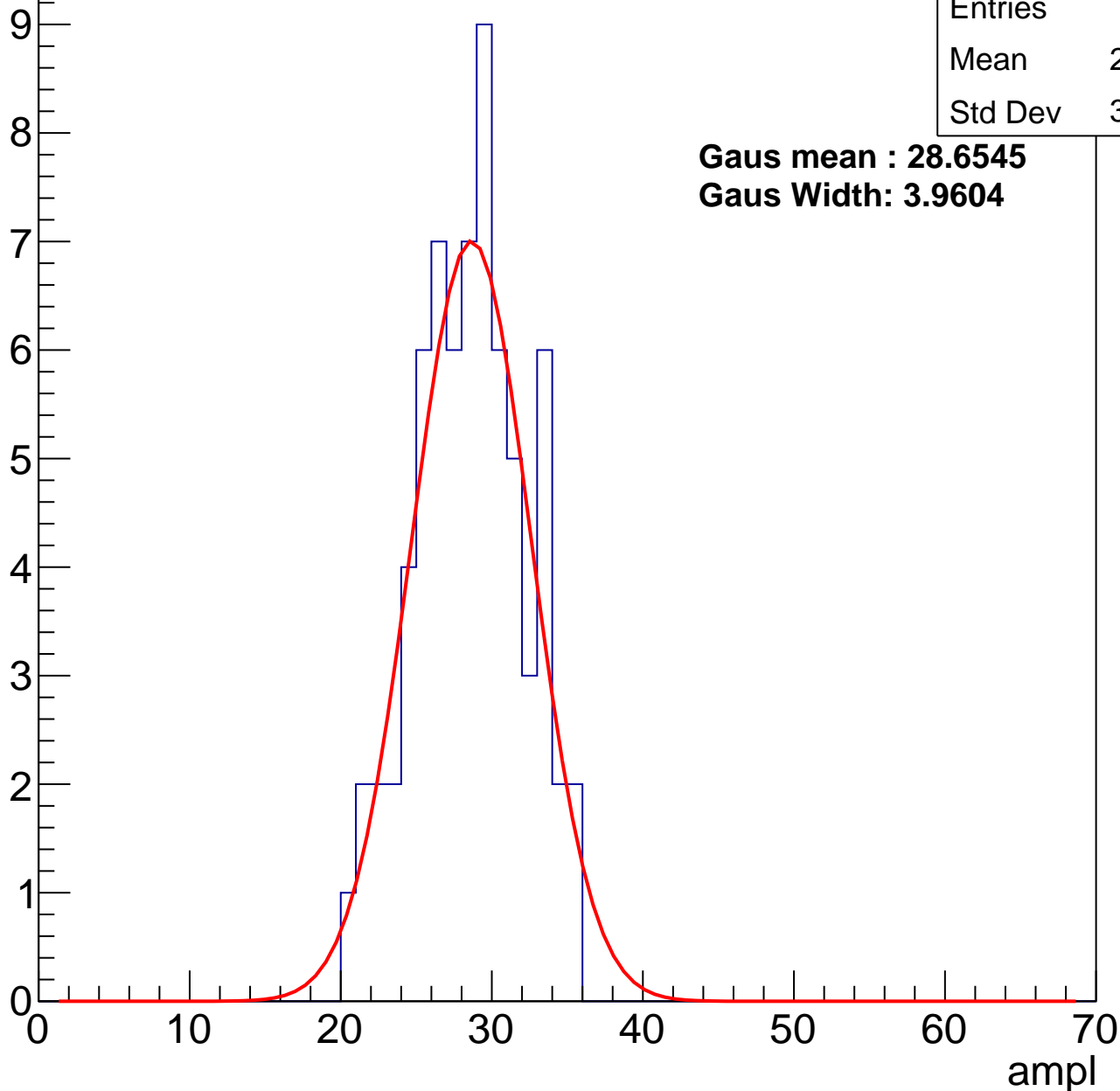
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.09
Std Dev	3.549

Gaus mean : 28.6545

Gaus Width: 3.9604



B1L102S, U2-ch125, adc1

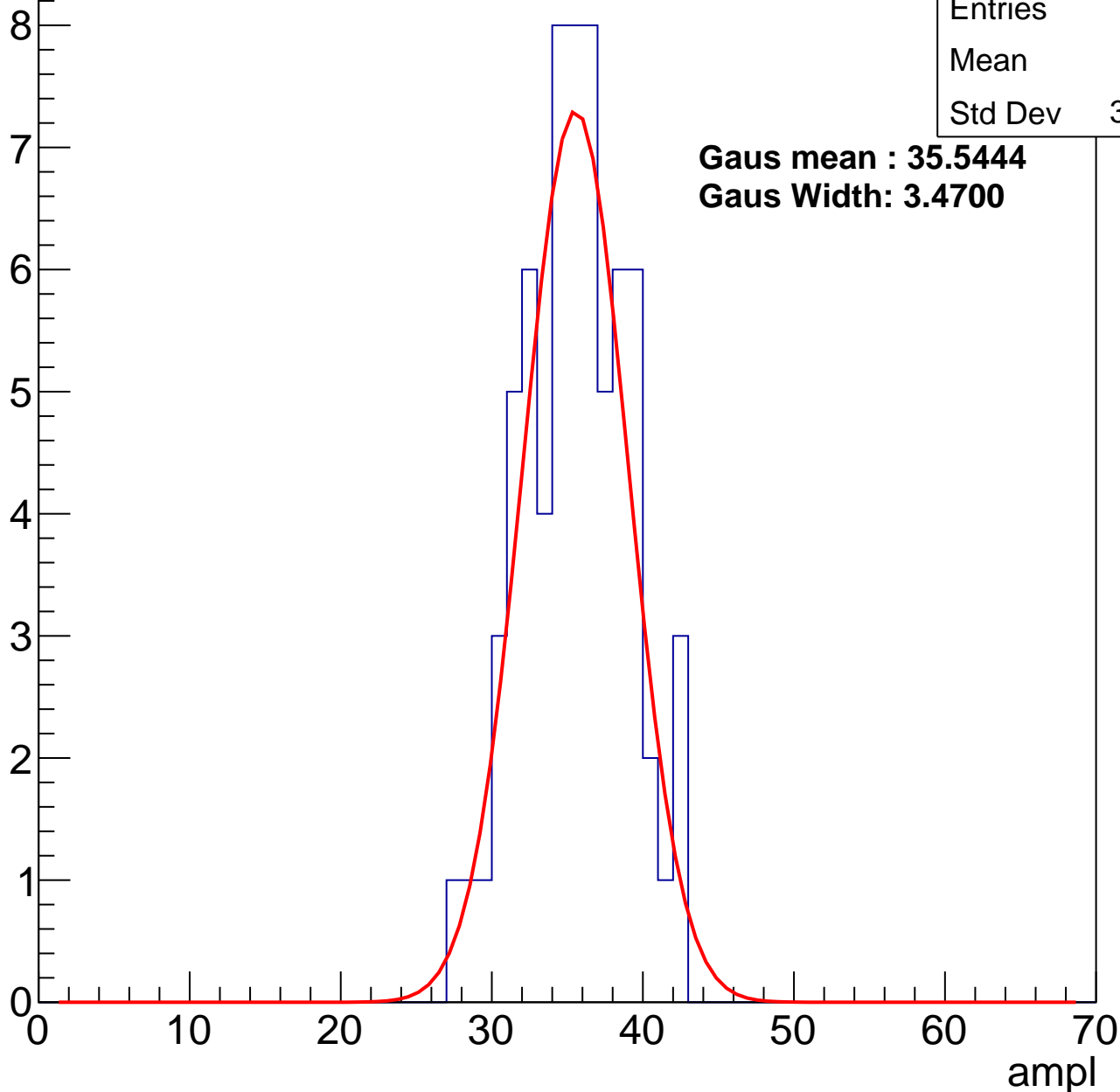
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.1
Std Dev	3.413

Gaus mean : 35.5444

Gaus Width: 3.4700



B1L102S, U2-ch125, adc2

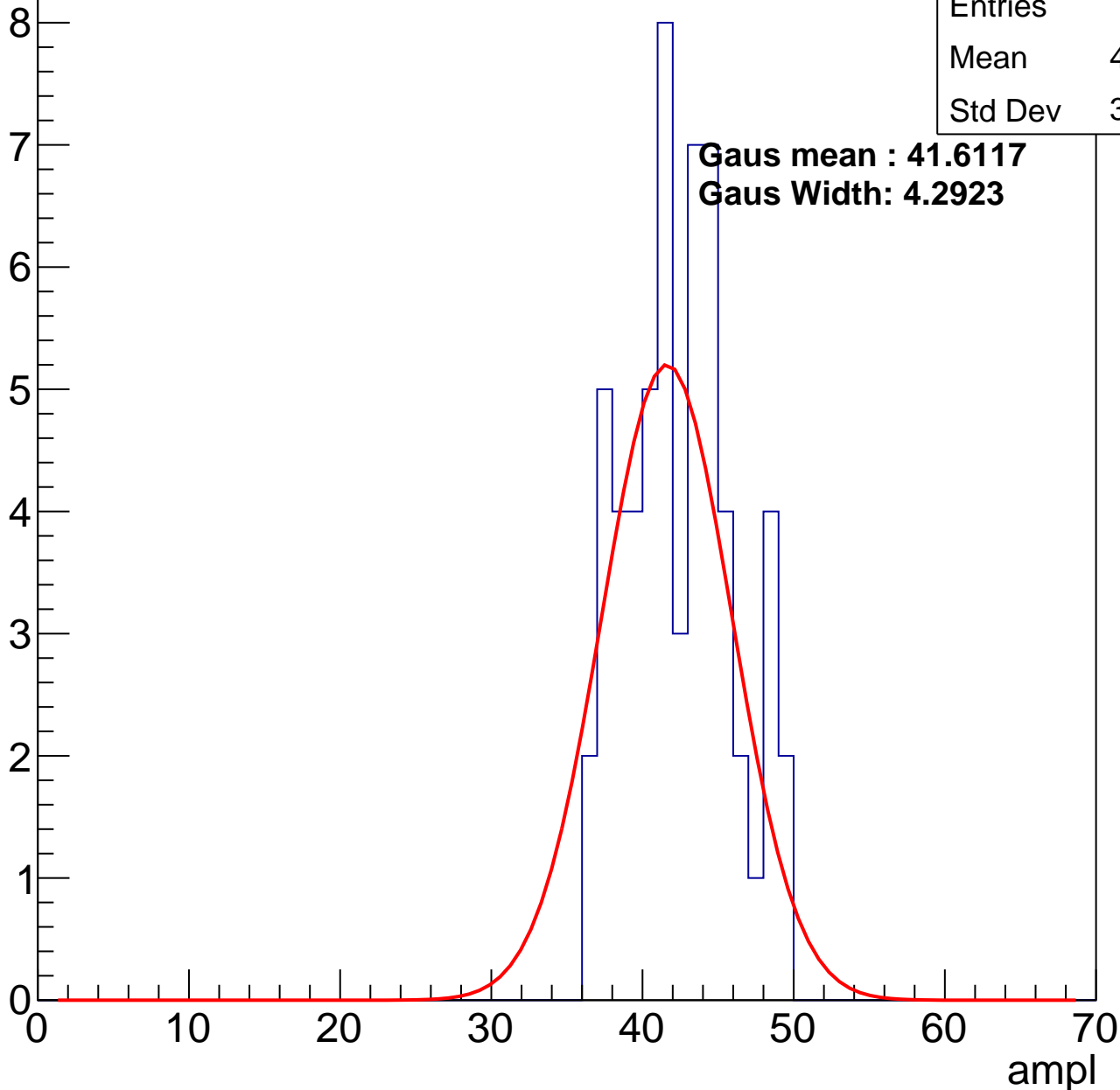
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.02
Std Dev	3.462

Gaus mean : 41.6117

Gaus Width: 4.2923

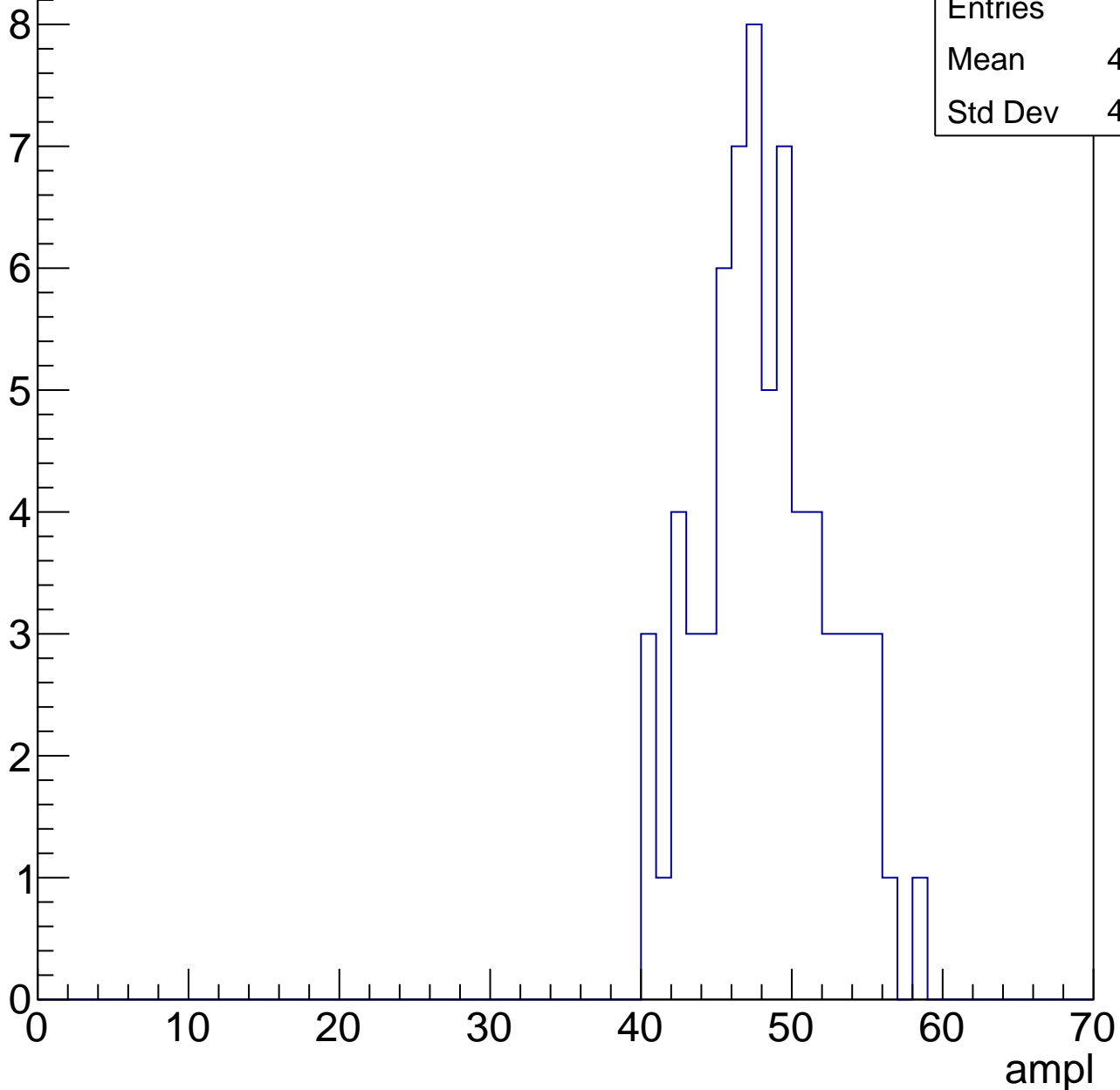


B1L102S, U2-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.84
Std Dev	4.166

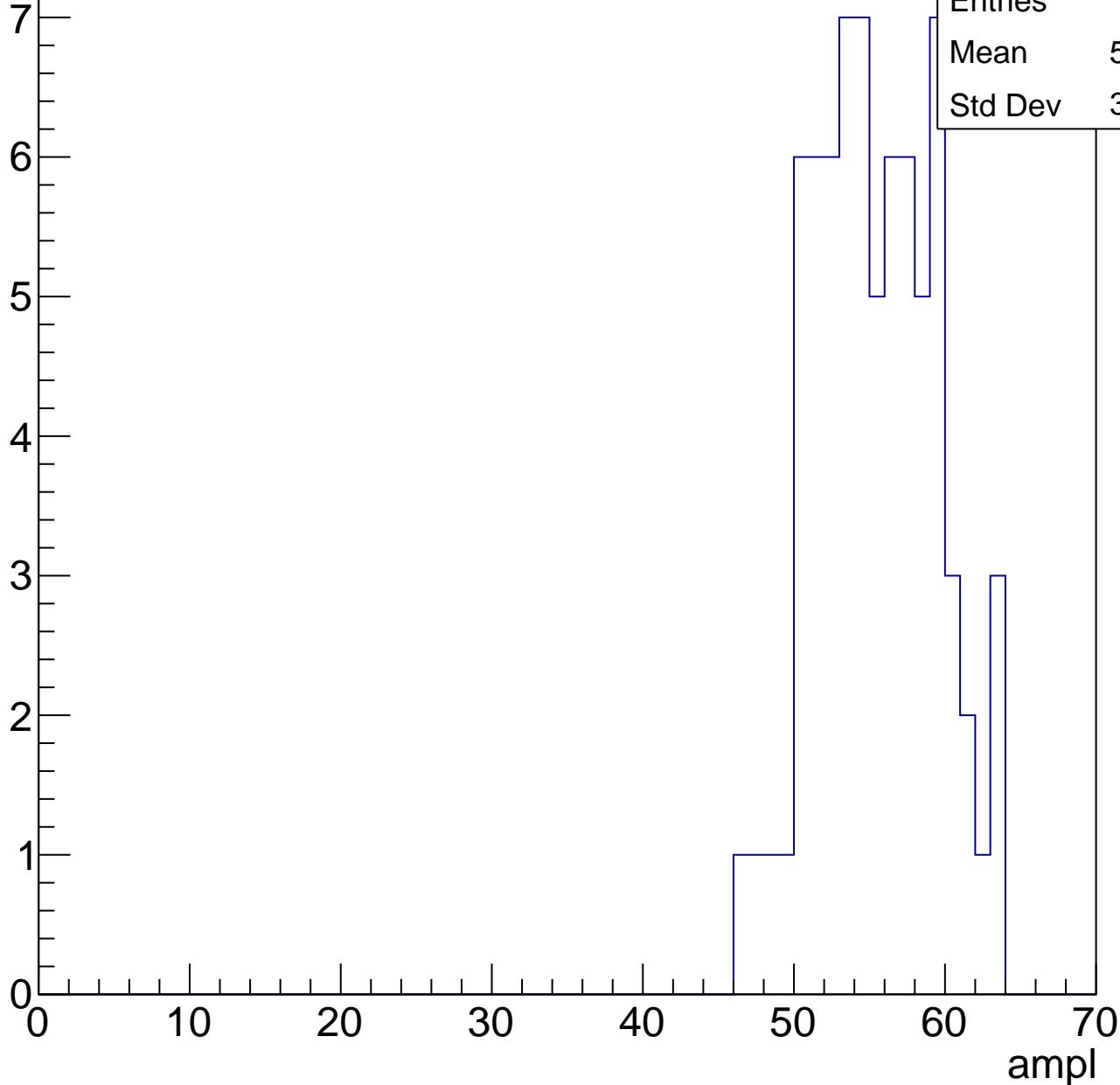


B1L102S, U2-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	54.95
Std Dev	3.925

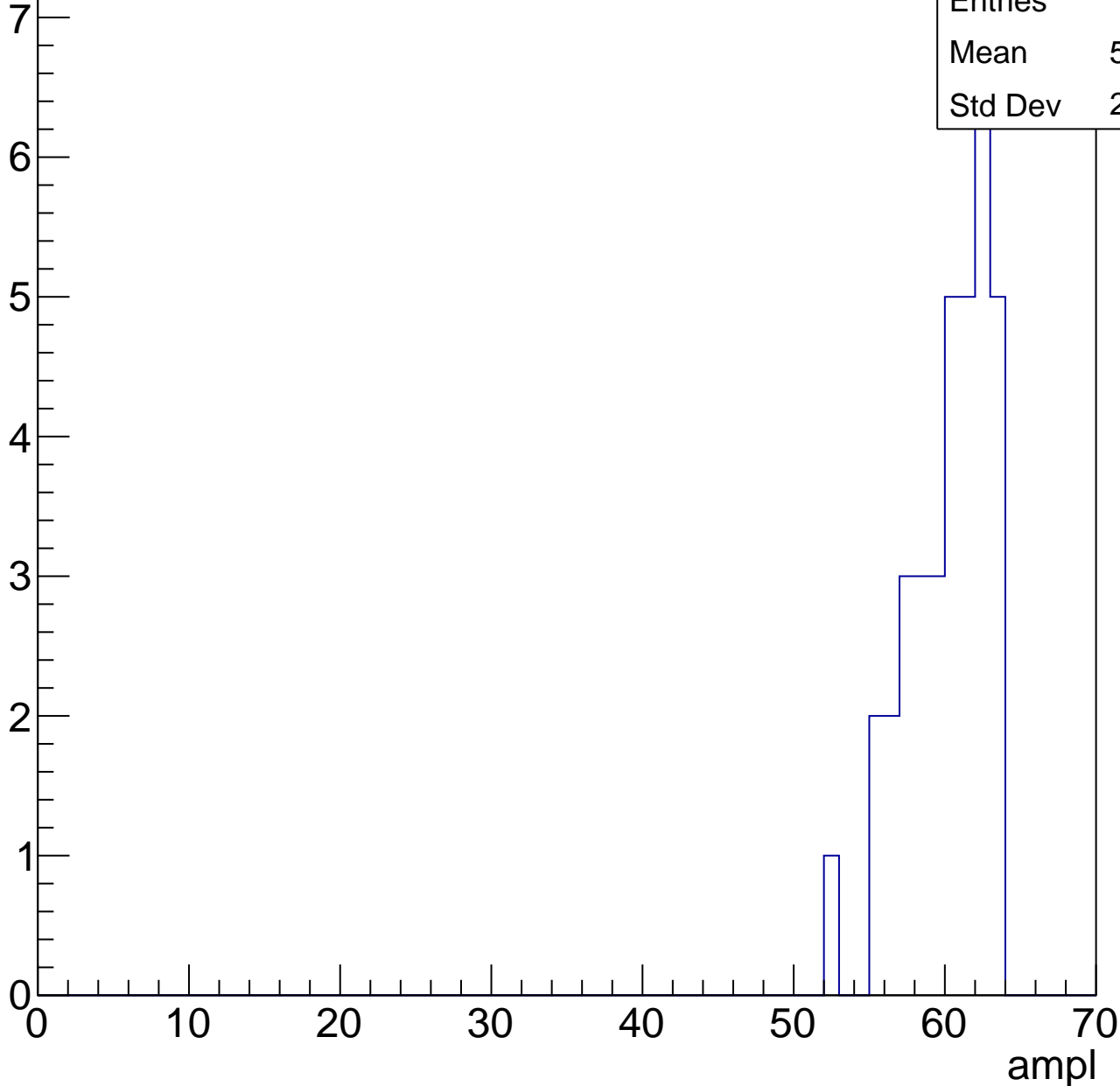


B1L102S, U2-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

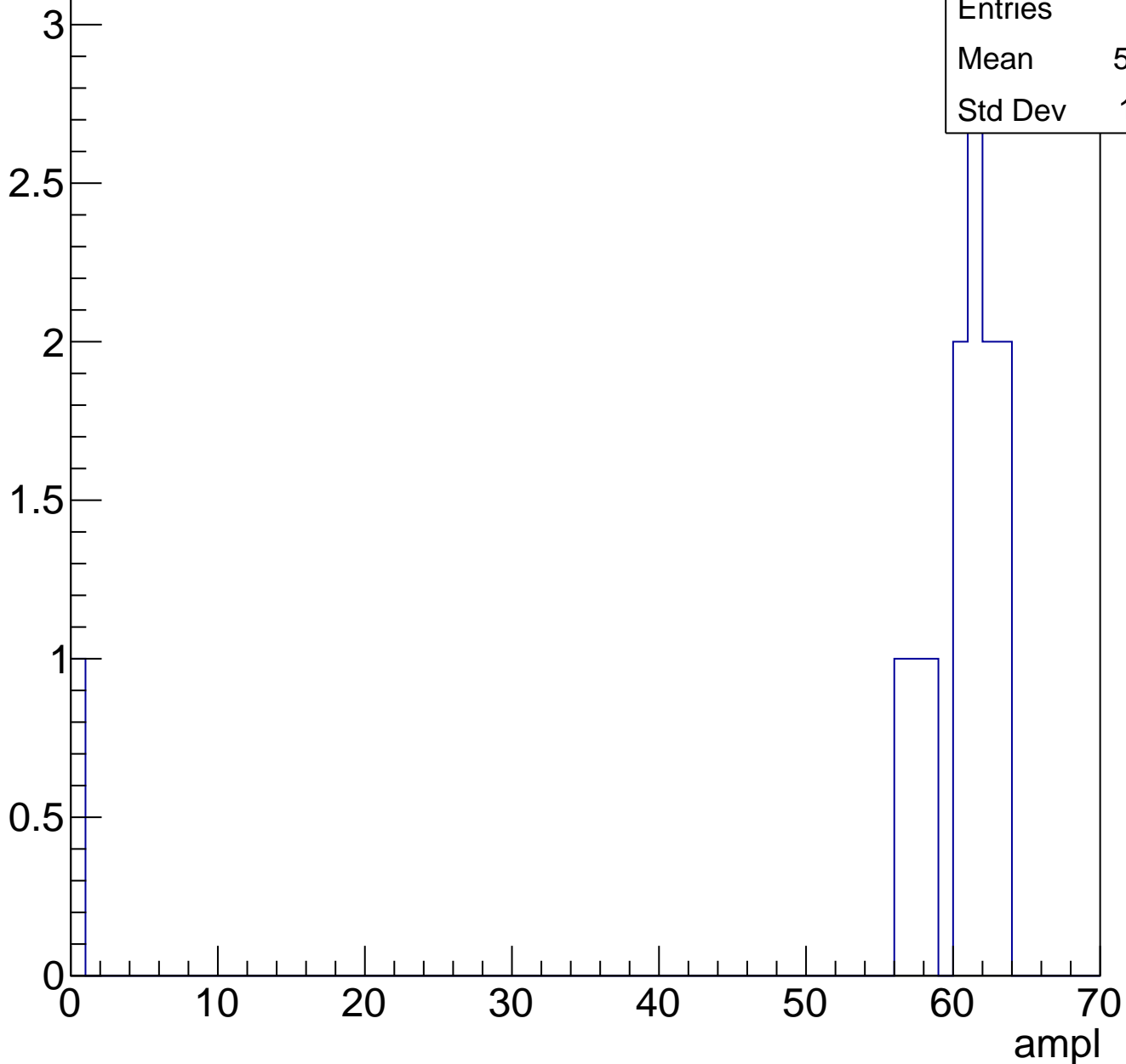
Entries	36
Mean	59.72
Std Dev	2.704



B1L102S, U2-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U2-ch126, adc0

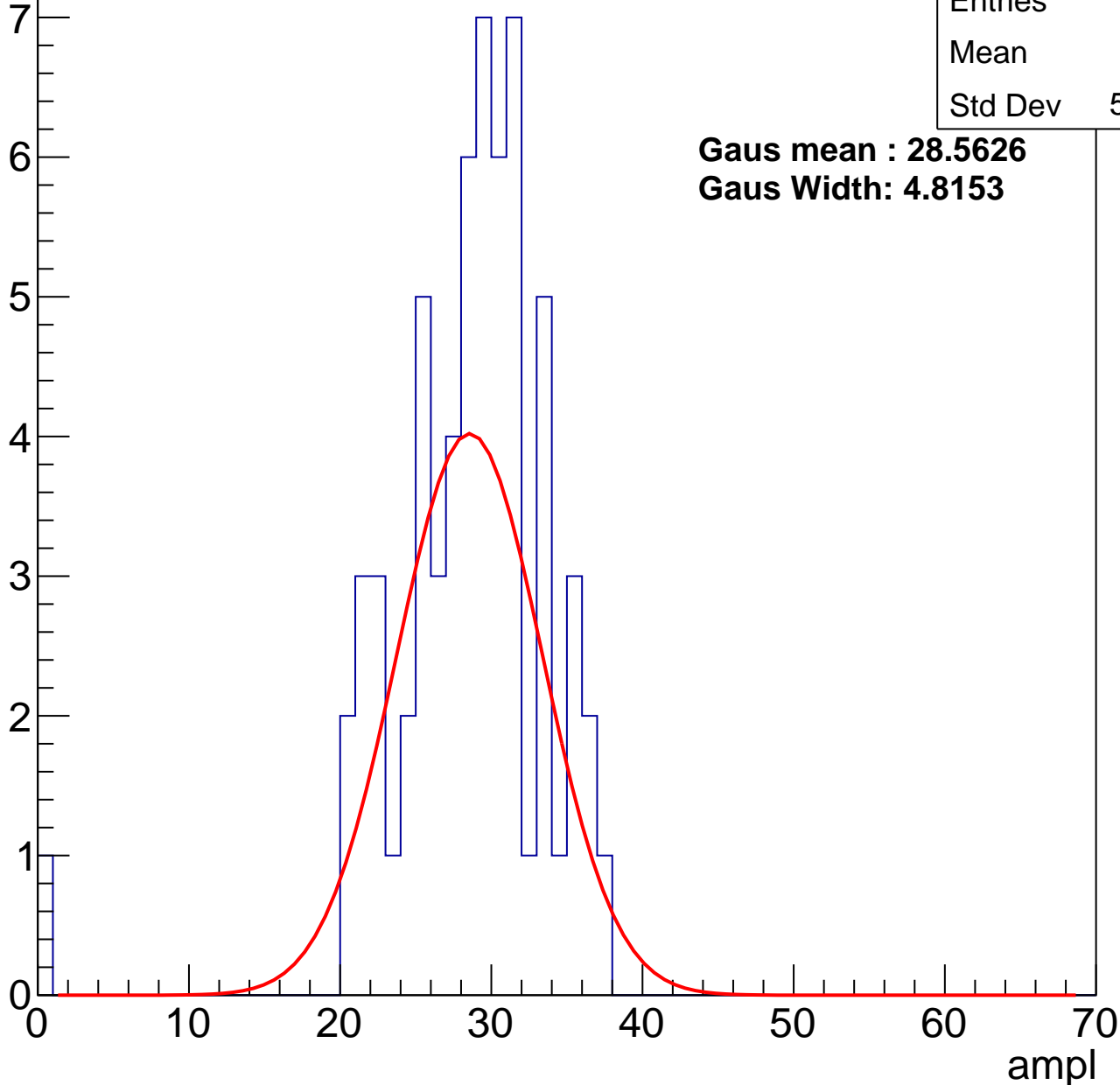
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28
Std Dev	5.509

Gaus mean : 28.5626

Gaus Width: 4.8153



B1L102S, U2-ch126, adc1

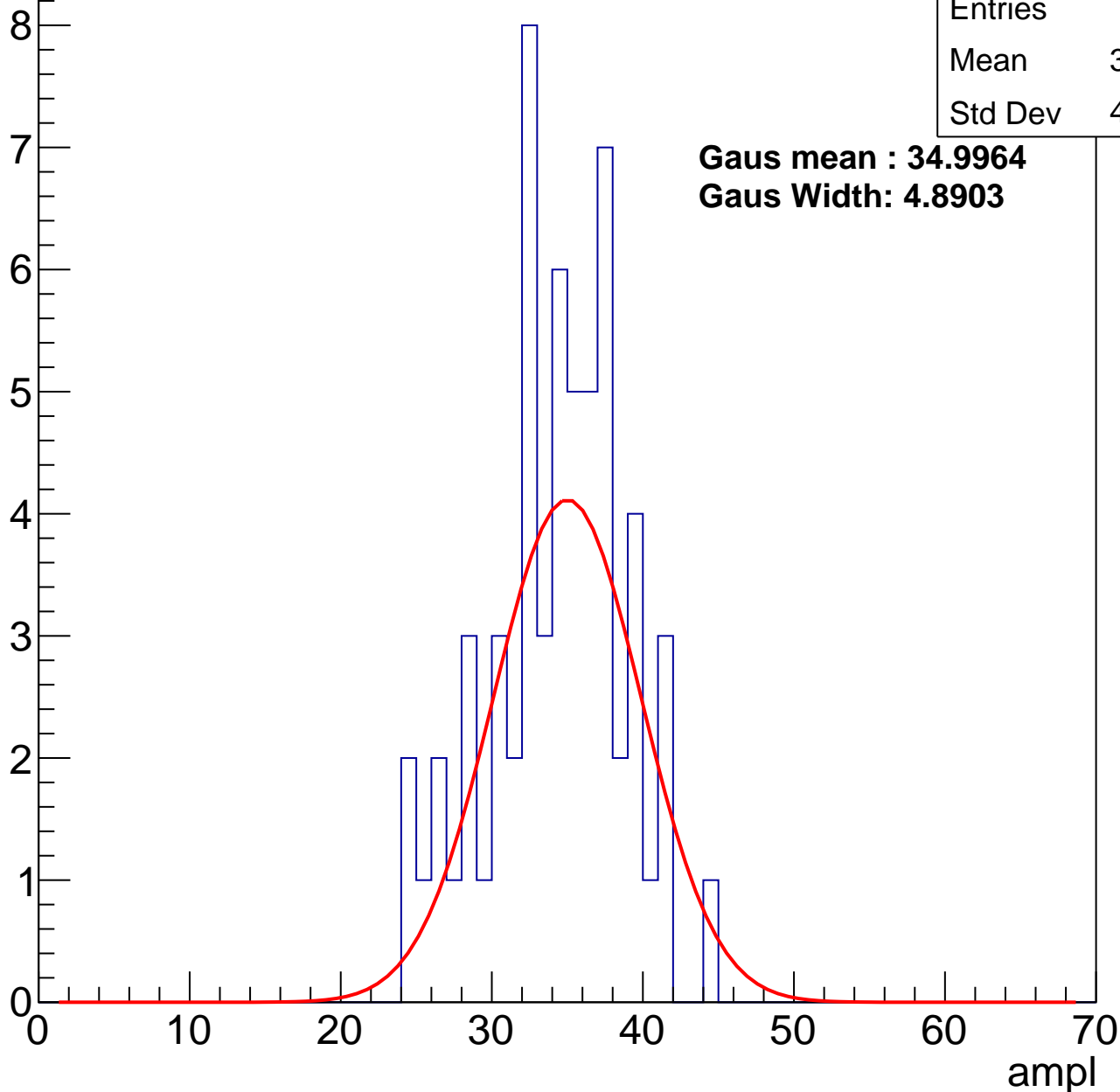
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	33.82
Std Dev	4.459

Gaus mean : 34.9964

Gaus Width: 4.8903



B1L102S, U2-ch126, adc2

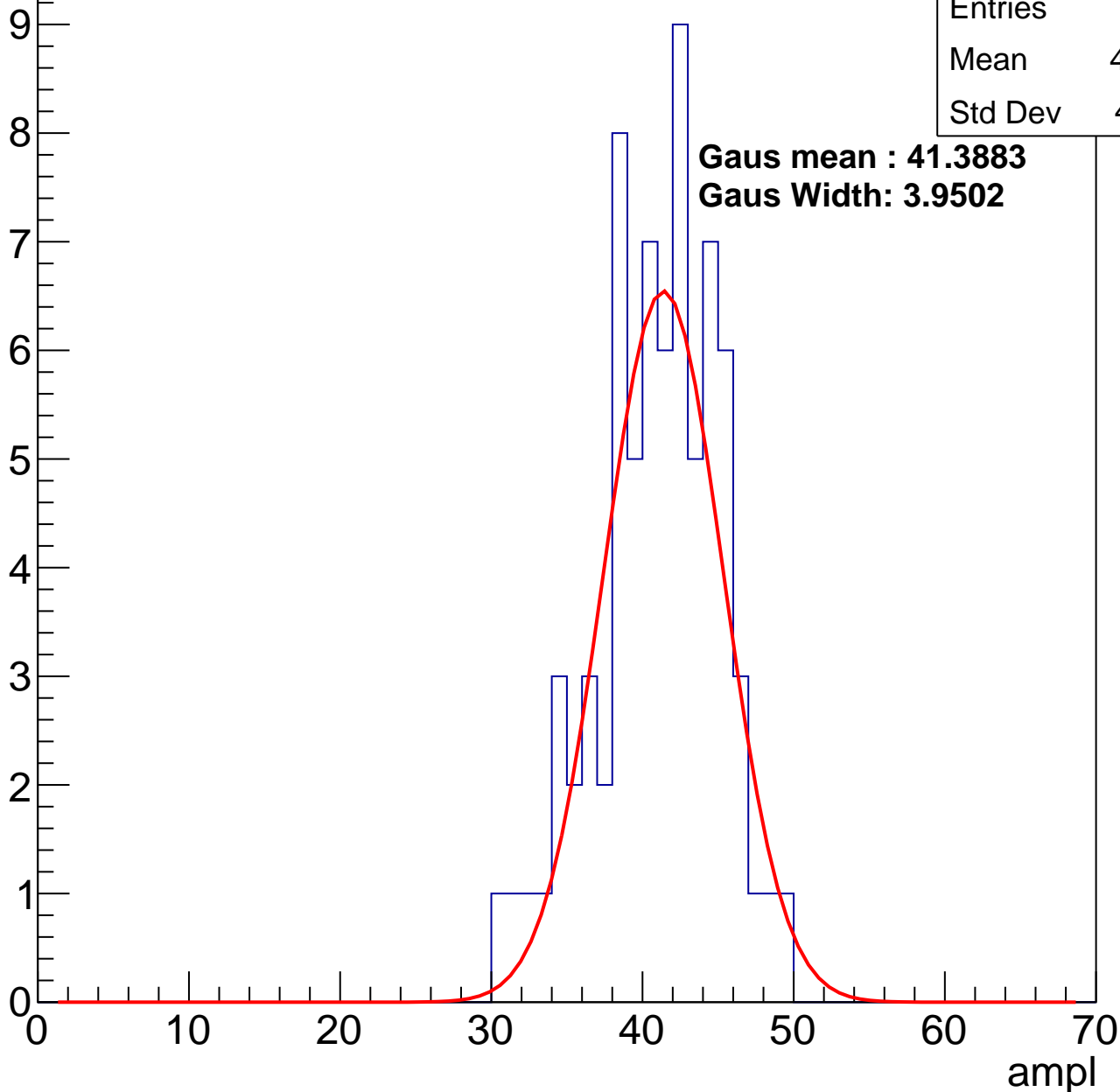
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	40.52
Std Dev	4.031

Gaus mean : 41.3883

Gaus Width: 3.9502

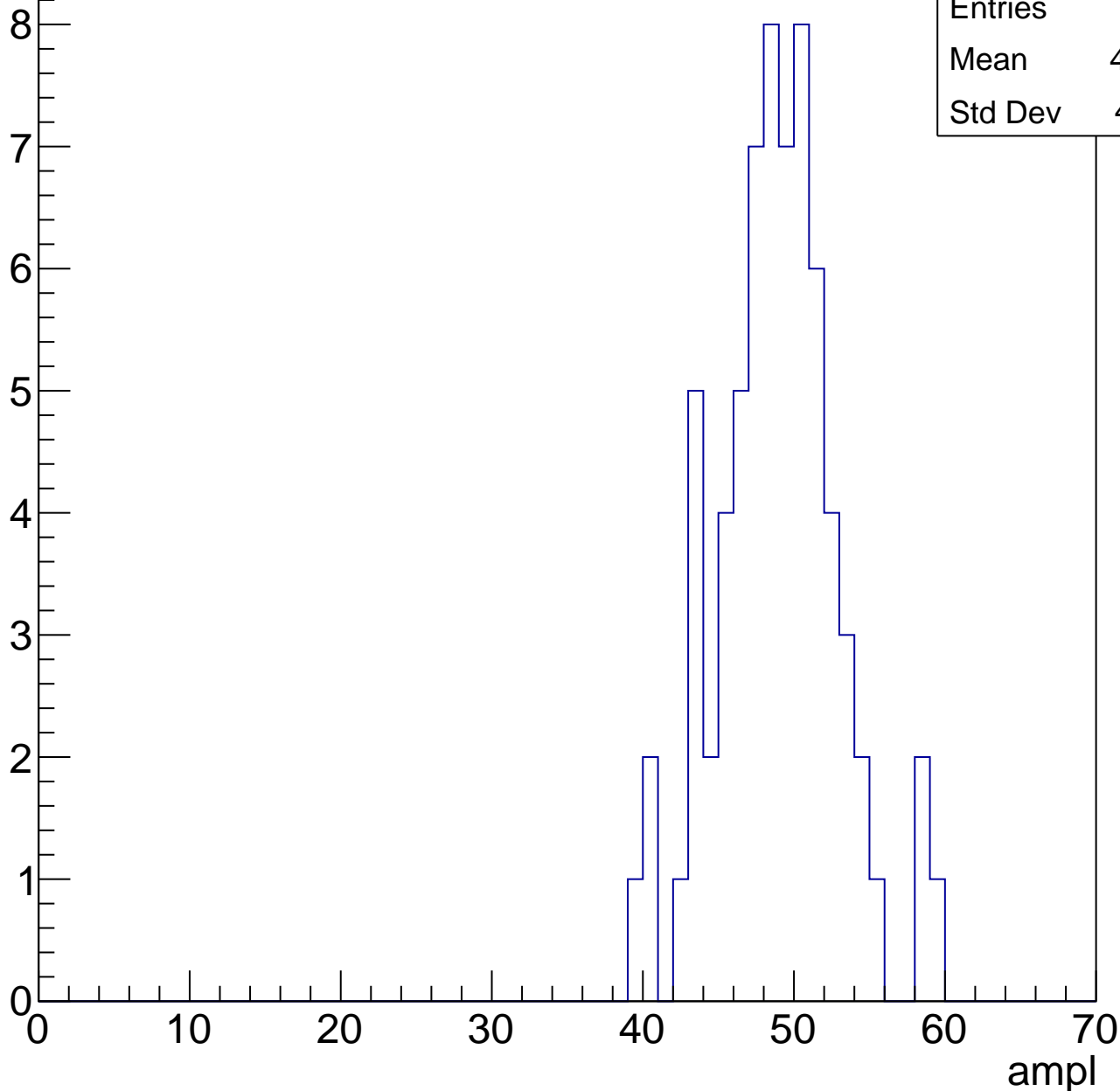


B1L102S, U2-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.42
Std Dev	4.041

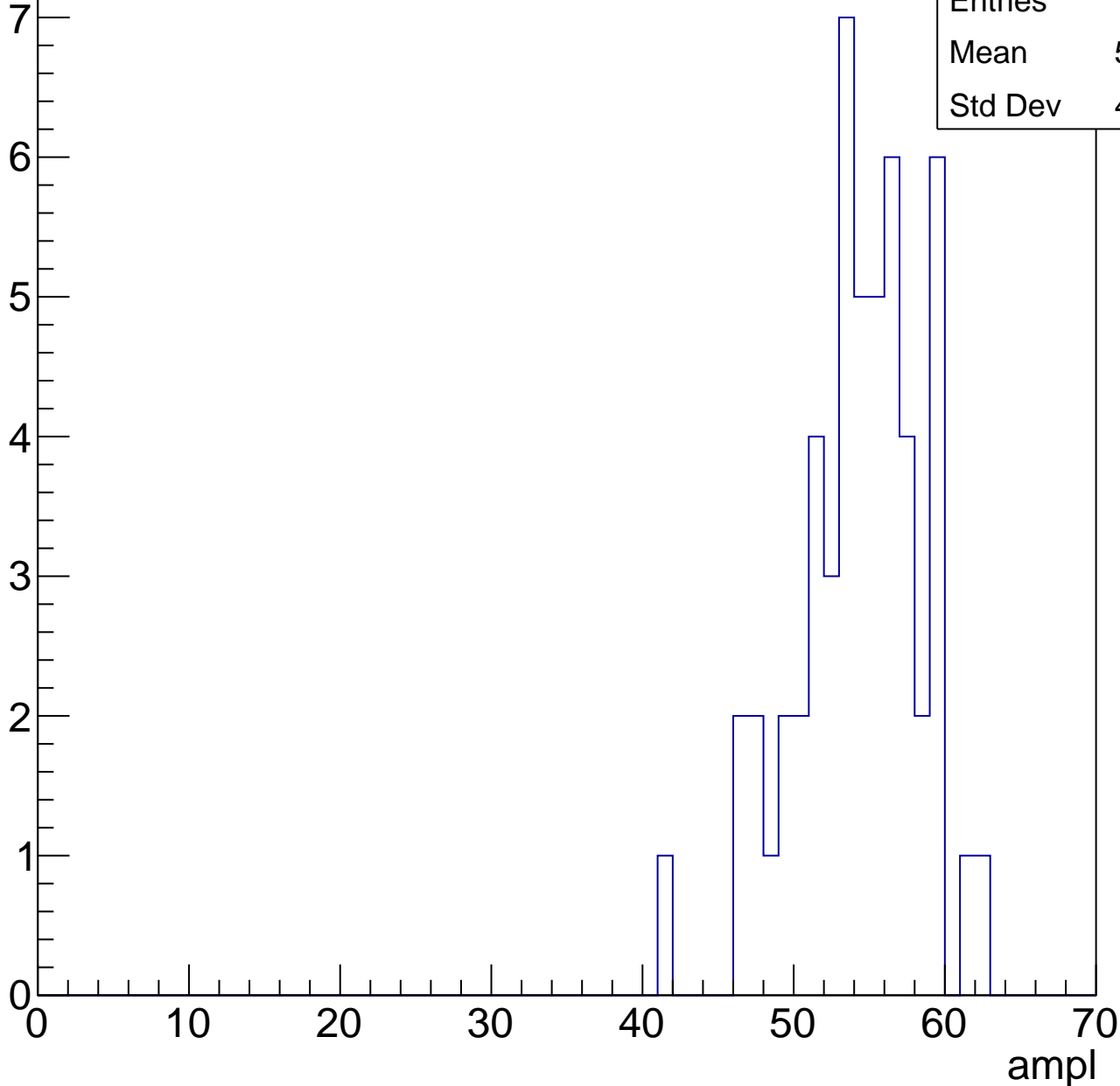


B1L102S, U2-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.81
Std Dev	4.141

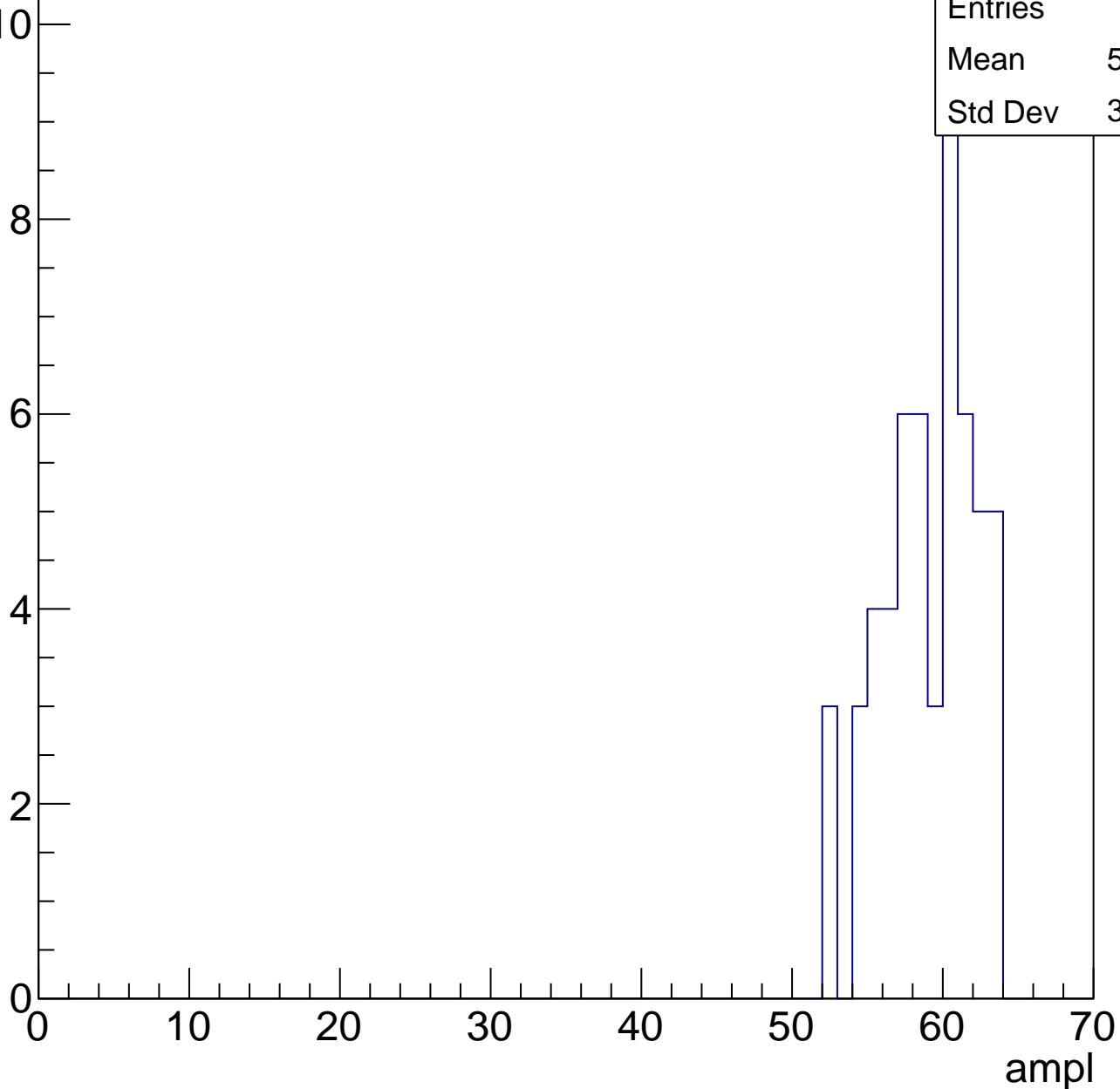


B1L102S, U2-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

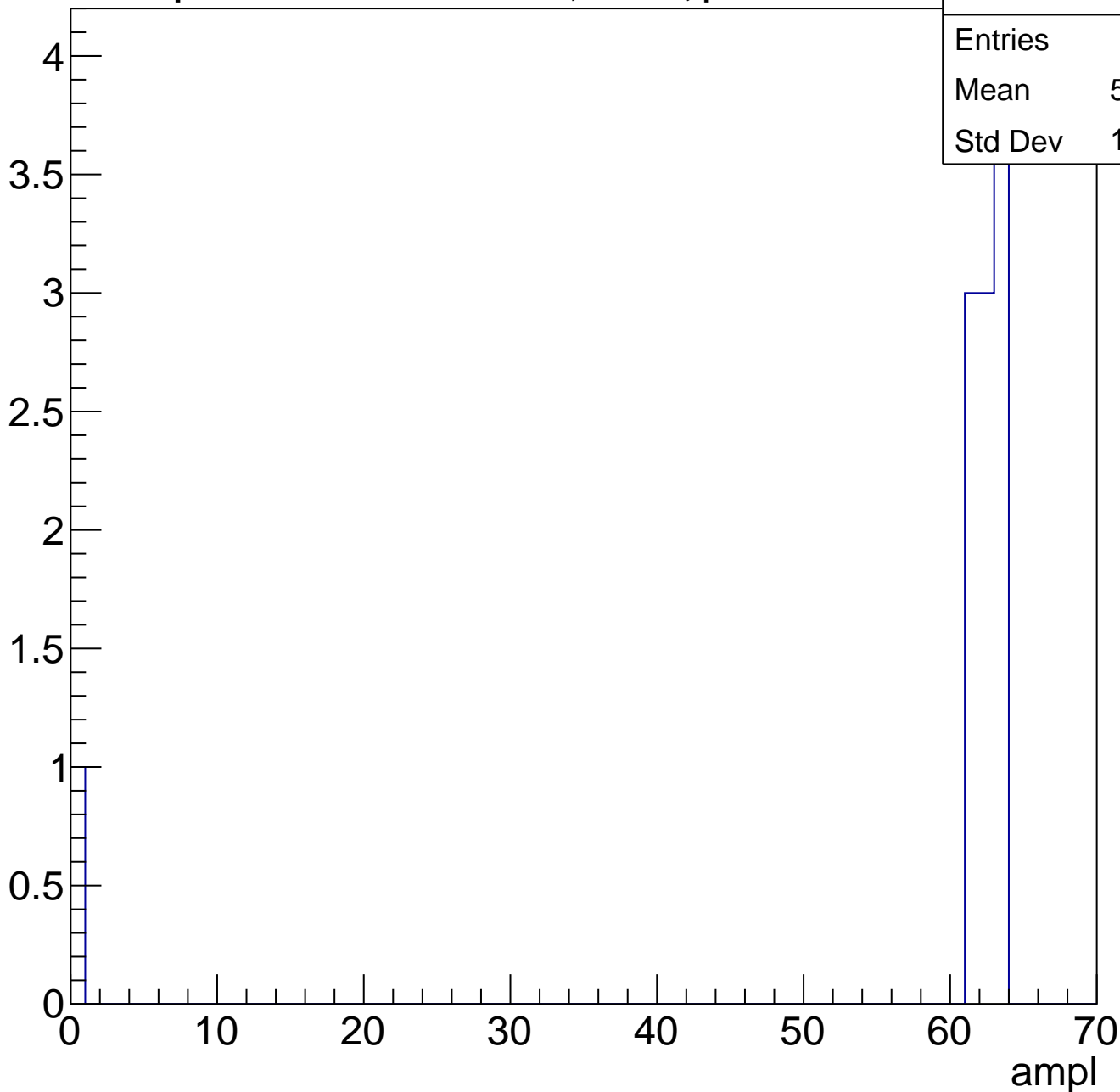
Entries	55
Mean	58.55
Std Dev	3.014



B1L102S, U2-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U2-ch127, adc0

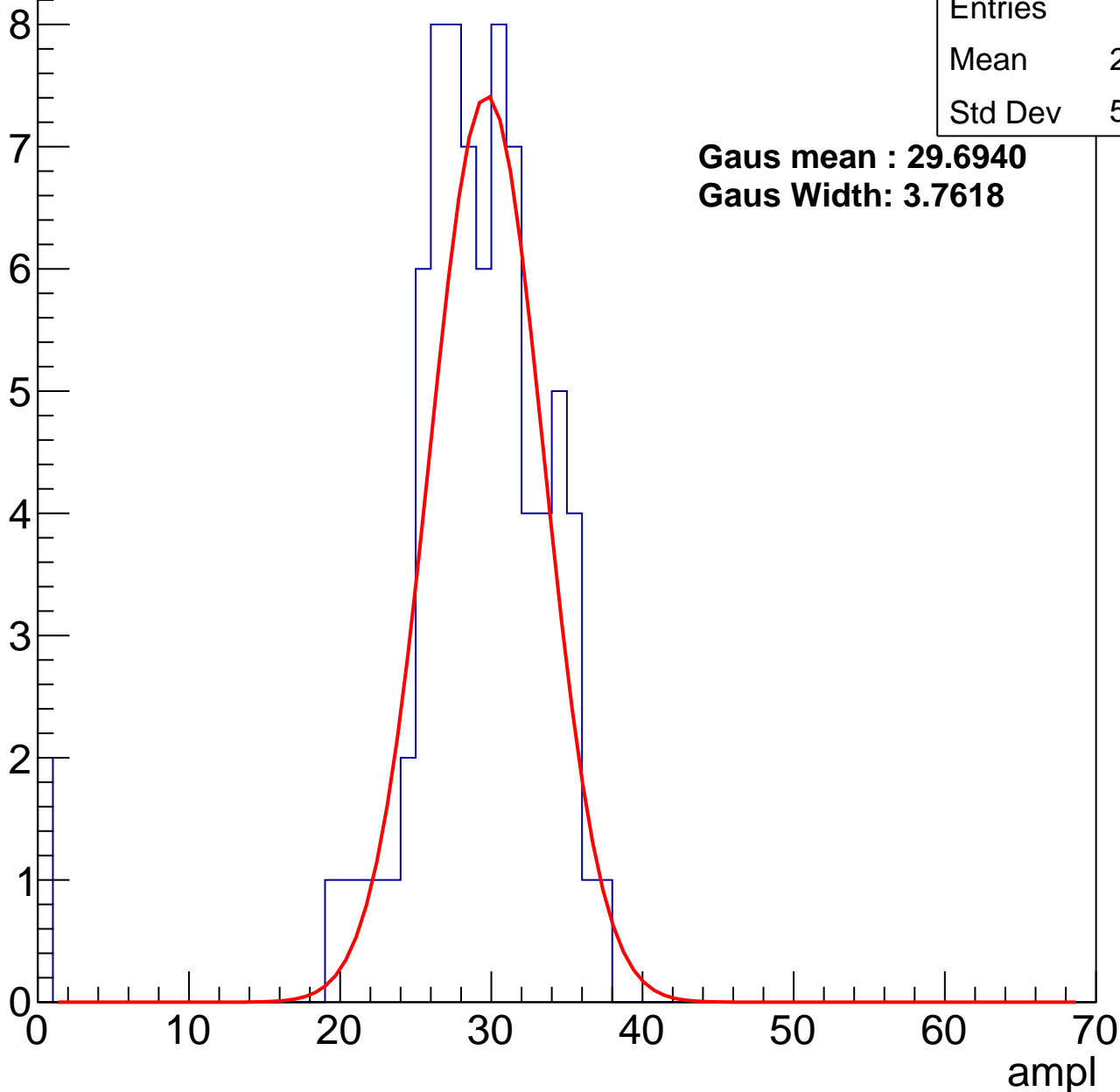
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.17
Std Dev	5.919

Gaus mean : 29.6940

Gaus Width: 3.7618



B1L102S, U2-ch127, adc1

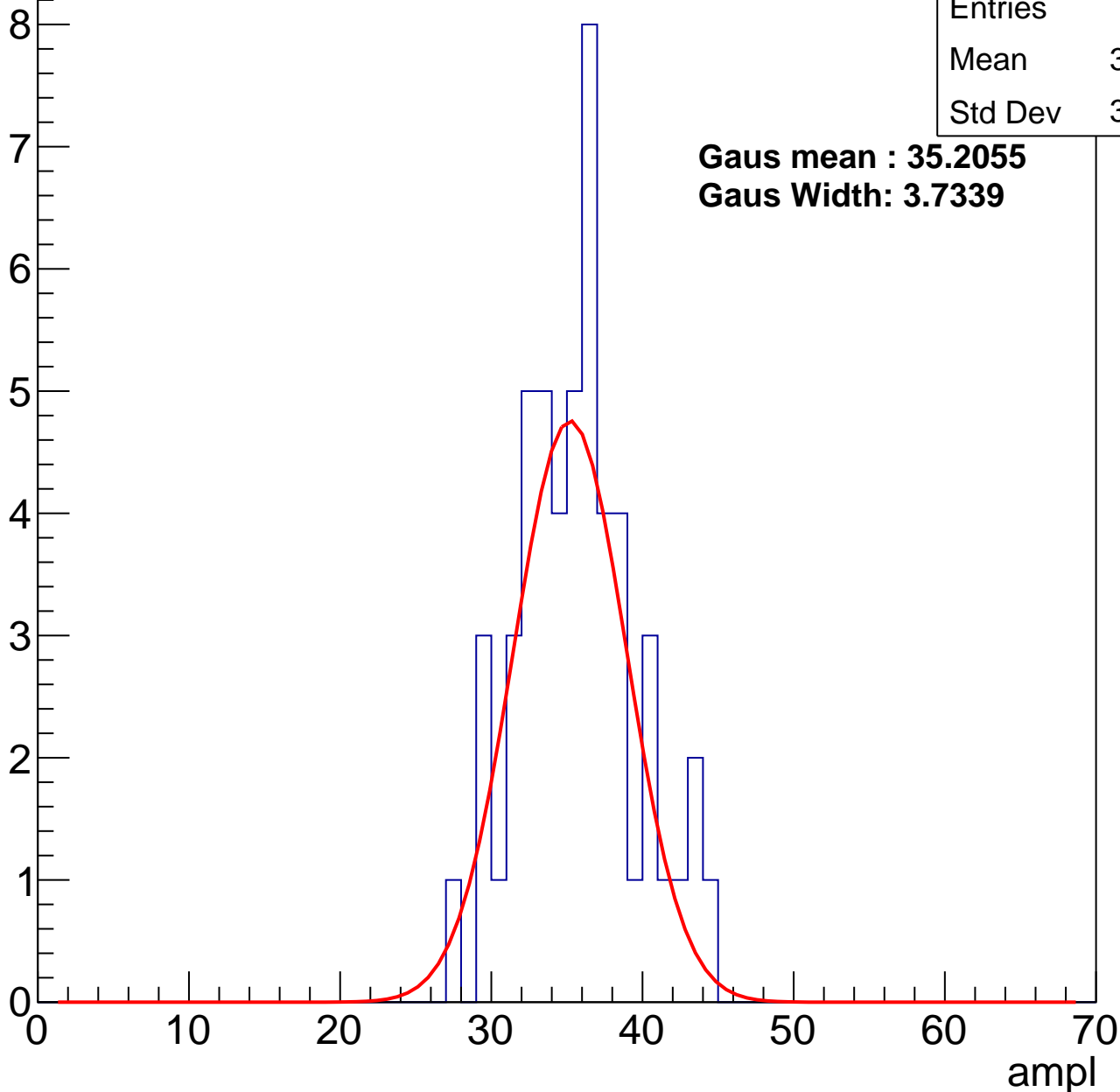
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	35.25
Std Dev	3.812

Gaus mean : 35.2055

Gaus Width: 3.7339



B1L102S, U2-ch127, adc2

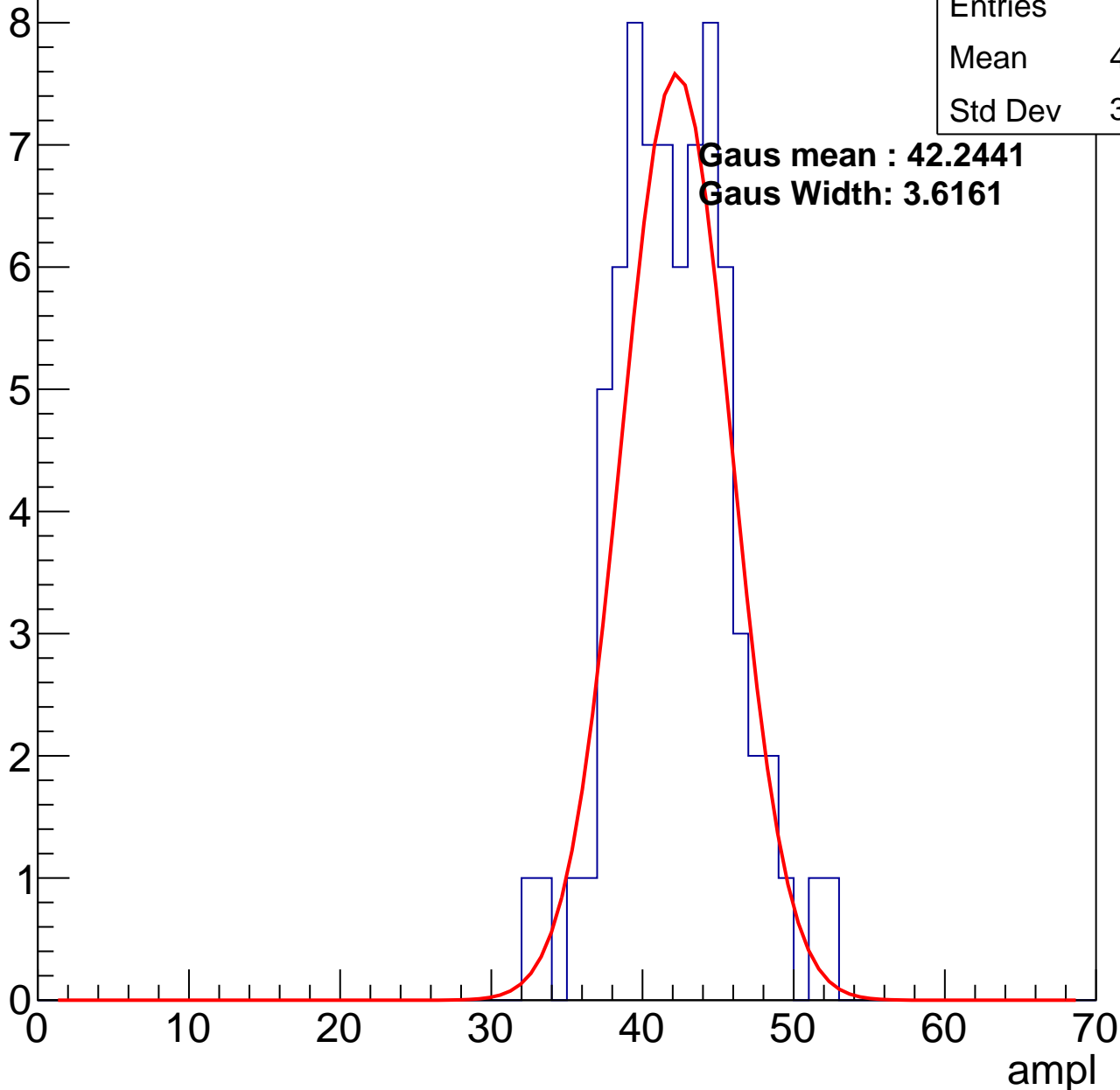
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	41.66
Std Dev	3.818

Gaus mean : 42.2441

Gaus Width: 3.6161

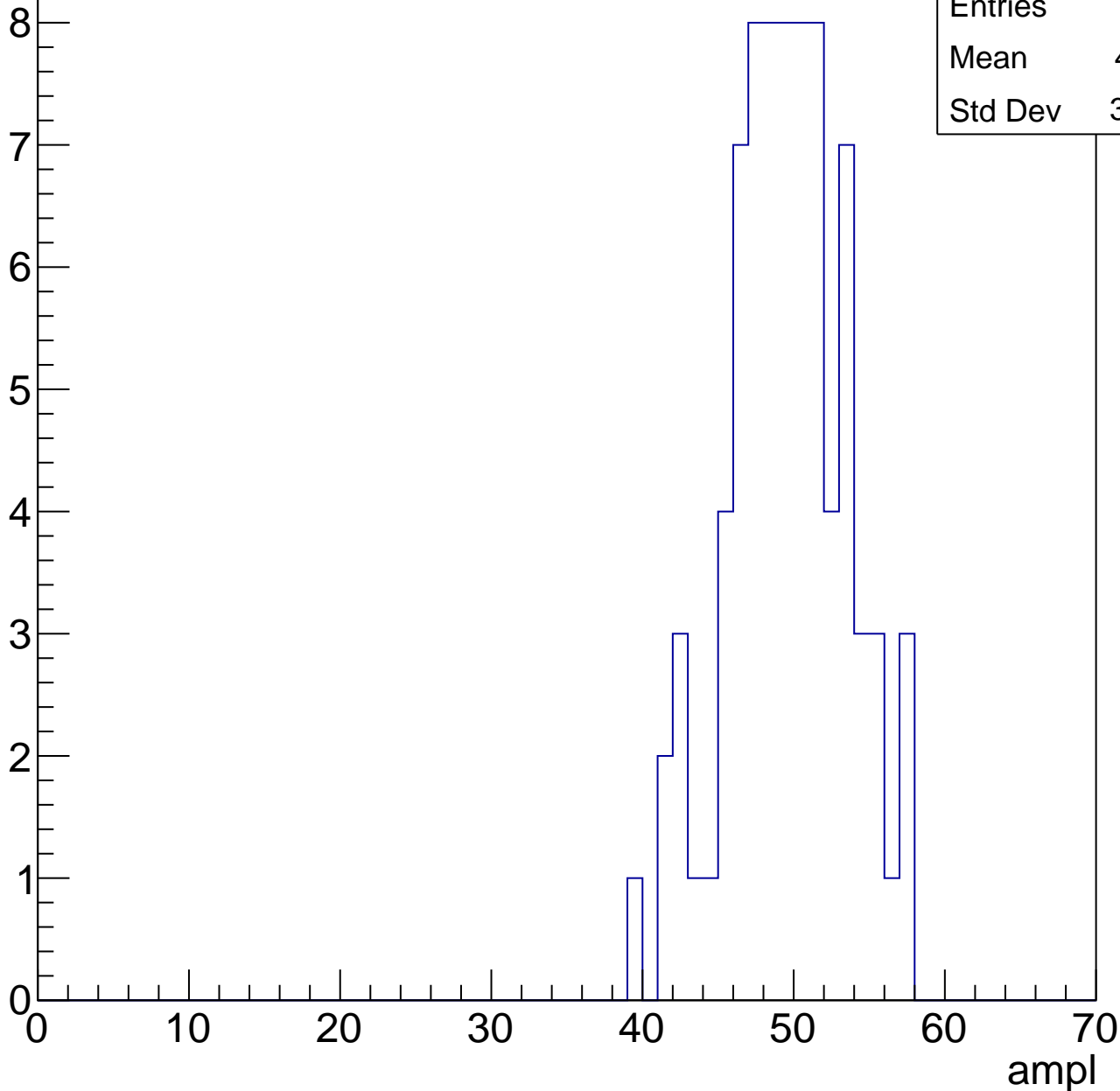


B1L102S, U2-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	49.11
Std Dev	3.895

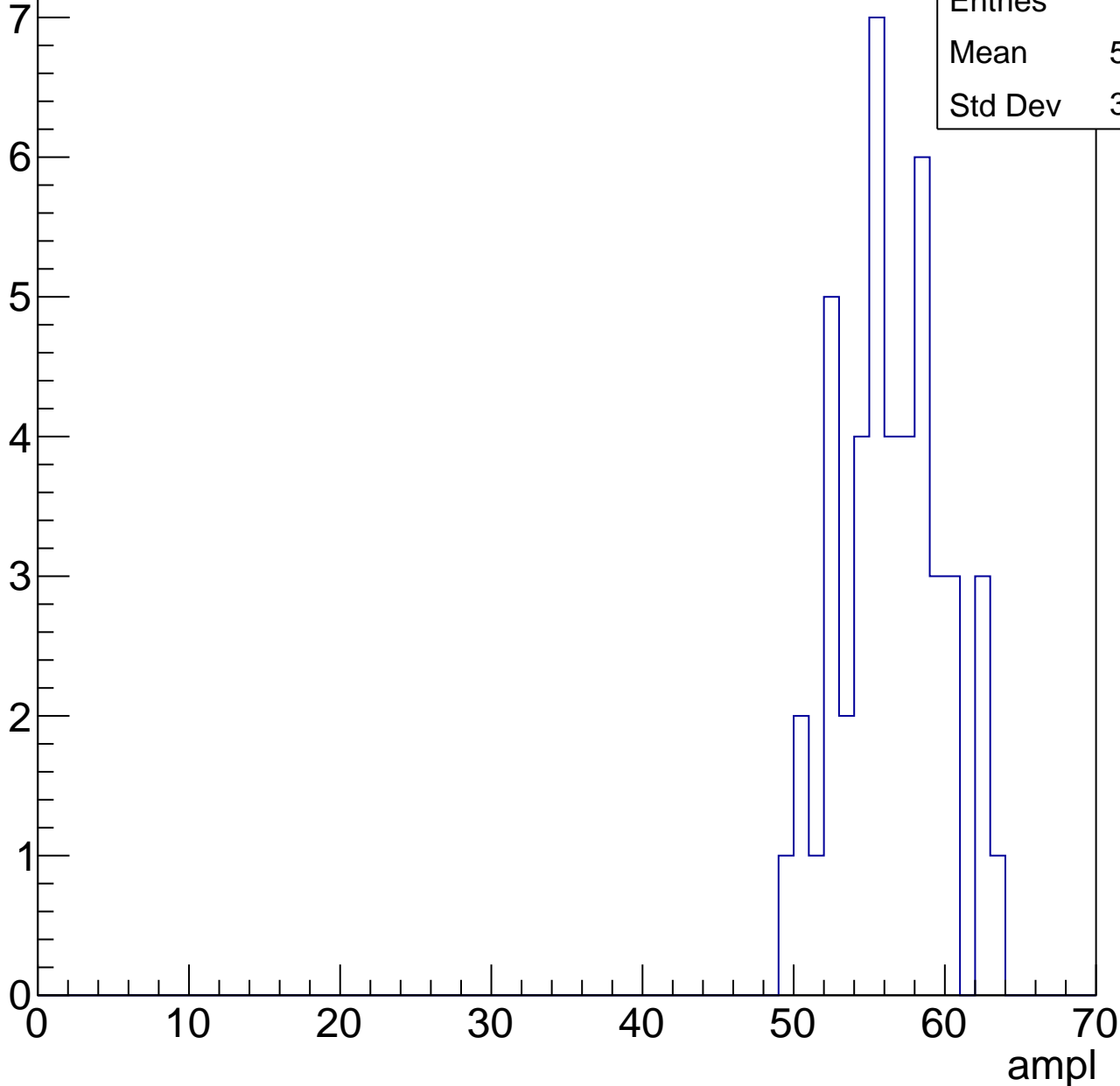


B1L102S, U2-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	55.93
Std Dev	3.384

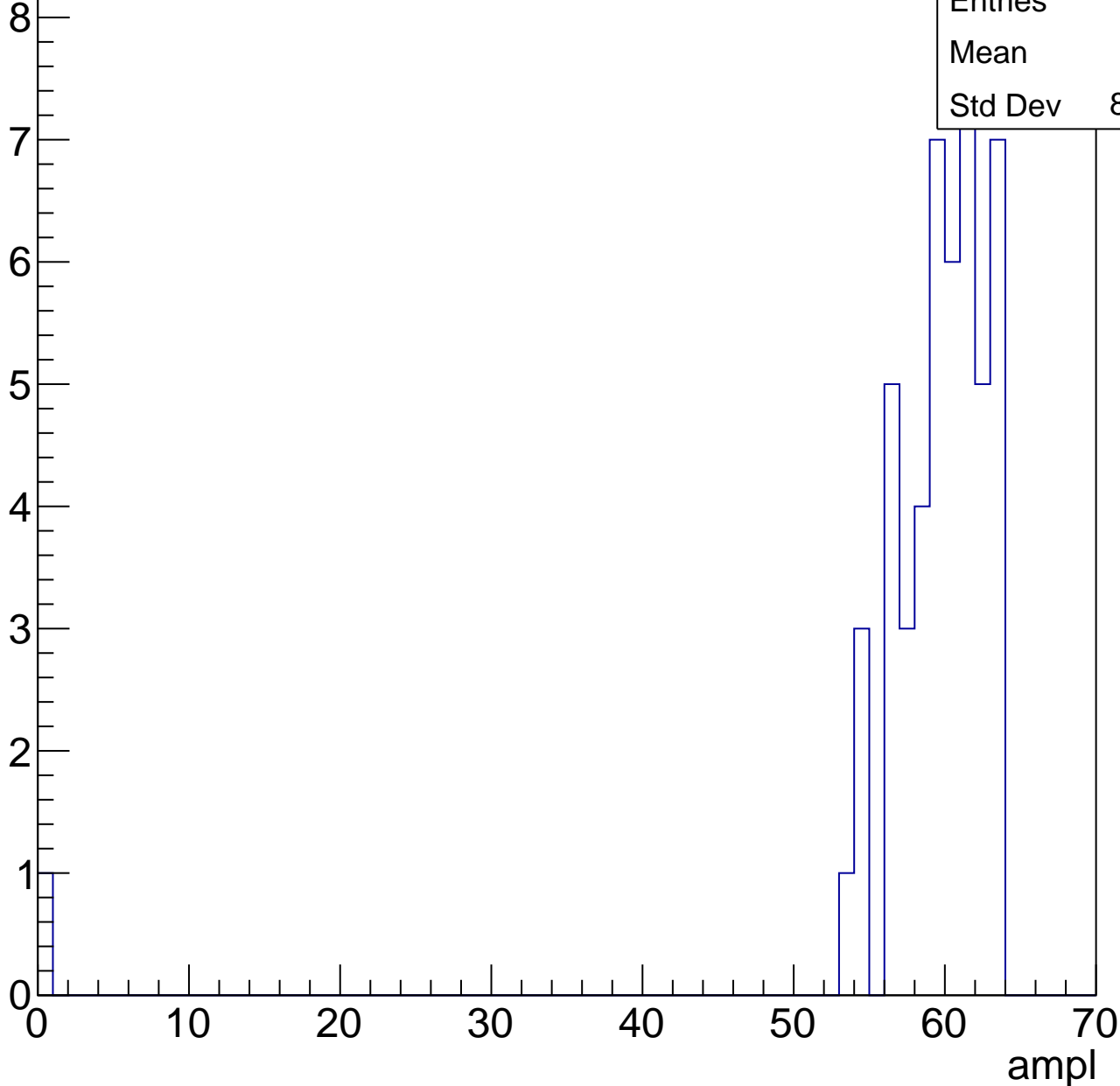


B1L102S, U2-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.2
Std Dev	8.736



B1L102S, U2-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.71
Std Dev	1.03

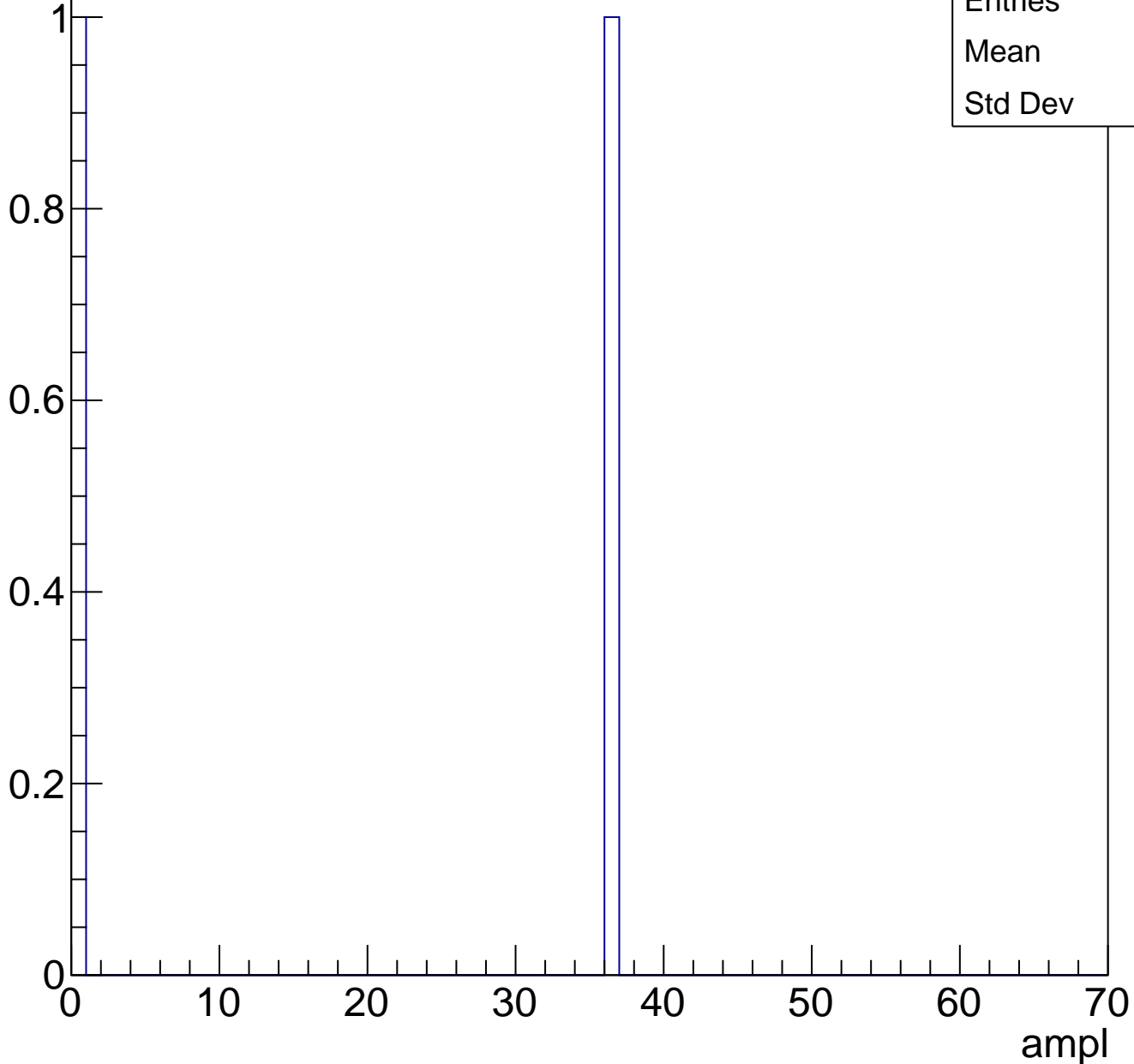
ampl

0 10 20 30 40 50 60 70

B1L102S, U2-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

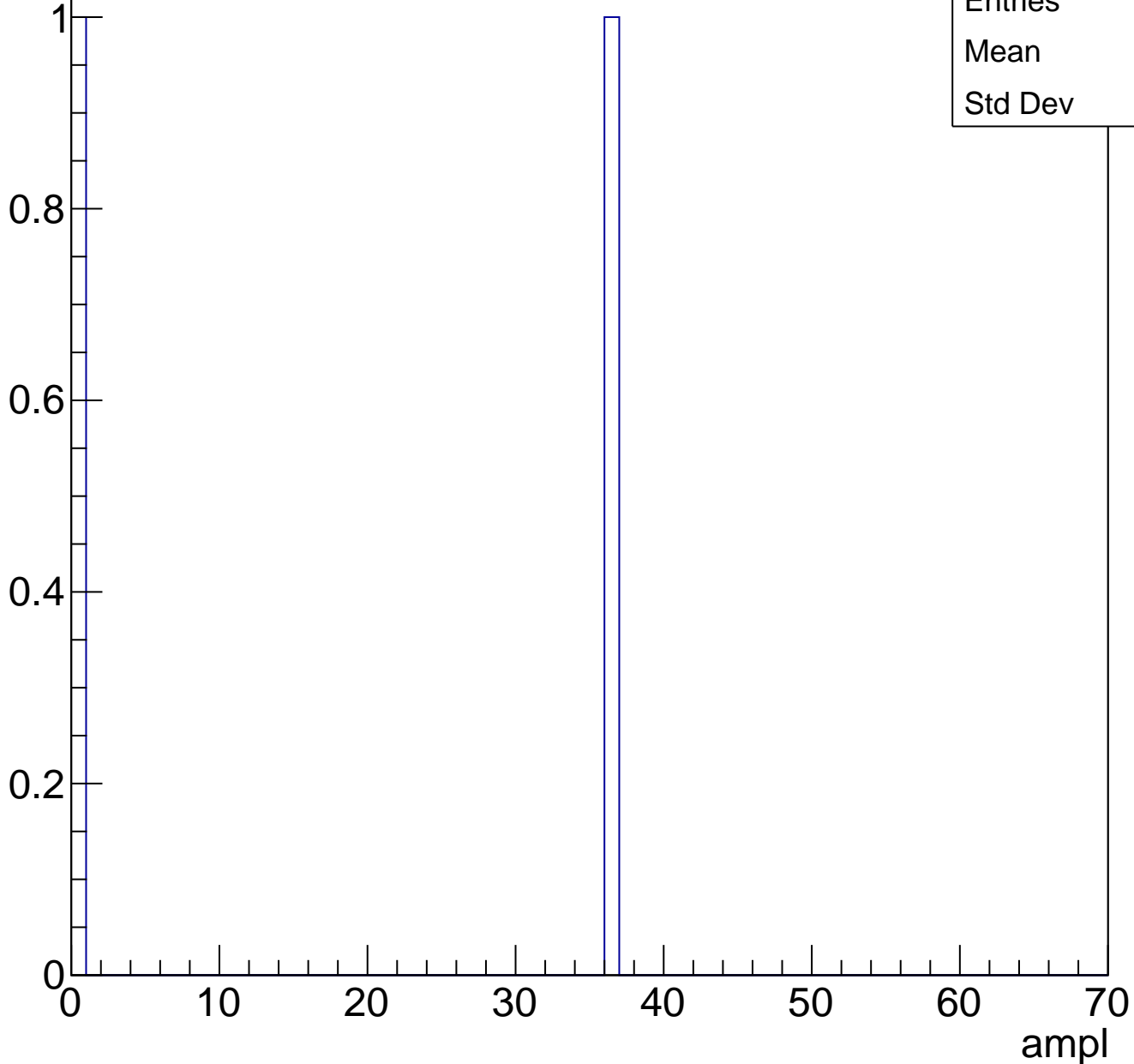


Entries	2
Mean	18
Std Dev	18

B1L102S, U2-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	18
Std Dev	18