

B1L101S, U23-ch0, adc0

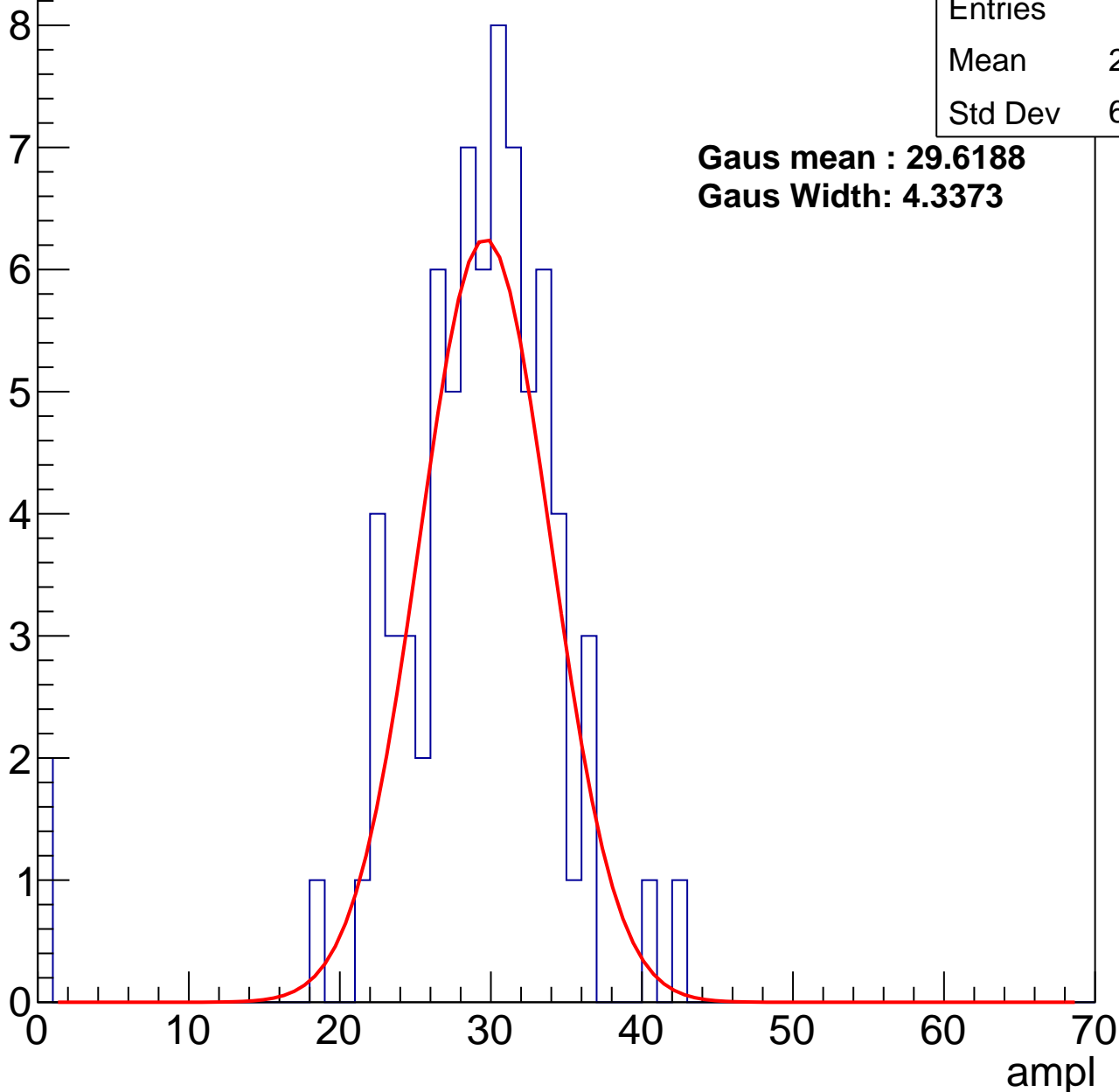
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	28.36
Std Dev	6.363

Gaus mean : 29.6188

Gaus Width: 4.3373



B1L101S, U23-ch0, adc1

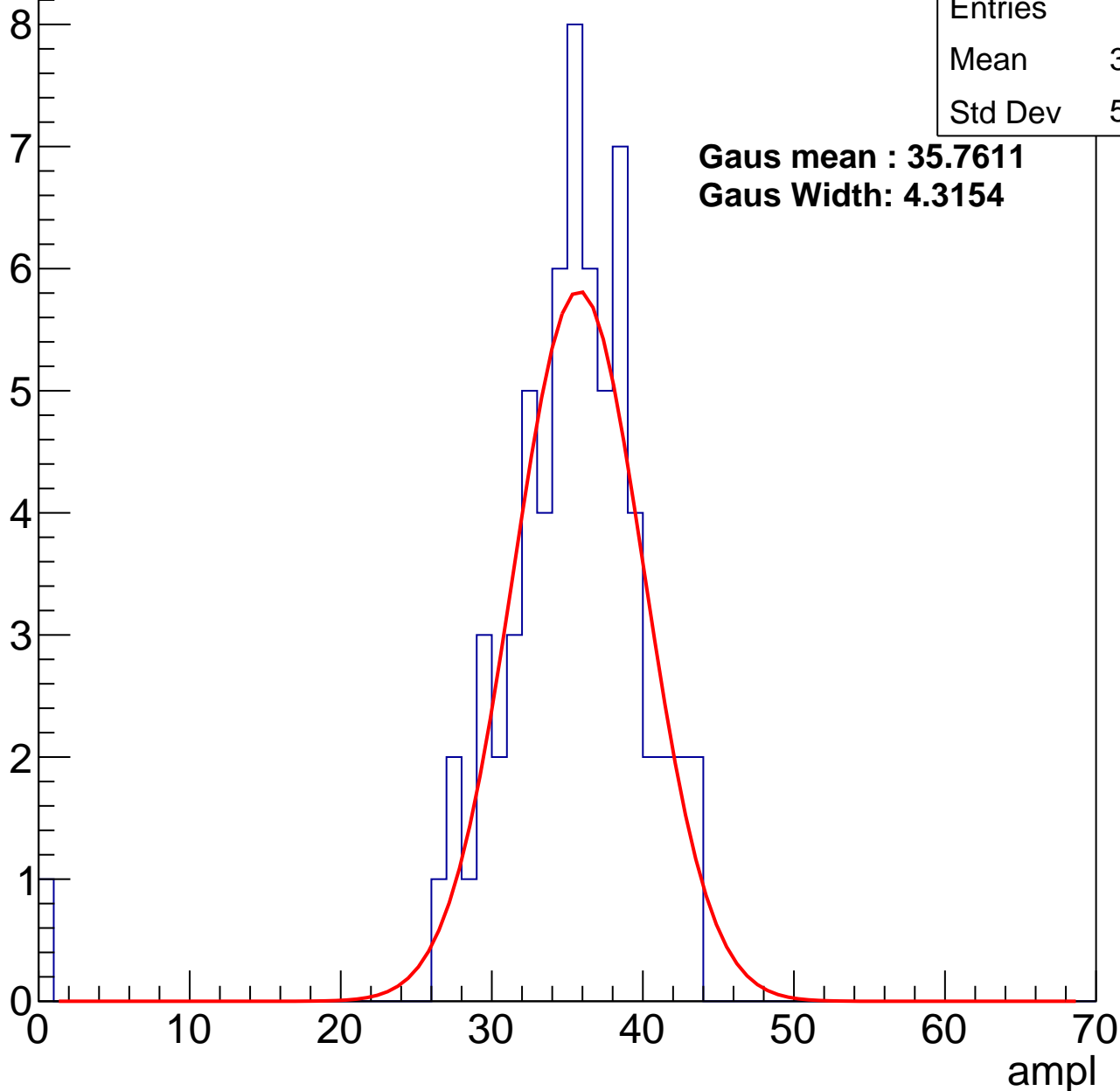
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.53
Std Dev	5.829

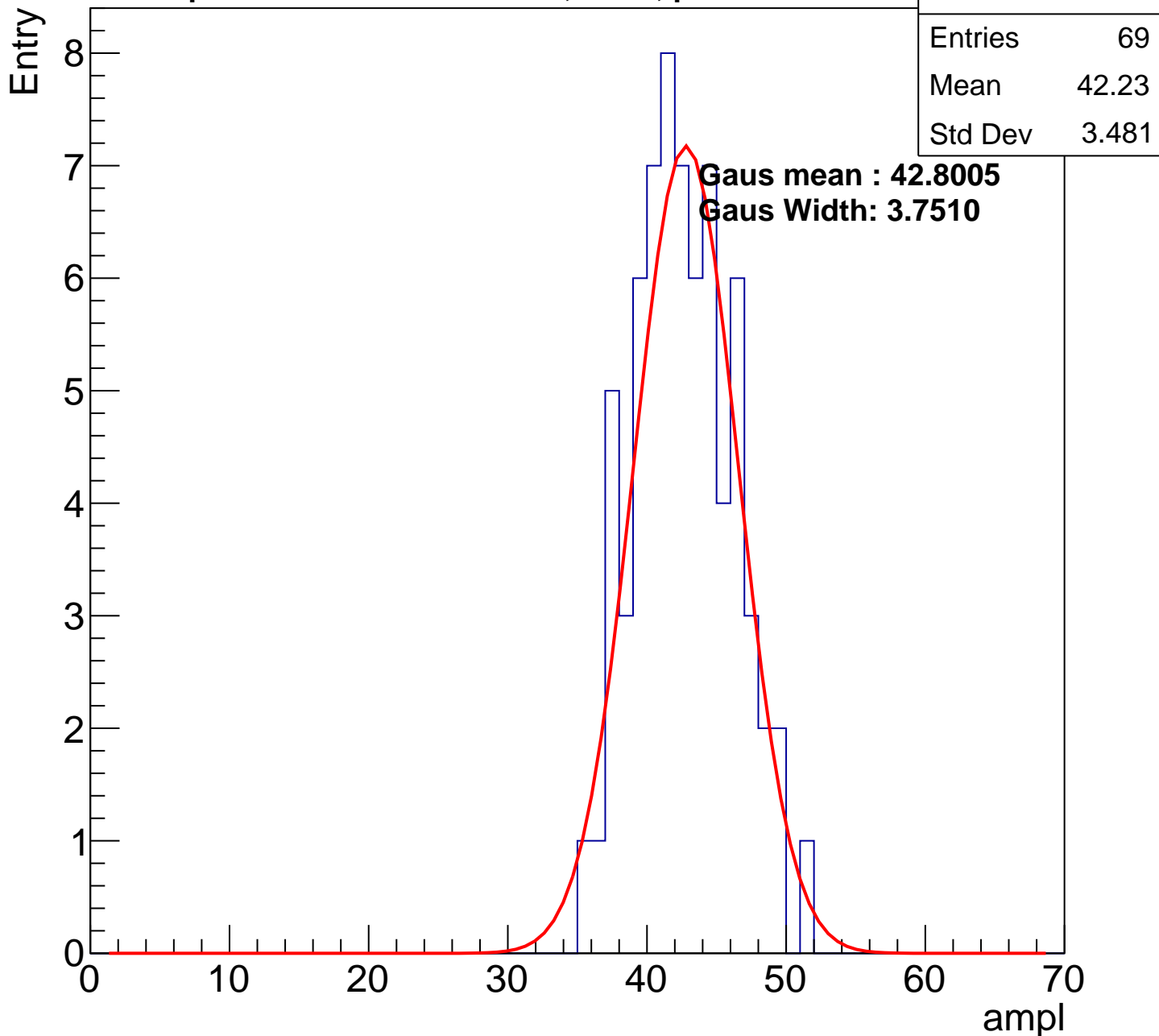
Gaus mean : 35.7611

Gaus Width: 4.3154



B1L101S, U23-ch0, adc2

calib_packv5_042523_0143.root, FC#0, port D2

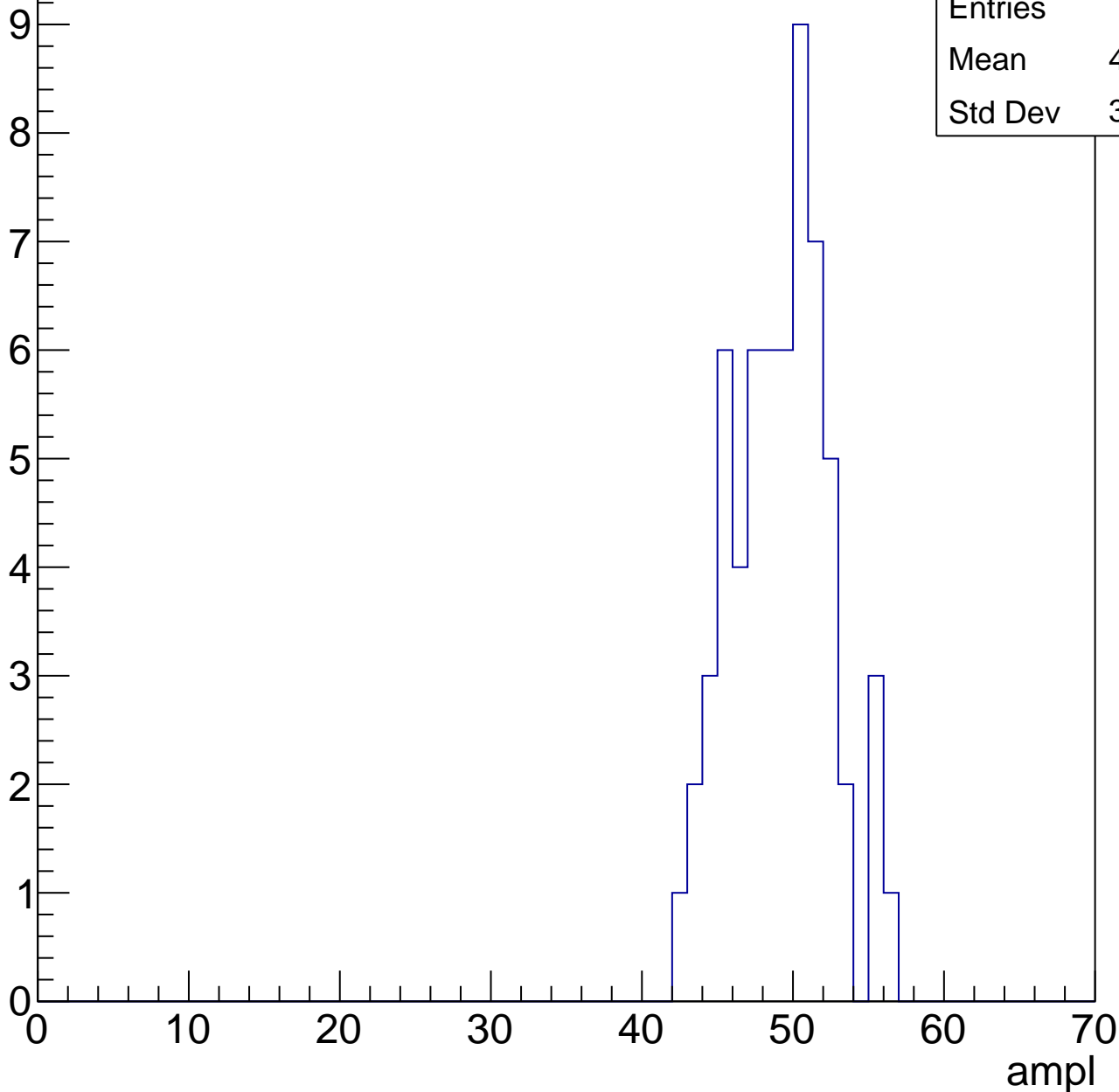


B1L101S, U23-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	48.72
Std Dev	3.199



B1L101S, U23-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

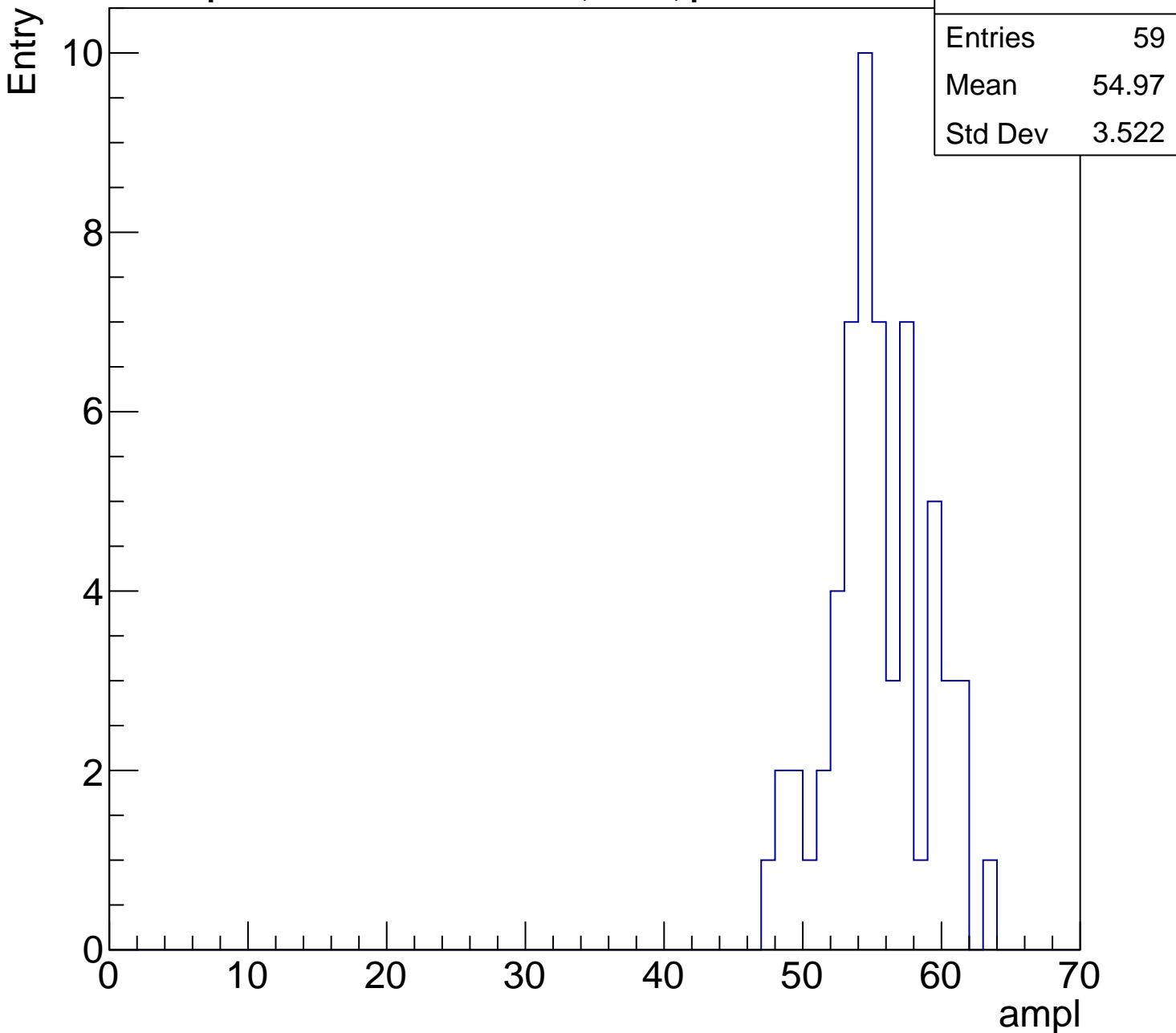
Entries	59
Mean	54.97
Std Dev	3.522

Entry

10
8
6
4
2
0

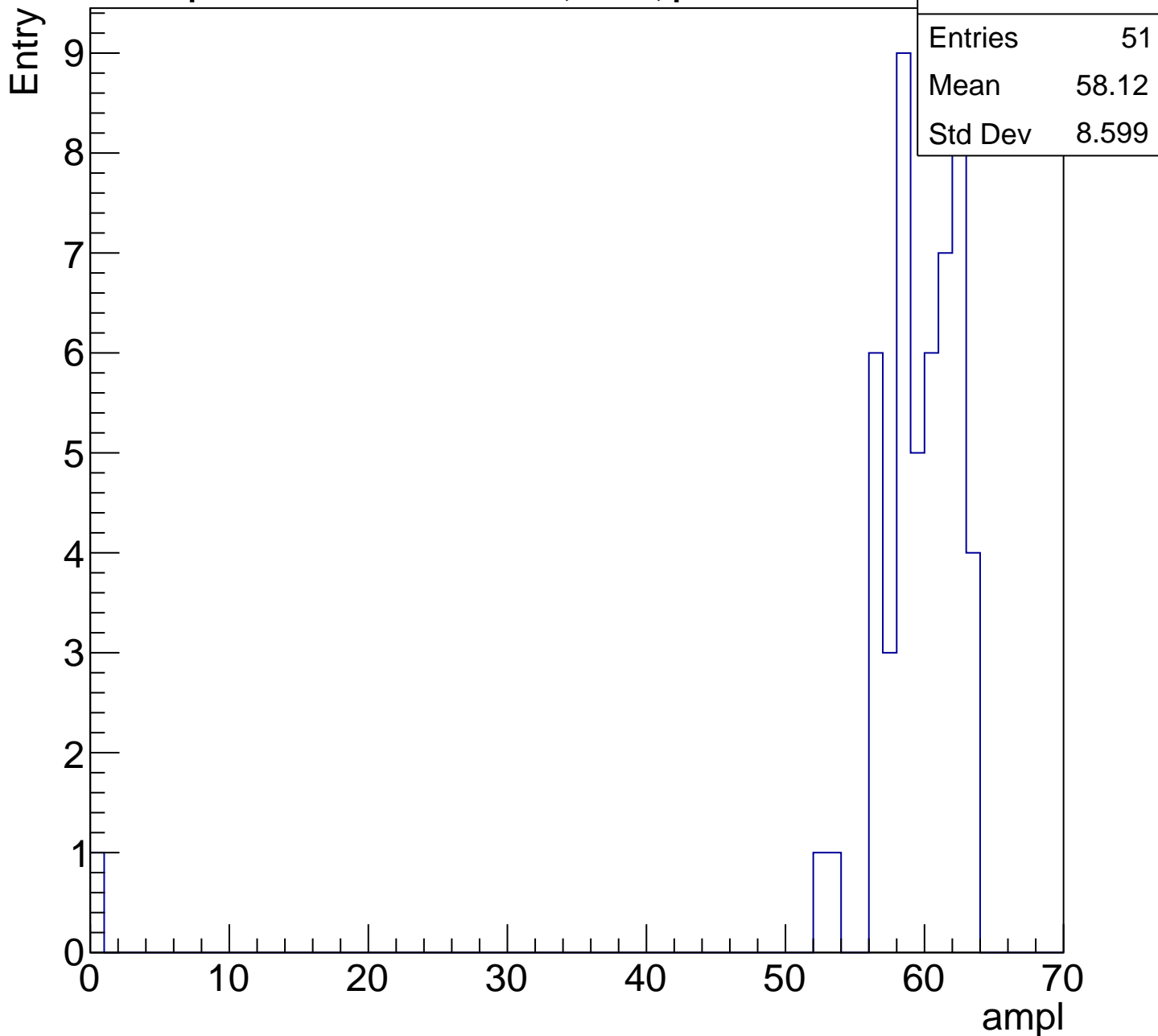
0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

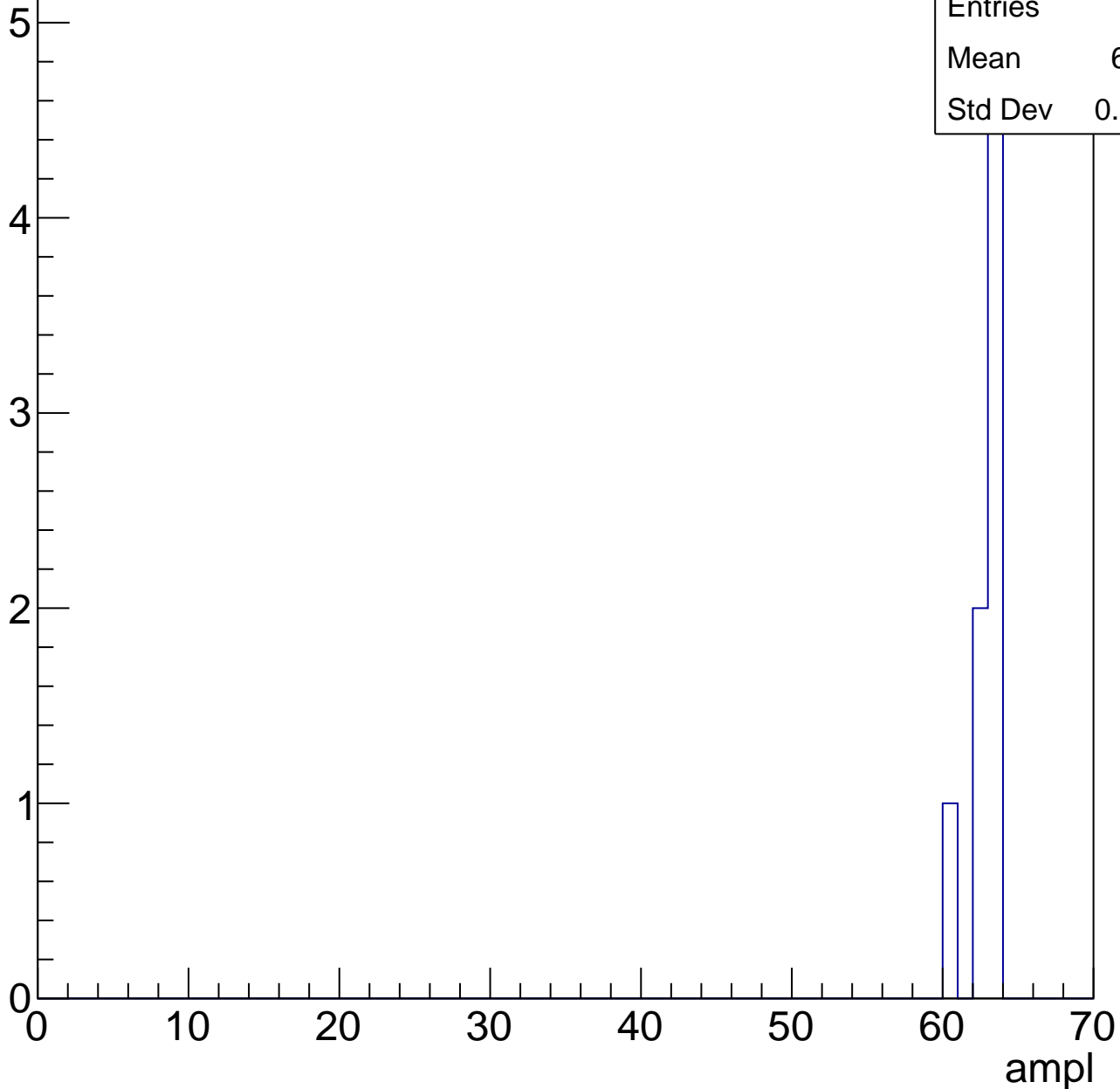


B1L101S, U23-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L101S, U23-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch1, adc0

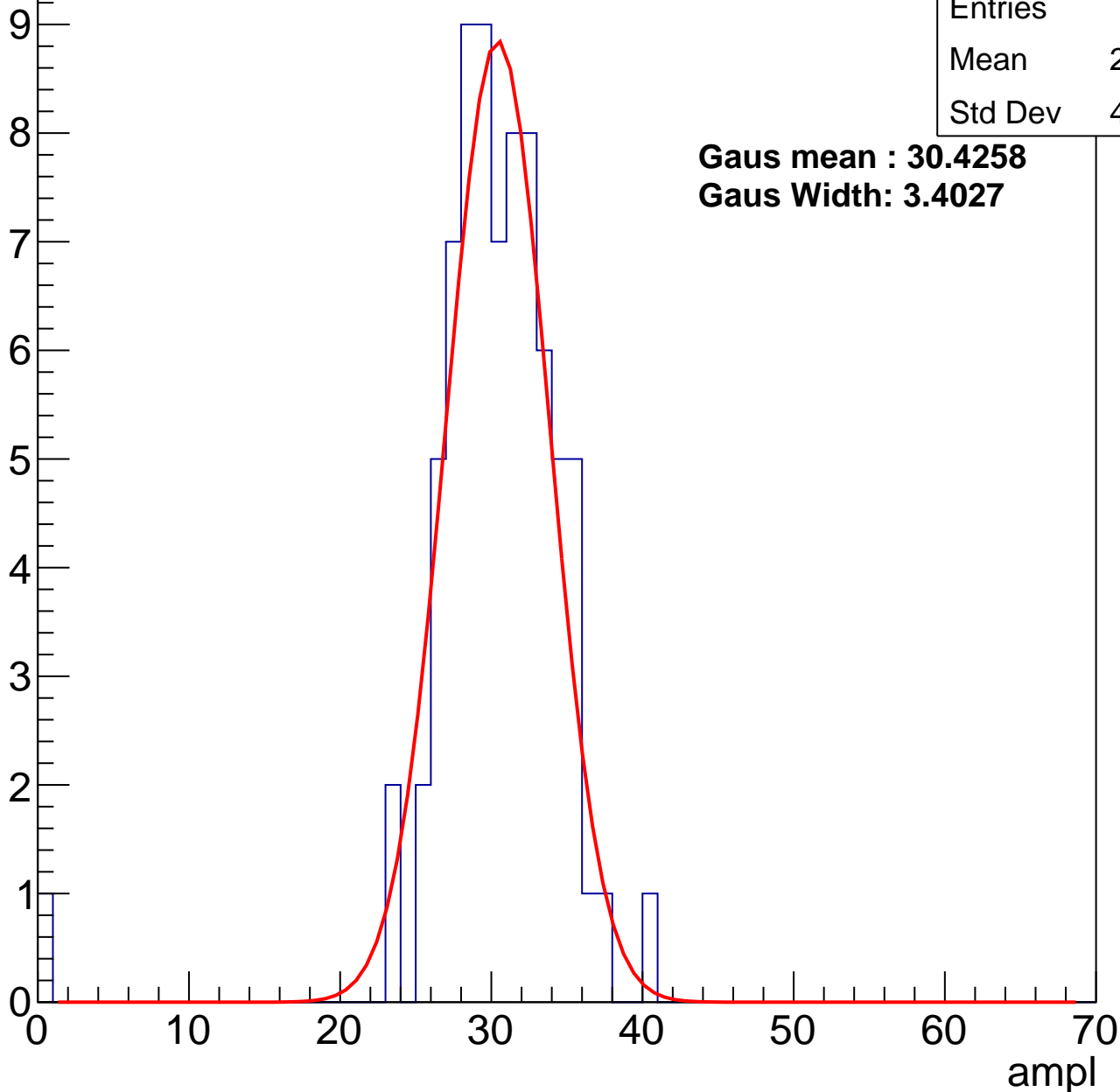
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	29.84
Std Dev	4.724

Gaus mean : 30.4258

Gaus Width: 3.4027



B1L101S, U23-ch1, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	38.67
Std Dev	3.663

Gaus mean : 38.9050

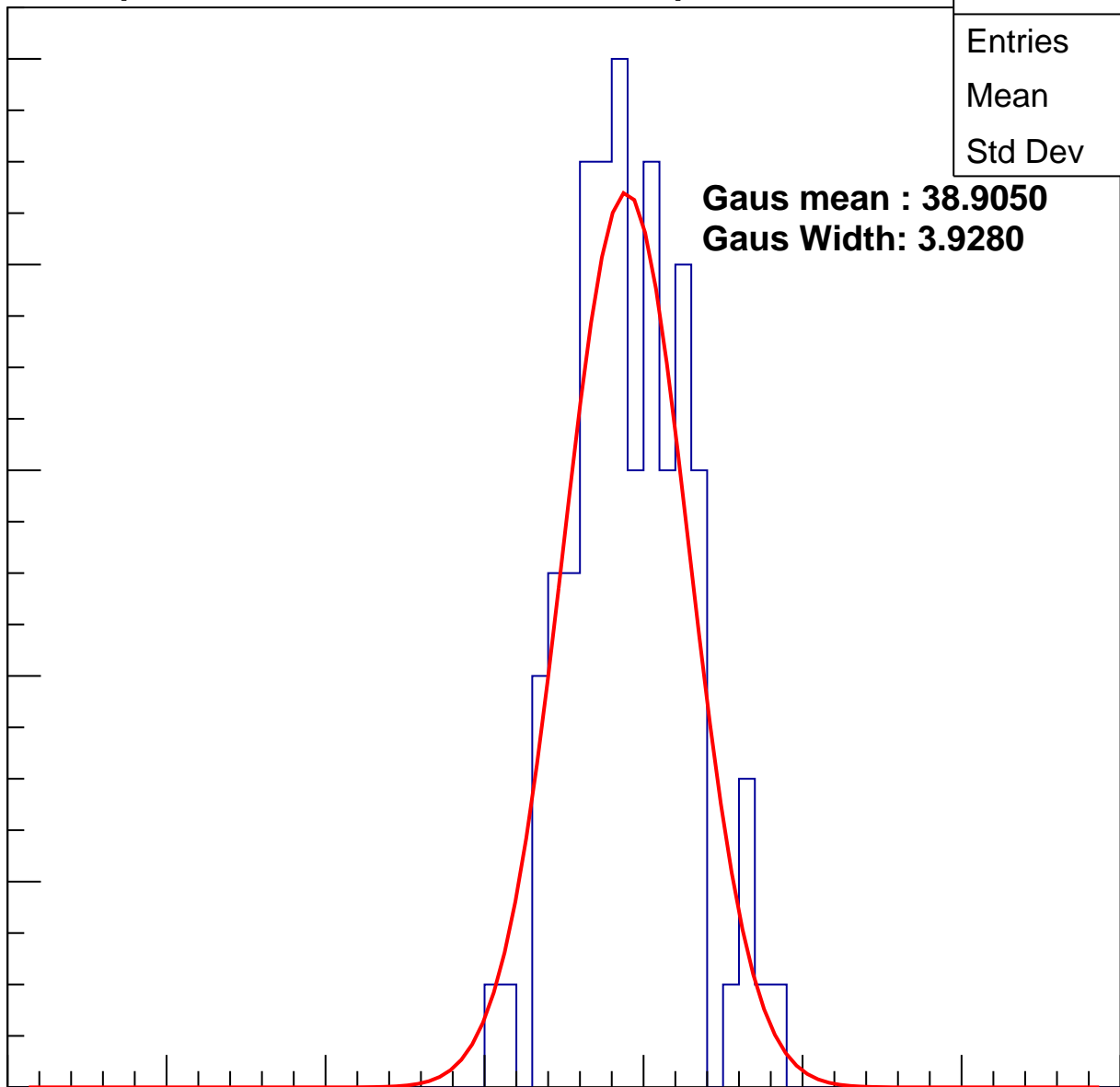
Gaus Width: 3.9280

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch1, adc2

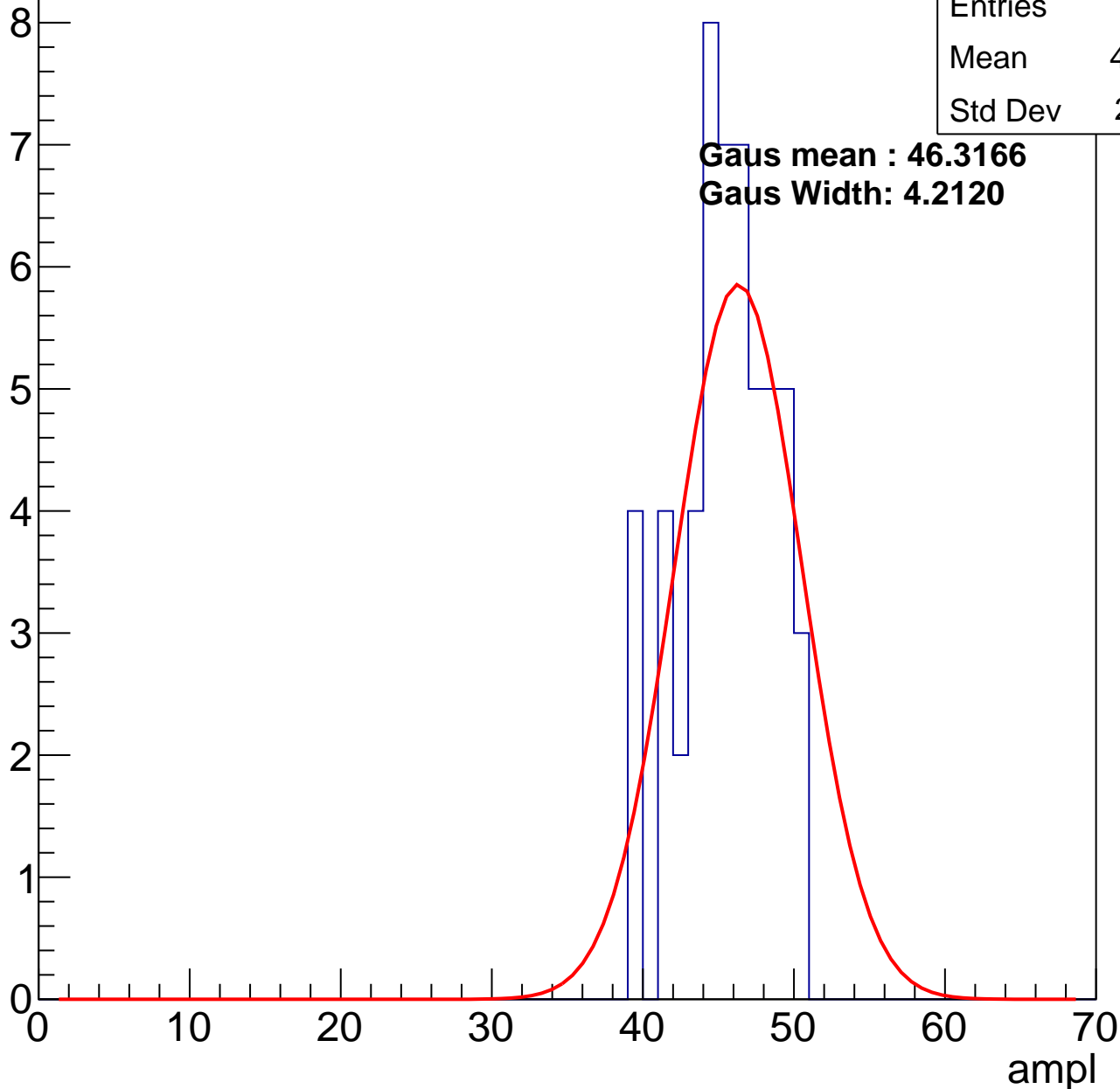
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	45.09
Std Dev	2.971

Gaus mean : 46.3166

Gaus Width: 4.2120

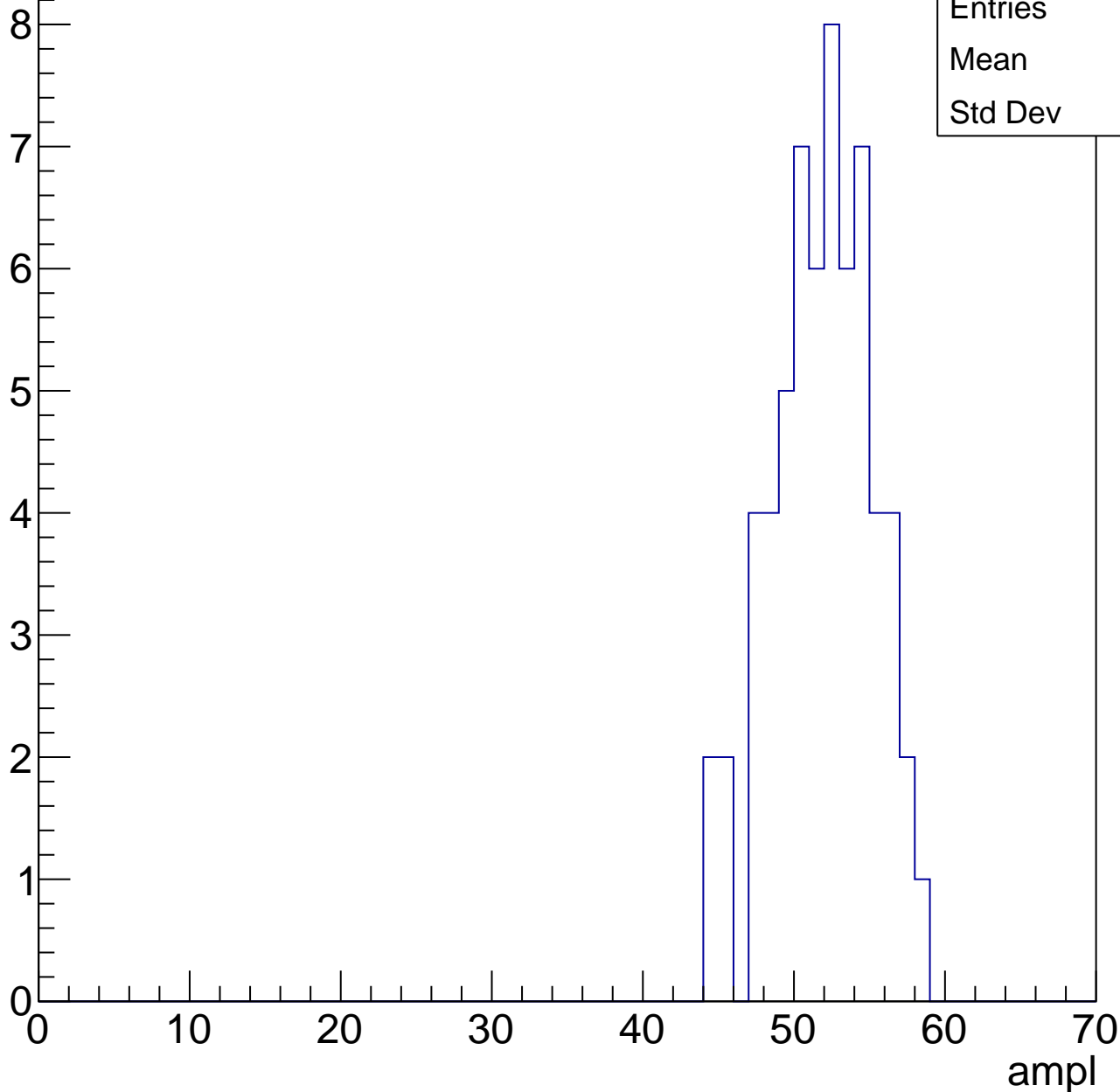


B1L101S, U23-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	51.4
Std Dev	3.28

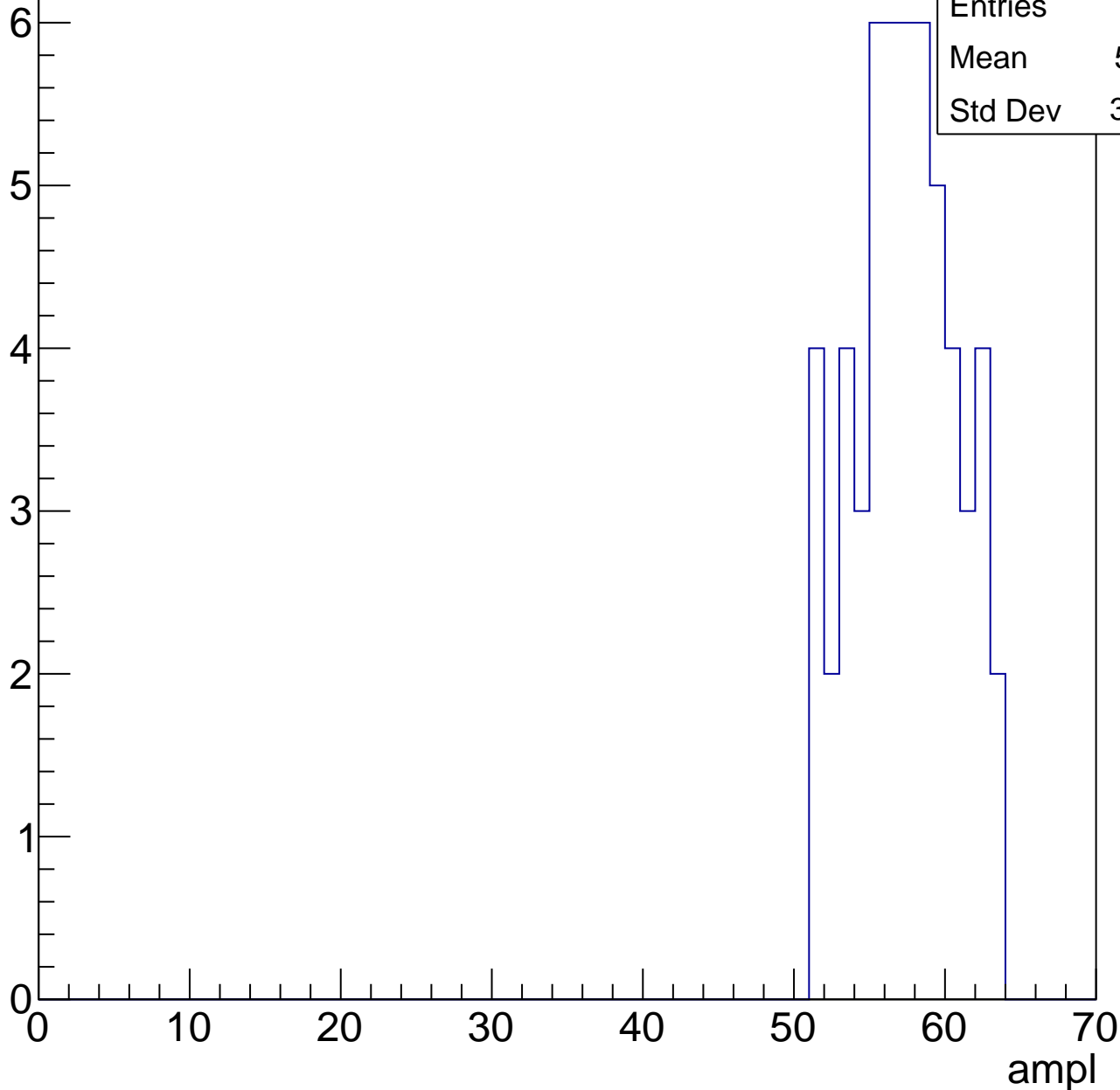


B1L101S, U23-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.91
Std Dev	3.293

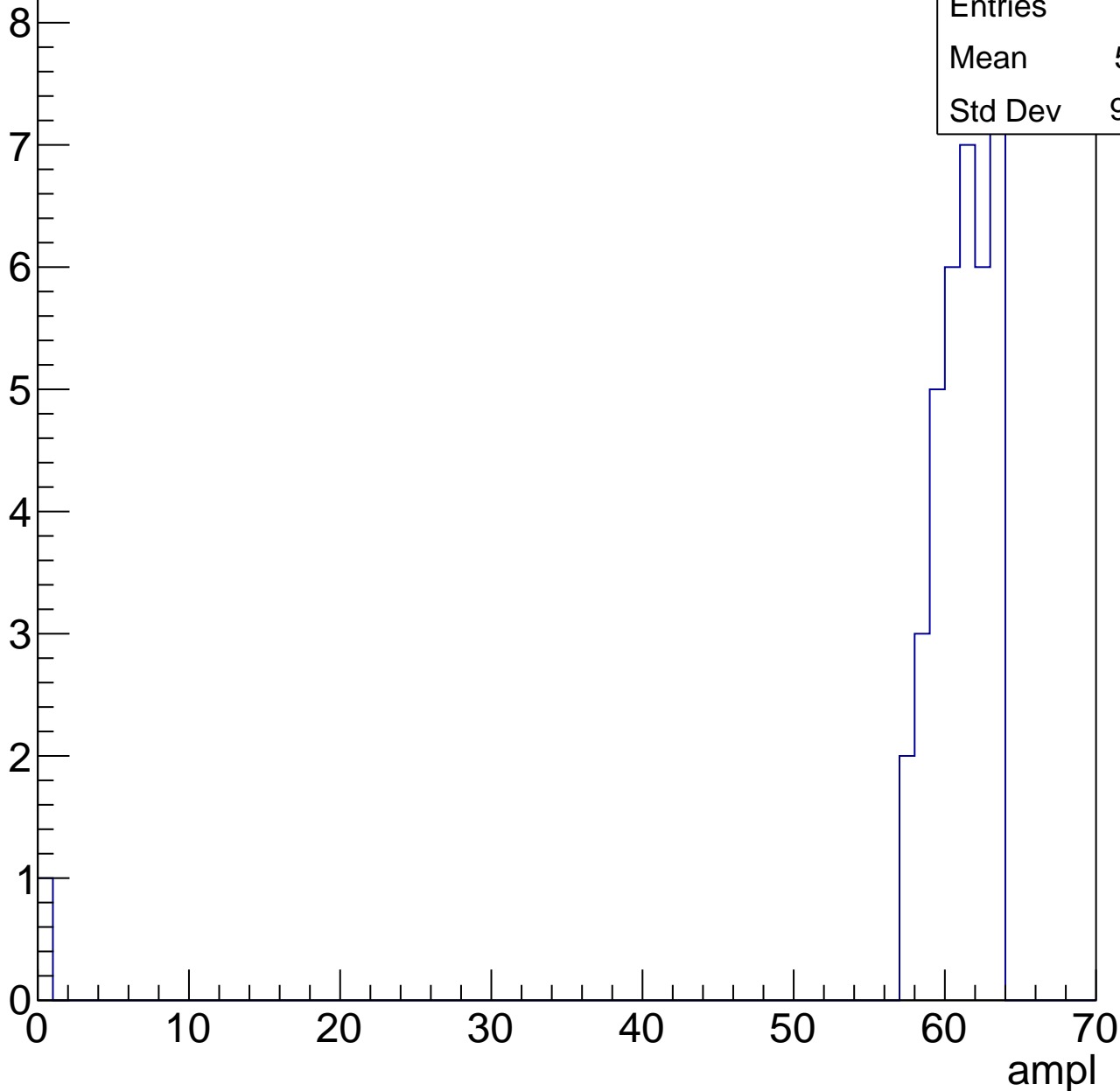


B1L101S, U23-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59.11
Std Dev	9.878



B1L101S, U23-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch2, adc0

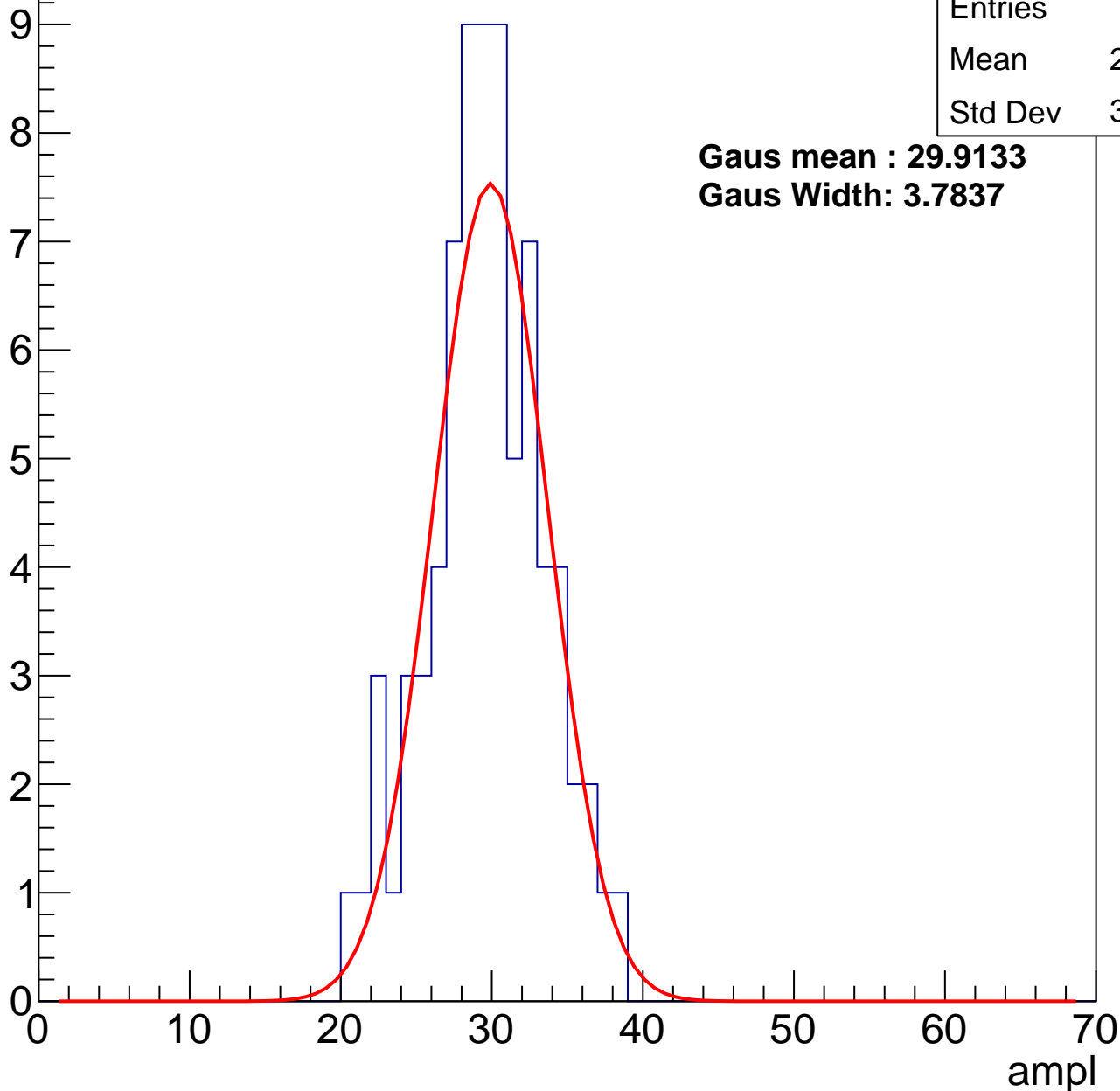
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.17
Std Dev	3.792

Gaus mean : 29.9133

Gaus Width: 3.7837



B1L101S, U23-ch2, adc1

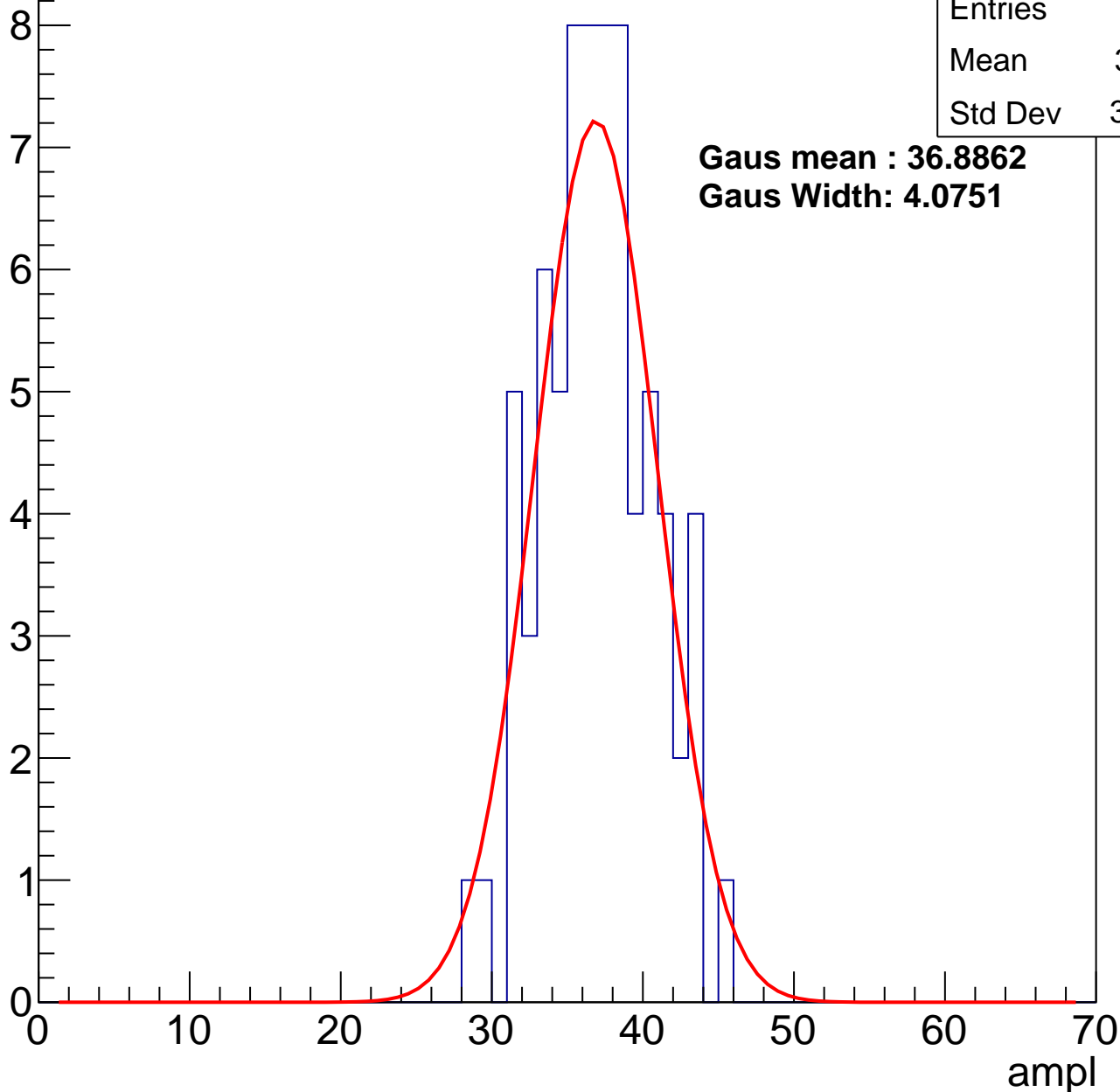
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.51
Std Dev	3.616

Gaus mean : 36.8862

Gaus Width: 4.0751



B1L101S, U23-ch2, adc2

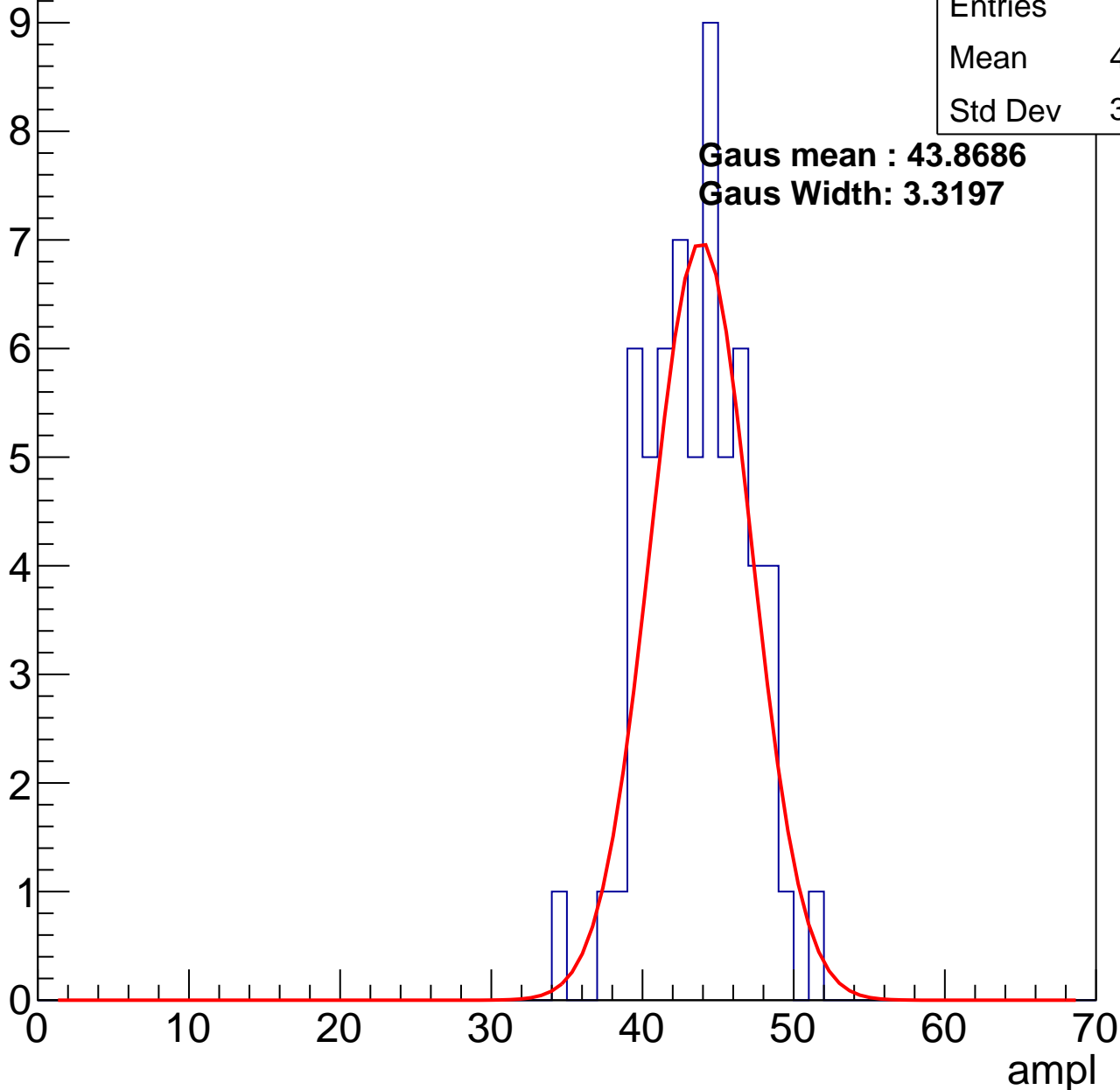
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.15
Std Dev	3.262

Gaus mean : 43.8686

Gaus Width: 3.3197

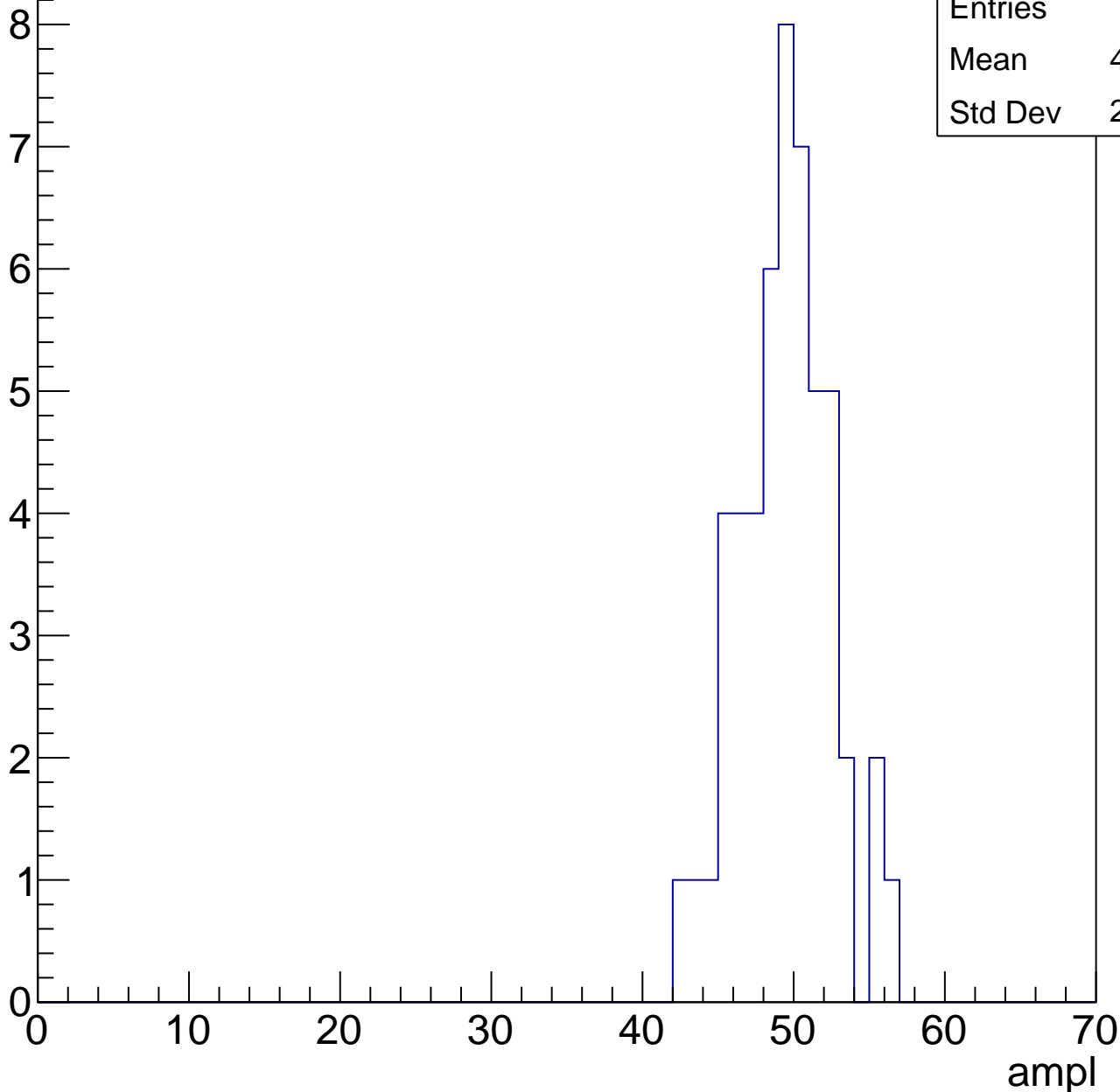


B1L101S, U23-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	48.98
Std Dev	2.993

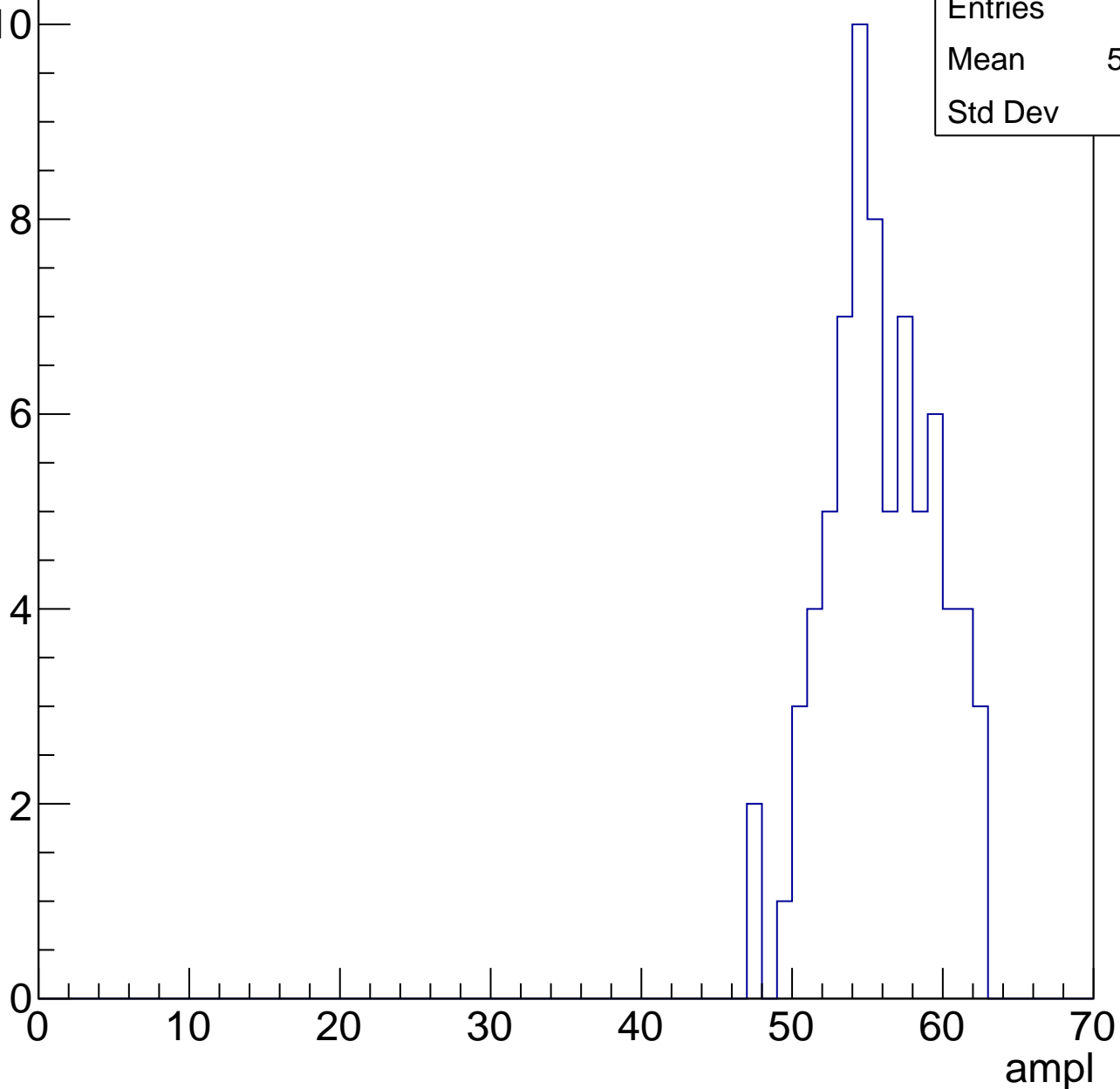


B1L101S, U23-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	55.42
Std Dev	3.56

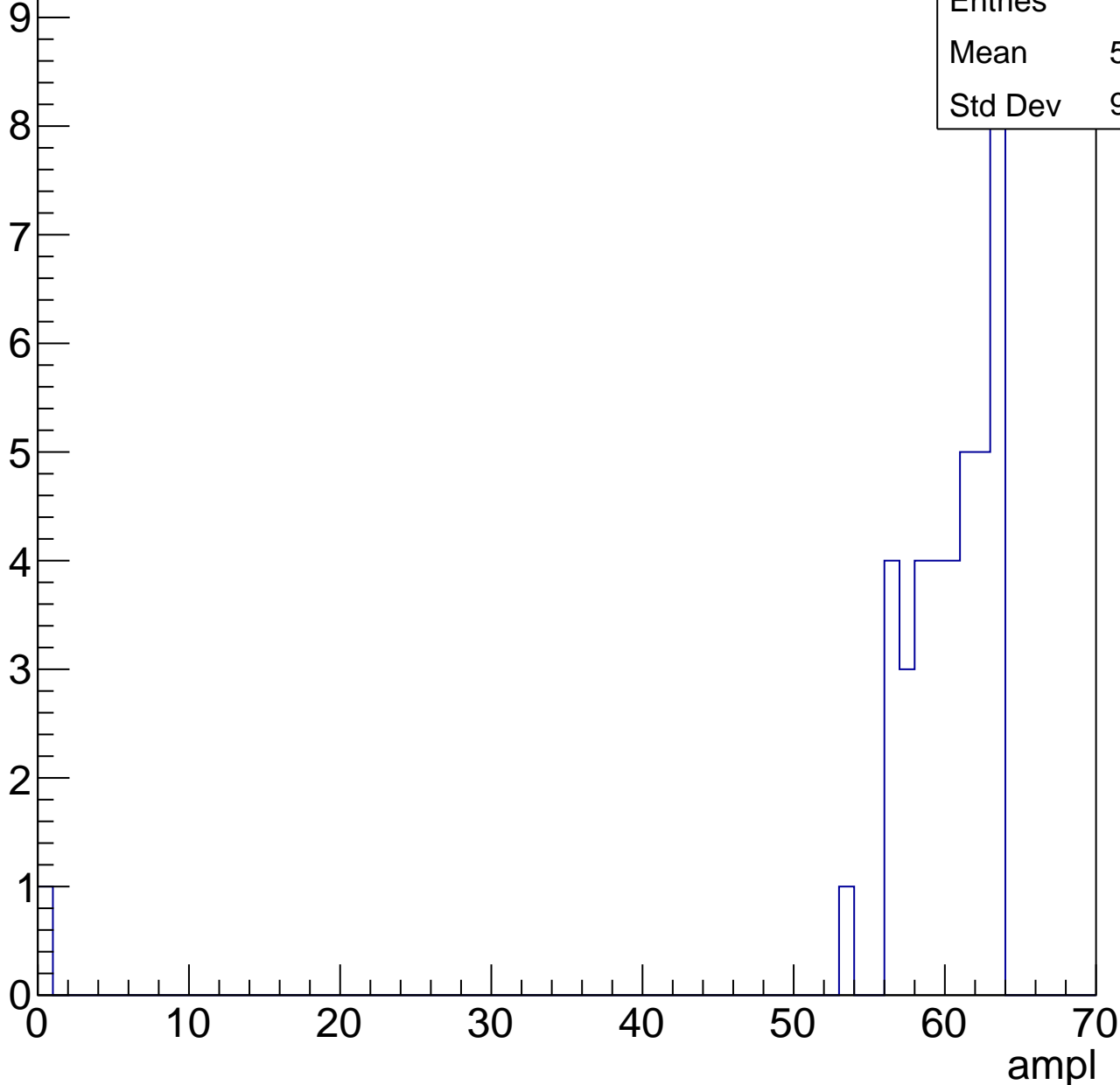


B1L101S, U23-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.45
Std Dev	9.708



B1L101S, U23-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

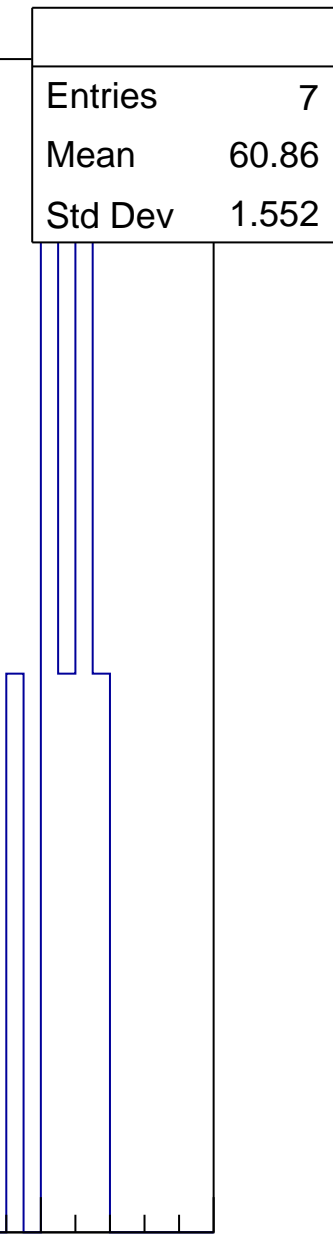
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.86
Std Dev	1.552

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch3, adc0

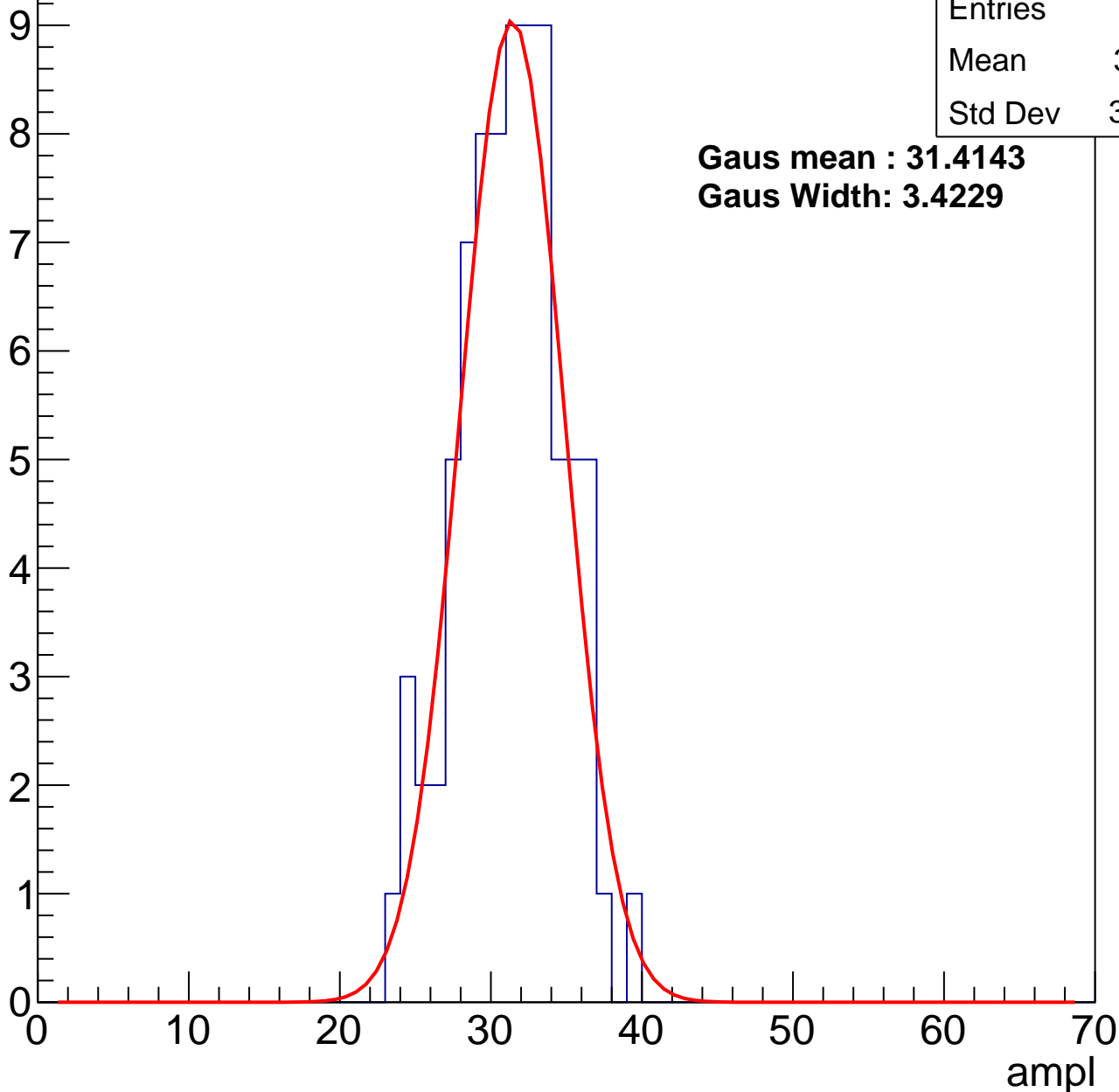
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.81
Std Dev	3.369

Gaus mean : 31.4143

Gaus Width: 3.4229



B1L101S, U23-ch3, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	38.92
Std Dev	2.992

Gaus mean : 39.3413

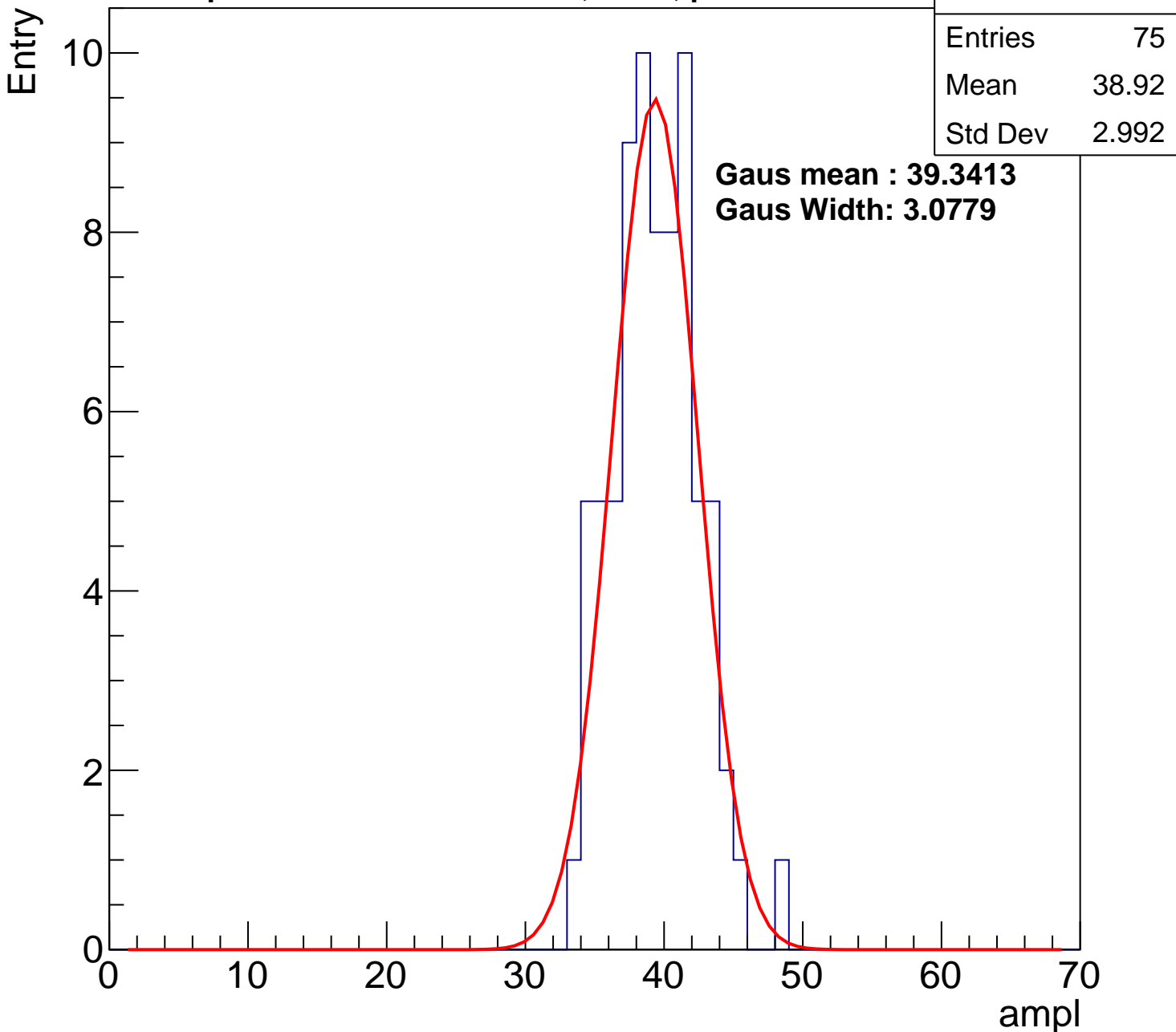
Gaus Width: 3.0779

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch3, adc2

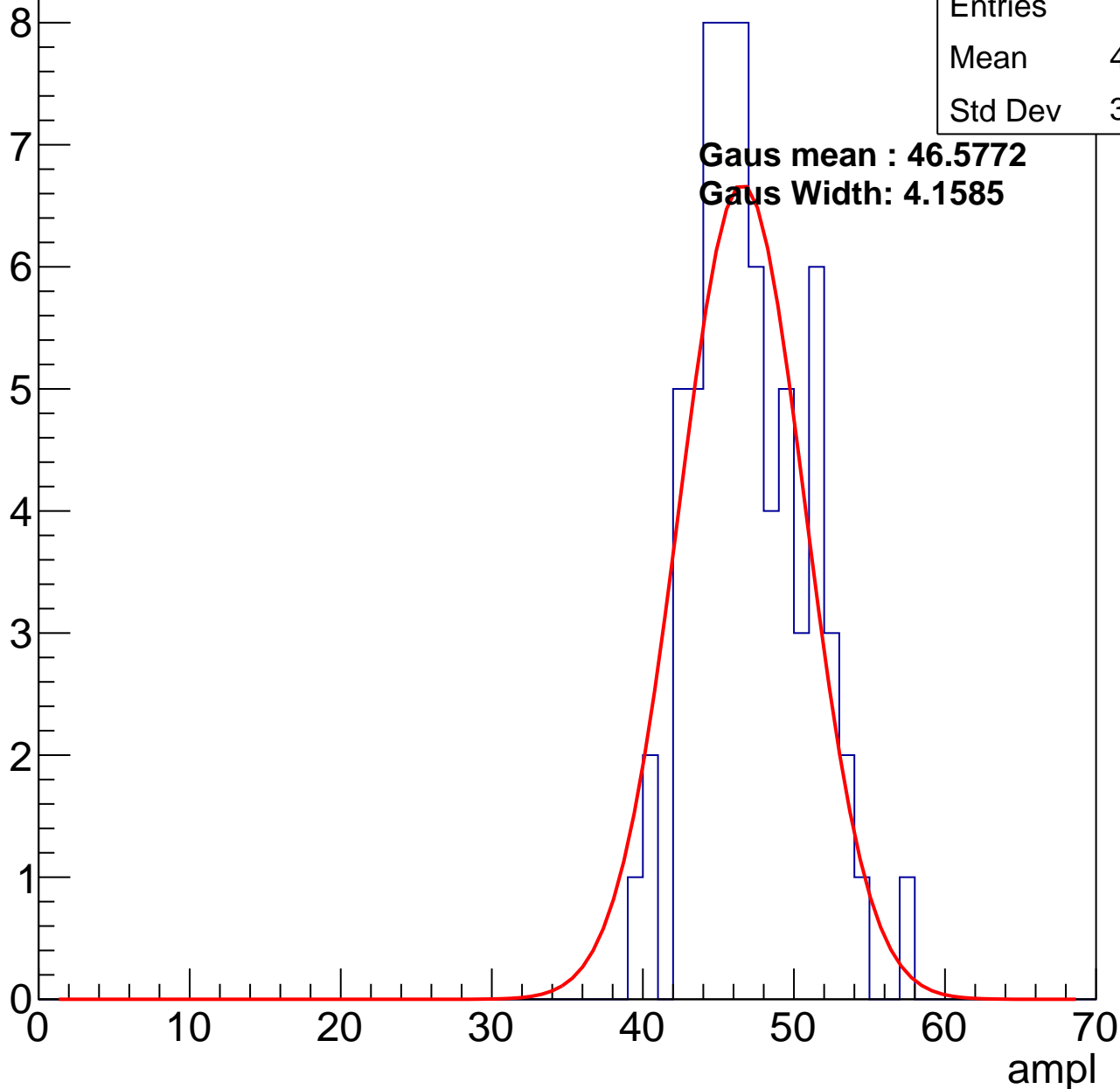
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	46.65
Std Dev	3.665

Gaus mean : 46.5772

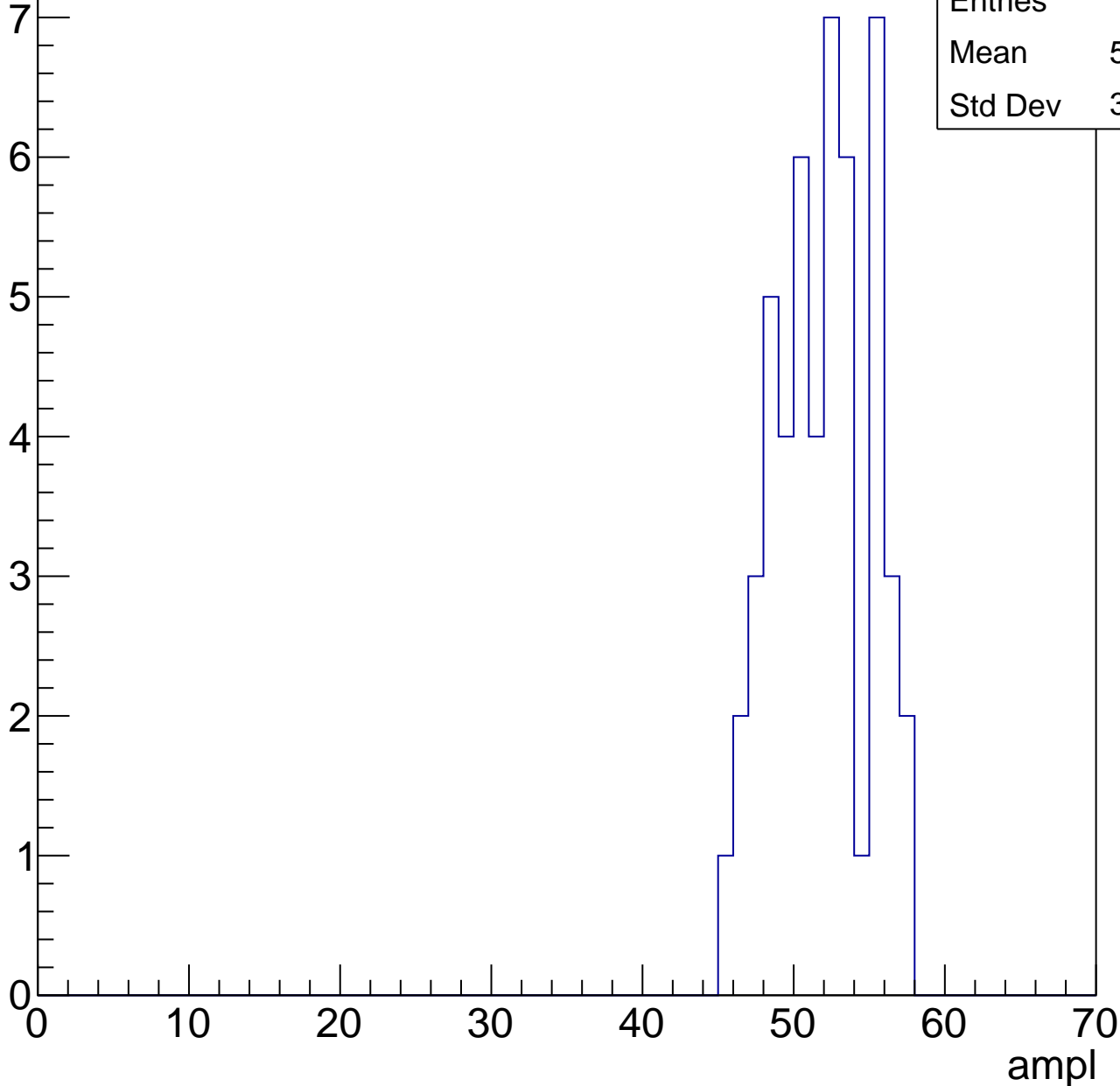
Gaus Width: 4.1585



B1L101S, U23-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



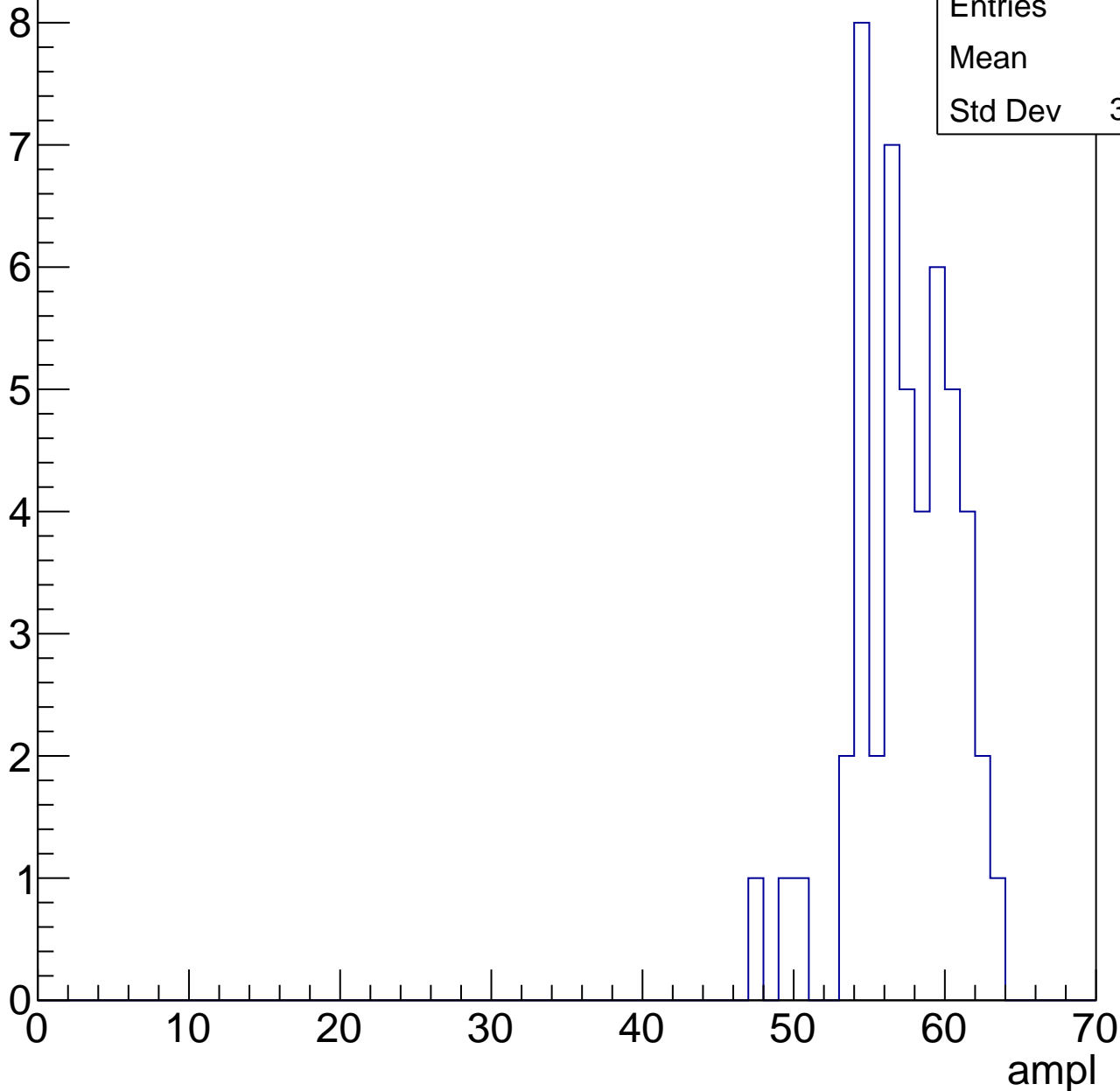
Entries	51
Mean	51.39
Std Dev	3.106

B1L101S, U23-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.9
Std Dev	3.364



B1L101S, U23-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.1
Std Dev	9.456

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L101S, U23-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch4, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	32.05
Std Dev	4.832

Gaus mean : 32.9332

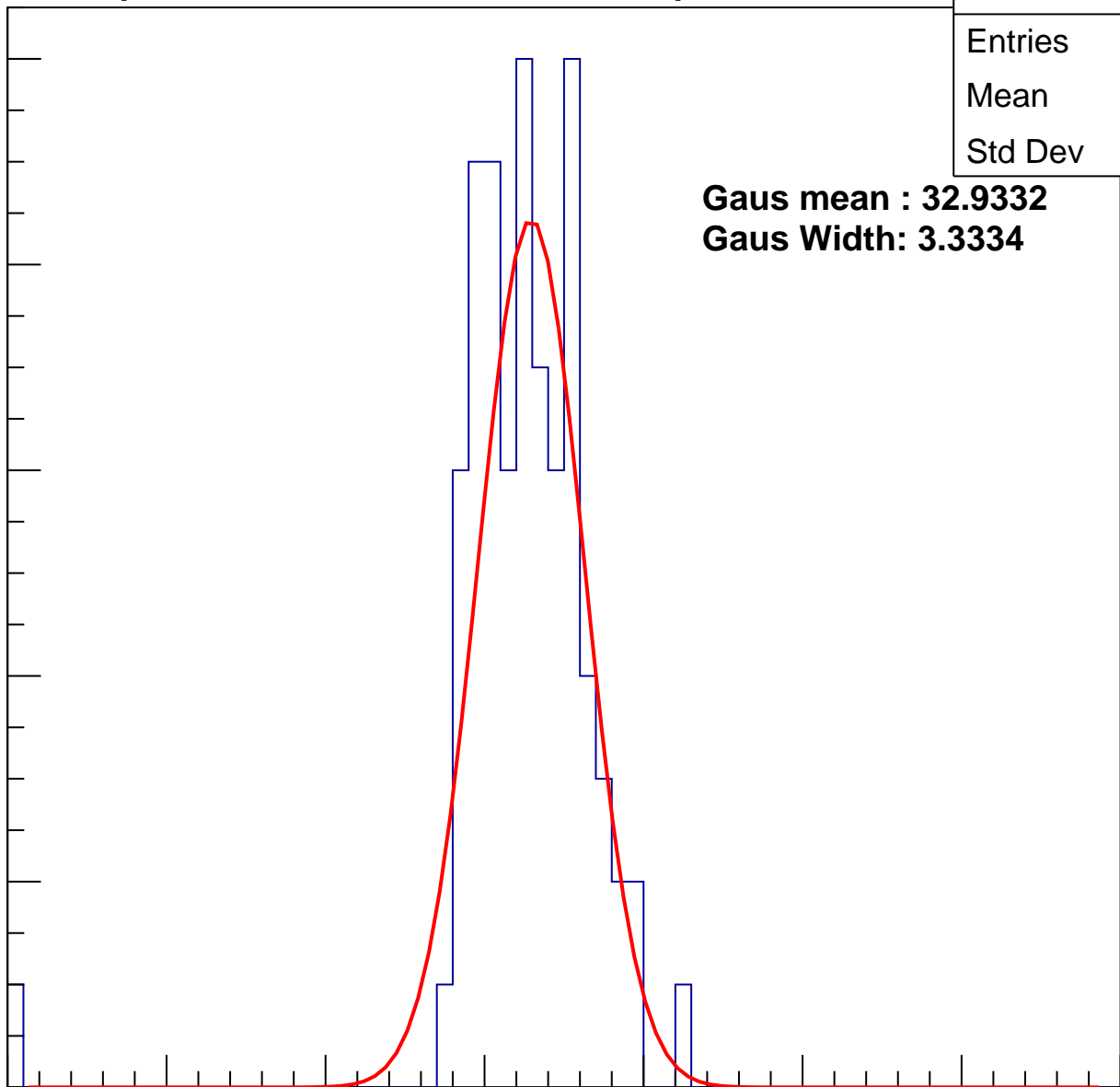
Gaus Width: 3.3334

Entry

10
8
6
4
2
0

ampl

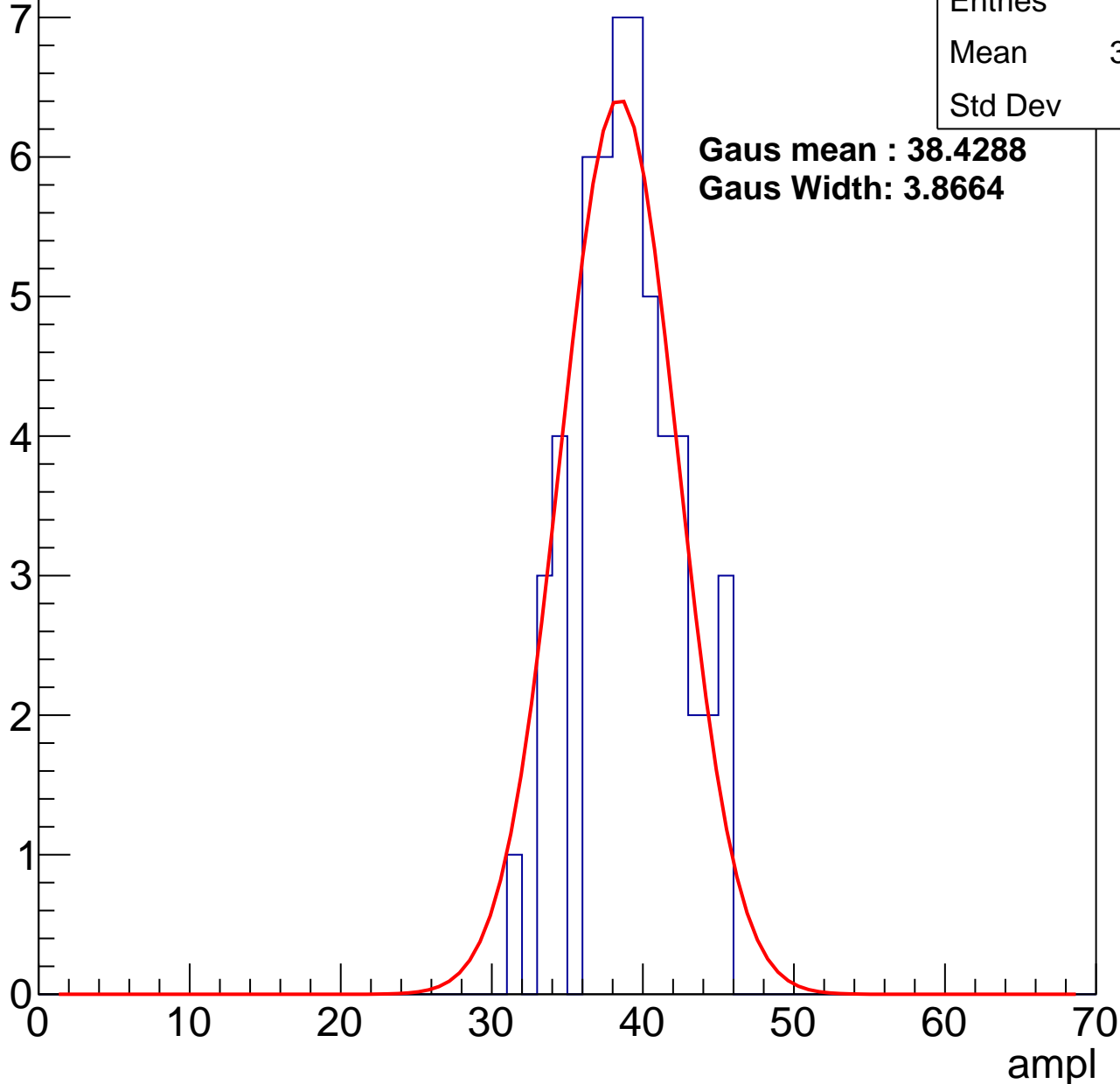
0 10 20 30 40 50 60 70



B1L101S, U23-ch4, adc1

calib_packv5_042523_0143.root, FC#0, port D2

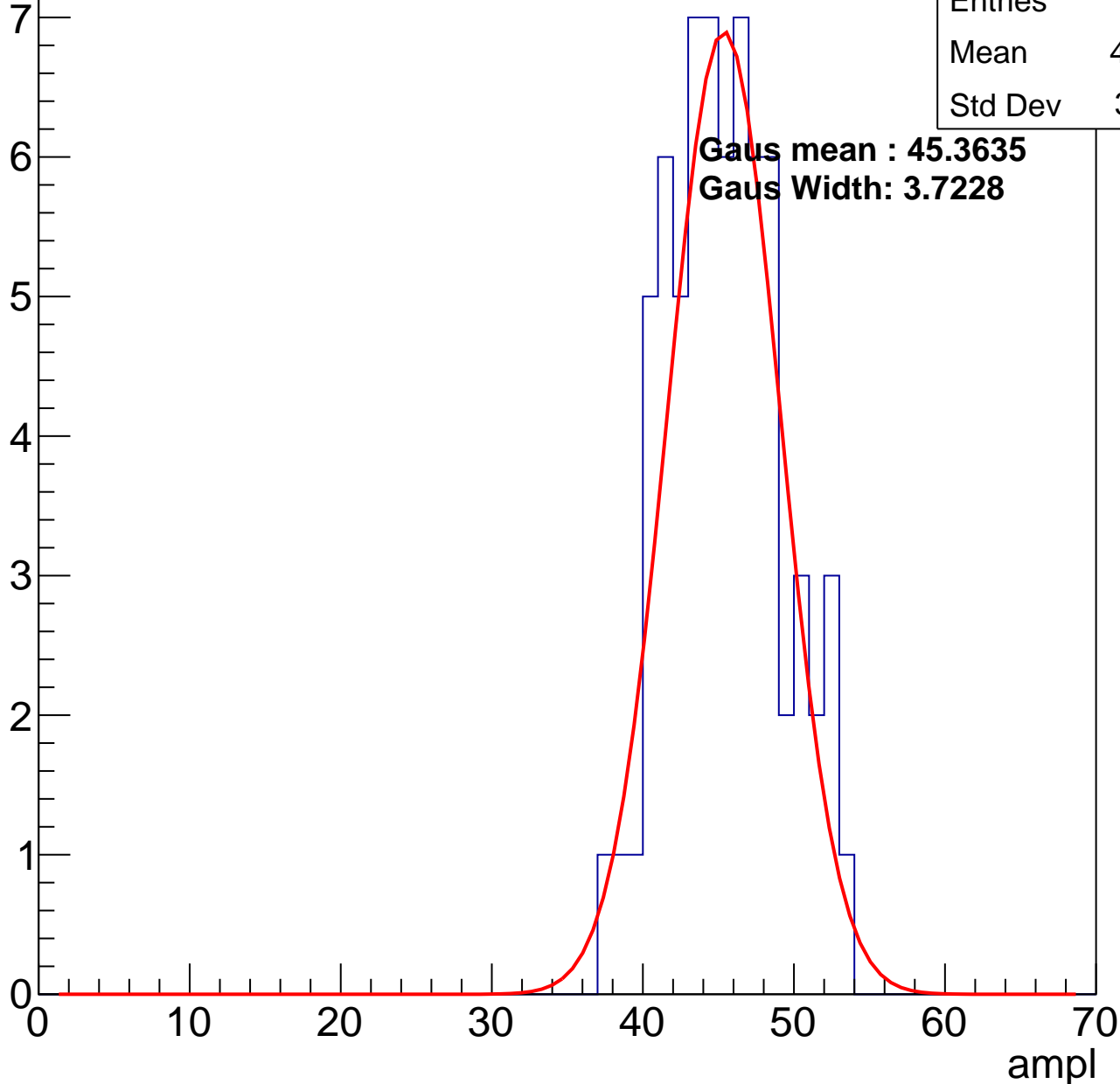
Entry



B1L101S, U23-ch4, adc2

calib_packv5_042523_0143.root, FC#0, port D2

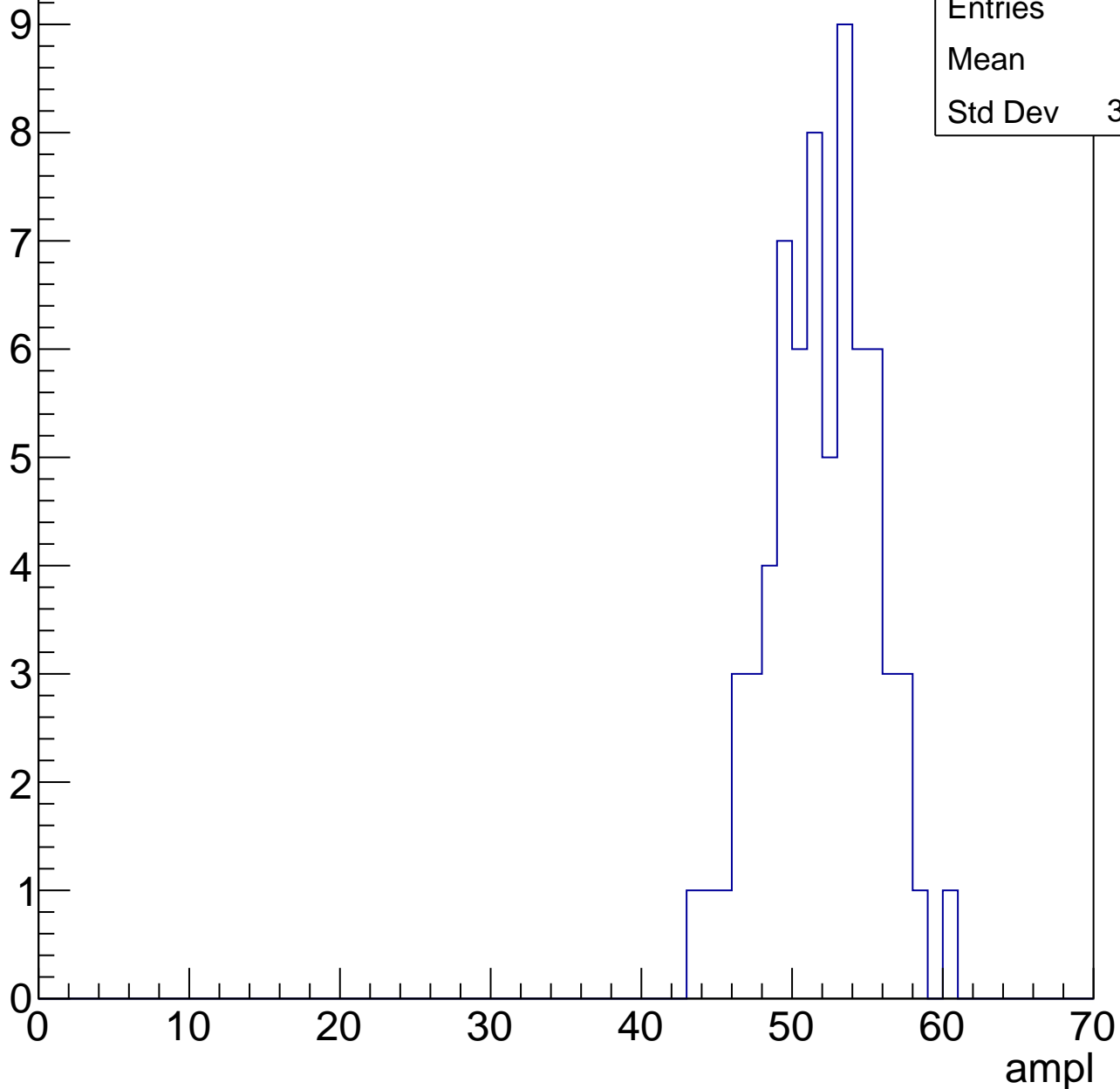
Entry



B1L101S, U23-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

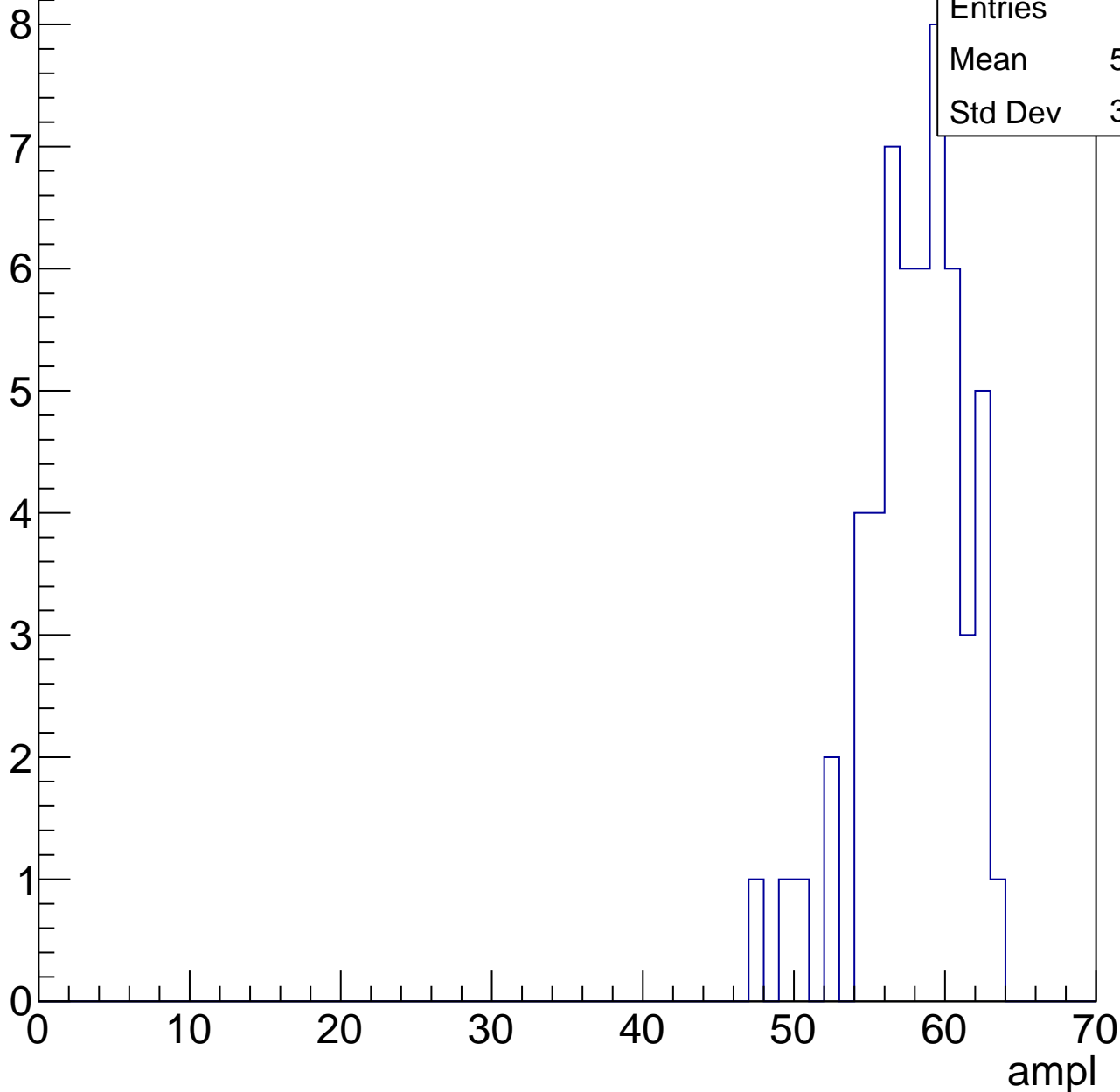


B1L101S, U23-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.38
Std Dev	3.344

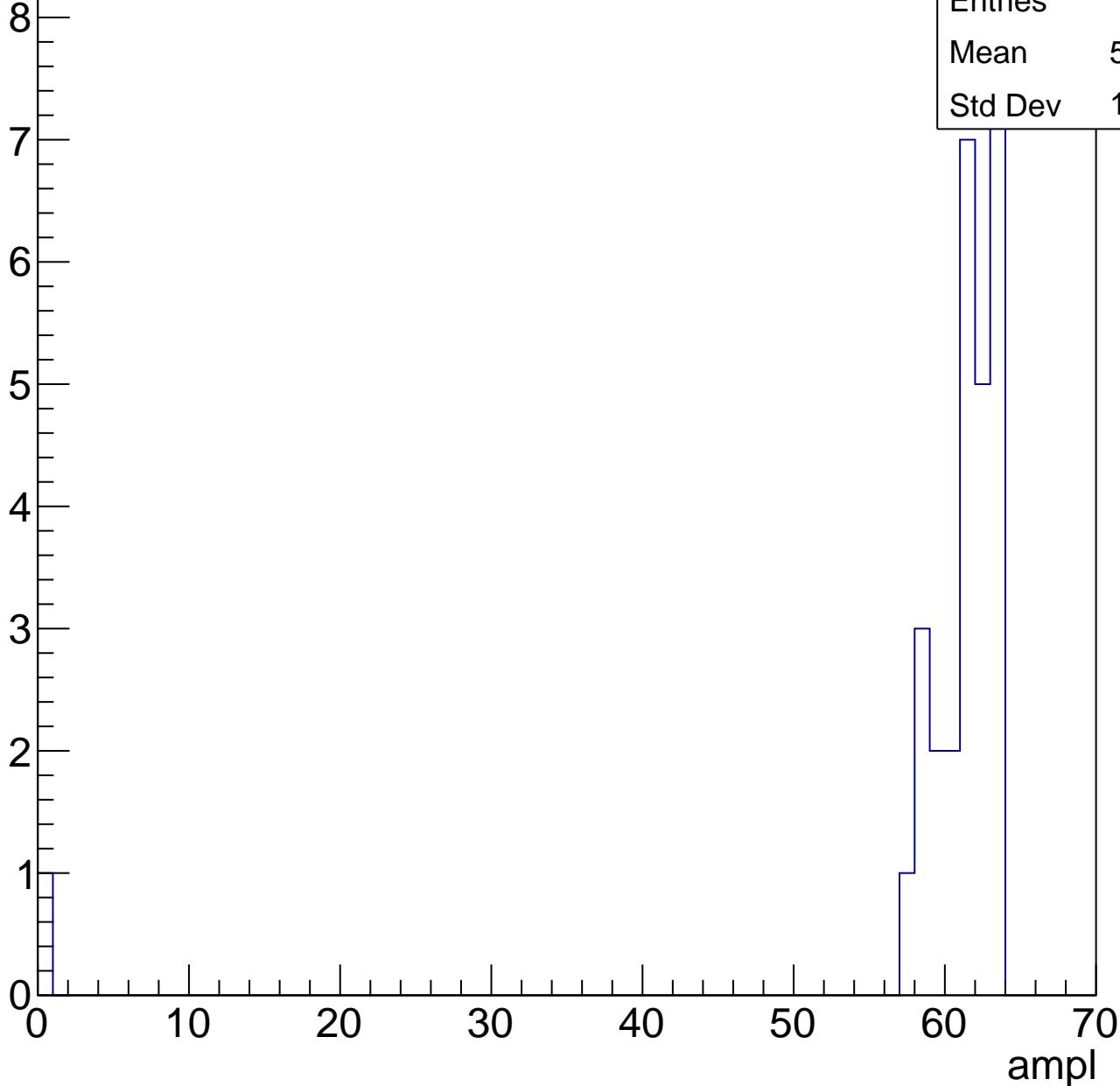


B1L101S, U23-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.97
Std Dev	11.28



B1L101S, U23-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

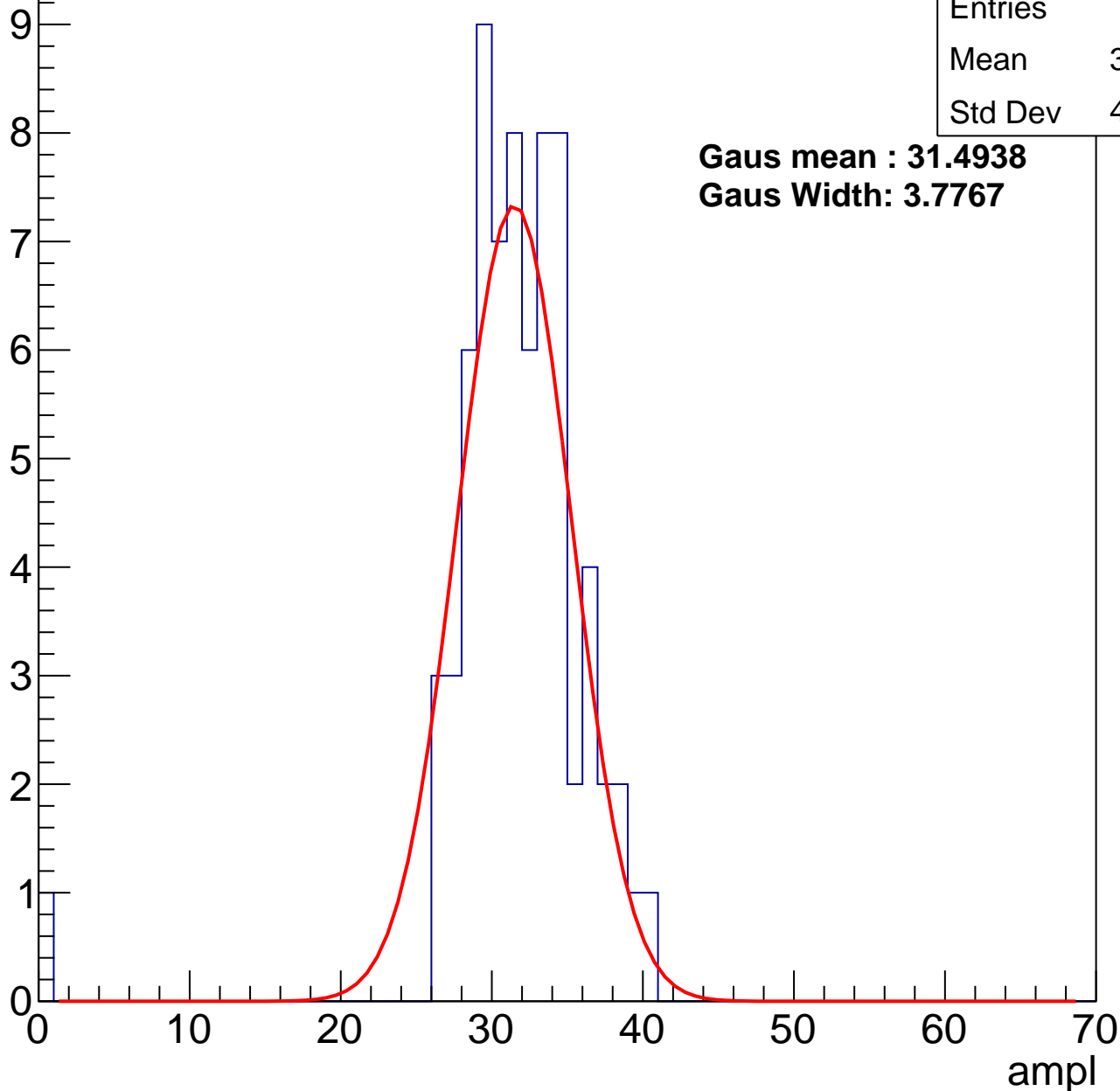
Entry



B1L101S, U23-ch5, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch5, adc1

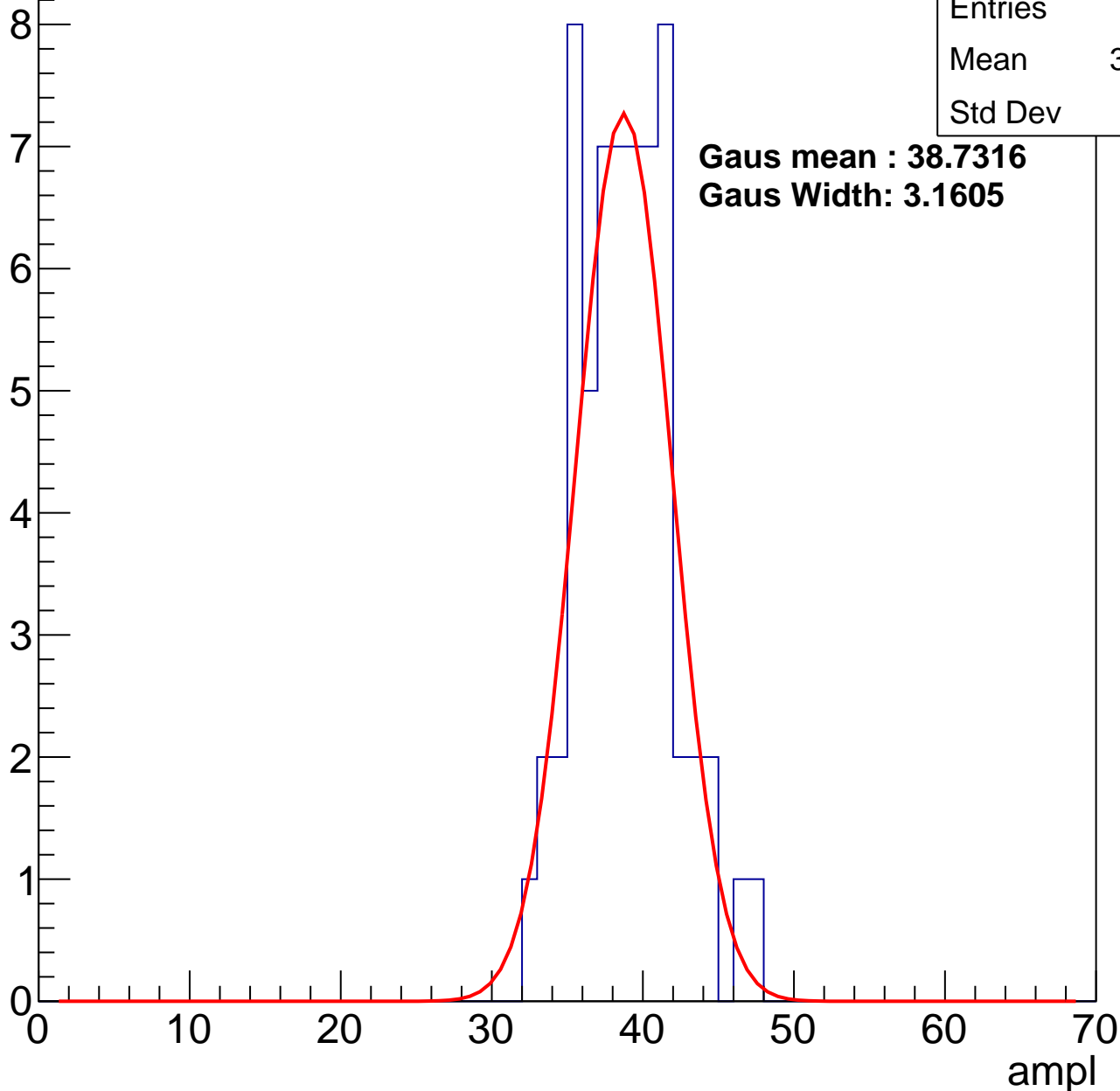
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	38.44
Std Dev	3.14

Gaus mean : 38.7316

Gaus Width: 3.1605



B1L101S, U23-ch5, adc2

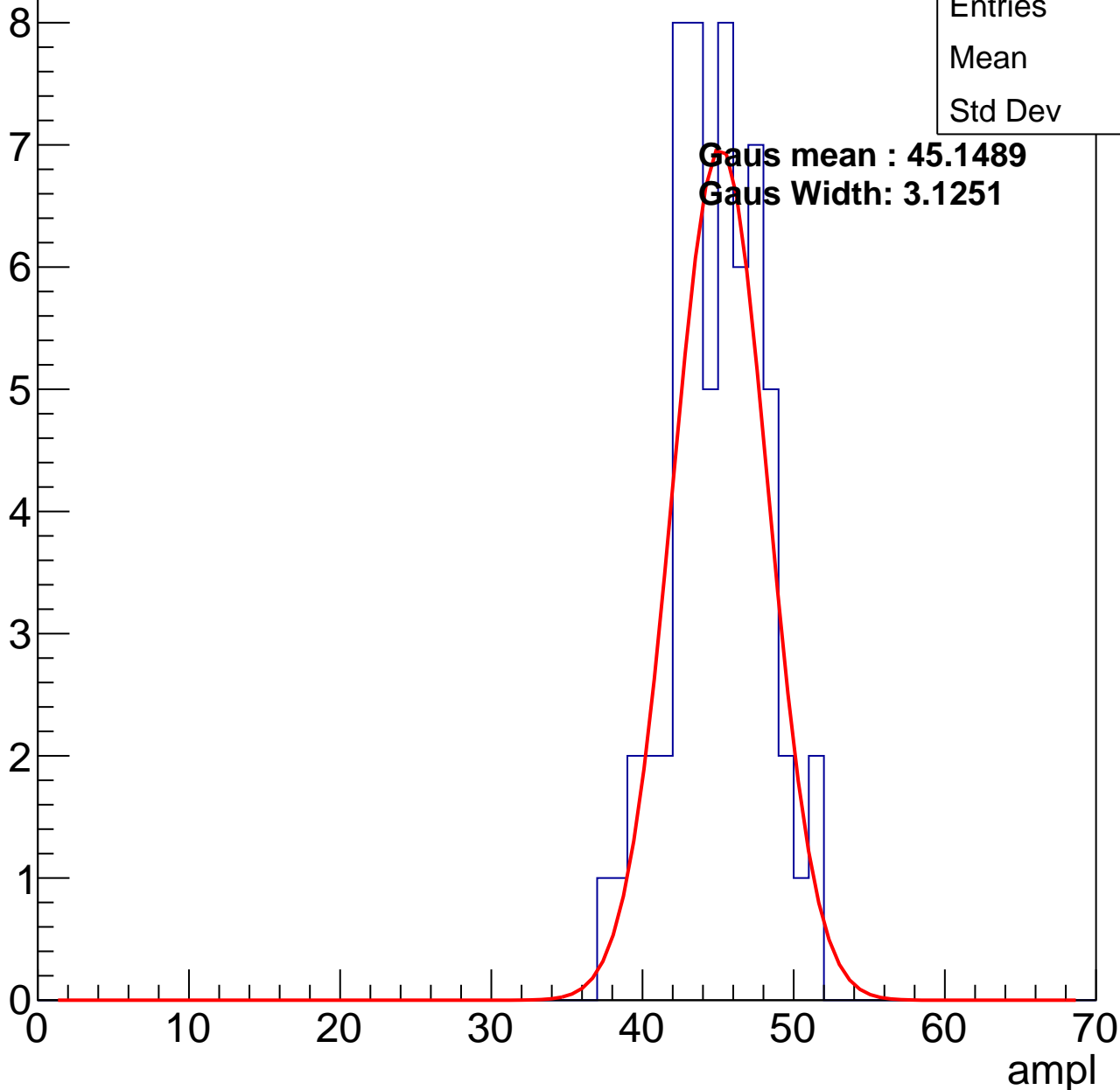
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.5
Std Dev	3.08

Gaus mean : 45.1489

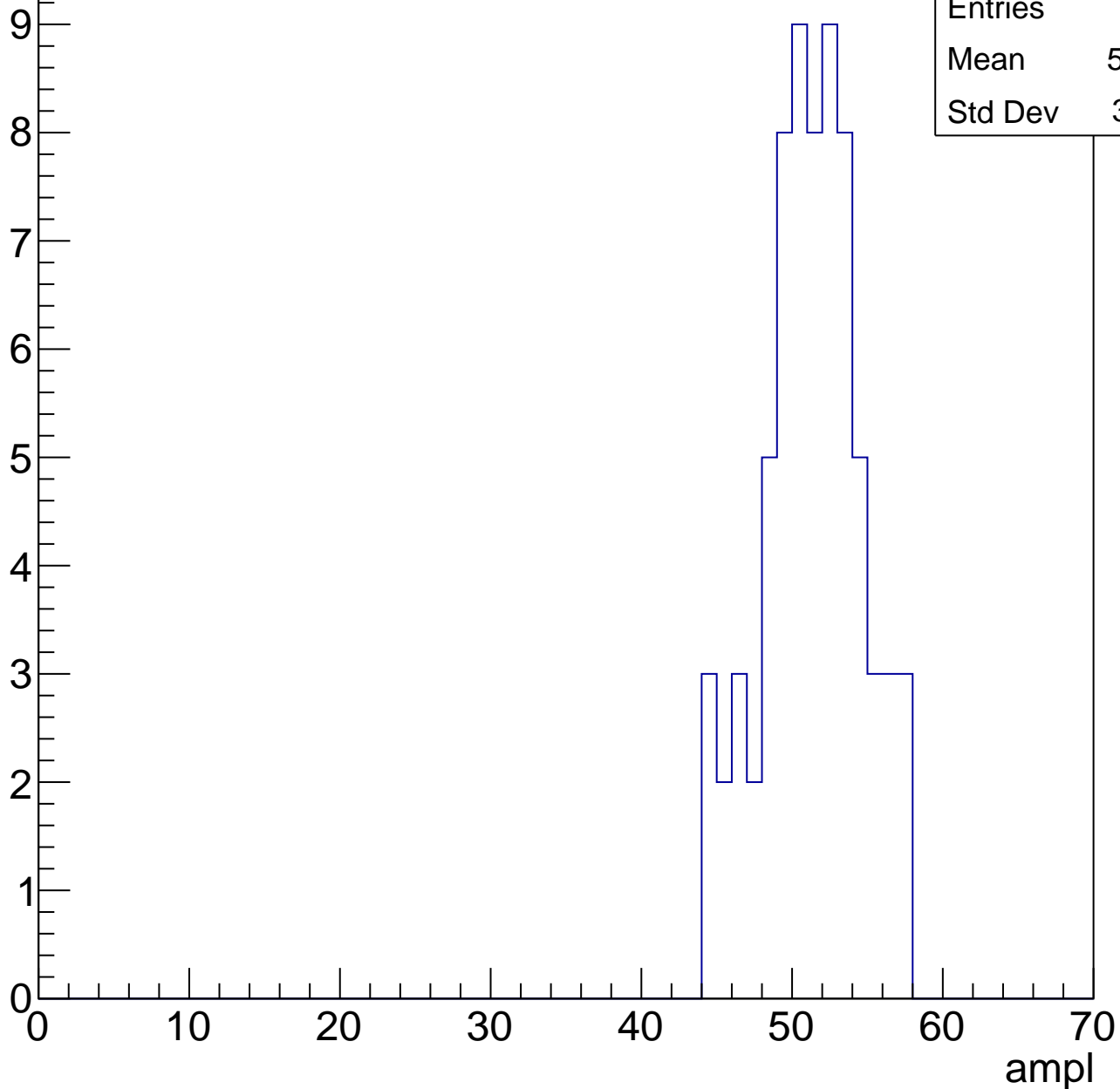
Gaus Width: 3.1251



B1L101S, U23-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

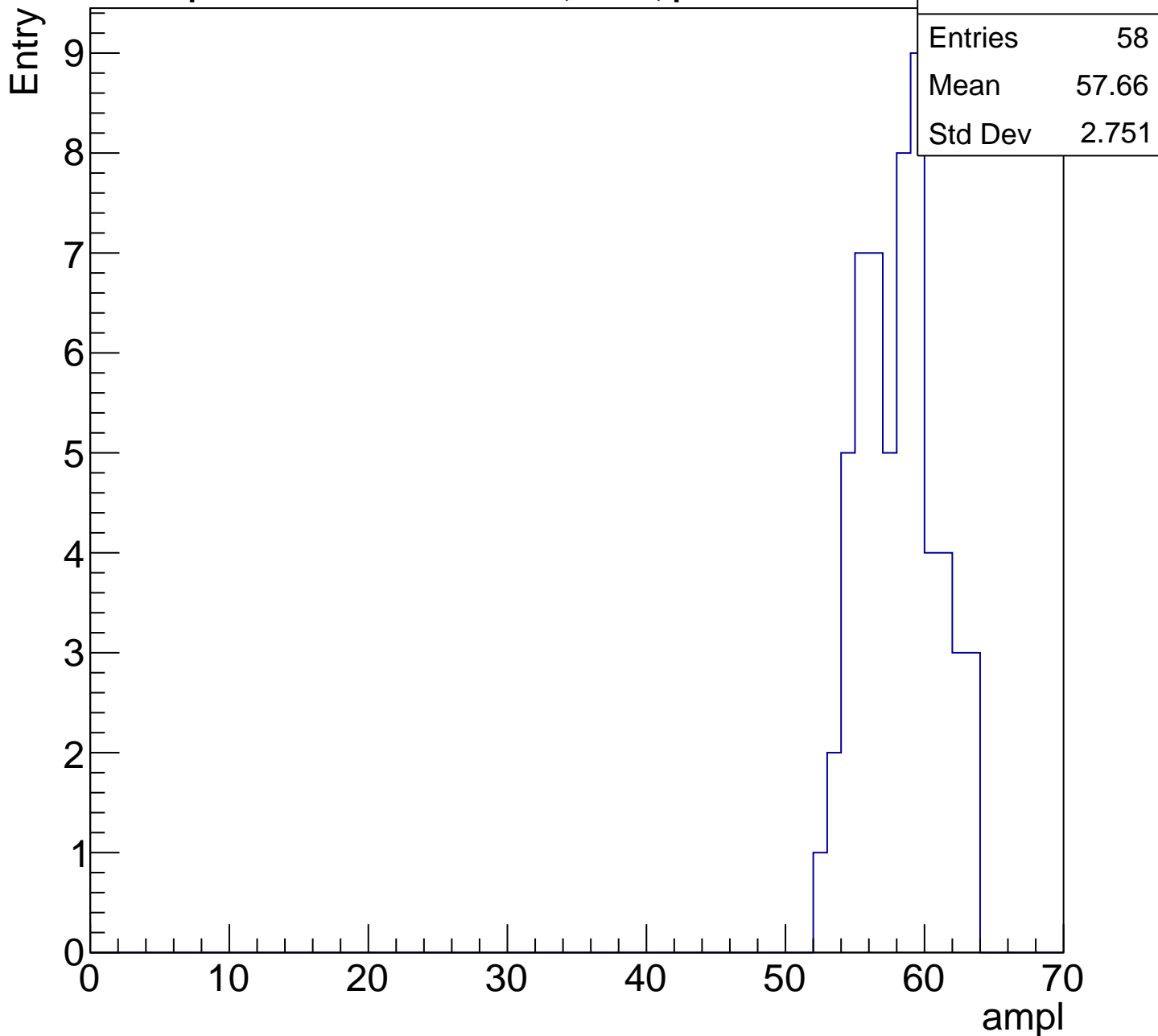
Entry



Entries	71
Mean	50.85
Std Dev	3.201

B1L101S, U23-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries	29
Mean	58.69
Std Dev	11.22

ampl

0

10

20

30

40

50

60

70

B1L101S, U23-ch5, adc6

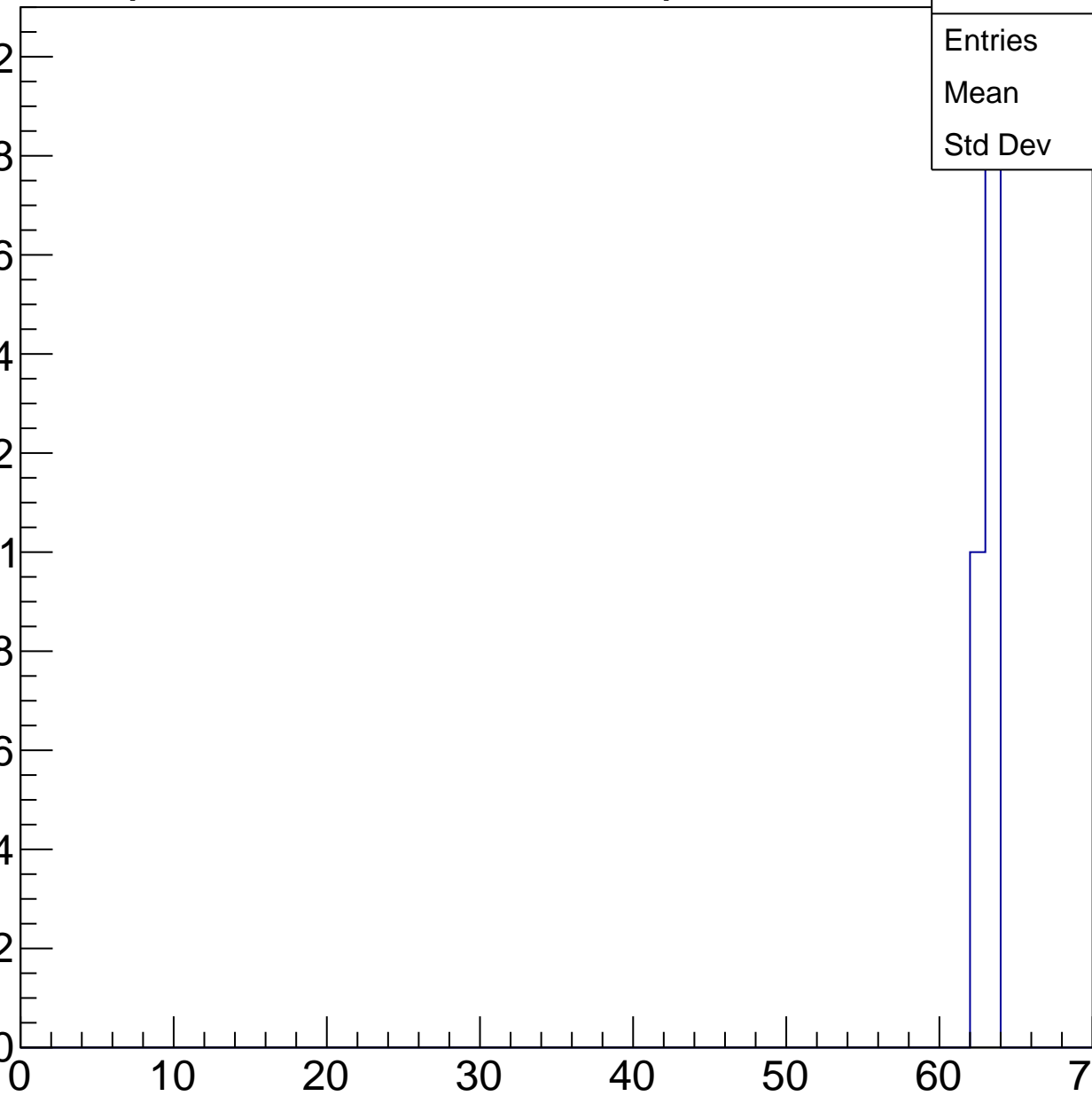
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

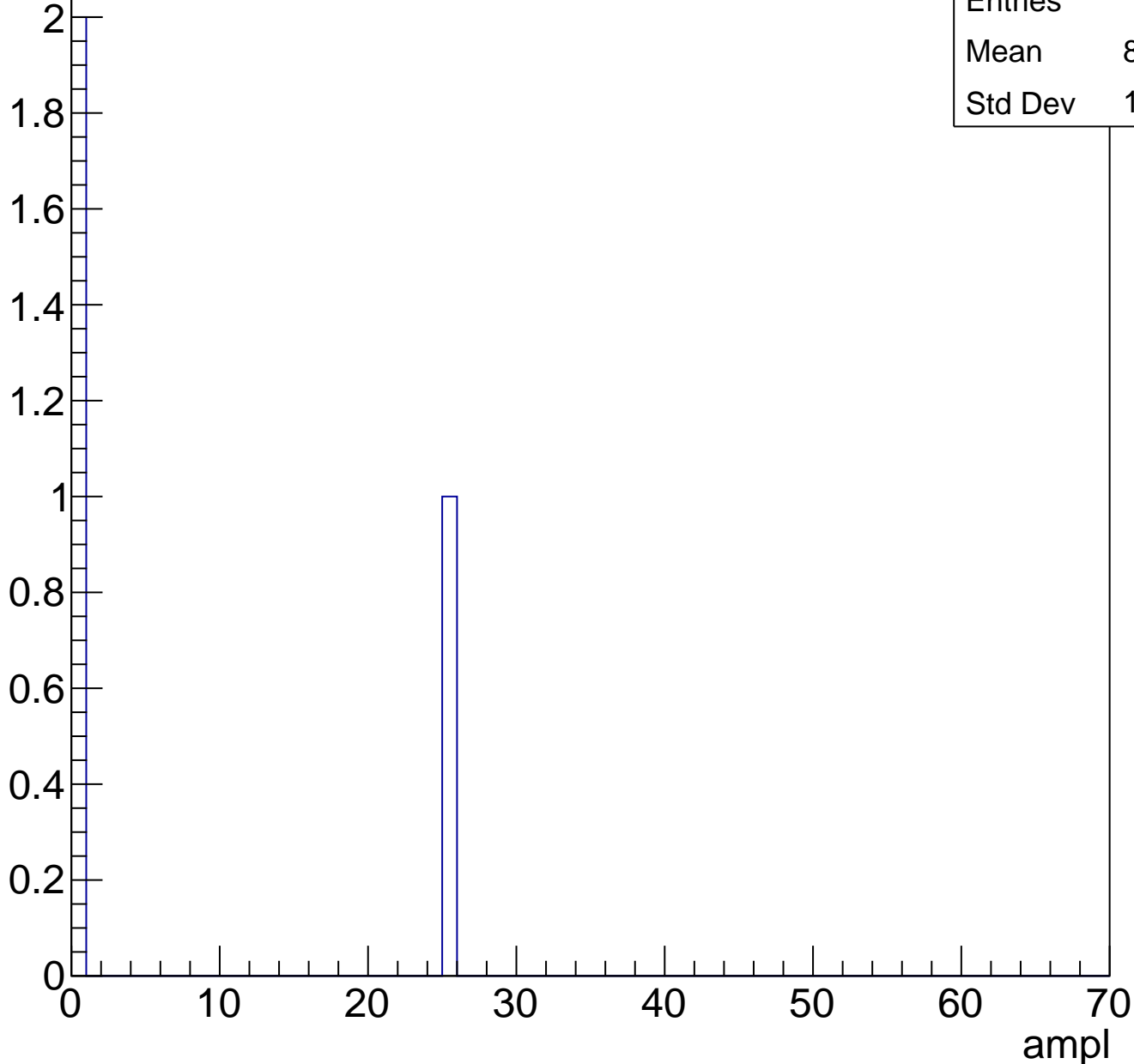
ampl



B1L101S, U23-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L101S, U23-ch6, adc0

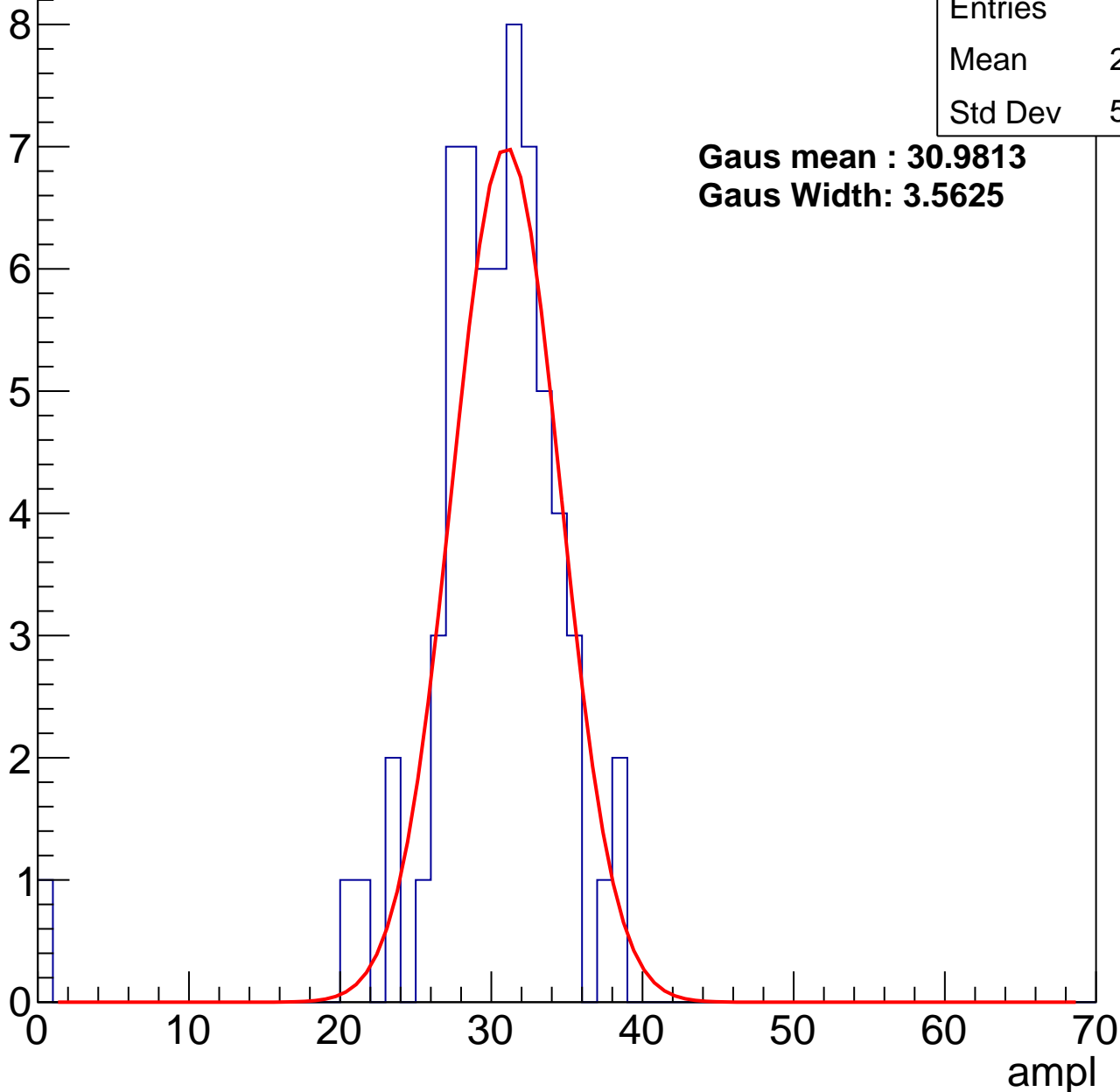
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.54
Std Dev	5.162

Gaus mean : 30.9813

Gaus Width: 3.5625



B1L101S, U23-ch6, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.03
Std Dev	3.769

Gaus mean : 37.1308

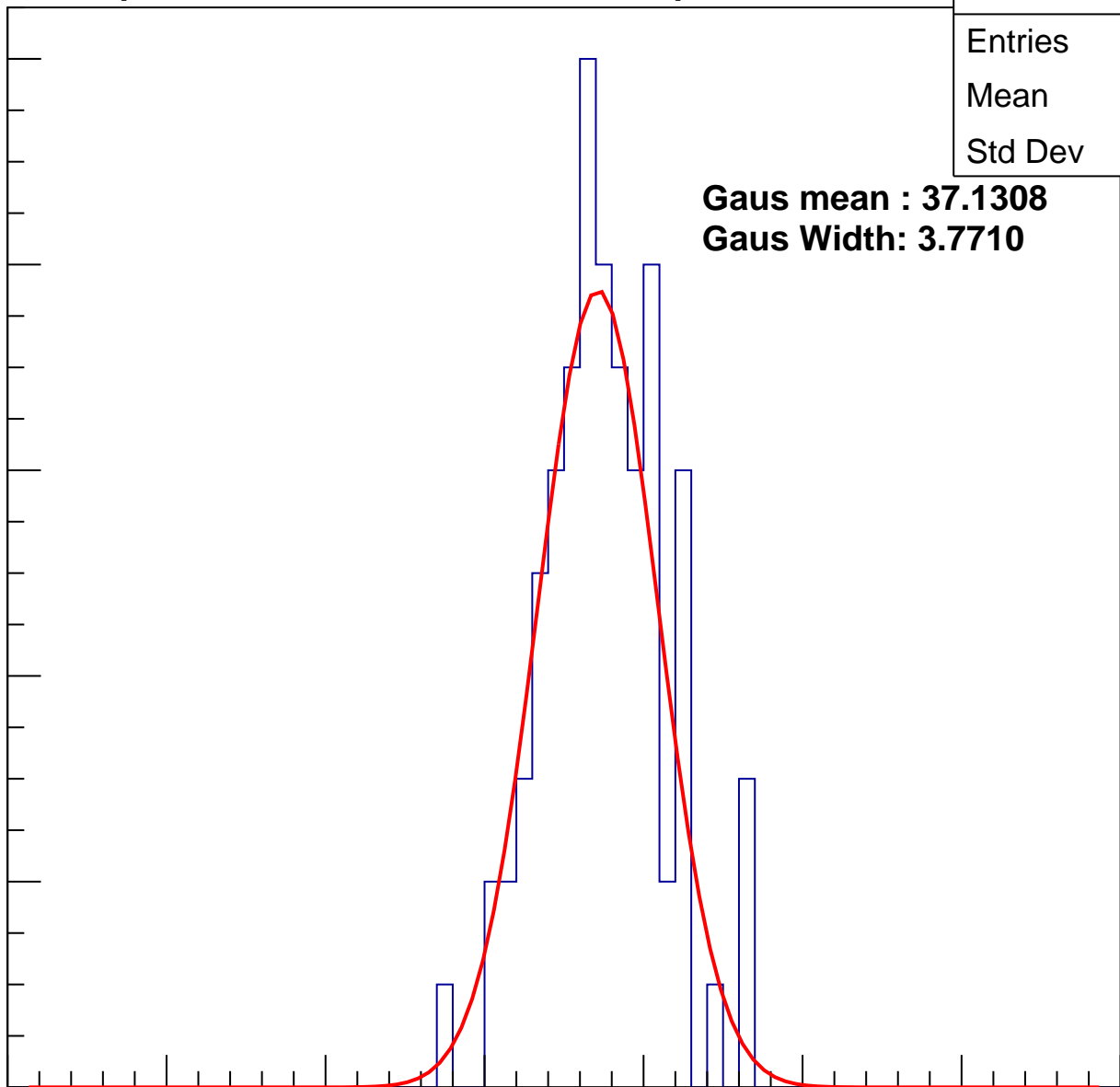
Gaus Width: 3.7710

Entry

10
8
6
4
2
0

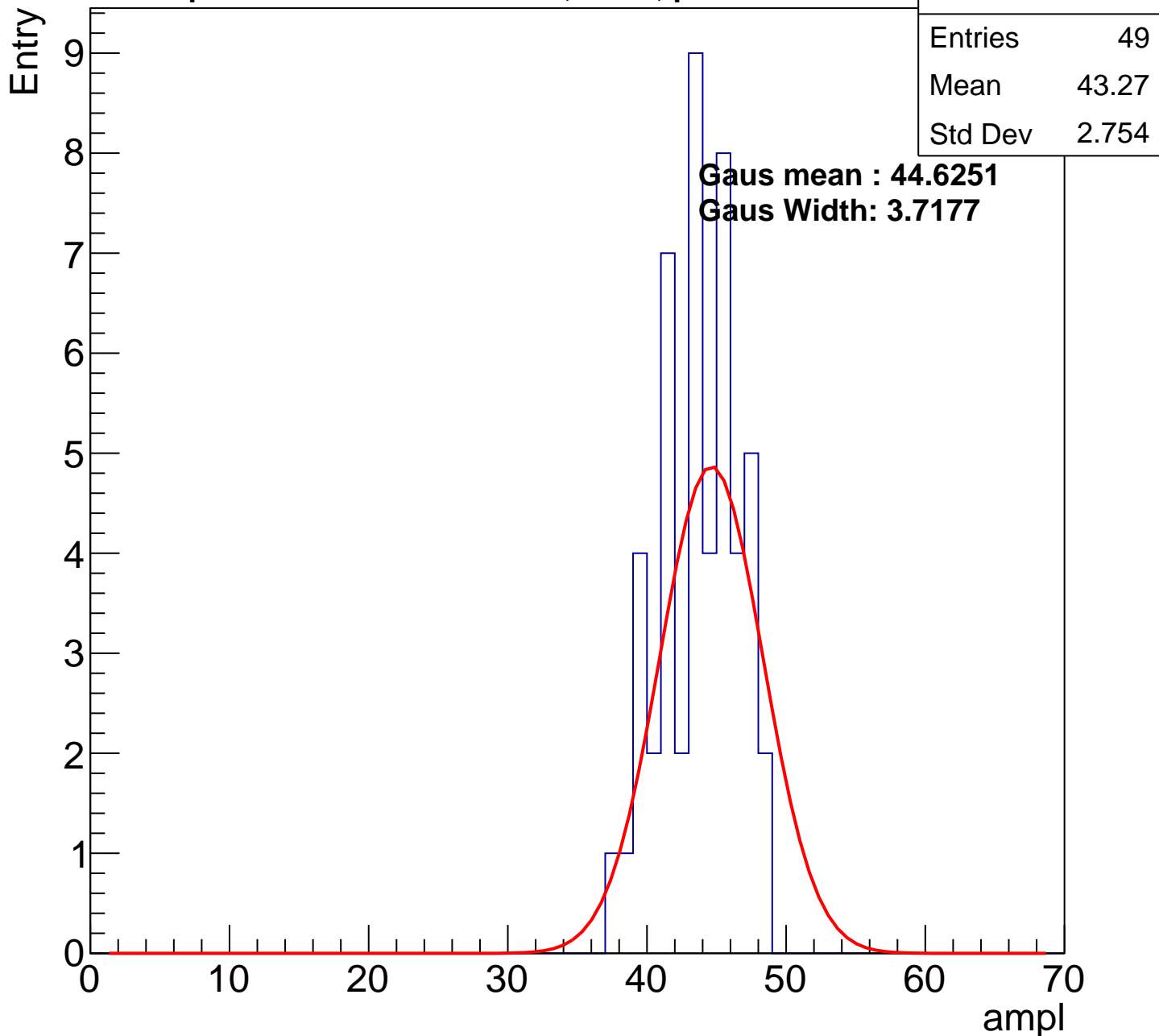
0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

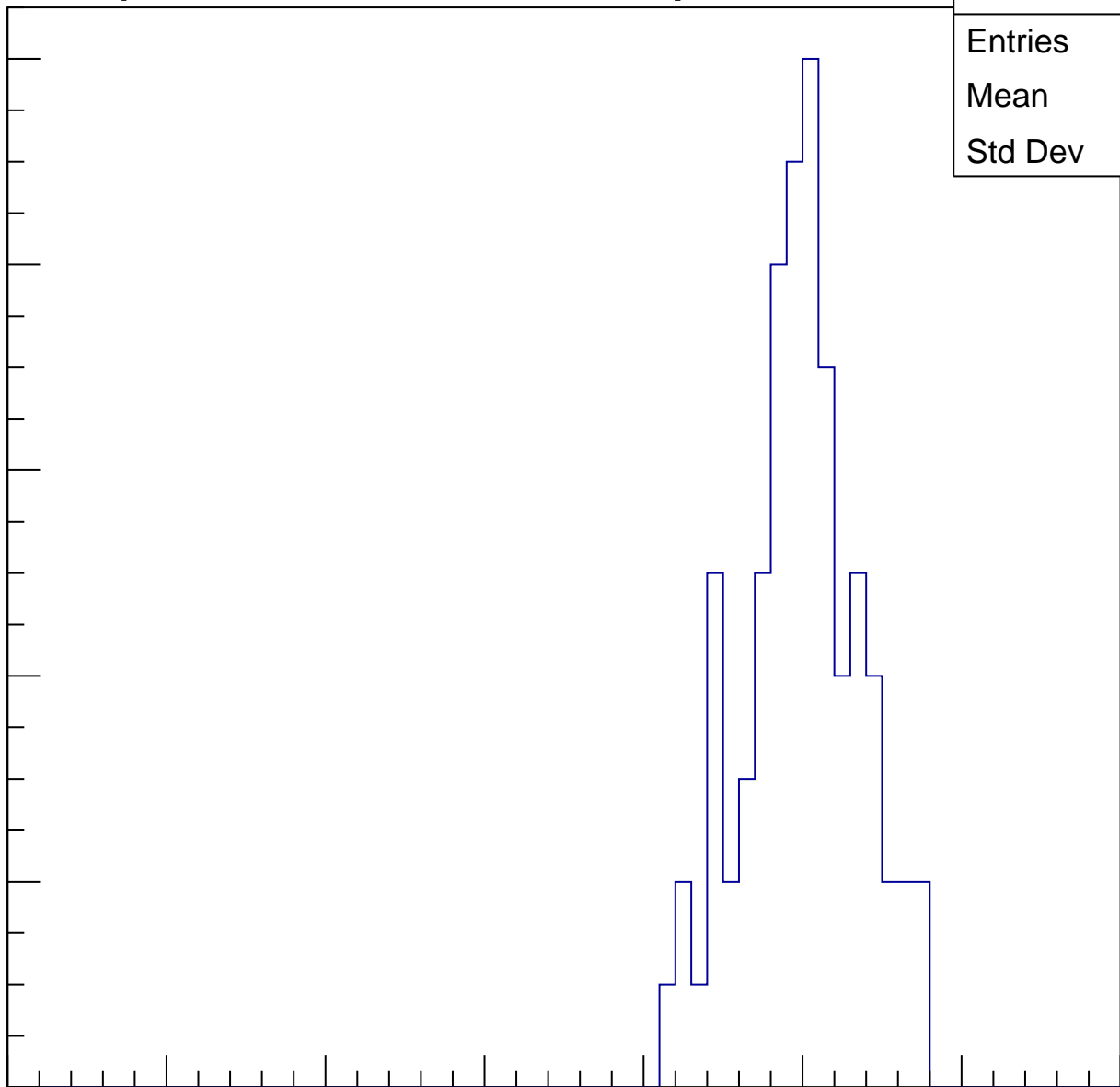
Entries	72
Mean	49.42
Std Dev	3.631

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

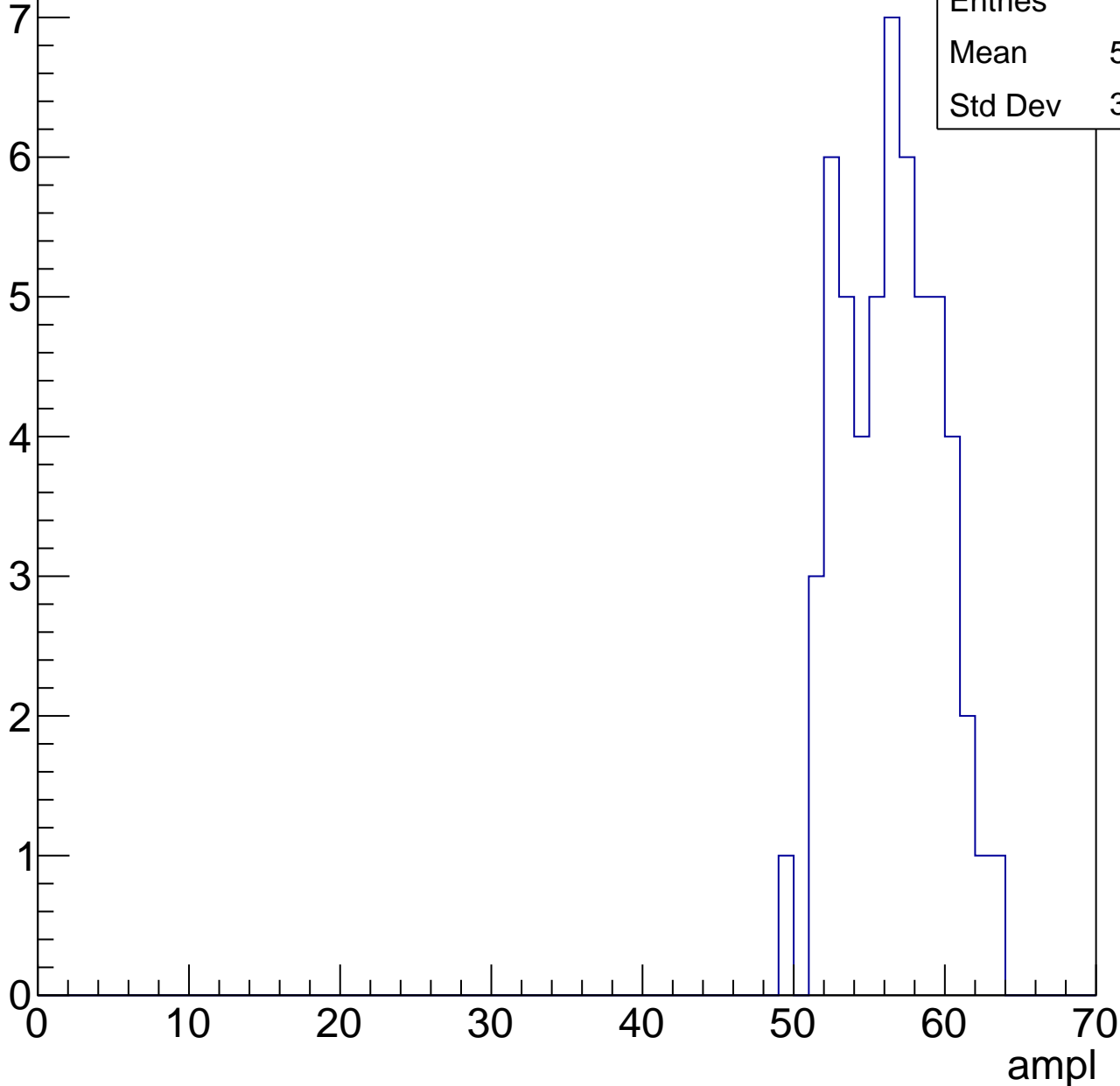


B1L101S, U23-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.93
Std Dev	3.179

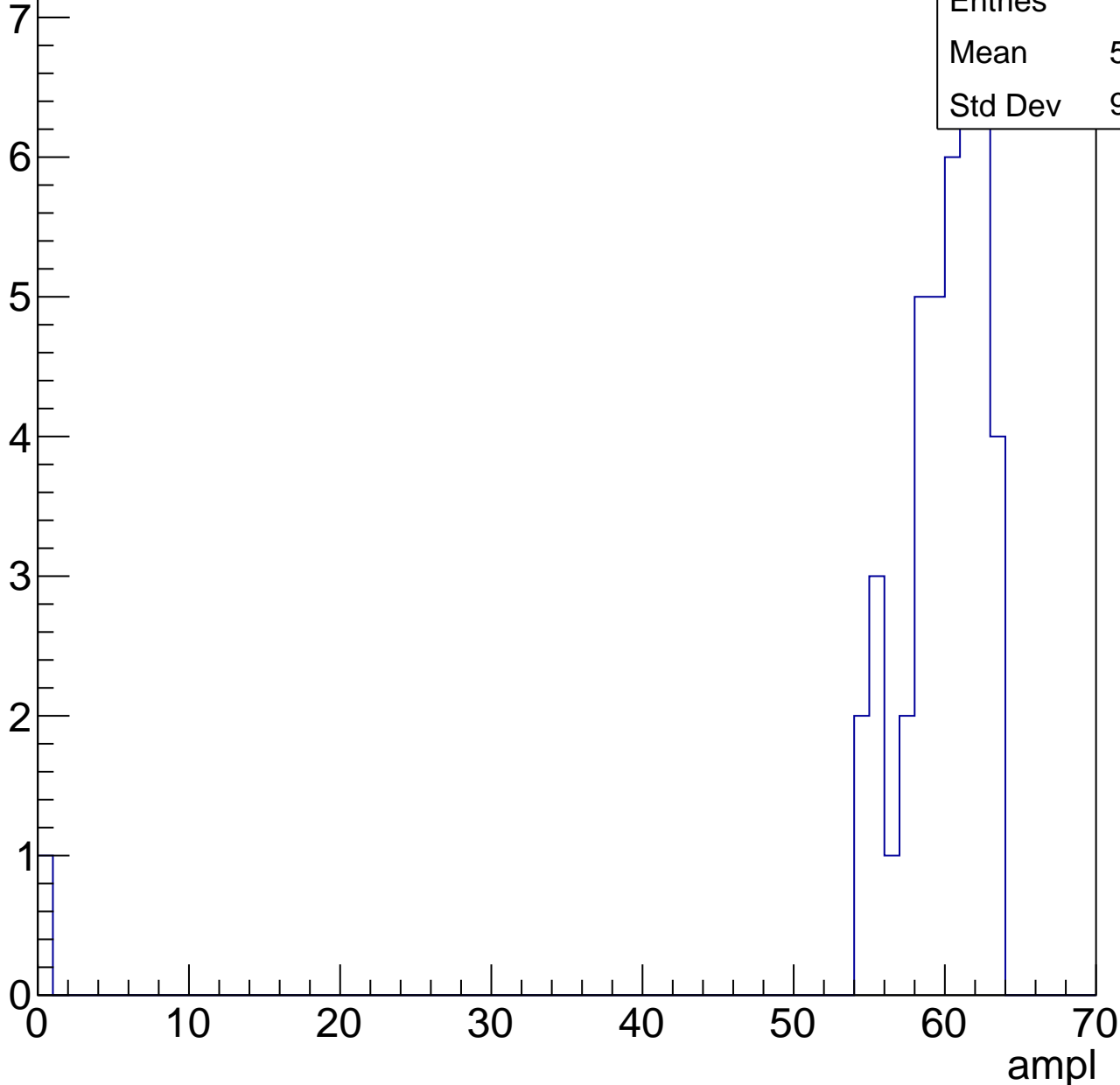


B1L101S, U23-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.16
Std Dev	9.316

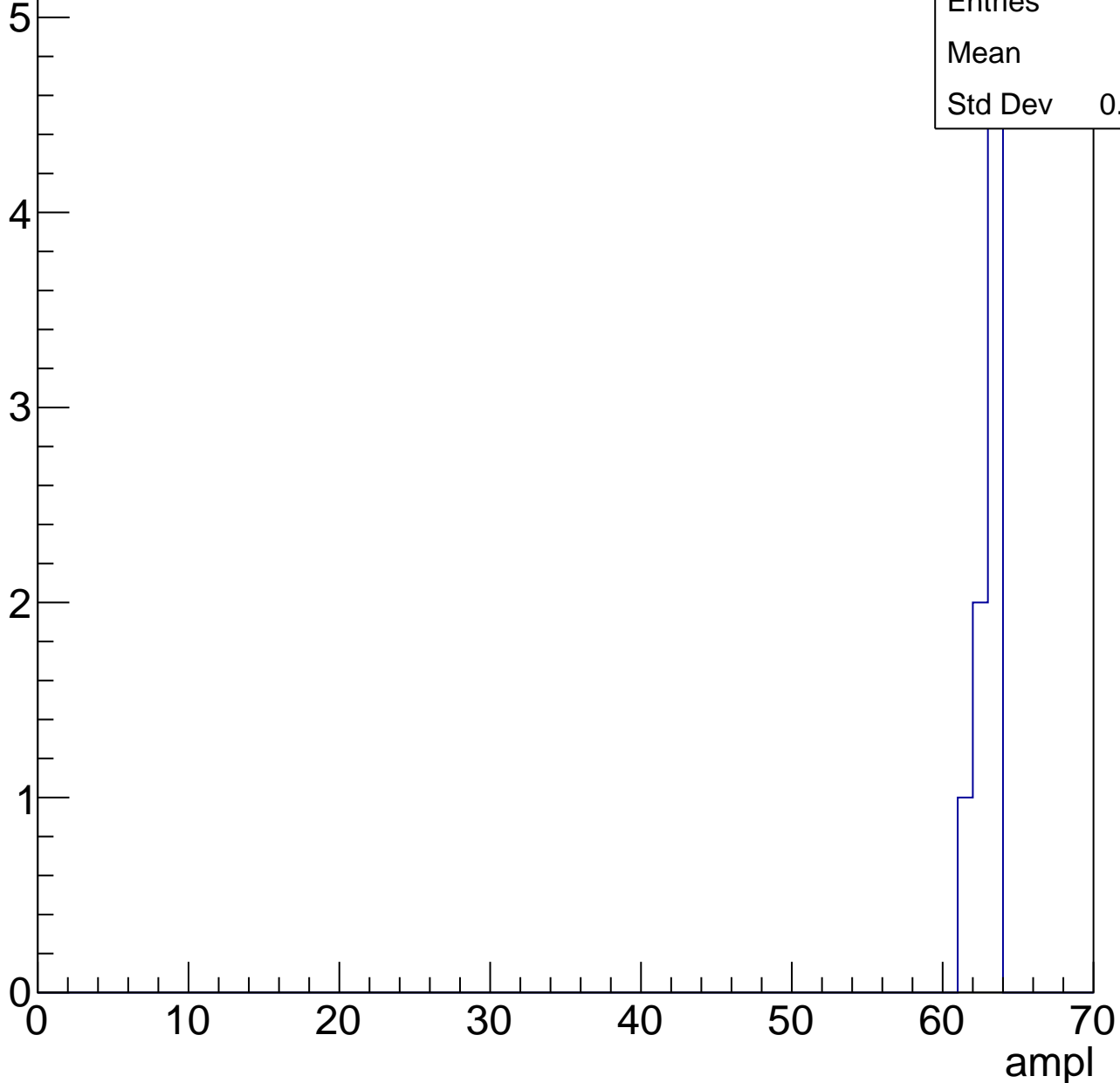


B1L101S, U23-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B1L101S, U23-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch7, adc0

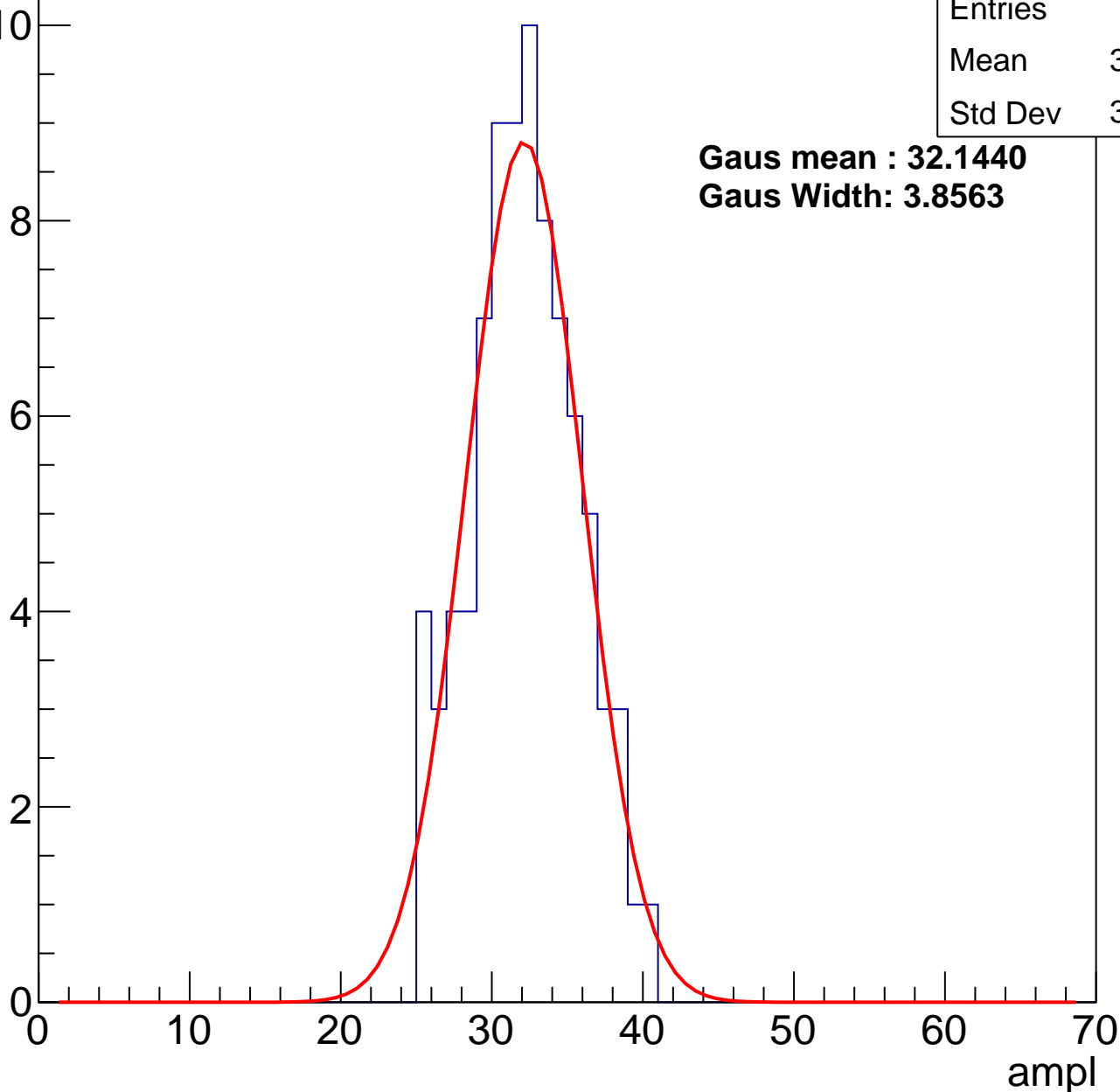
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	31.74
Std Dev	3.512

Gaus mean : 32.1440

Gaus Width: 3.8563



B1L101S, U23-ch7, adc1

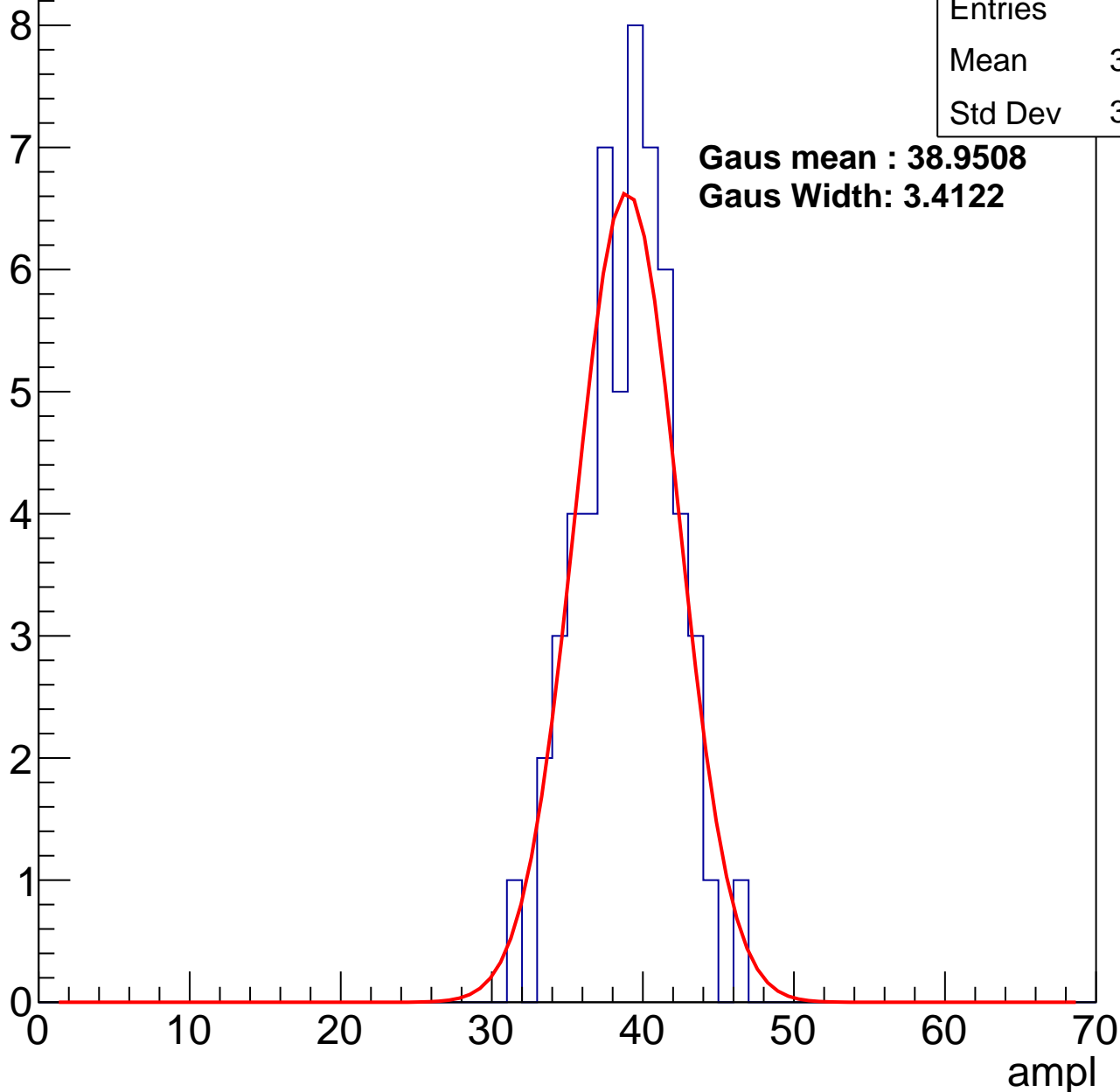
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	38.52
Std Dev	3.047

Gaus mean : 38.9508

Gaus Width: 3.4122



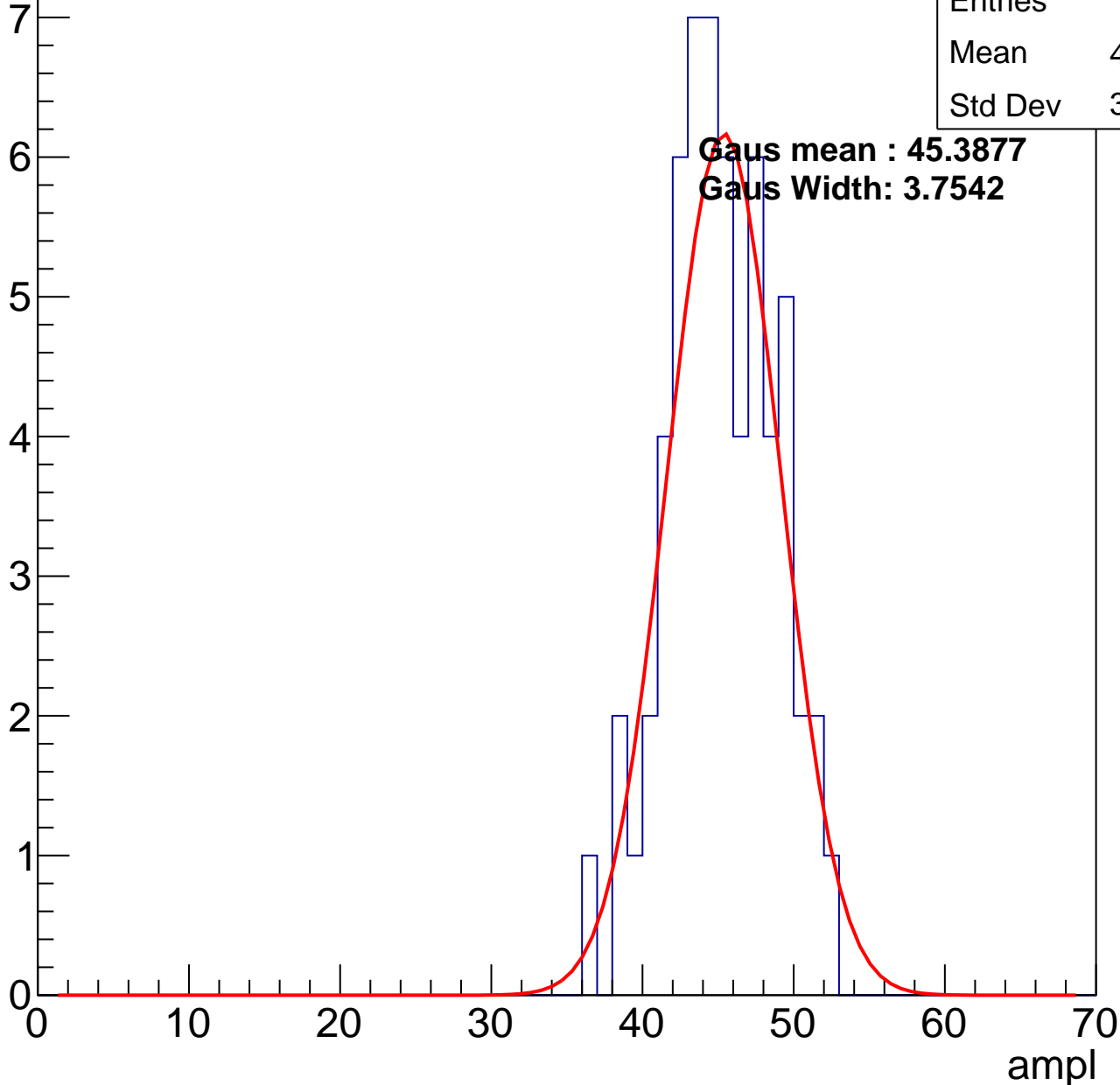
B1L101S, U23-ch7, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.72
Std Dev	3.484

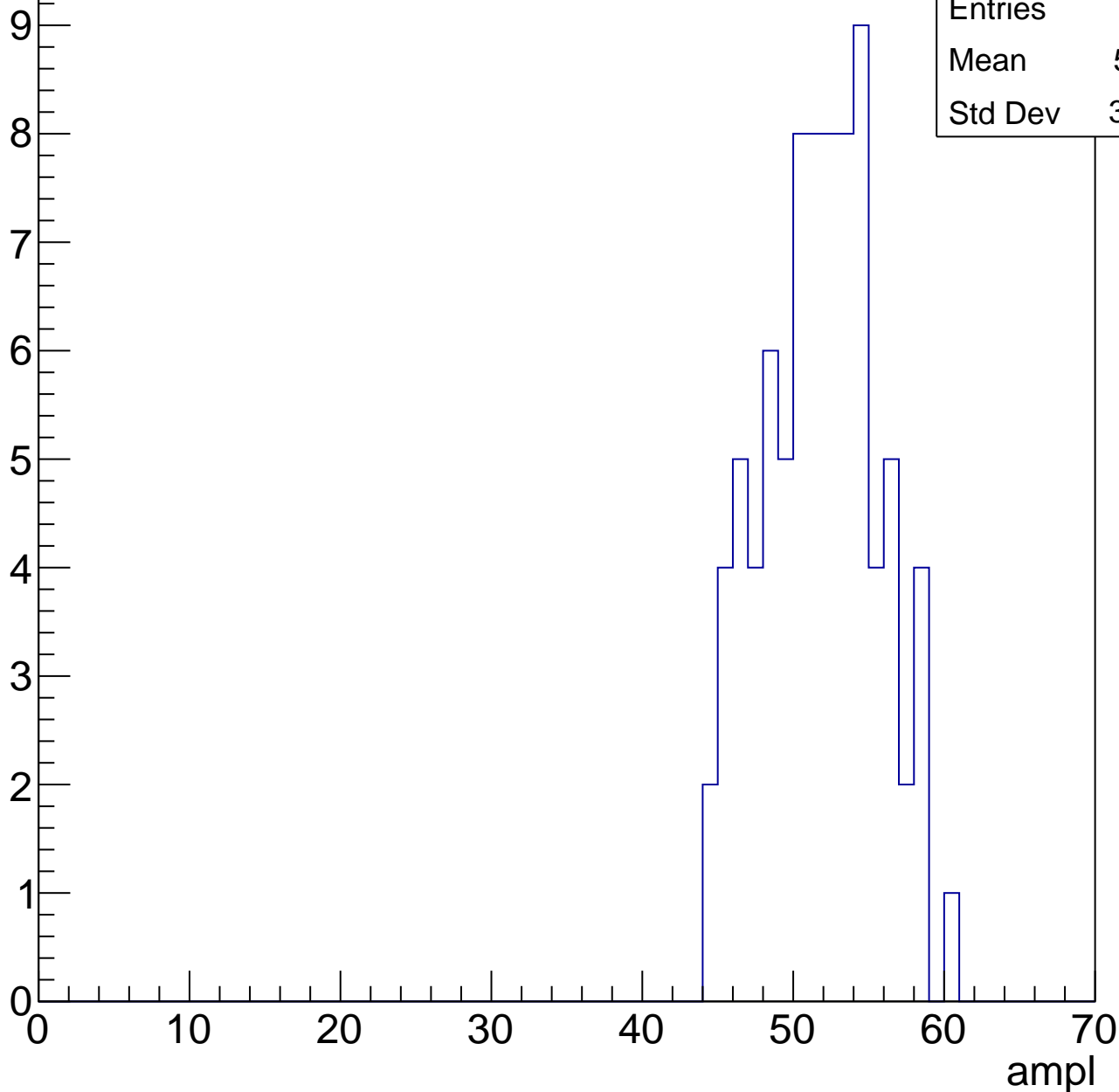
Gaus mean : 45.3877
Gaus Width: 3.7542



B1L101S, U23-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

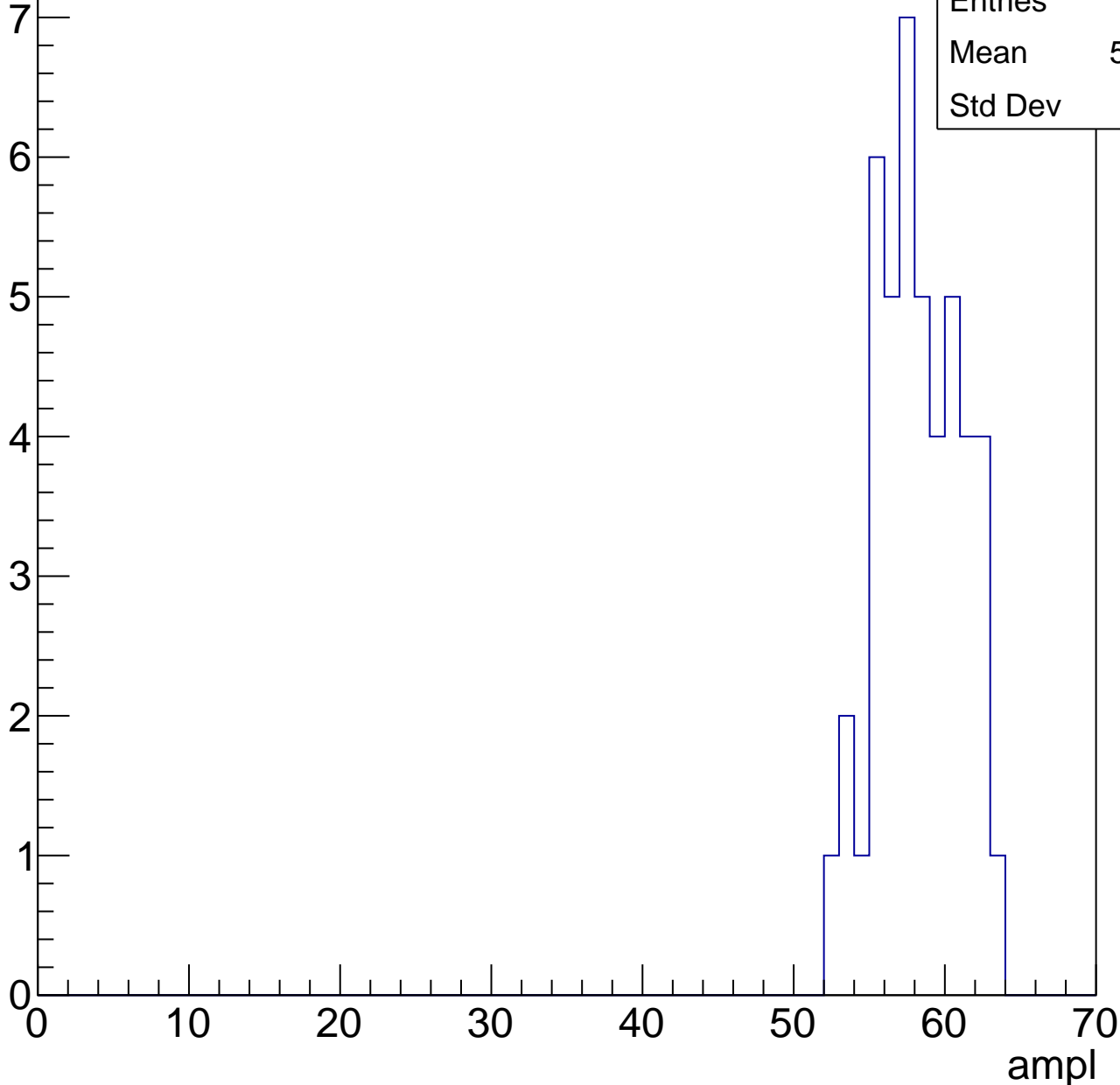


B1L101S, U23-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.82
Std Dev	2.71

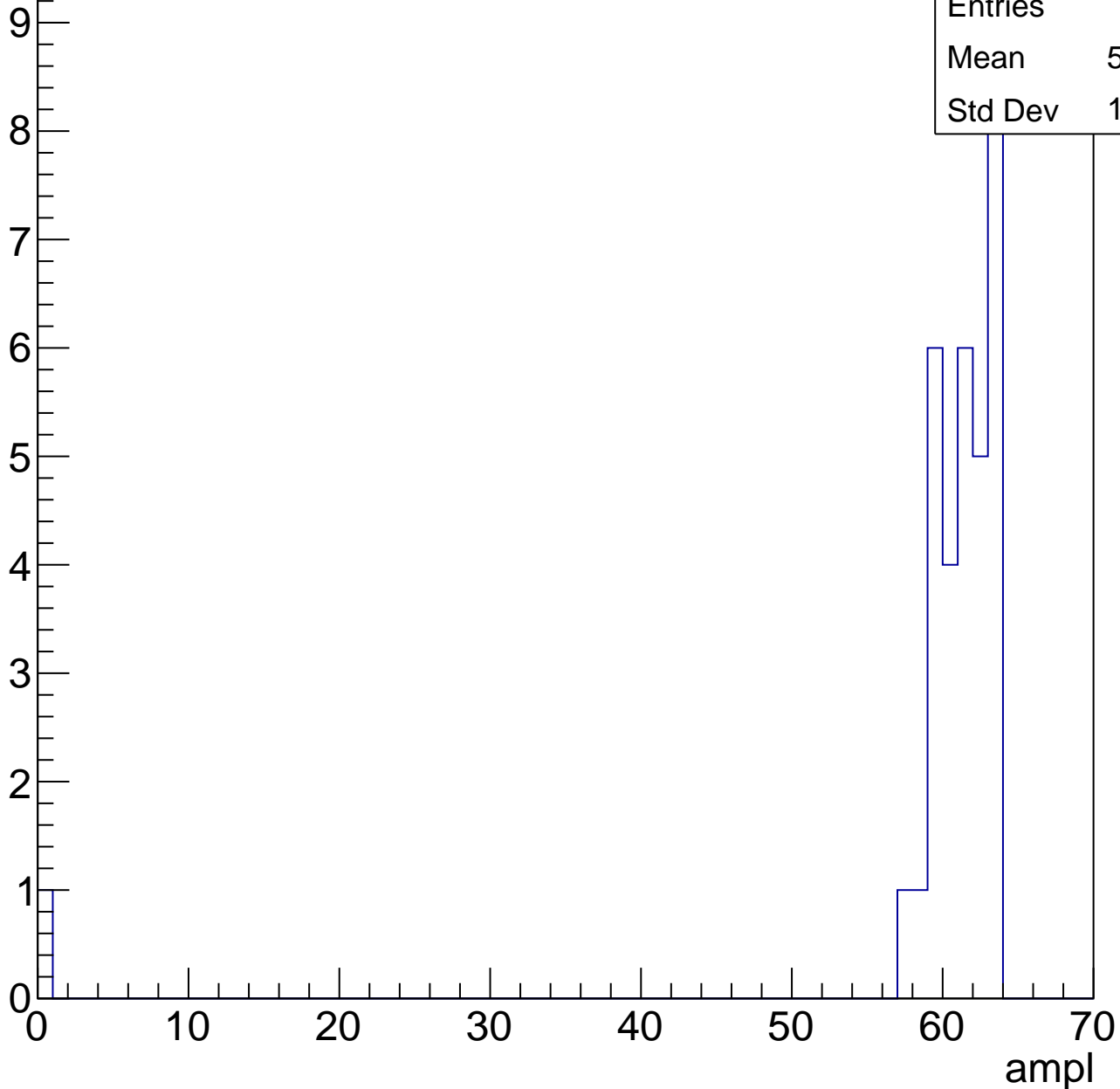


B1L101S, U23-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	59.15
Std Dev	10.59



B1L101S, U23-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L101S, U23-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L101S, U23-ch8, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	29.83
Std Dev	3.084

Gaus mean : 30.1219

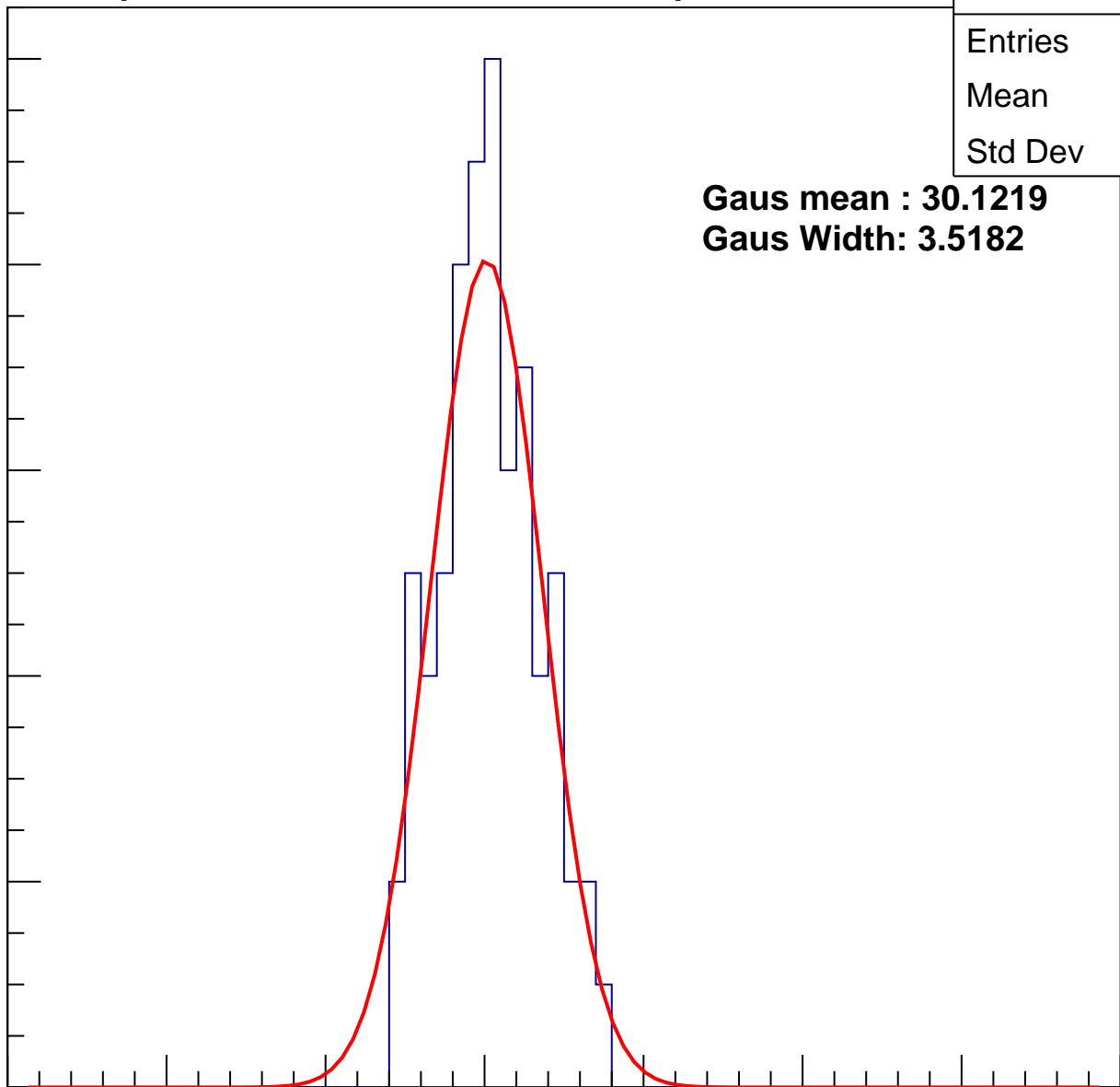
Gaus Width: 3.5182

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch8, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	37.32
Std Dev	3.471

Gaus mean : 37.6418

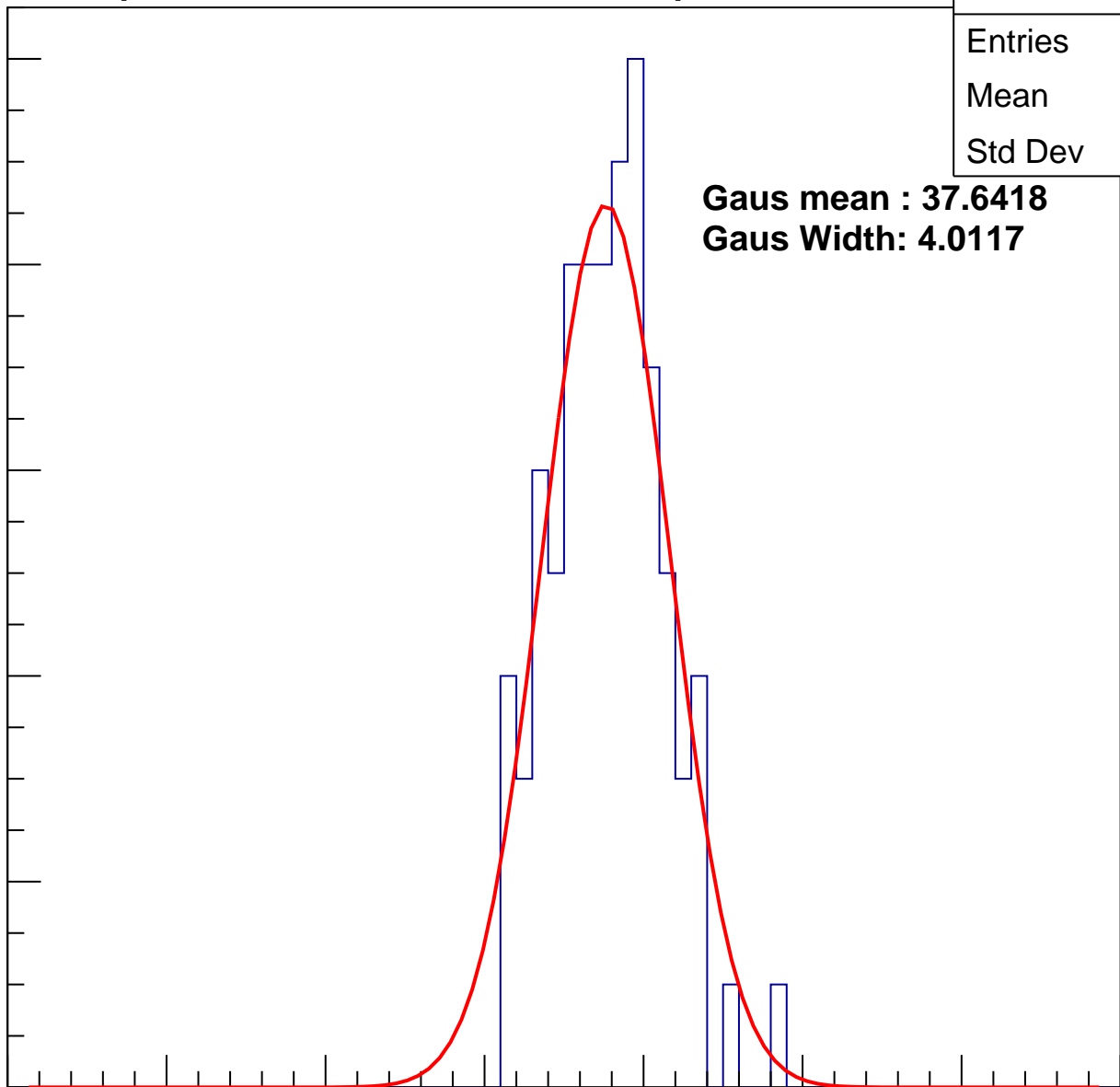
Gaus Width: 4.0117

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch8, adc2

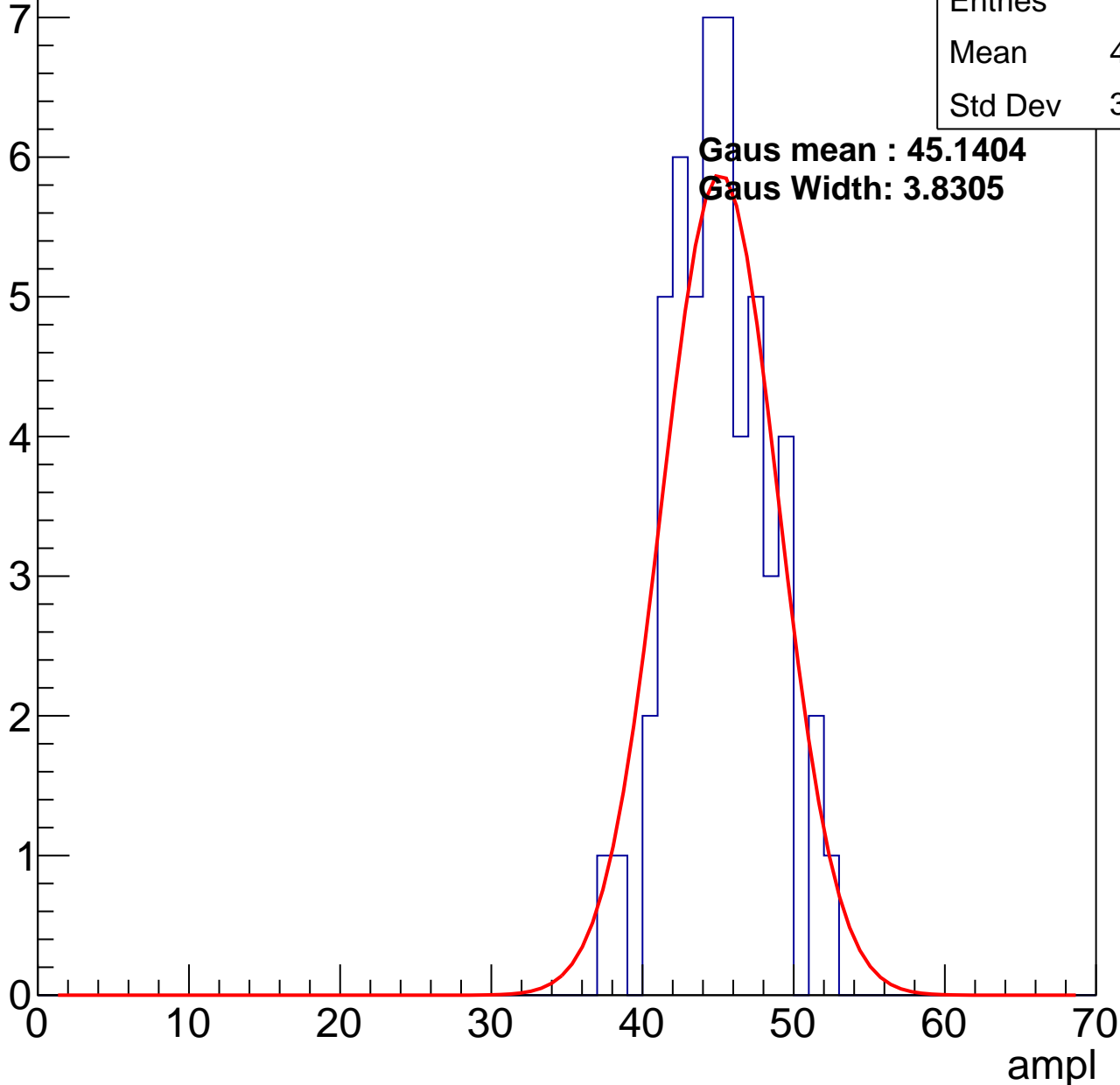
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	44.58
Std Dev	3.224

Gaus mean : 45.1404

Gaus Width: 3.8305

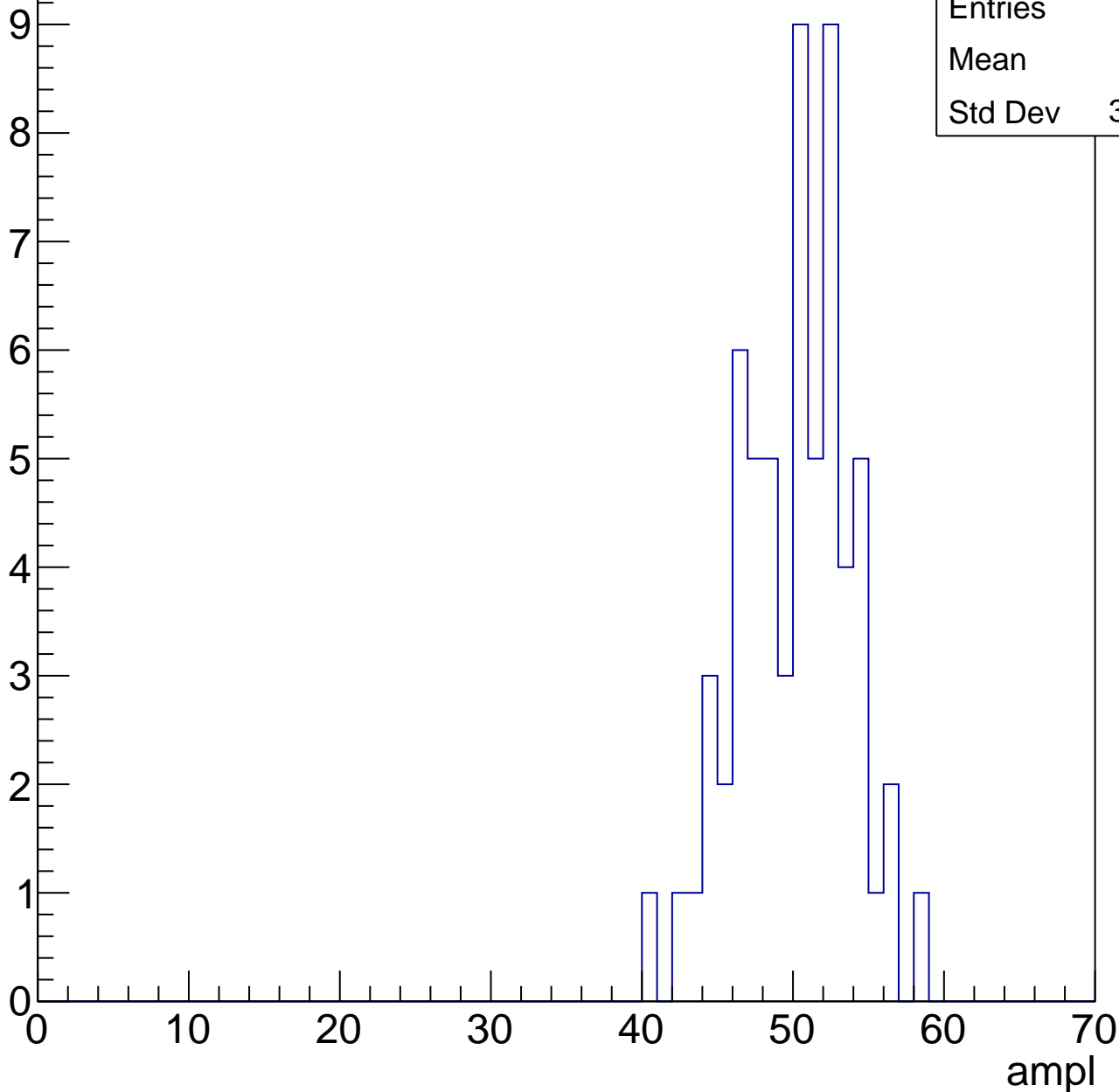


B1L101S, U23-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

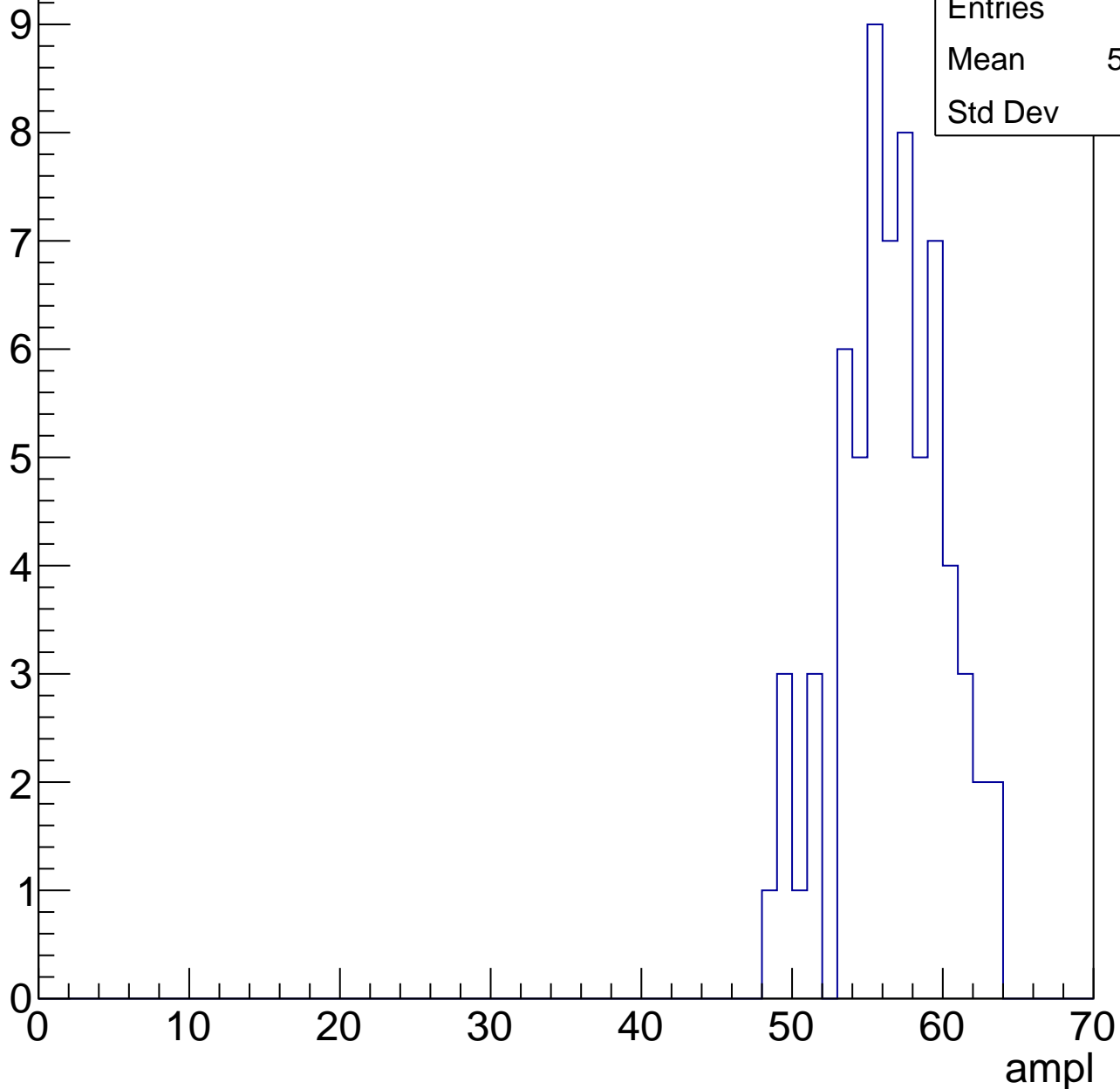
Entries	63
Mean	49.6
Std Dev	3.658



B1L101S, U23-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



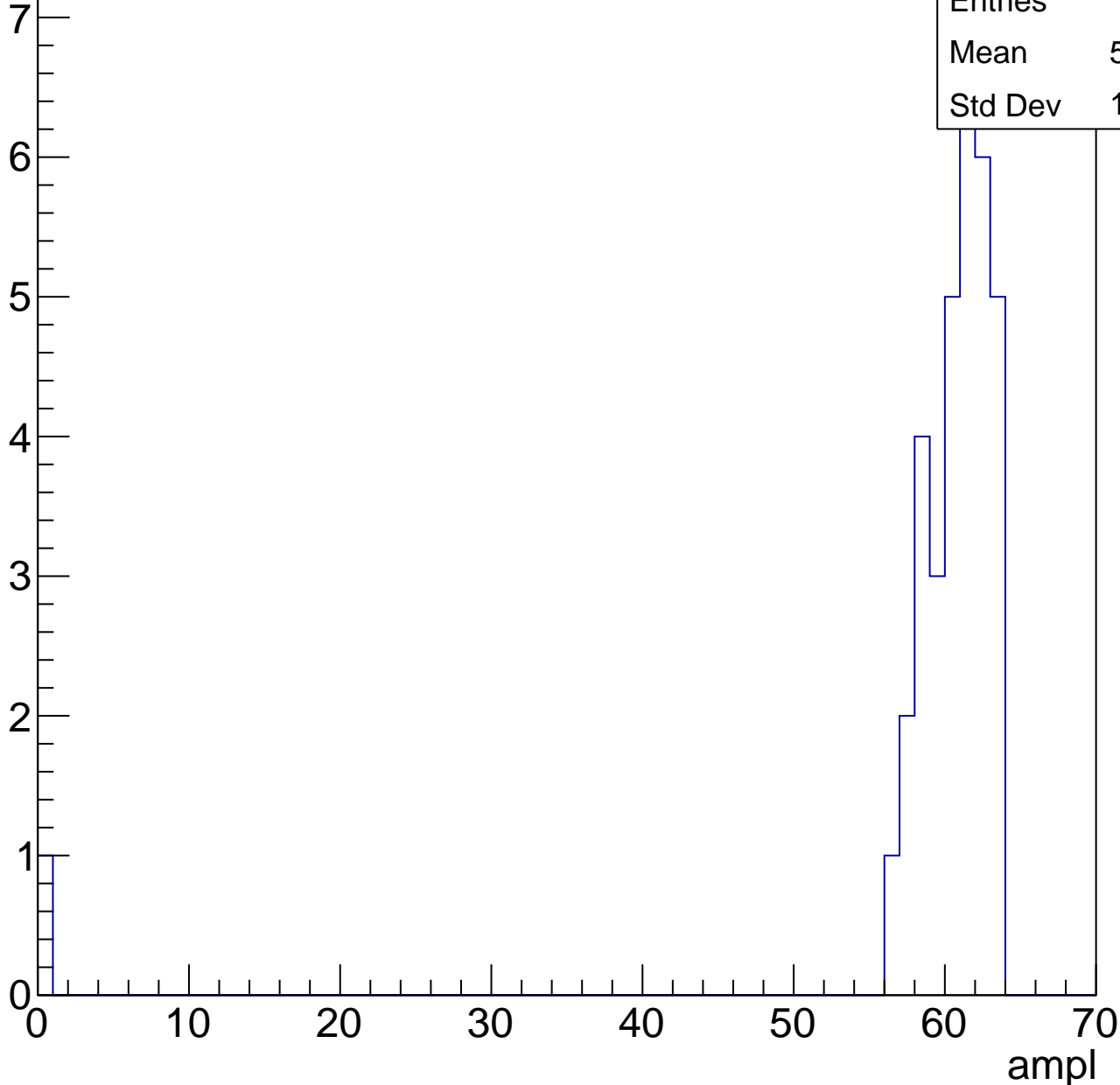
Entries	66
Mean	56.14
Std Dev	3.49

B1L101S, U23-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

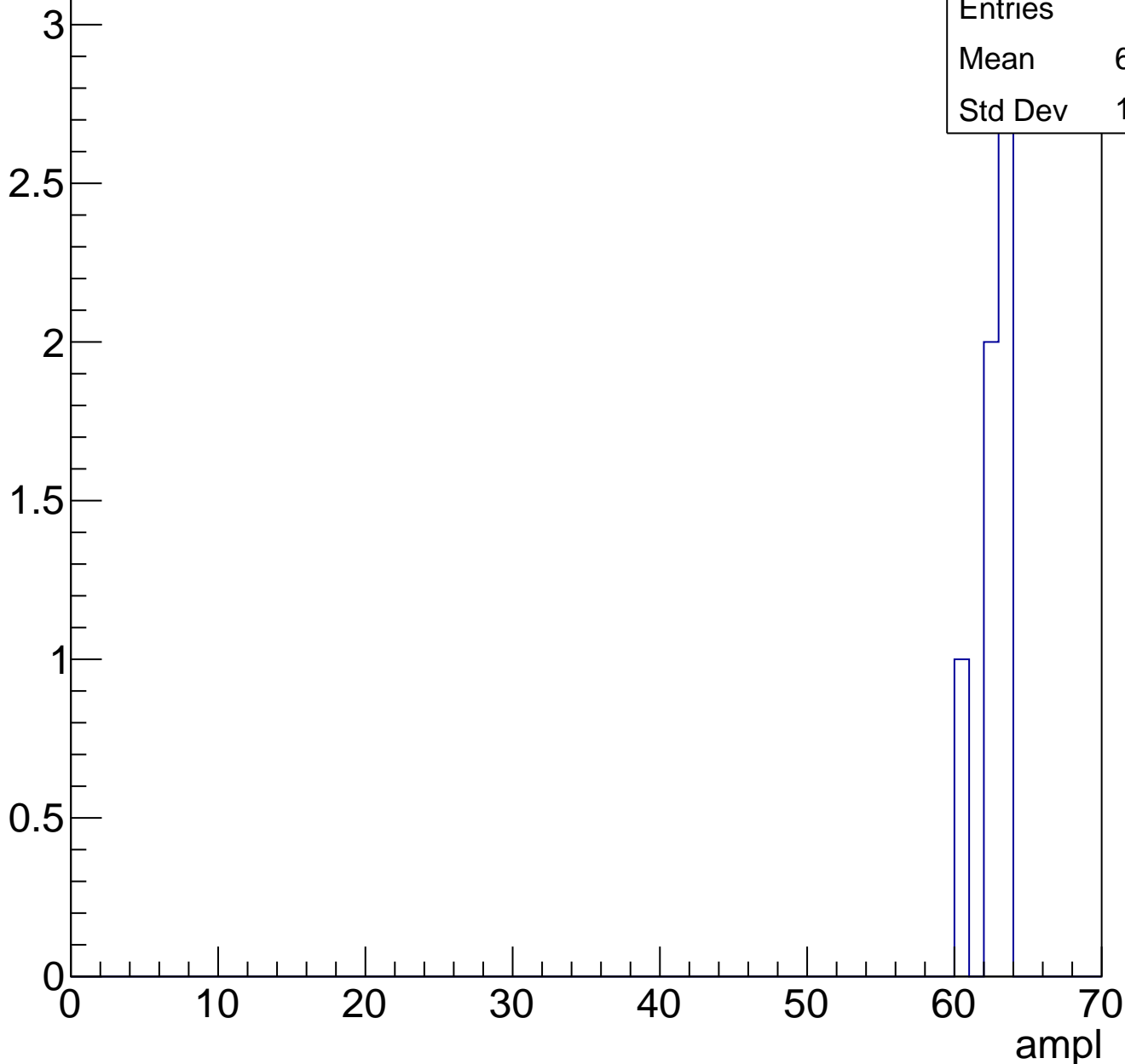
Entries	34
Mean	58.62
Std Dev	10.38



B1L101S, U23-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch9, adc0

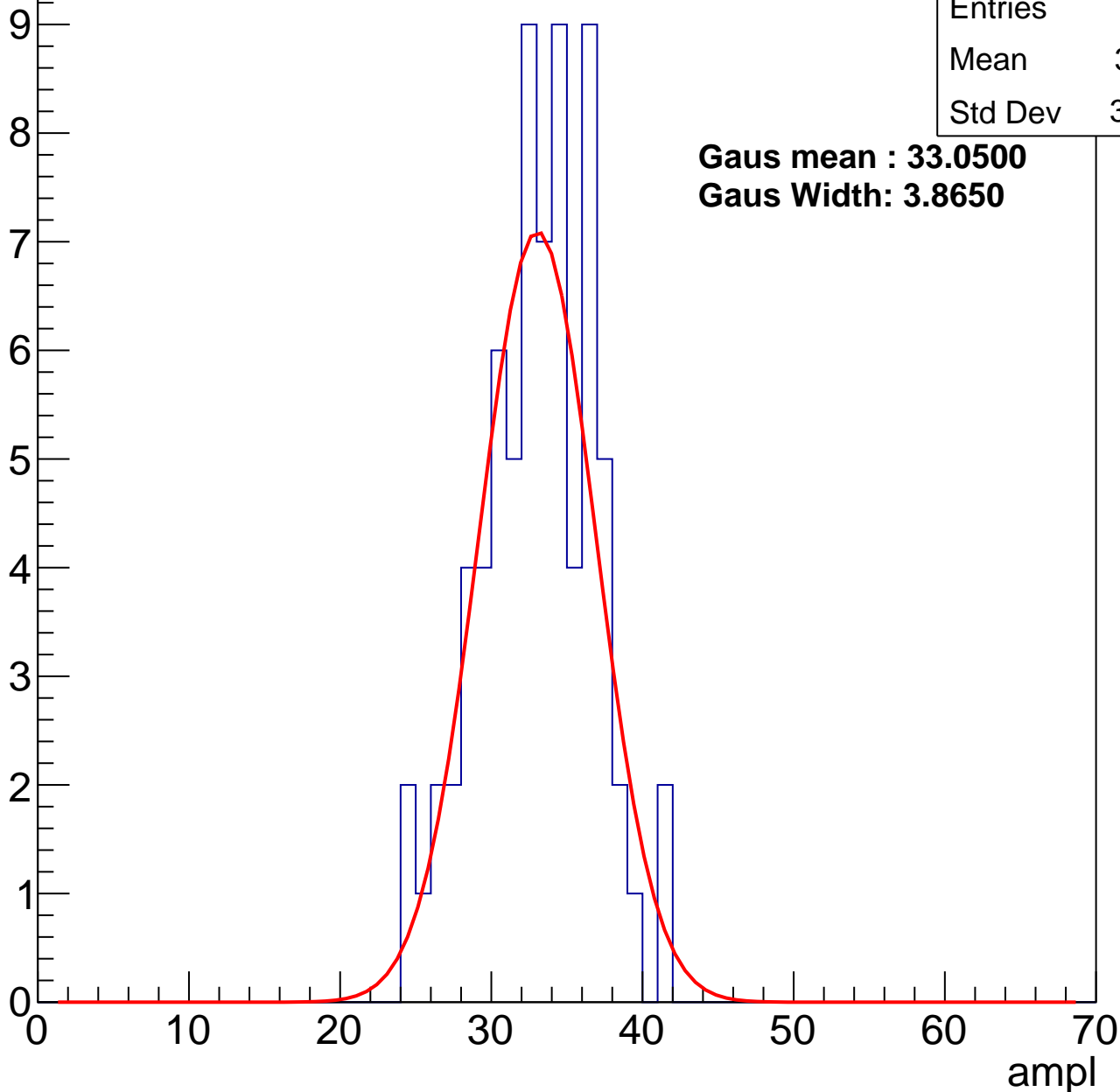
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	32.61
Std Dev	3.734

Gaus mean : 33.0500

Gaus Width: 3.8650



B1L101S, U23-ch9, adc1

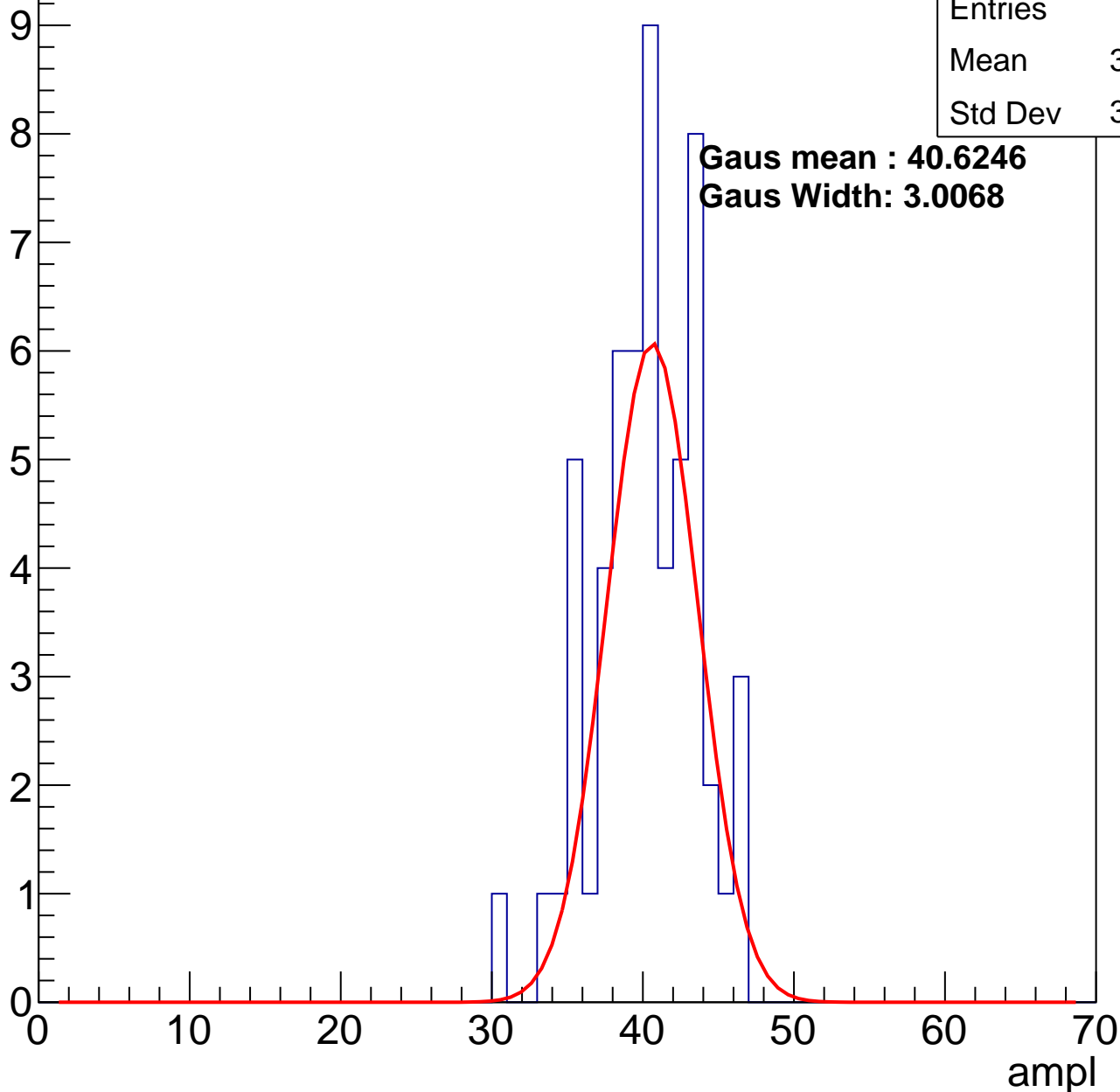
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	39.77
Std Dev	3.377

Gaus mean : 40.6246

Gaus Width: 3.0068



B1L101S, U23-ch9, adc2

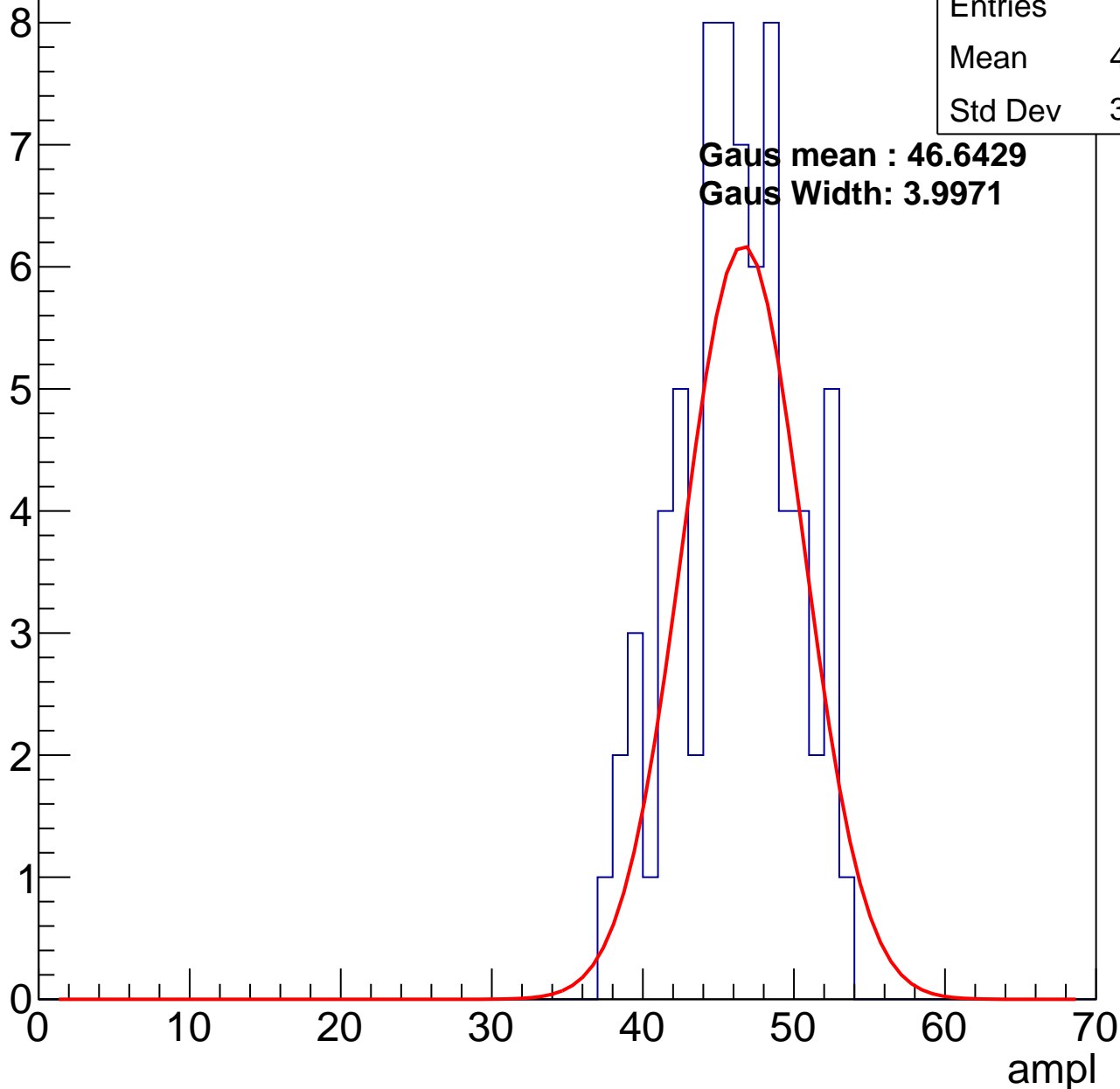
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	45.65
Std Dev	3.824

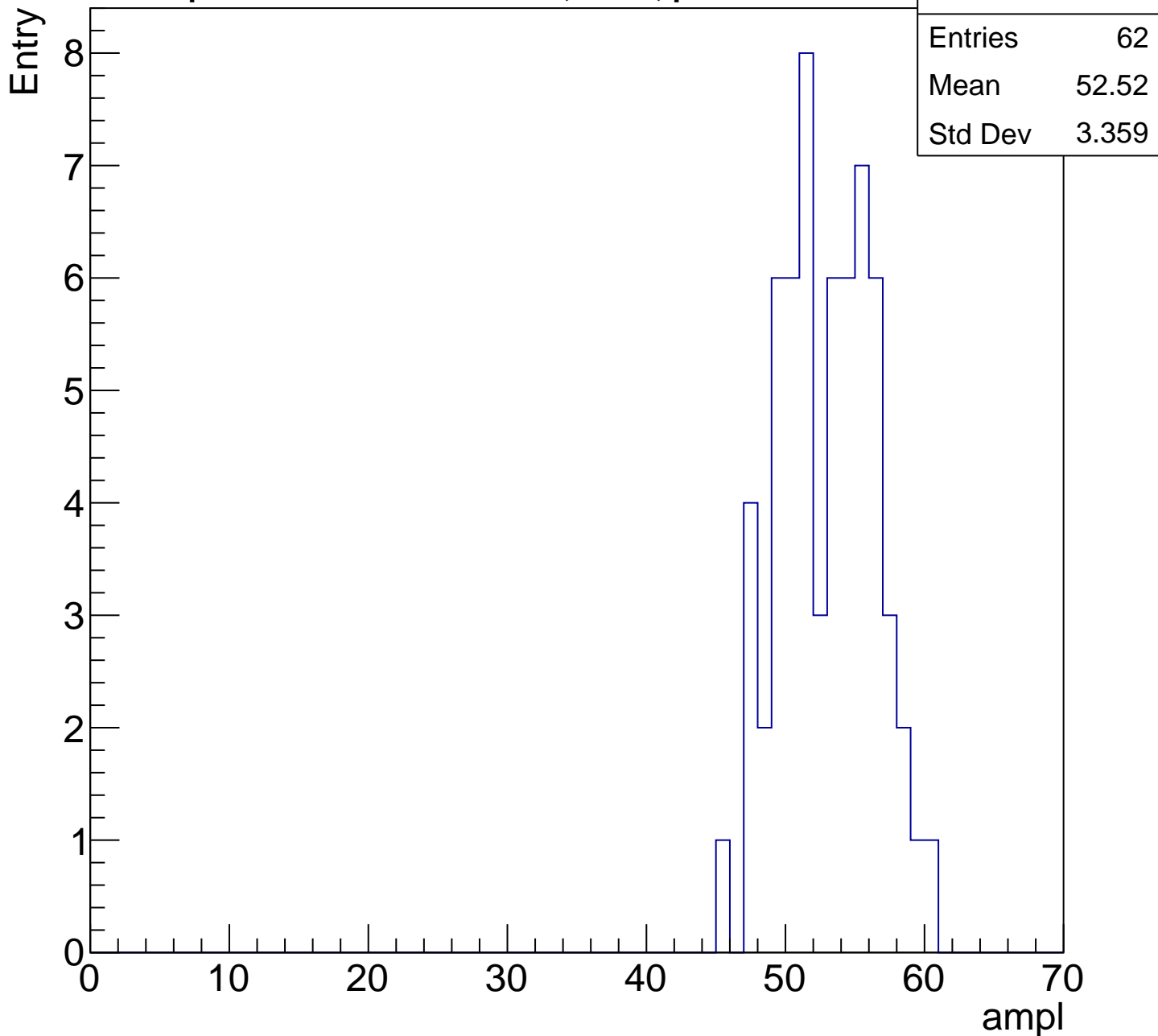
Gaus mean : 46.6429

Gaus Width: 3.9971



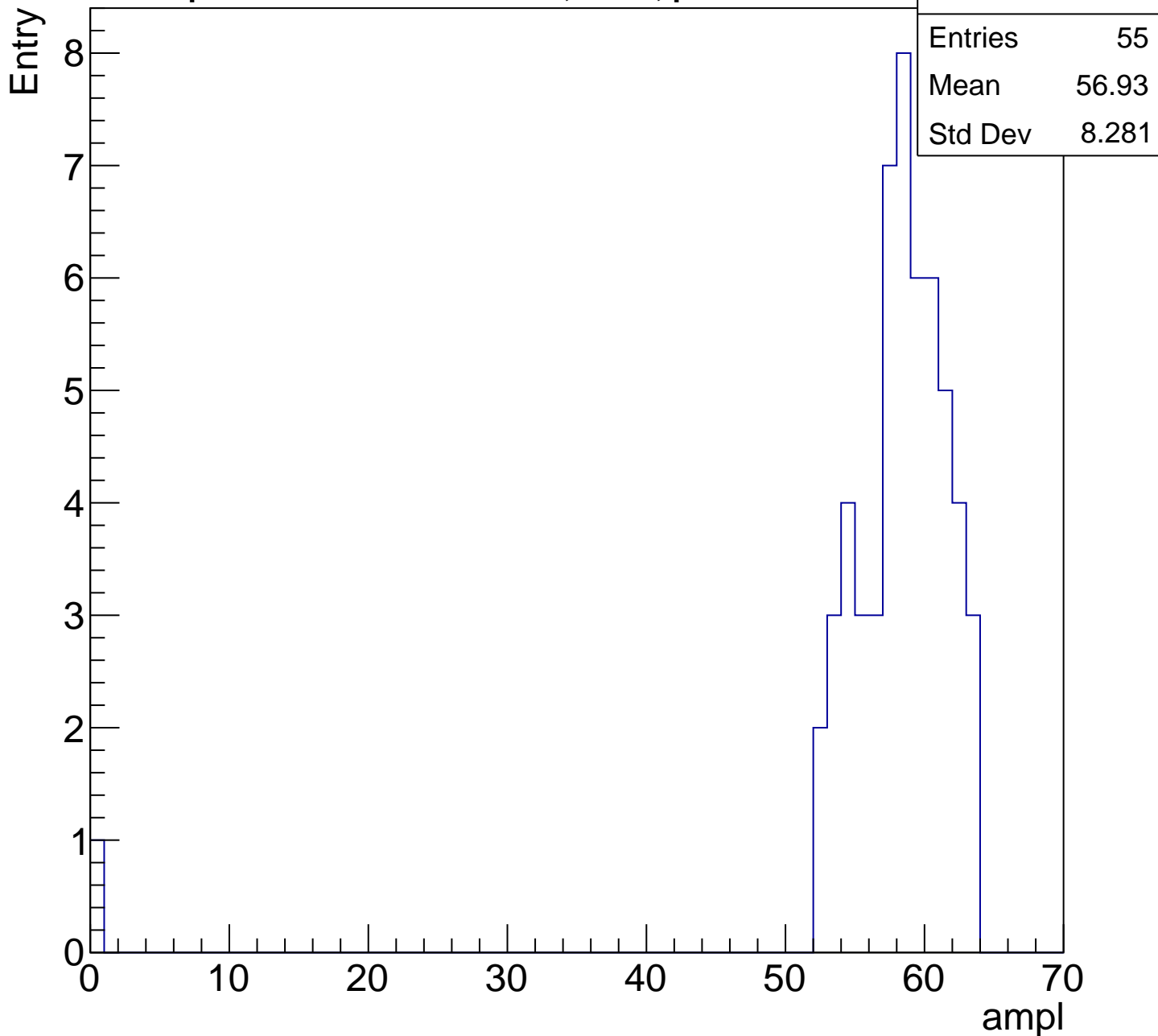
B1L101S, U23-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

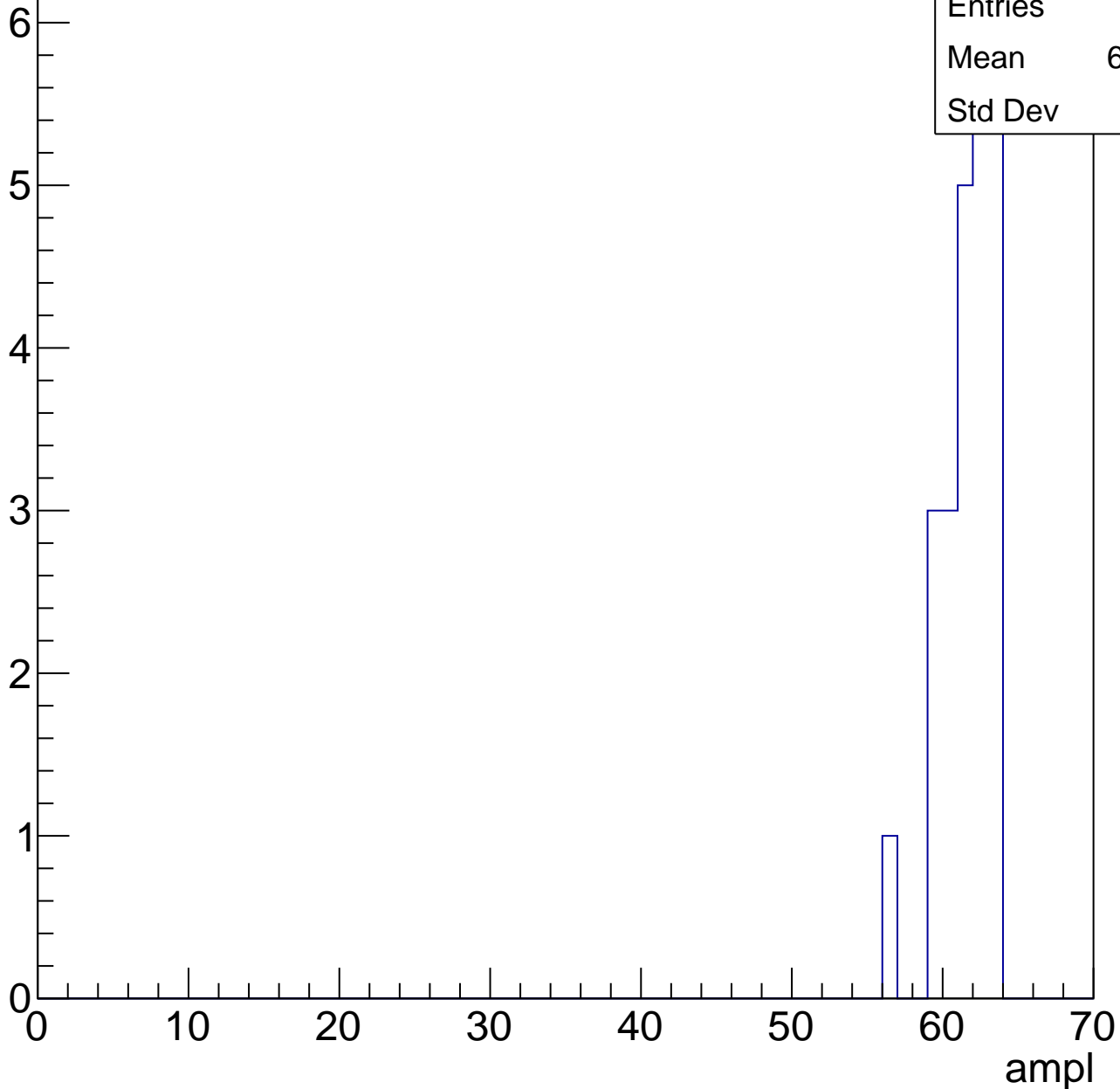


B1L101S, U23-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	61.17
Std Dev	1.7



B1L101S, U23-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch10, adc0

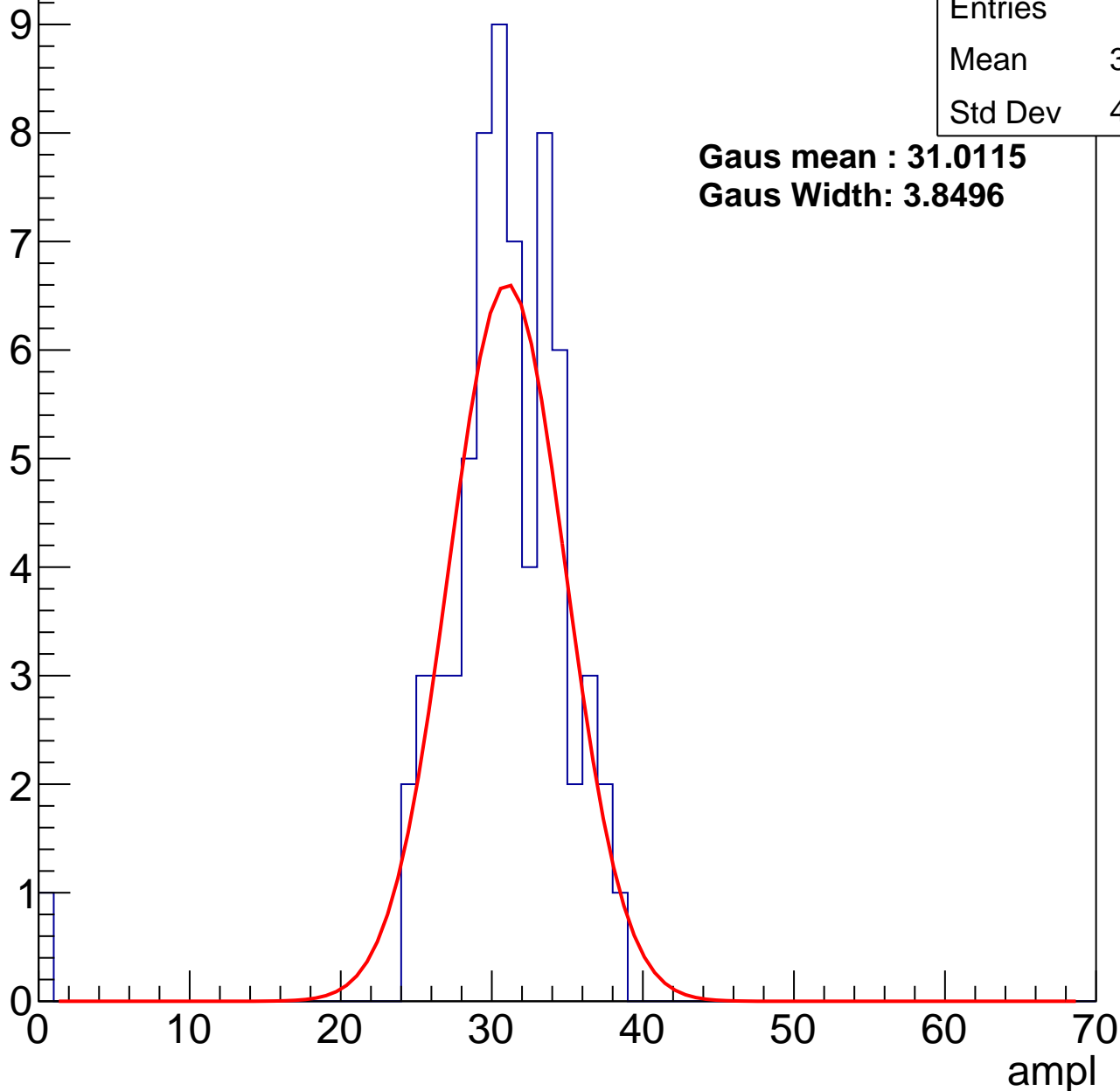
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.25
Std Dev	4.973

Gaus mean : 31.0115

Gaus Width: 3.8496



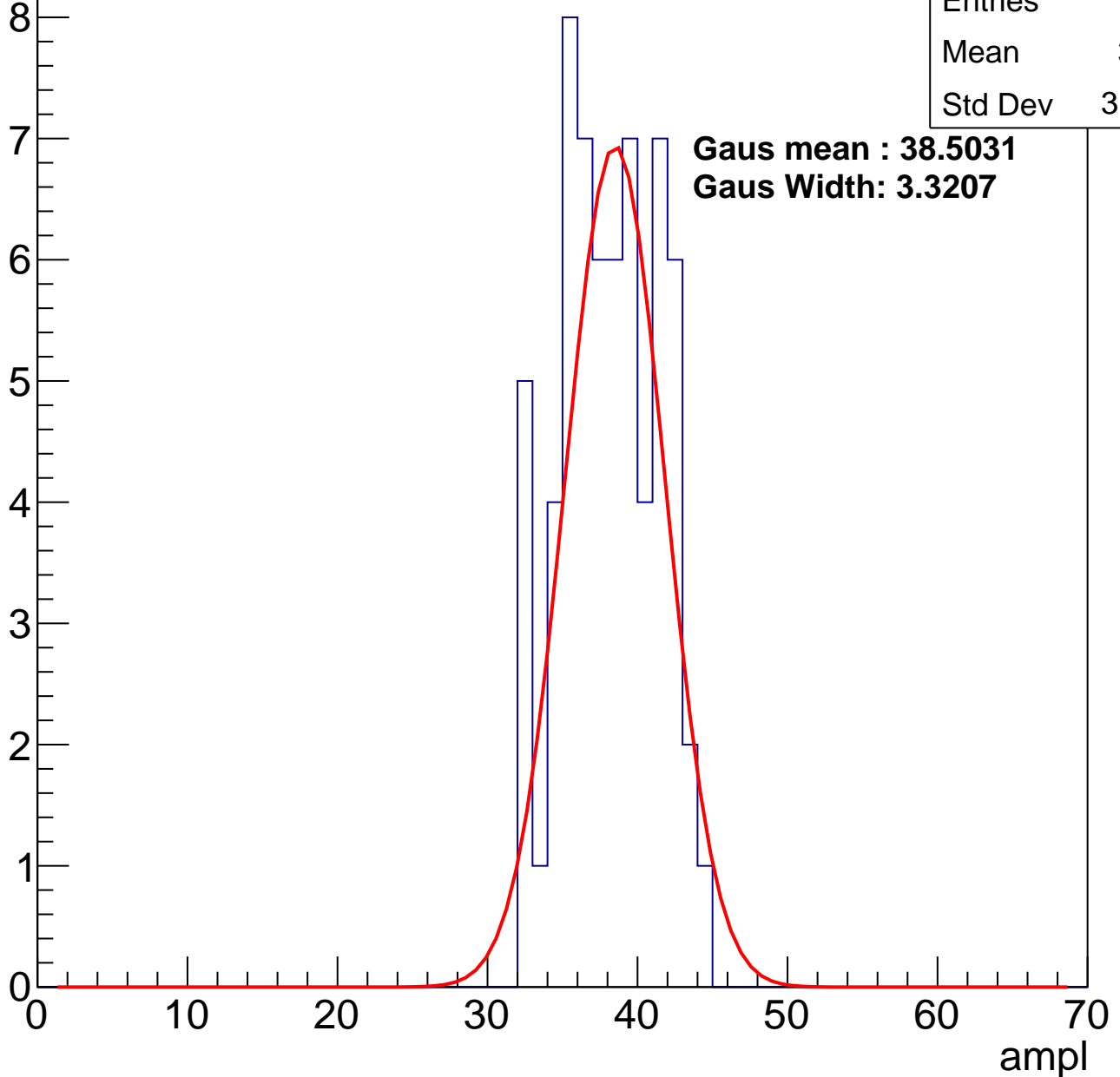
B1L101S, U23-ch10, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.7
Std Dev	3.156

Gaus mean : 38.5031
Gaus Width: 3.3207



B1L101S, U23-ch10, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.51
Std Dev	3.615

Gaus mean : 45.0923

Gaus Width: 4.3592

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

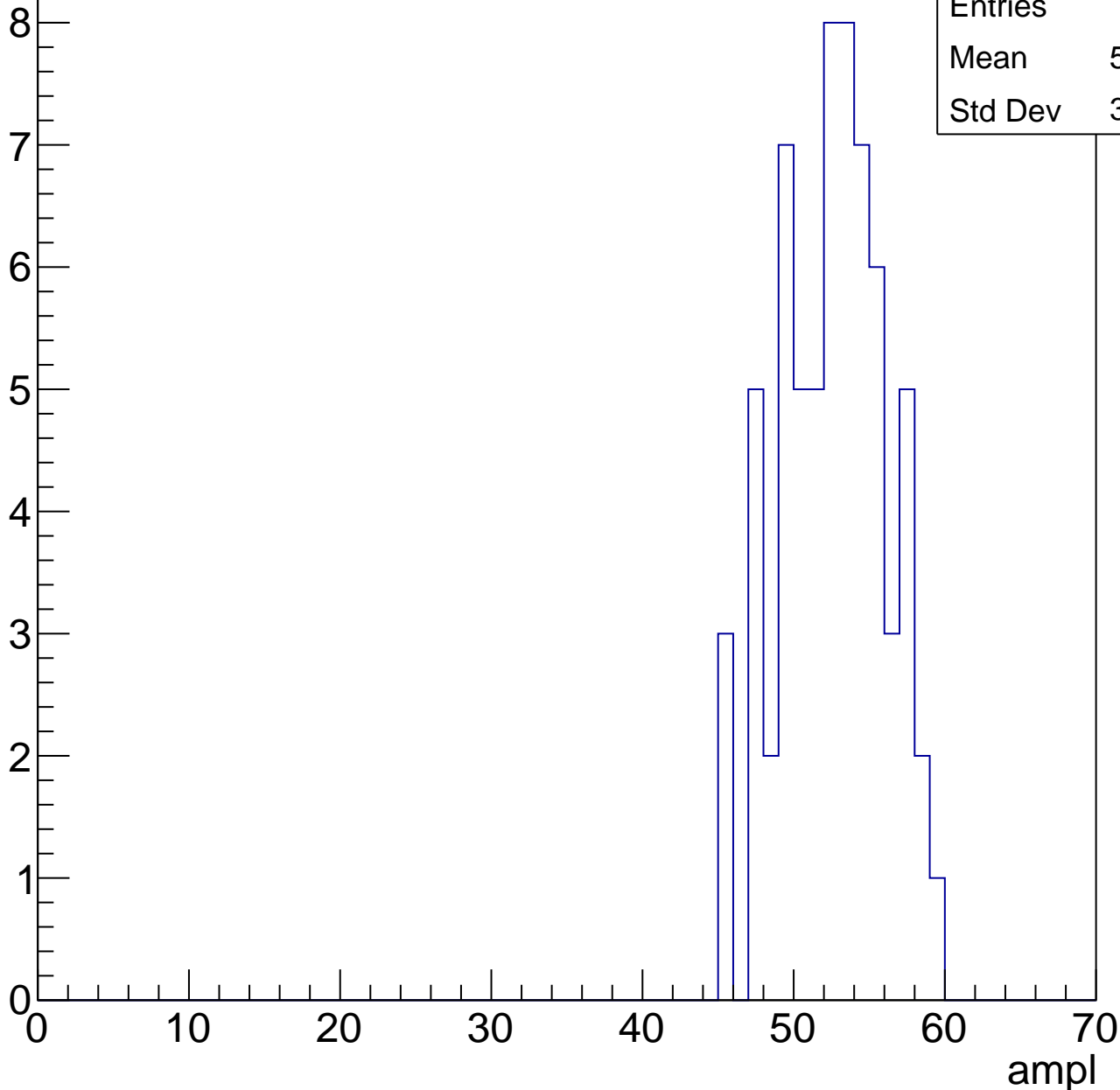
70

B1L101S, U23-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

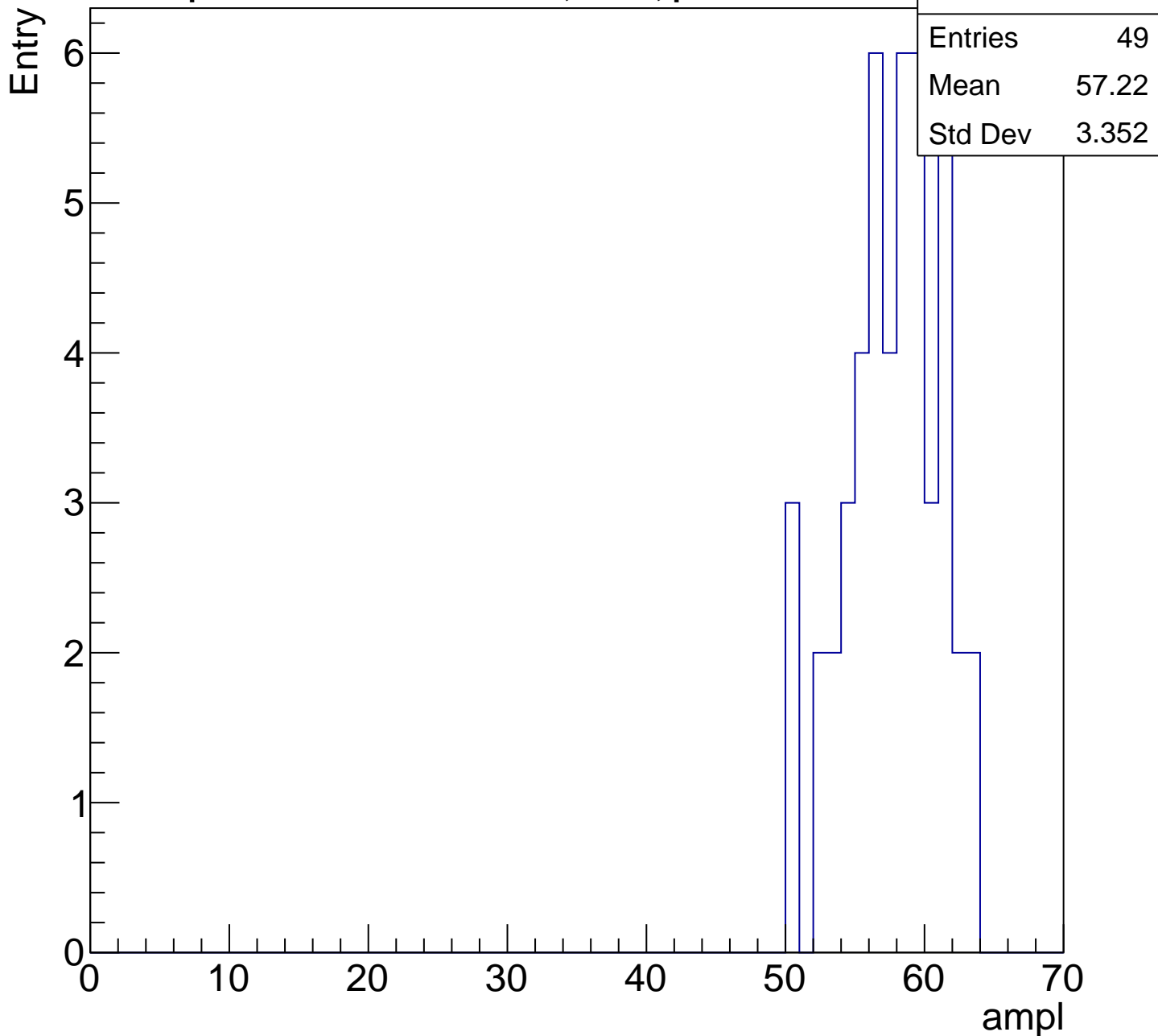
Entry

Entries	67
Mean	52.09
Std Dev	3.402



B1L101S, U23-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

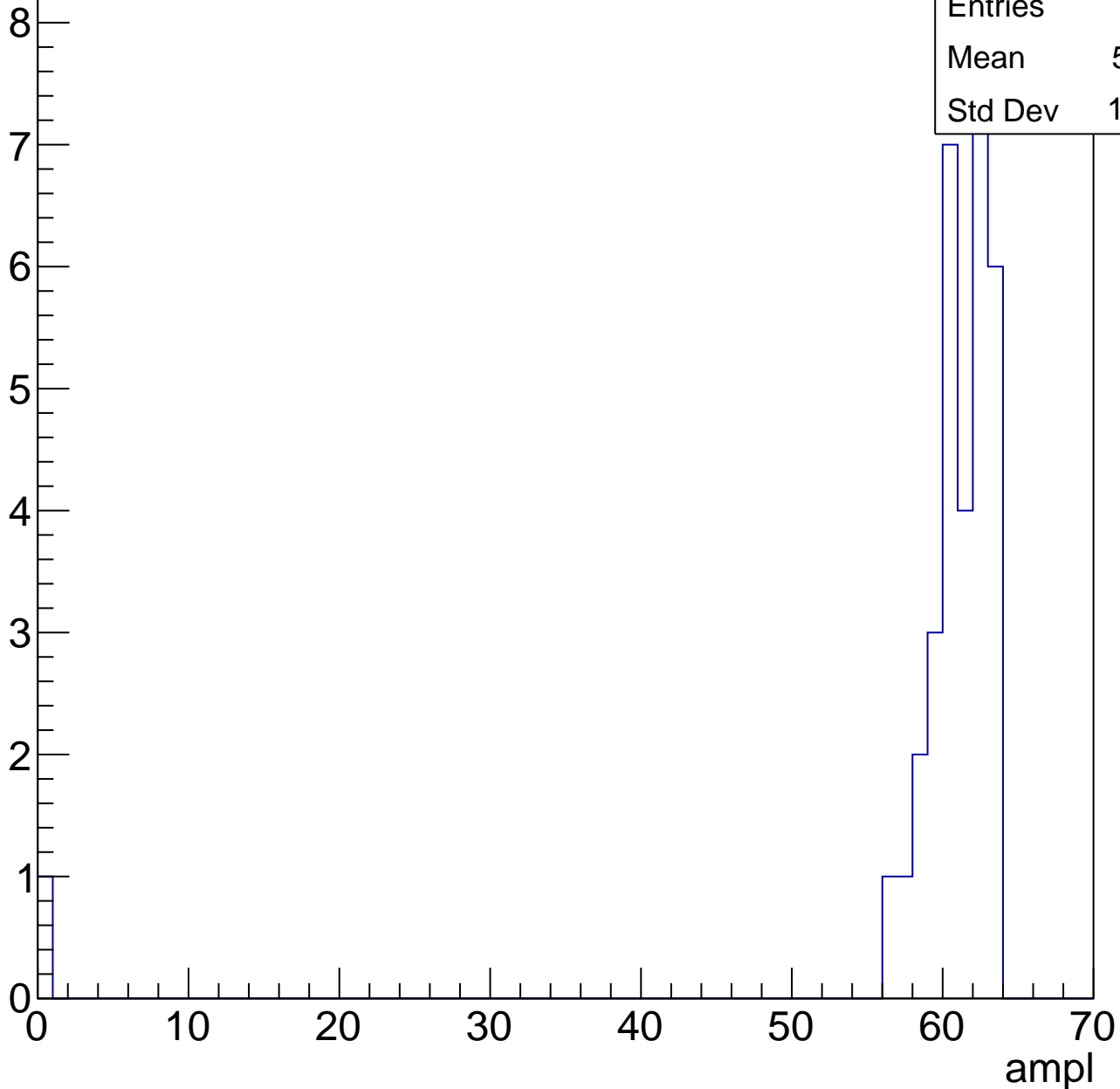


B1L101S, U23-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.91
Std Dev	10.57



B1L101S, U23-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U23-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch11, adc0

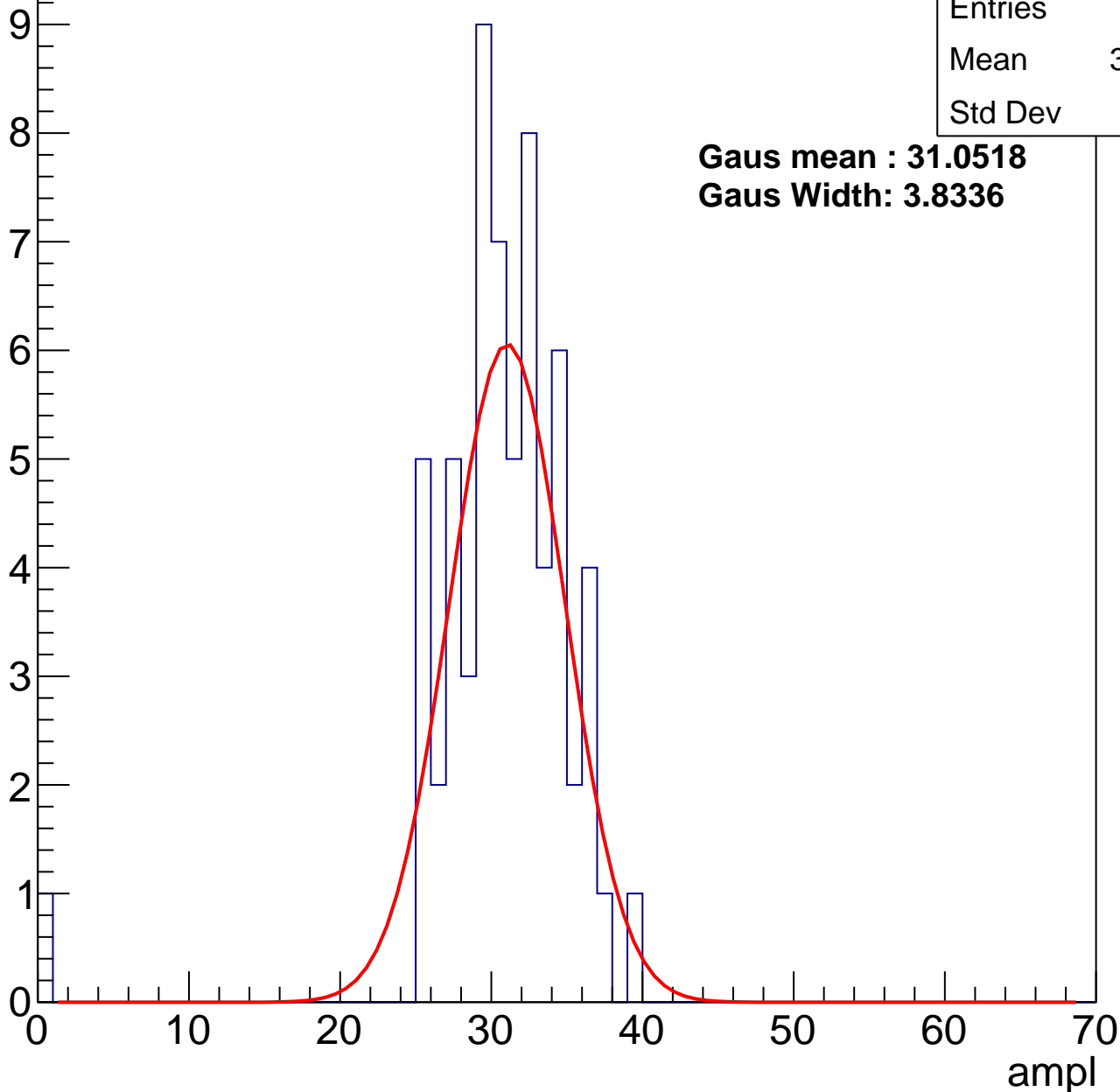
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.22
Std Dev	5.06

Gaus mean : 31.0518

Gaus Width: 3.8336



B1L101S, U23-ch11, adc1

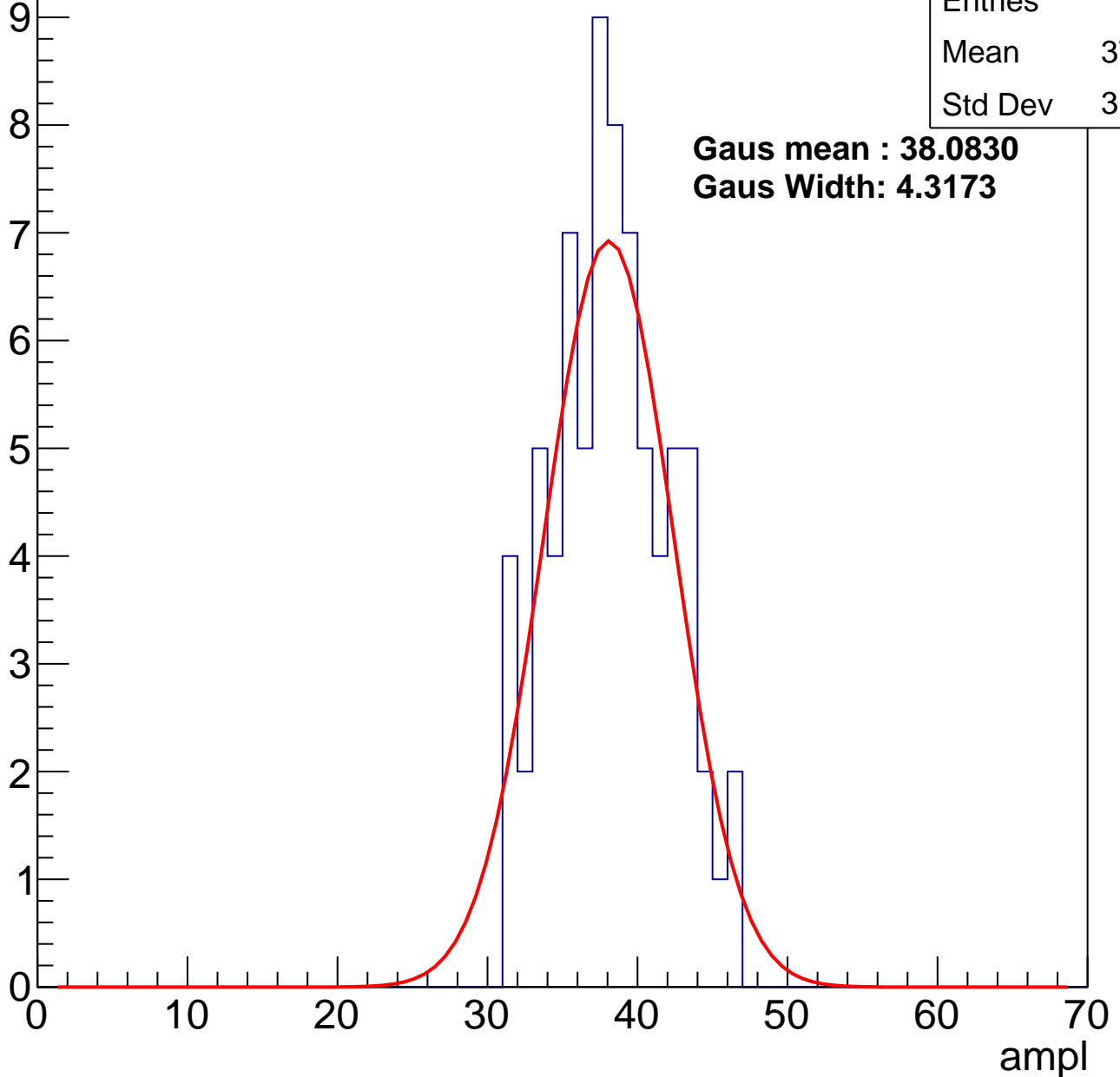
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.84
Std Dev	3.767

Gaus mean : 38.0830

Gaus Width: 4.3173



B1L101S, U23-ch11, adc2

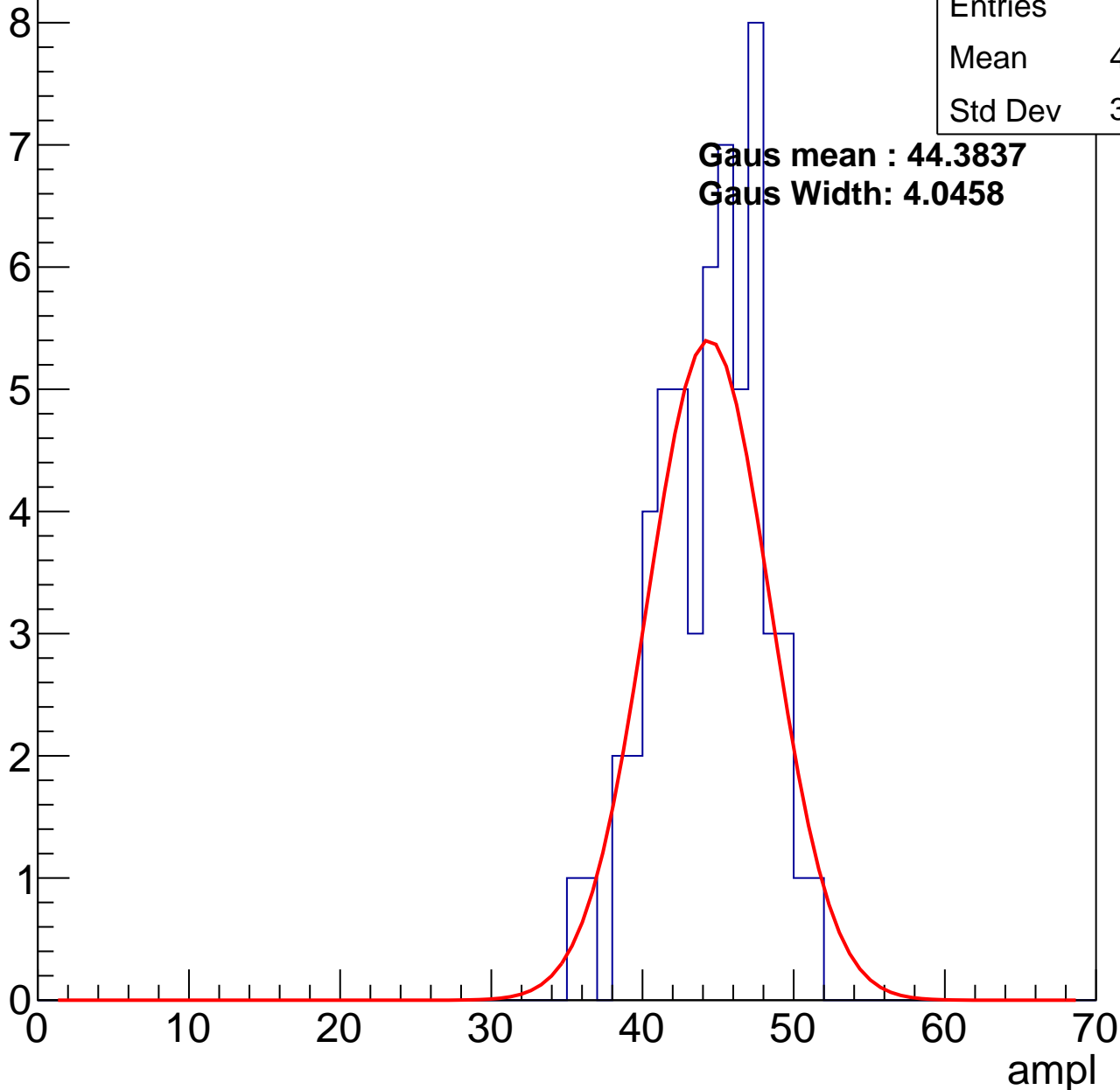
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.96
Std Dev	3.519

Gaus mean : 44.3837

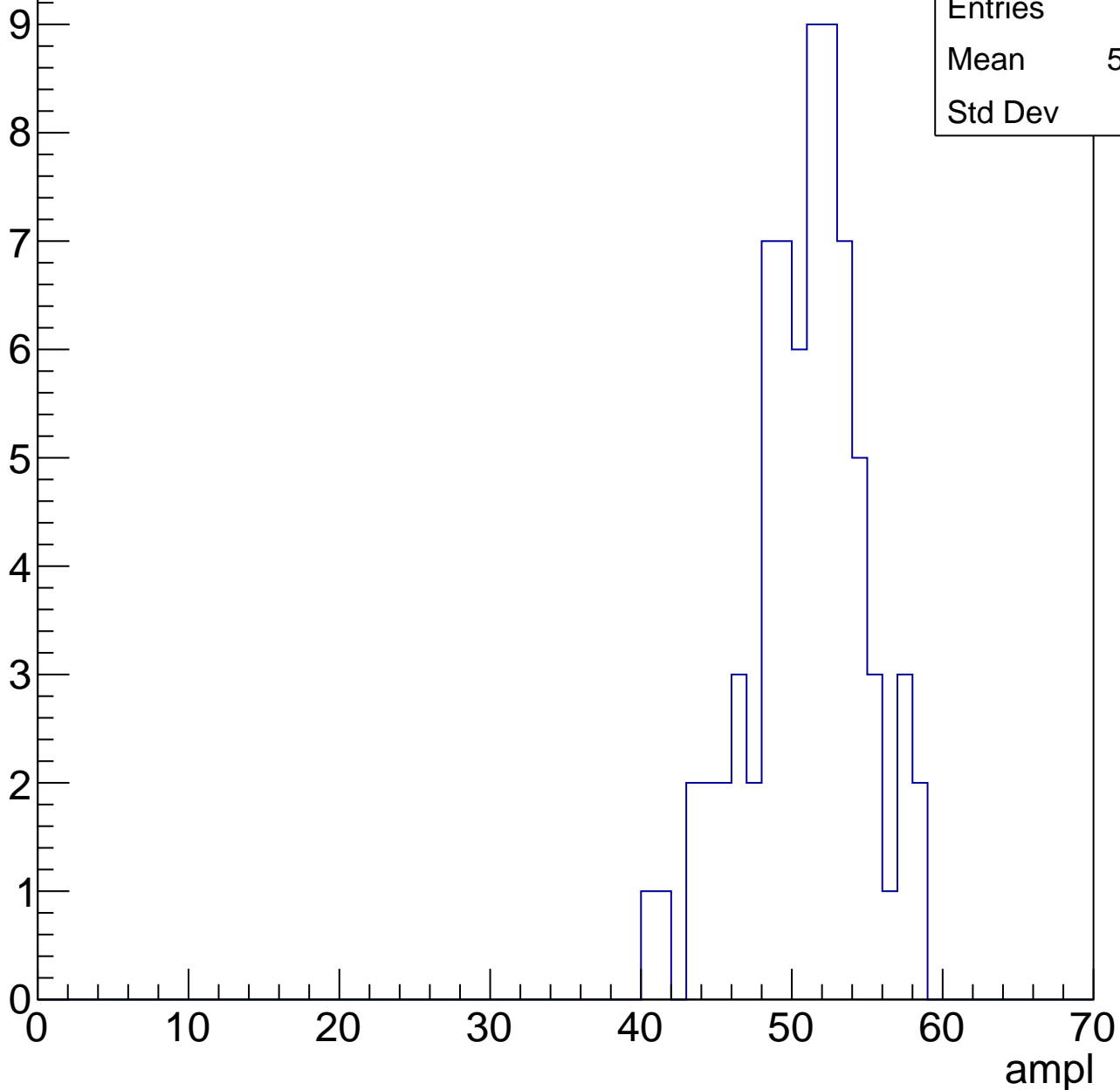
Gaus Width: 4.0458



B1L101S, U23-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

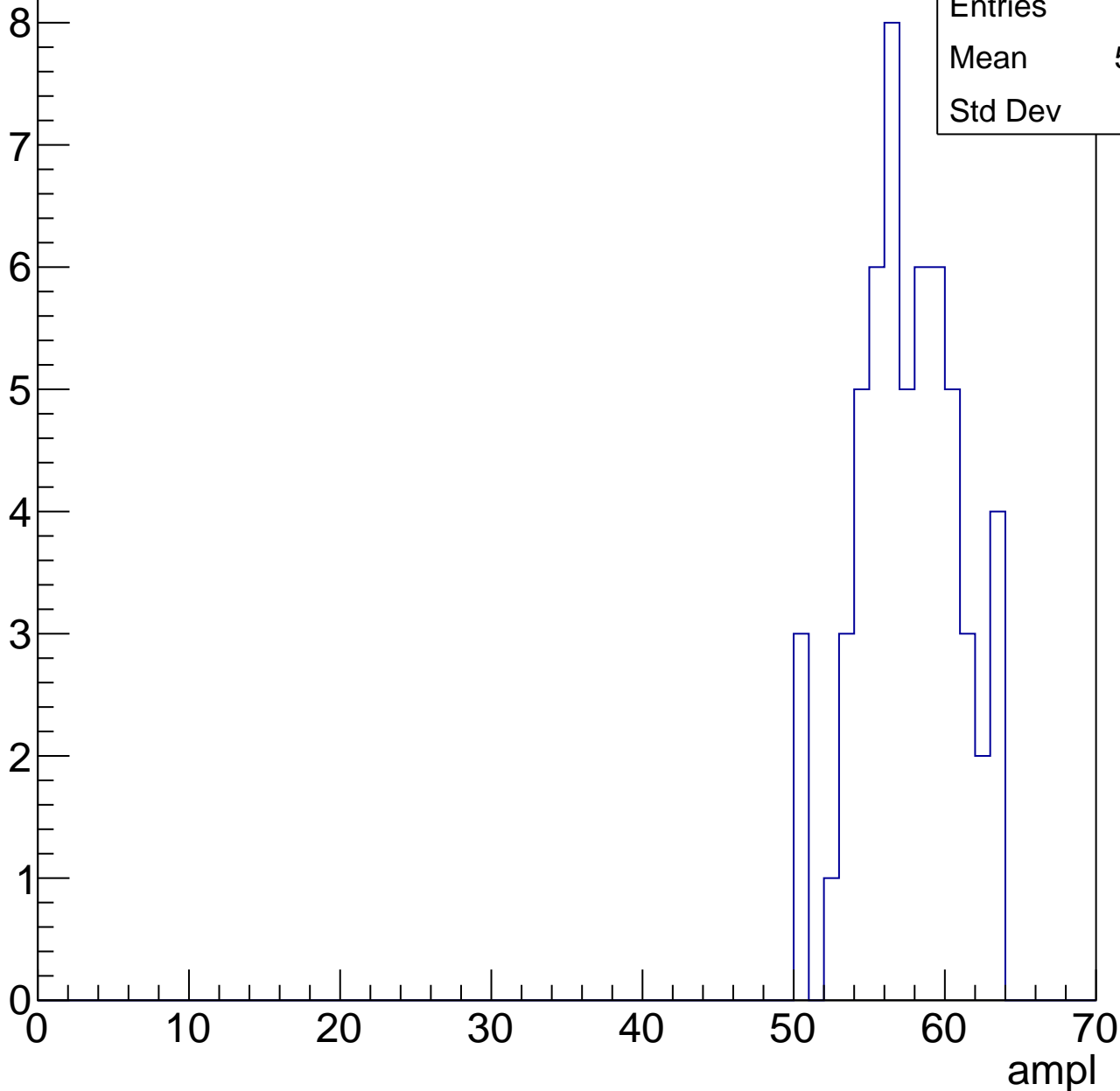


B1L101S, U23-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	57.11
Std Dev	3.28

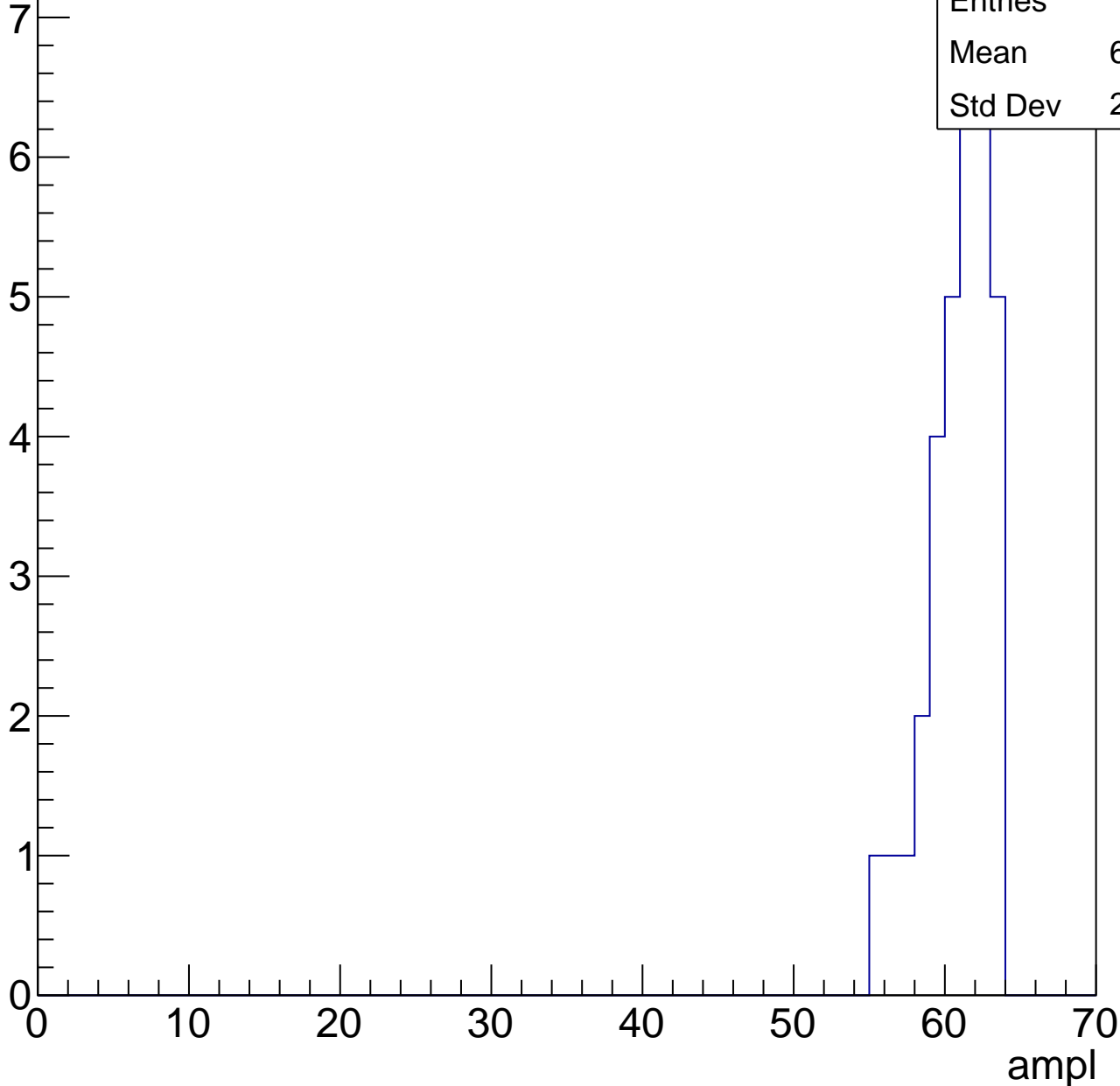


B1L101S, U23-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

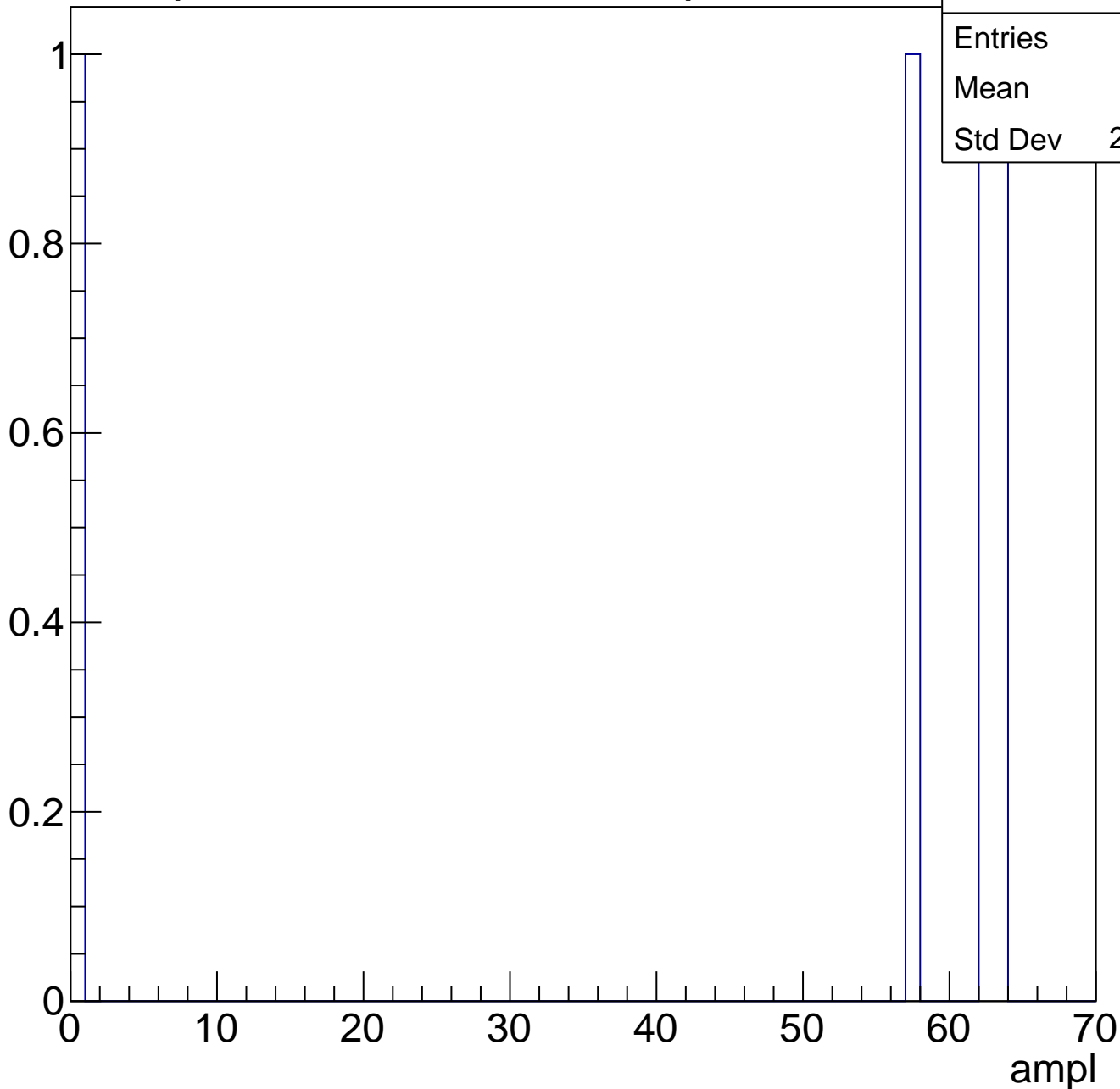
Entries	33
Mean	60.48
Std Dev	2.017



B1L101S, U23-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch12, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	31.55
Std Dev	5.048

Gaus mean : 32.5819

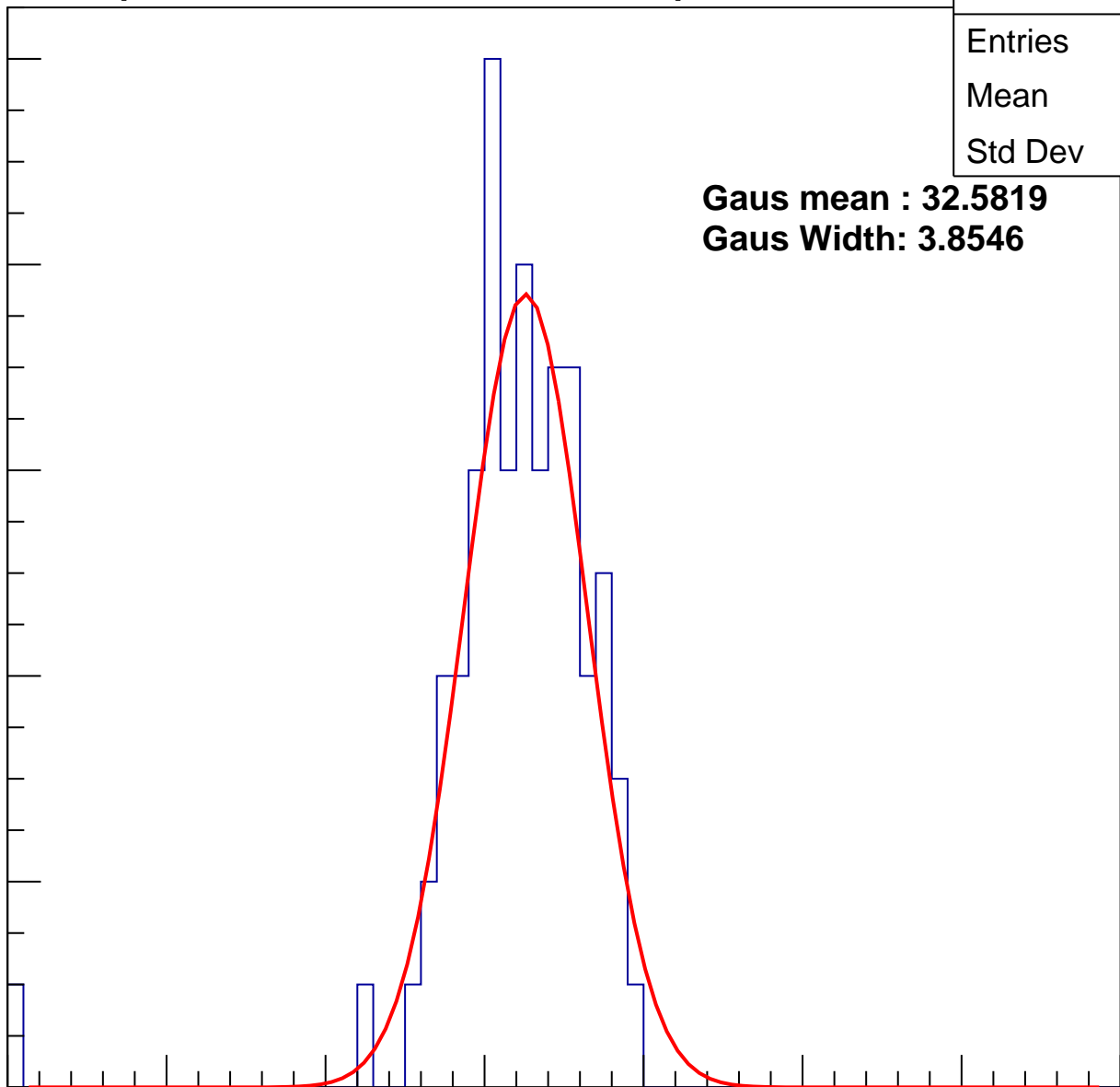
Gaus Width: 3.8546

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



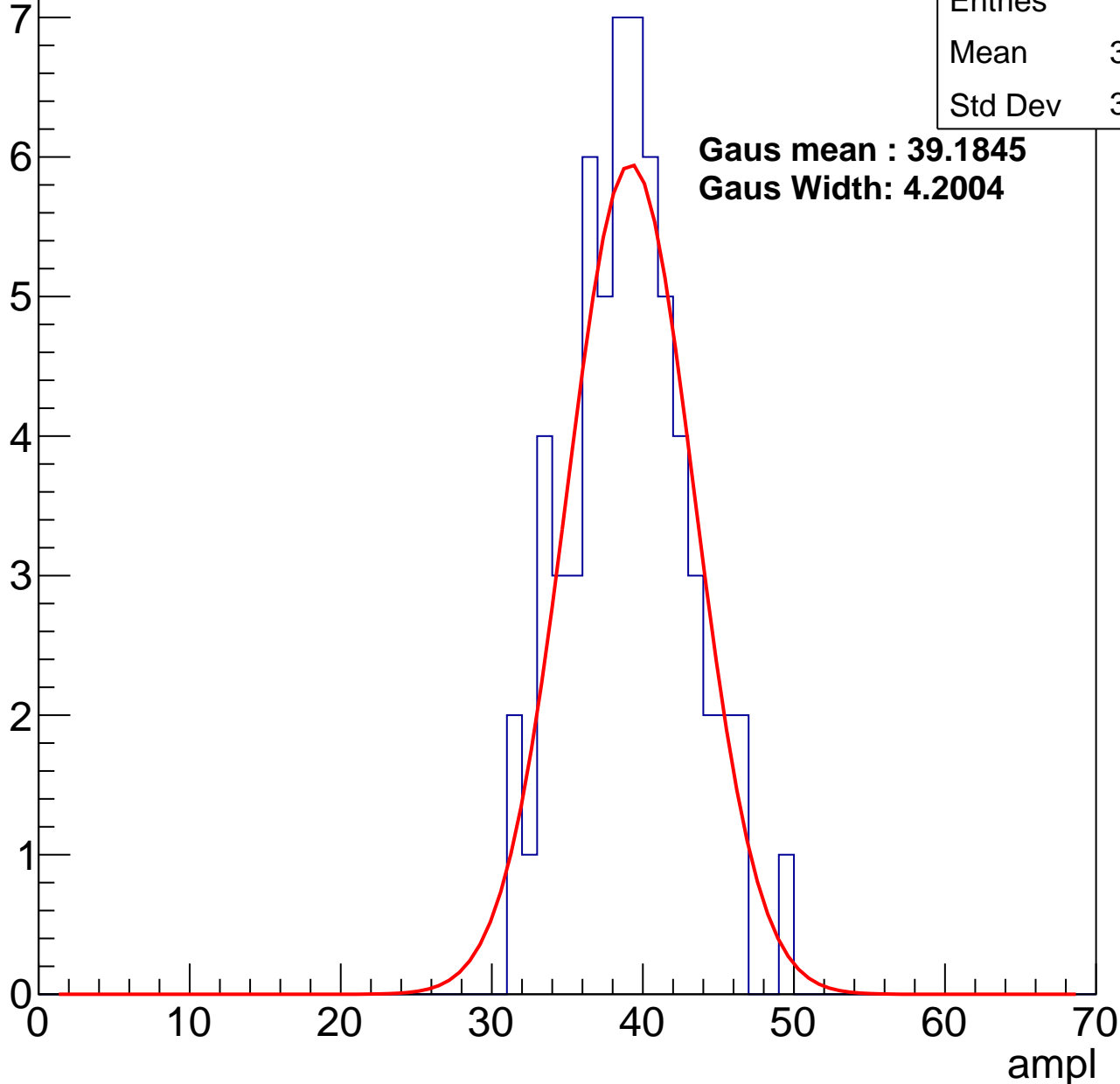
B1L101S, U23-ch12, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.63
Std Dev	3.876

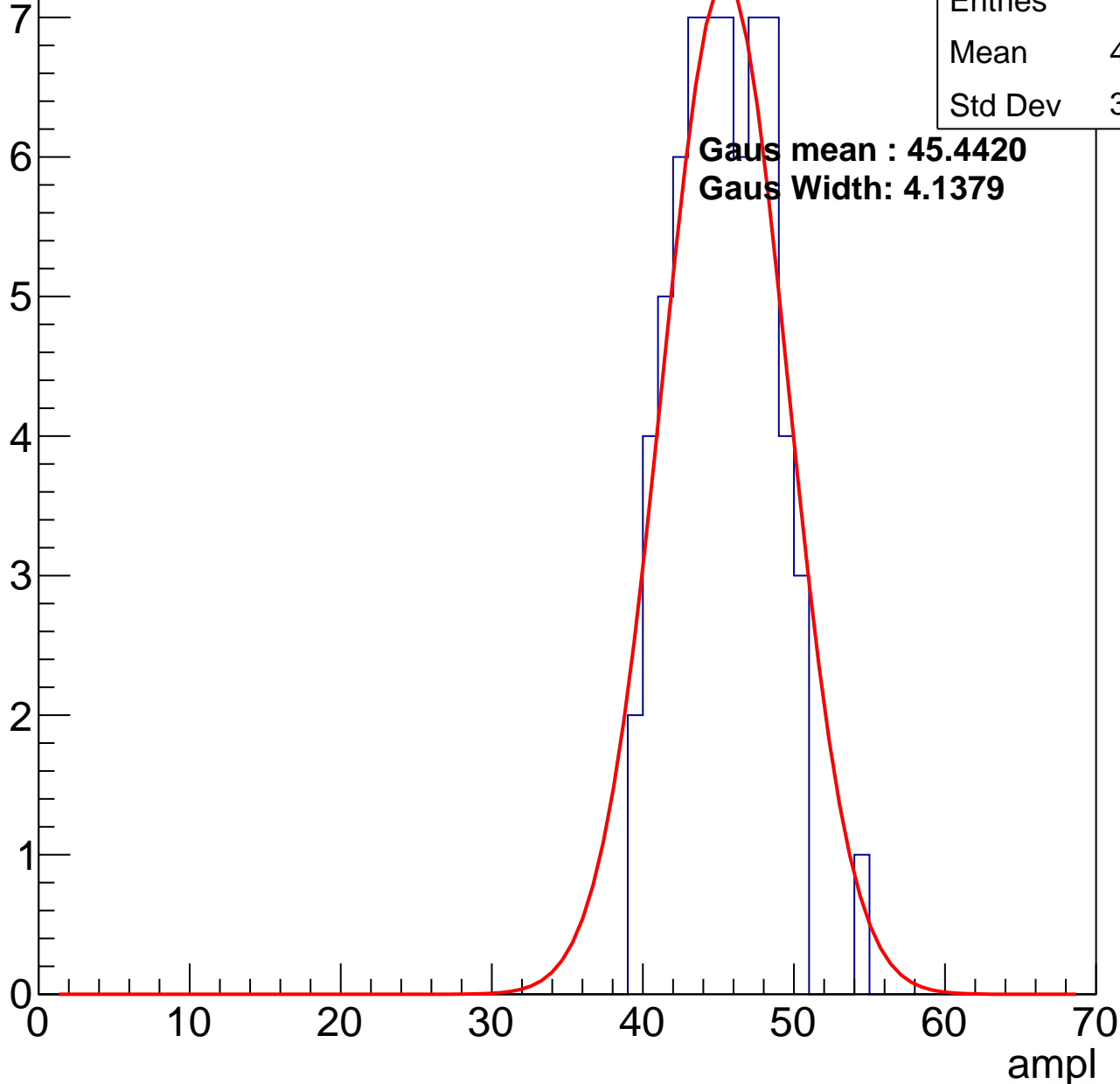
Gaus mean : 39.1845
Gaus Width: 4.2004



B1L101S, U23-ch12, adc2

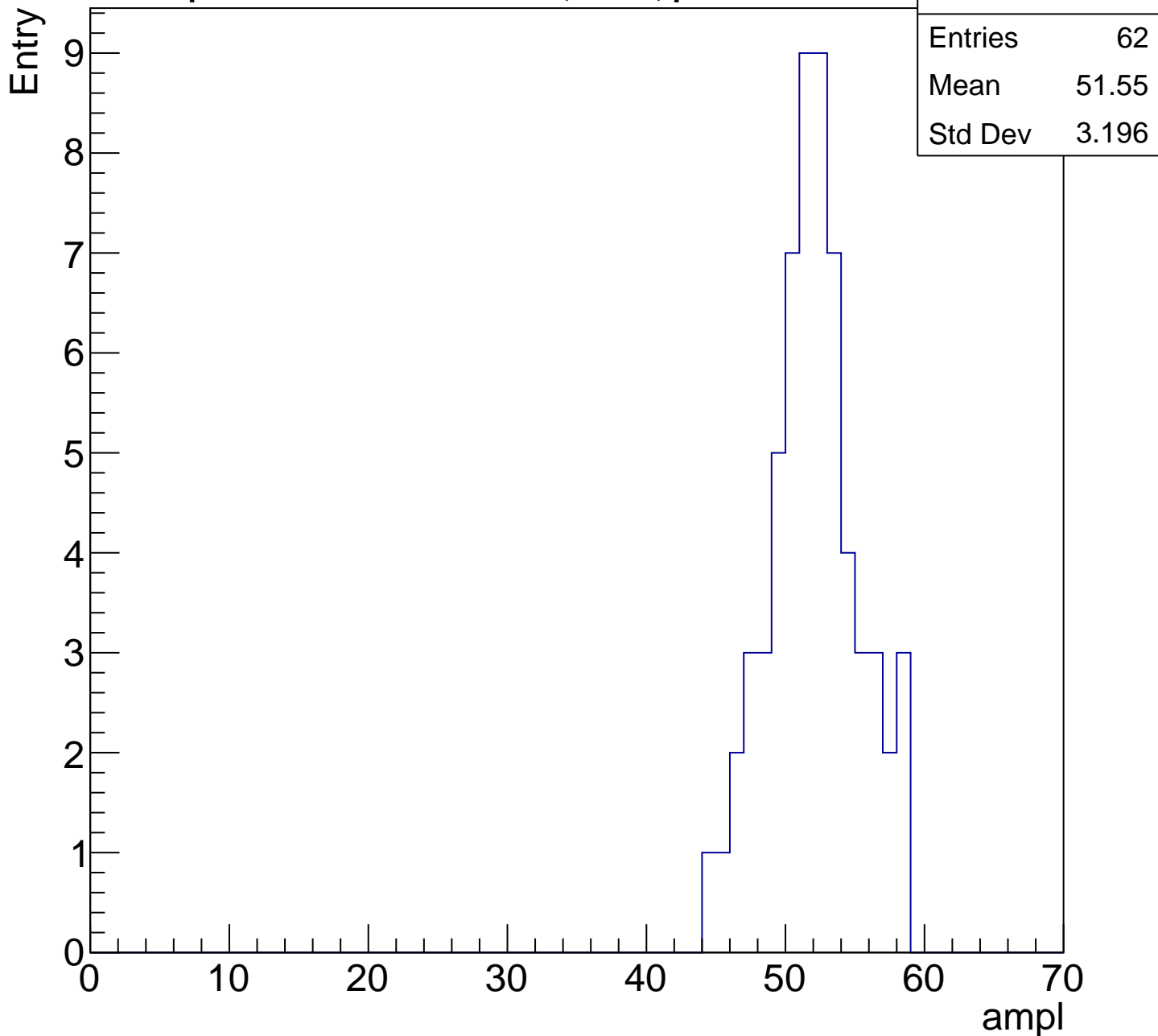
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

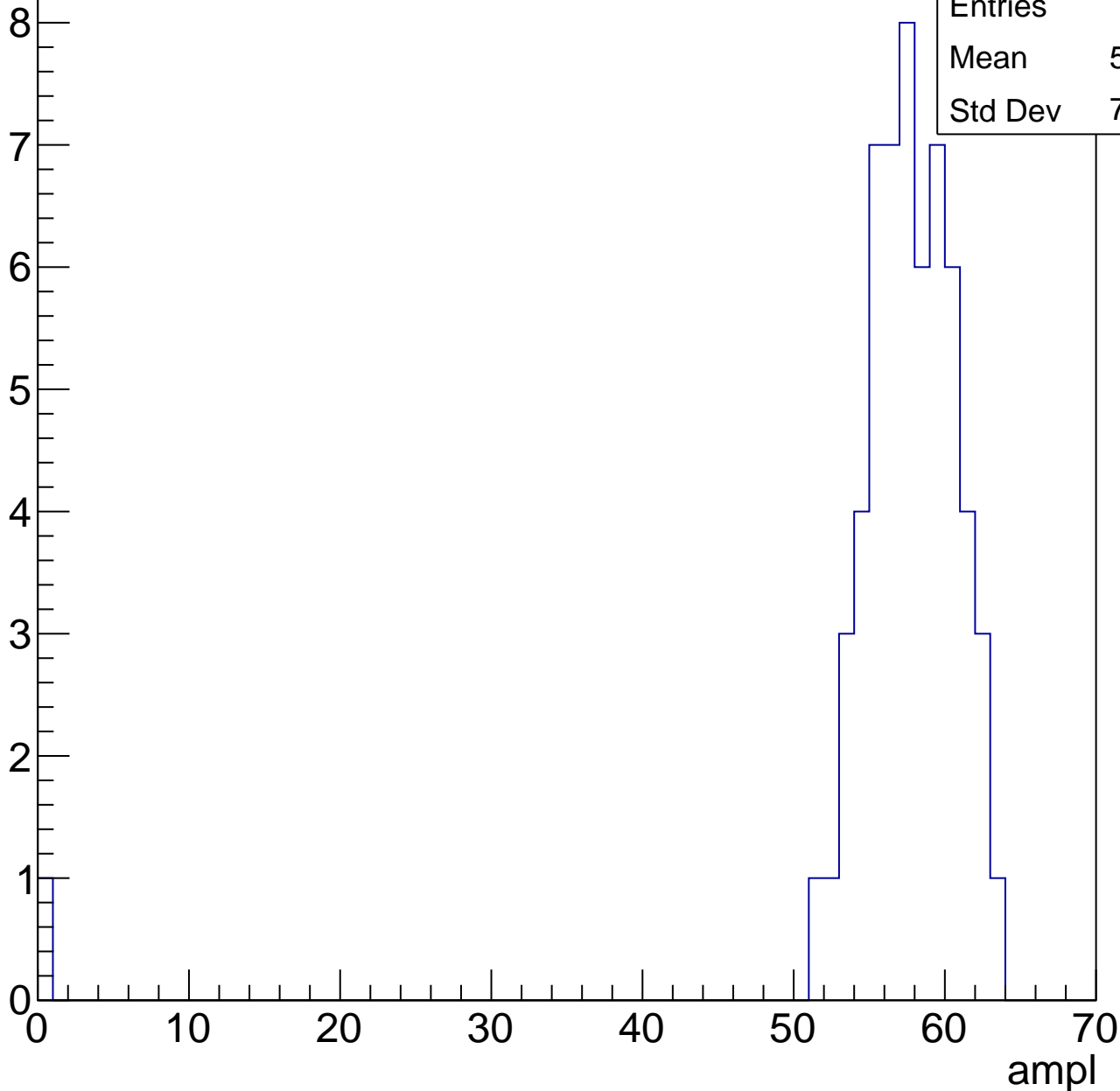


B1L101S, U23-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.36
Std Dev	7.884

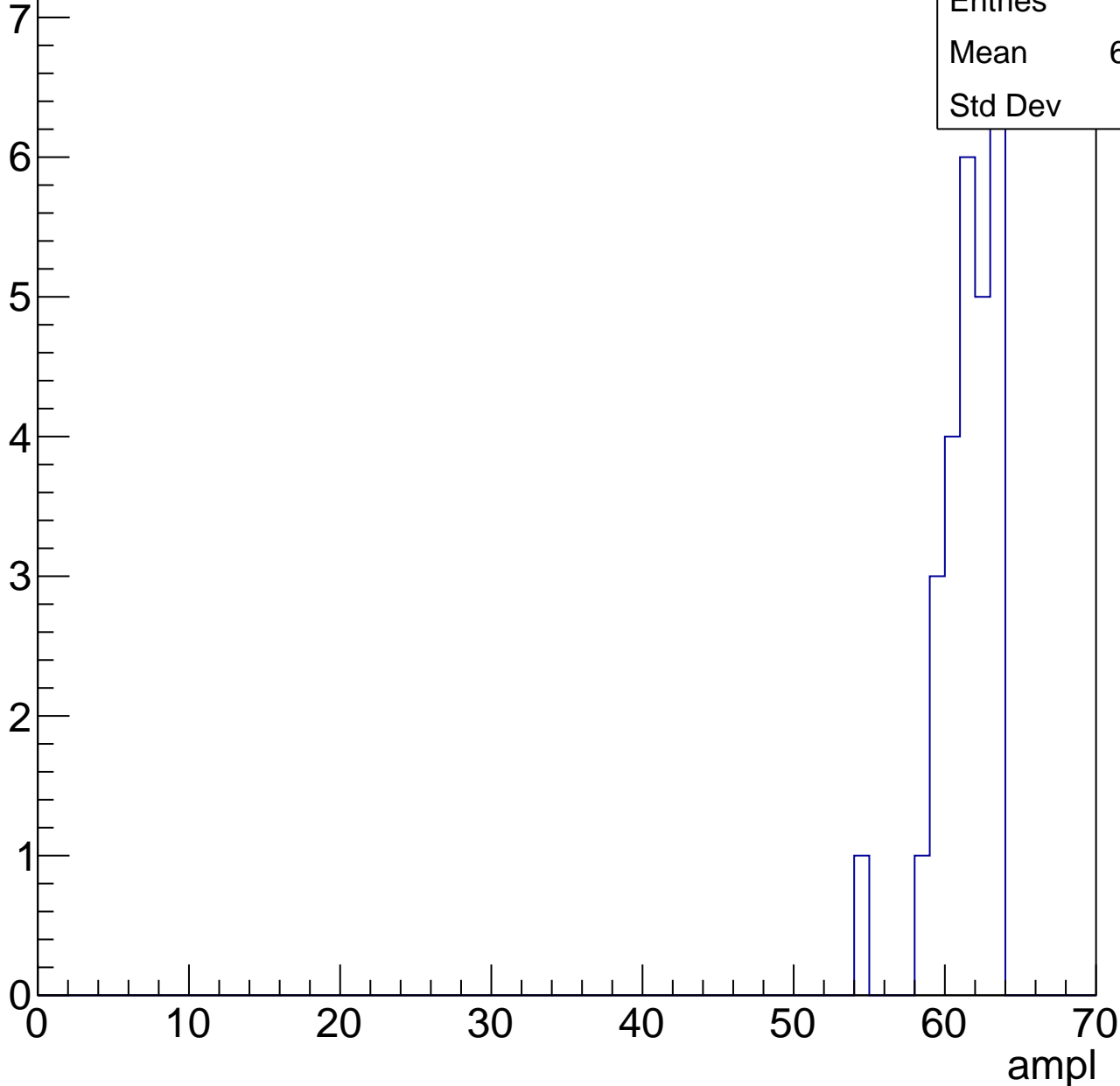


B1L101S, U23-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	60.96
Std Dev	1.99



B1L101S, U23-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	4
Mean	62.5
Std Dev	0.5

B1L101S, U23-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch13, adc0

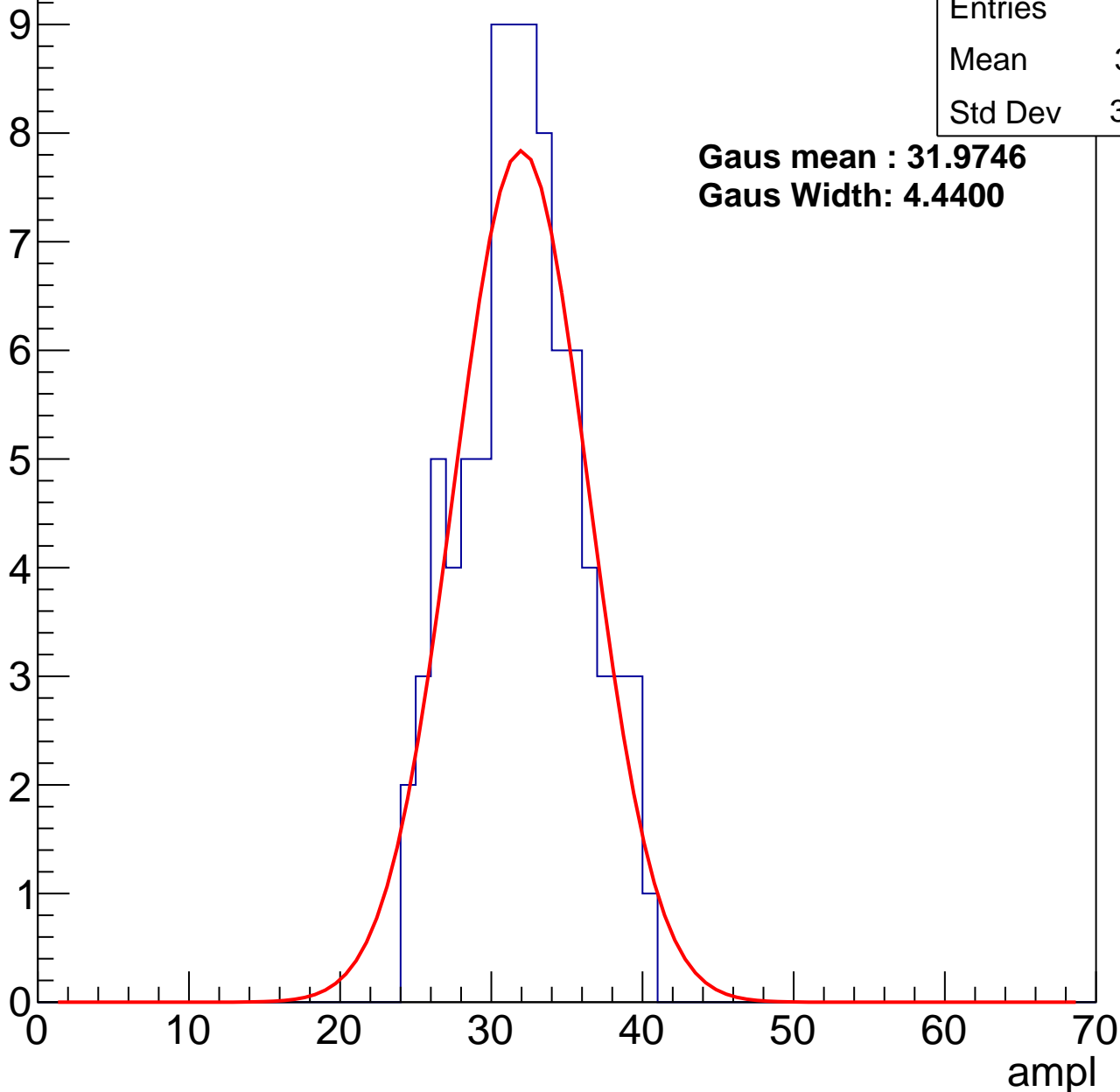
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	31.61
Std Dev	3.844

Gaus mean : 31.9746

Gaus Width: 4.4400



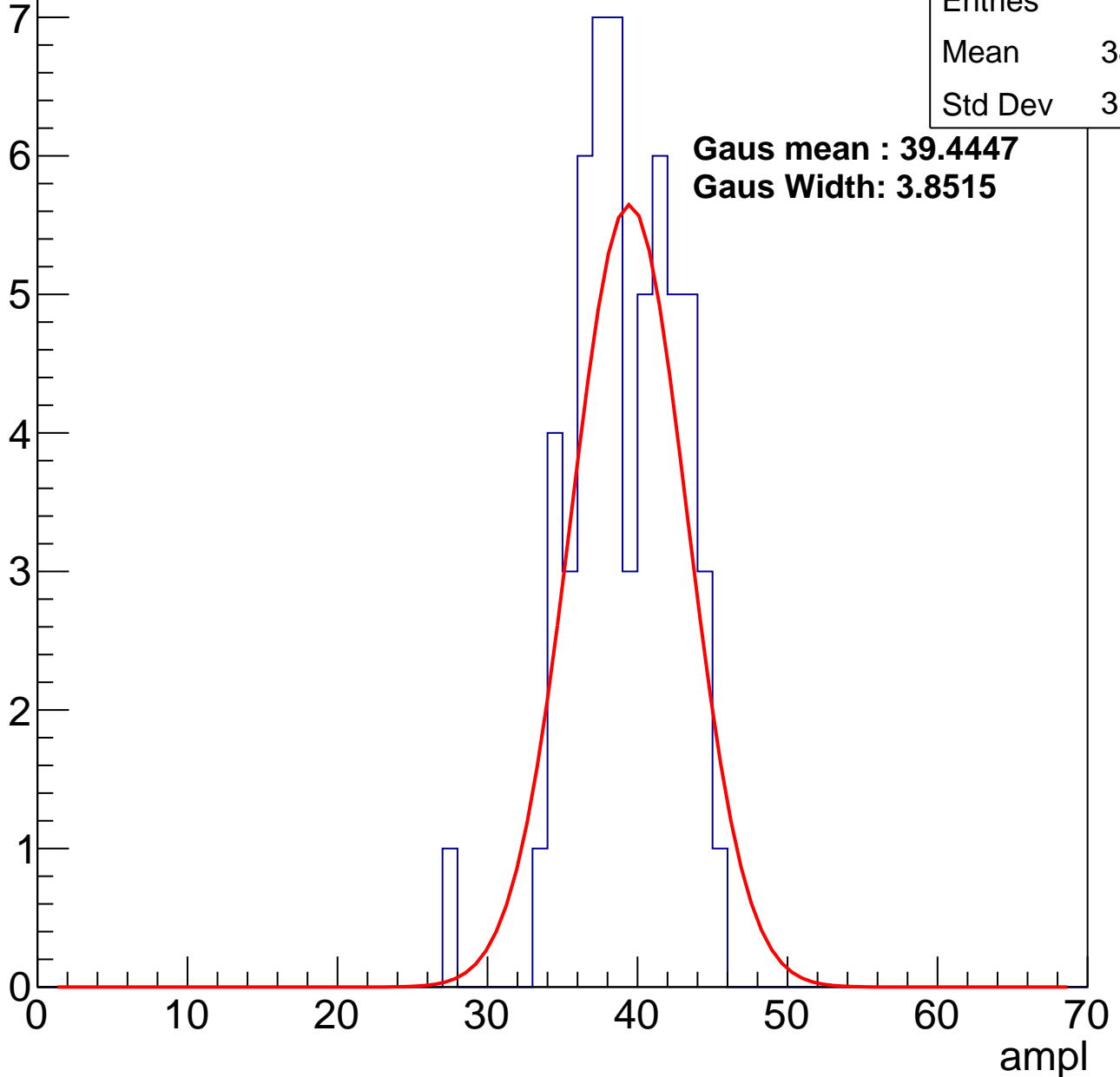
B1L101S, U23-ch13, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	38.72
Std Dev	3.448

Gaus mean : 39.4447
Gaus Width: 3.8515



B1L101S, U23-ch13, adc2

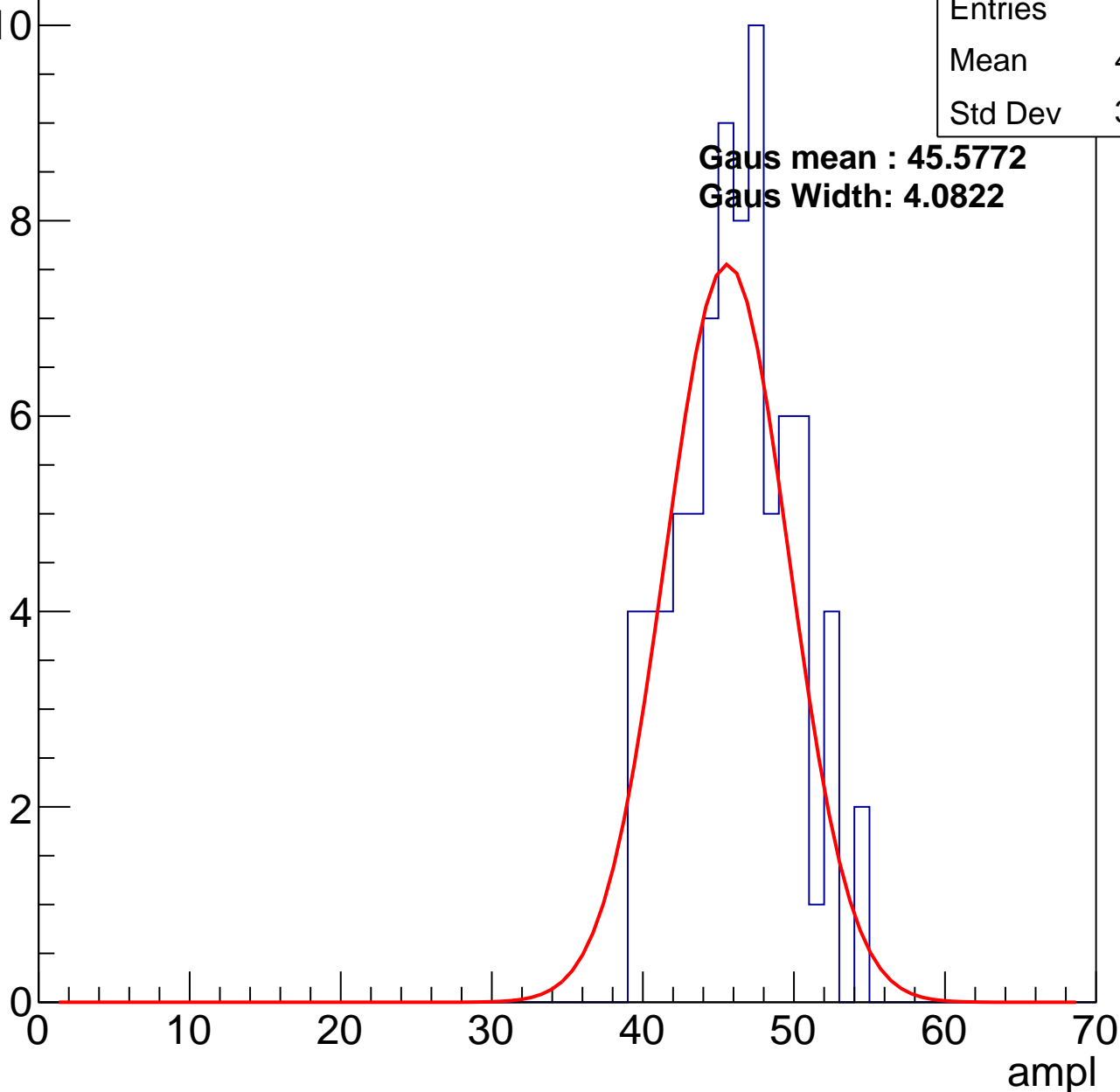
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	45.71
Std Dev	3.661

Gaus mean : 45.5772

Gaus Width: 4.0822

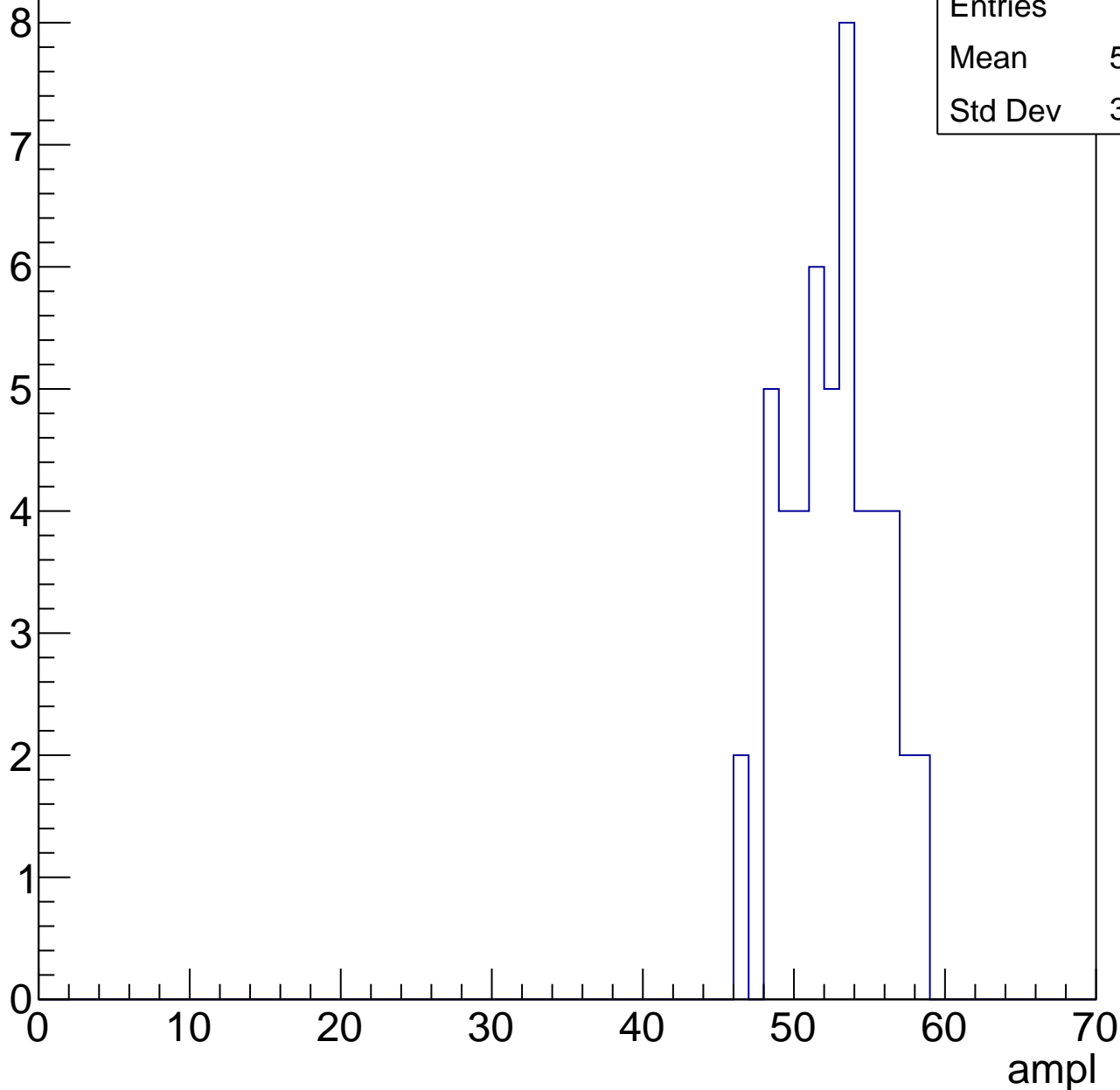


B1L101S, U23-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

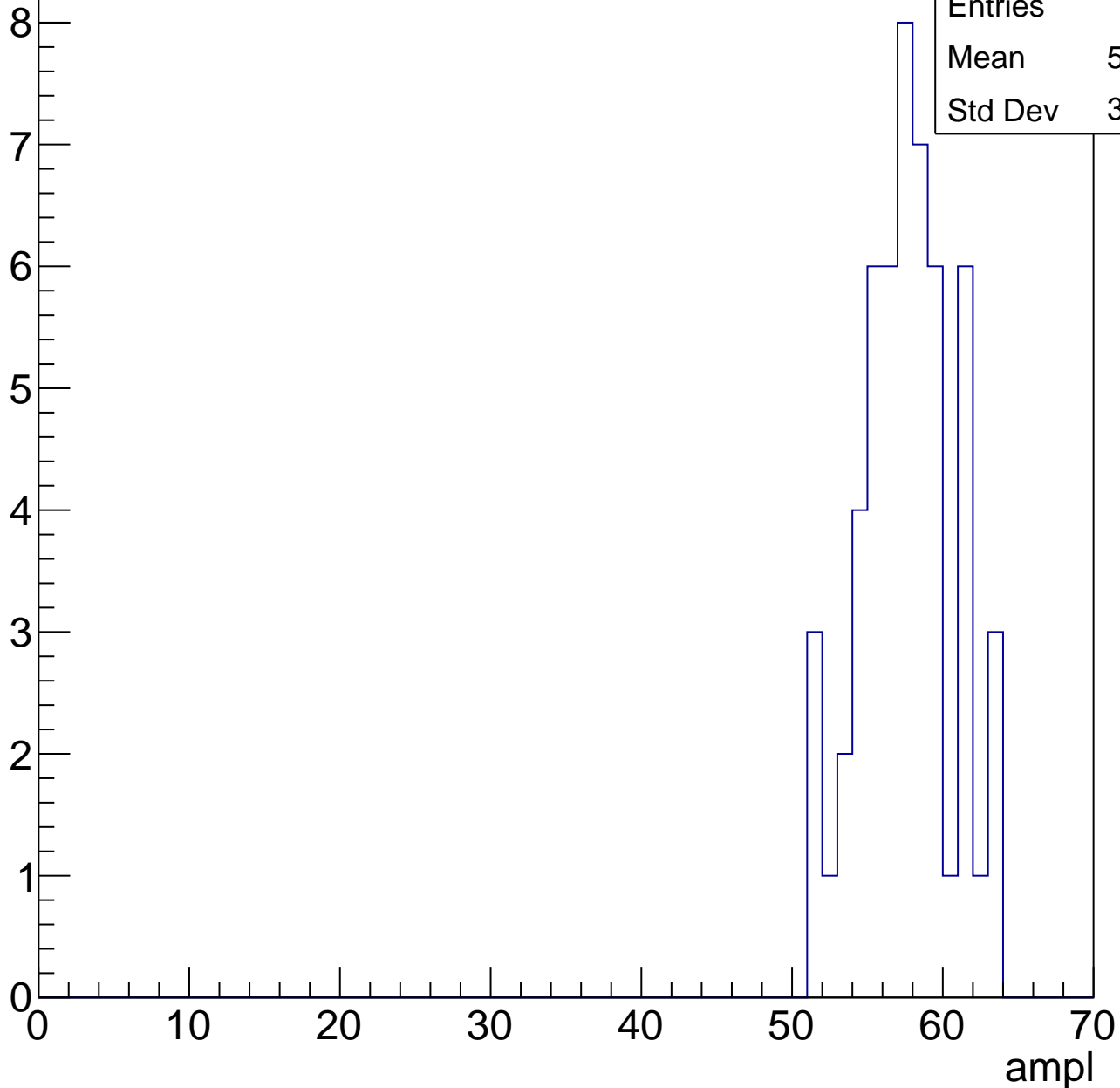
Entries	50
Mean	52.16
Std Dev	3.016



B1L101S, U23-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

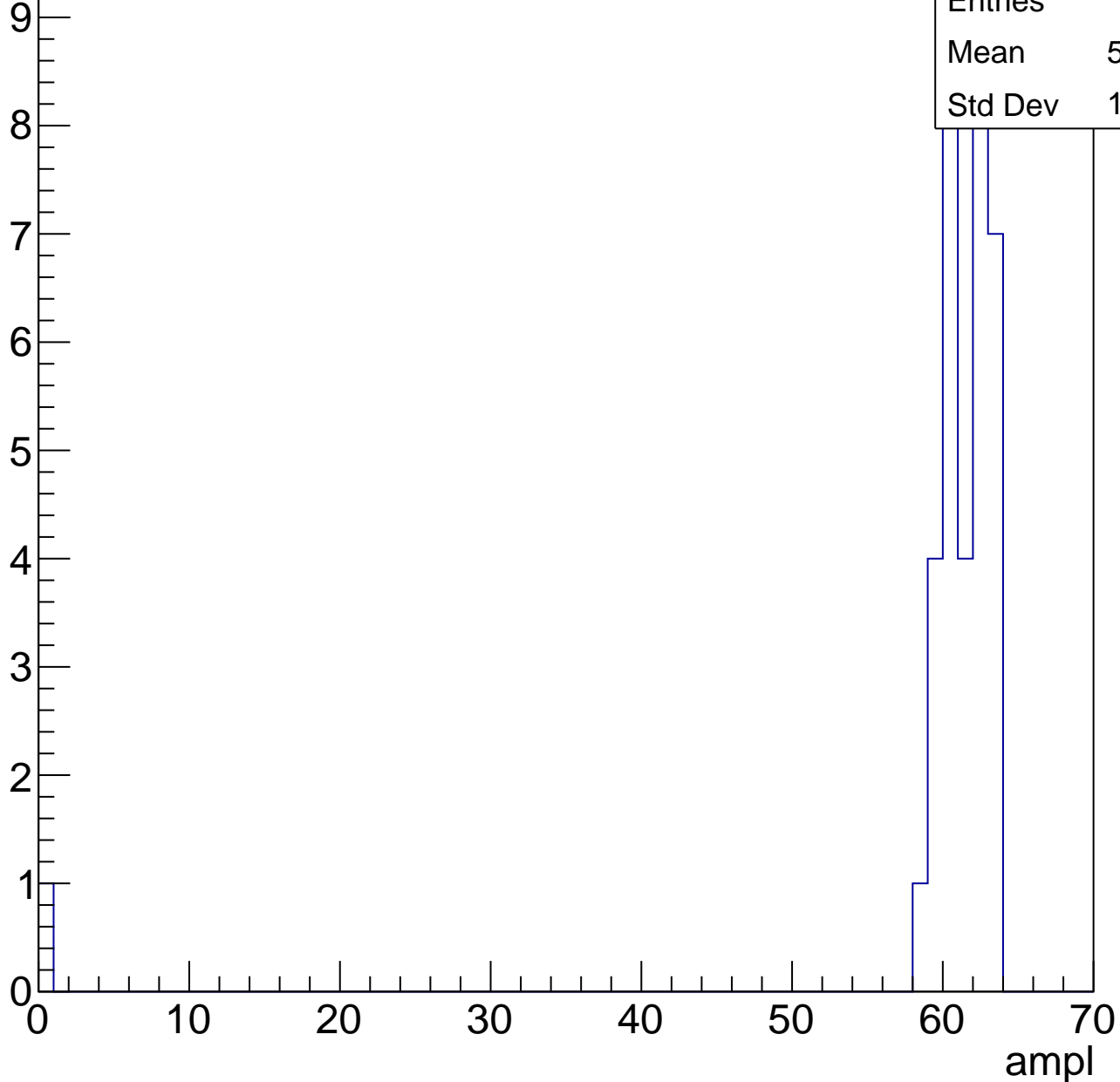
Entry



B1L101S, U23-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch14, adc0

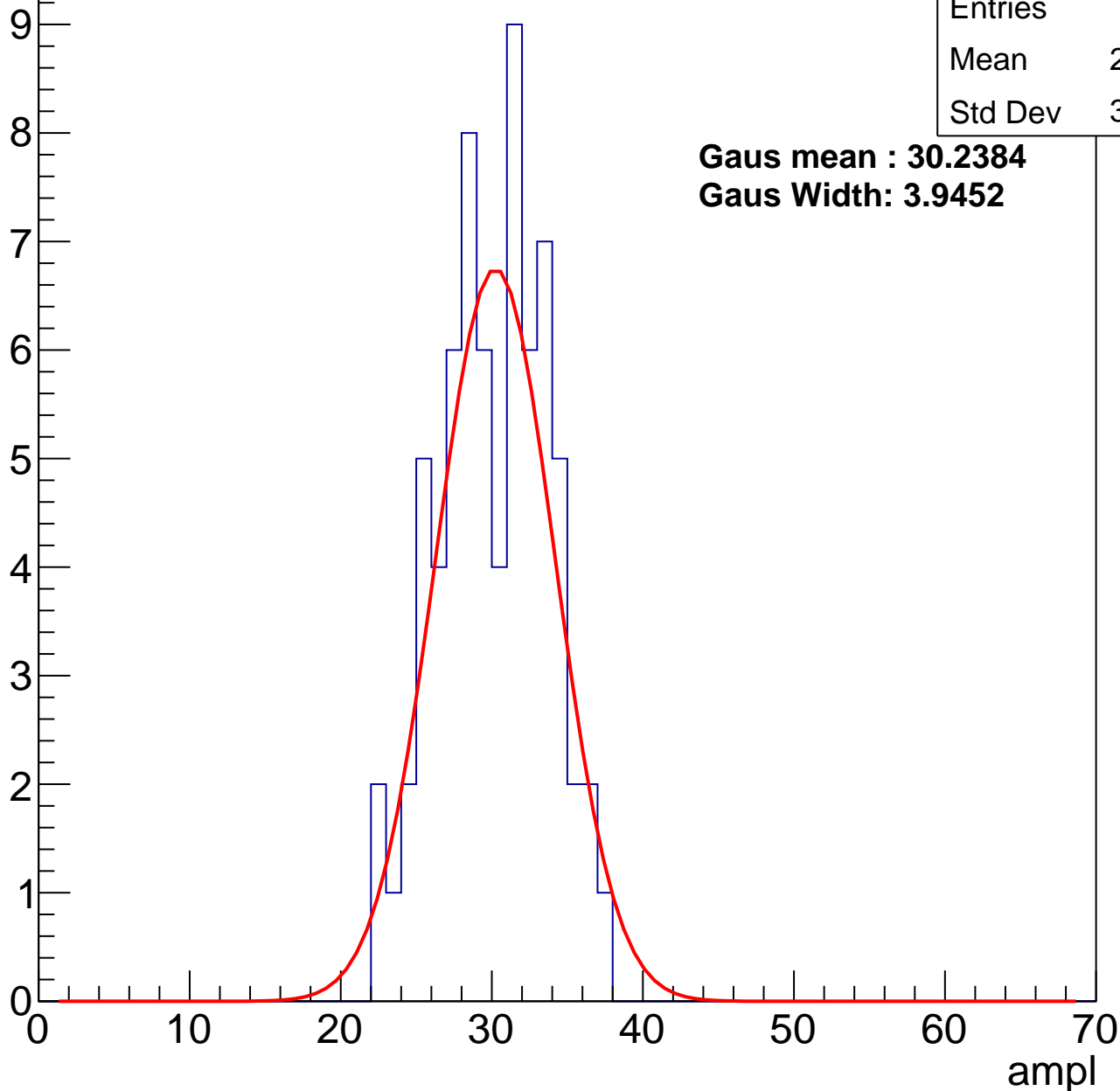
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.64
Std Dev	3.522

Gaus mean : 30.2384

Gaus Width: 3.9452



B1L101S, U23-ch14, adc1

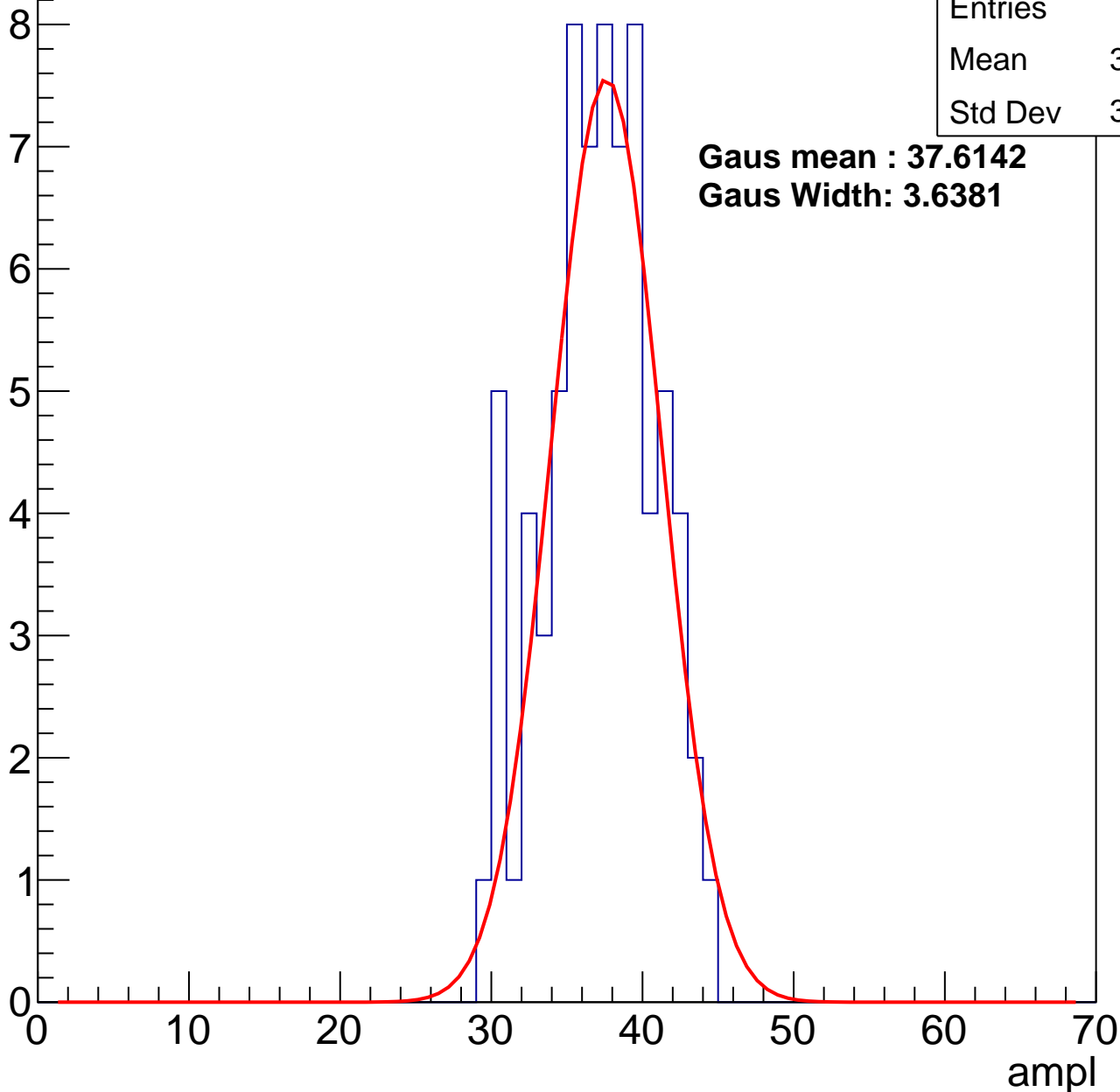
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.66
Std Dev	3.597

Gaus mean : 37.6142

Gaus Width: 3.6381



B1L101S, U23-ch14, adc2

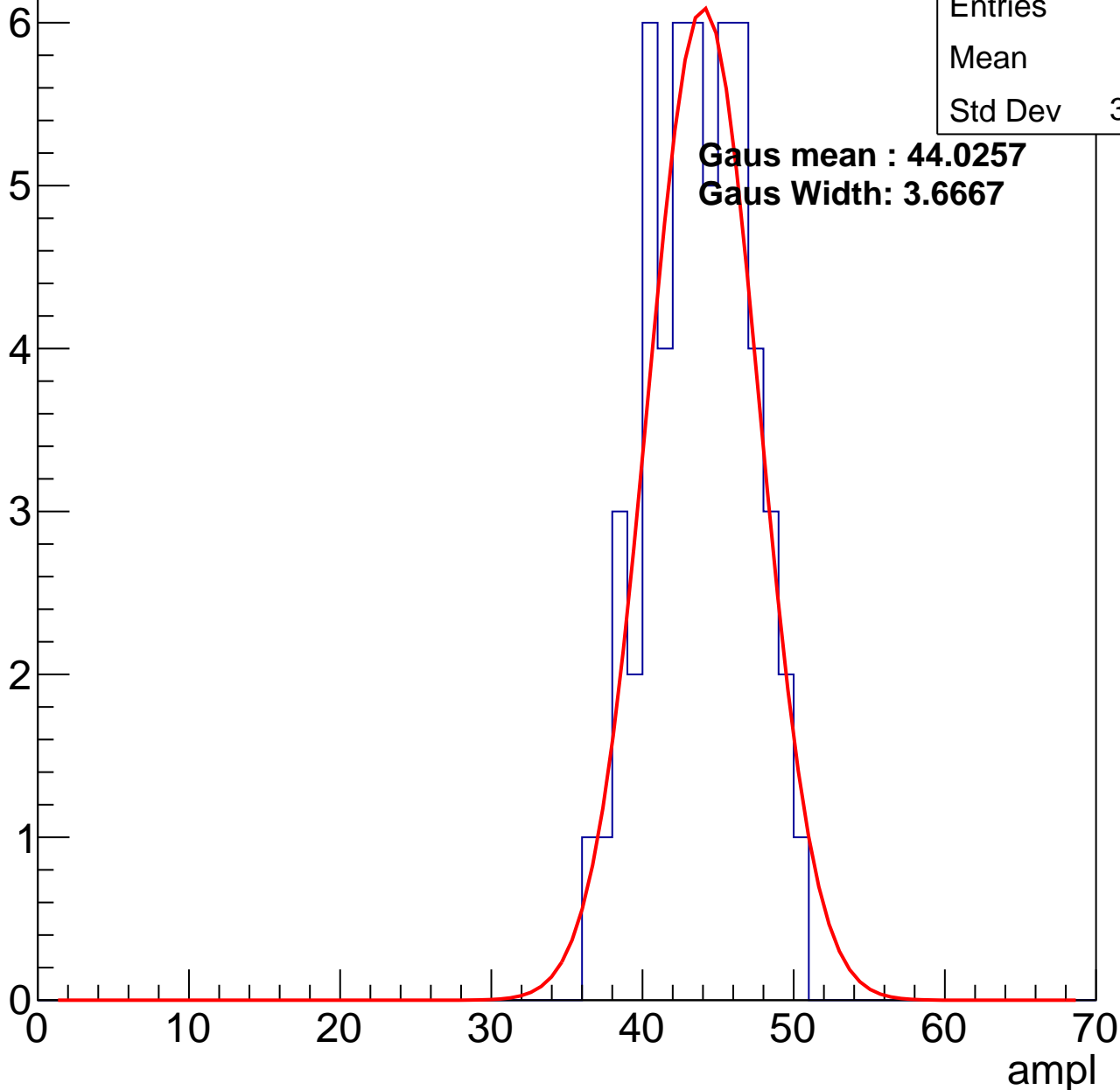
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.3
Std Dev	3.289

Gaus mean : 44.0257

Gaus Width: 3.6667

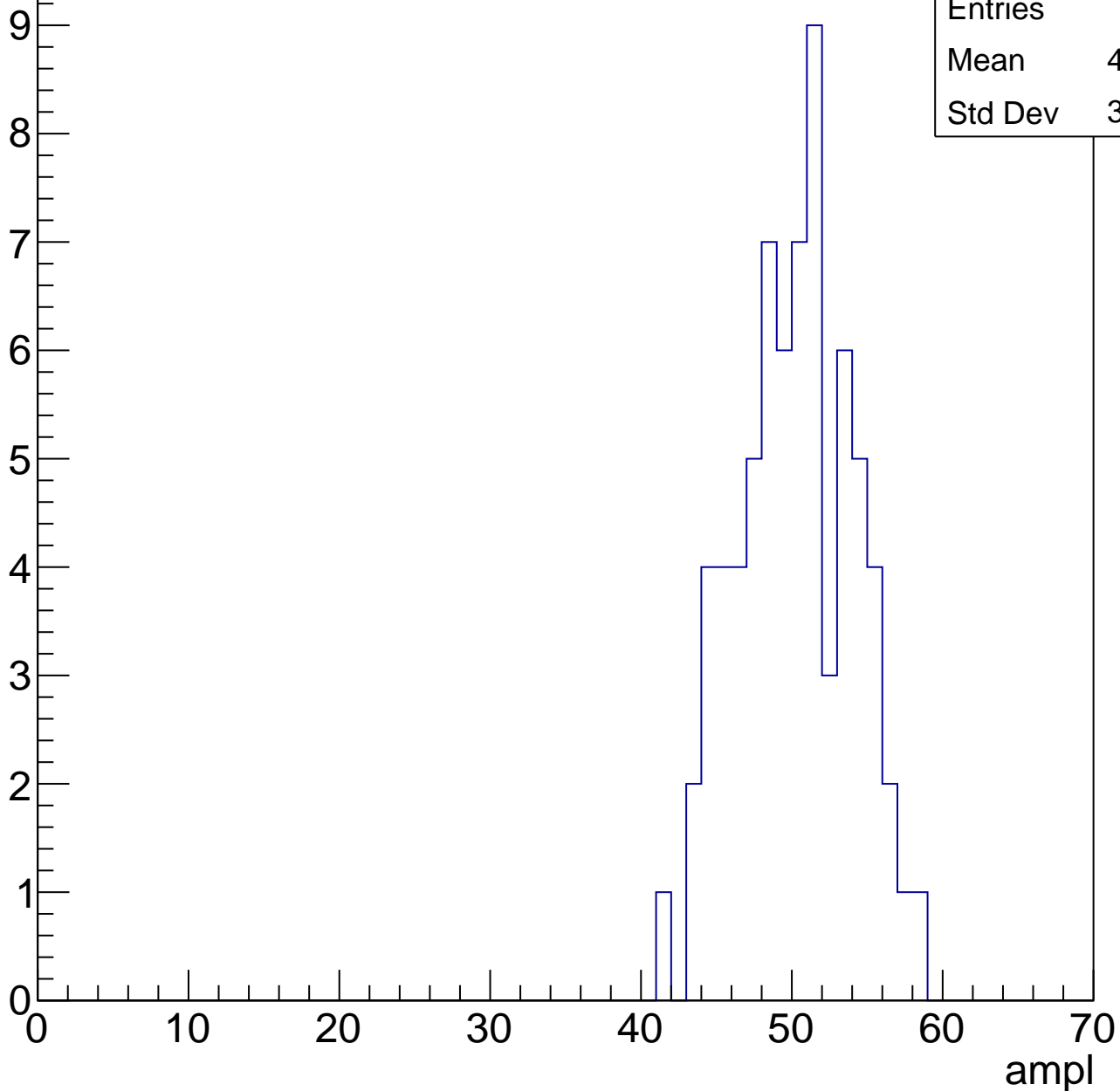


B1L101S, U23-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.75
Std Dev	3.756

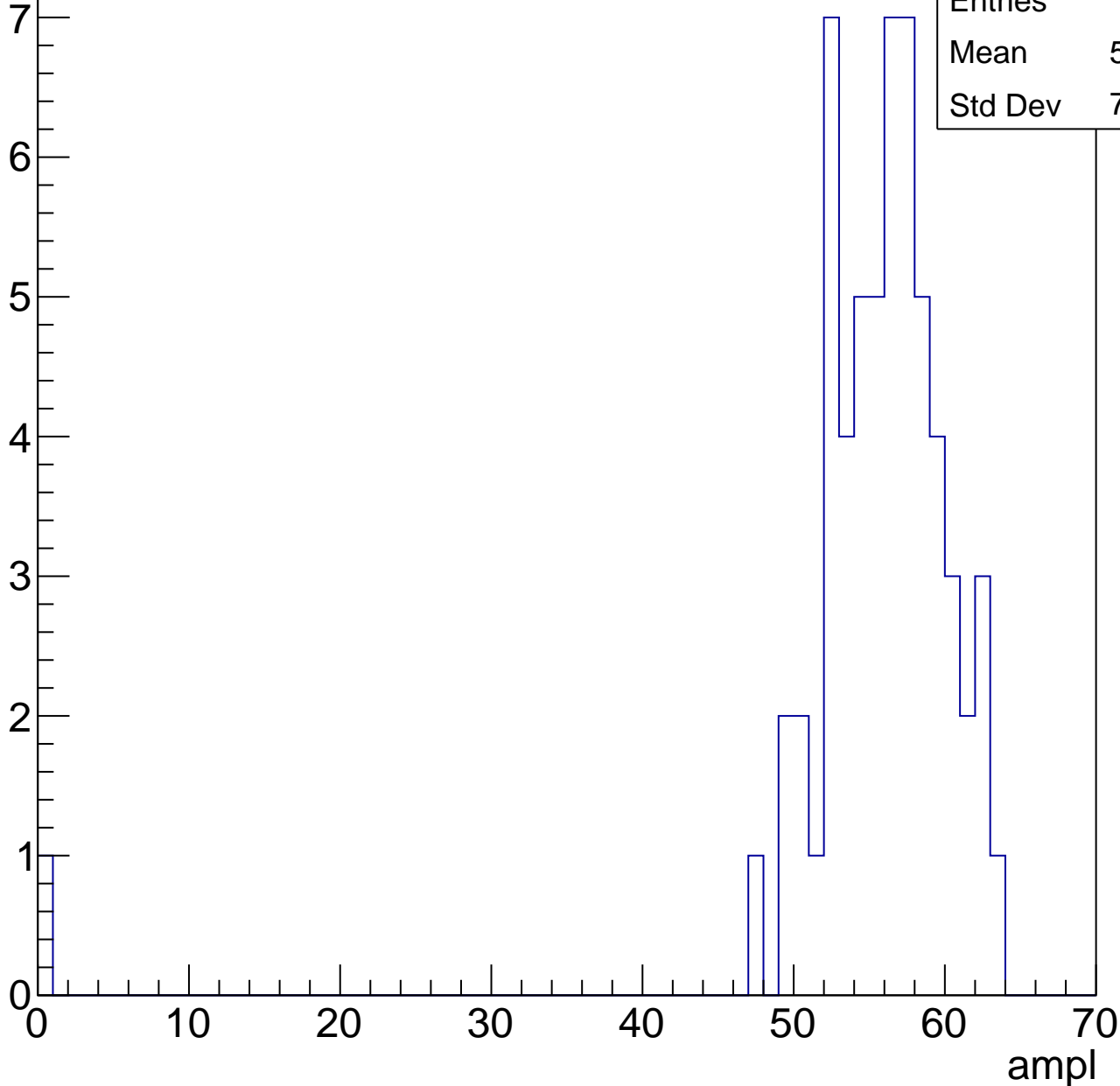


B1L101S, U23-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.75
Std Dev	7.968

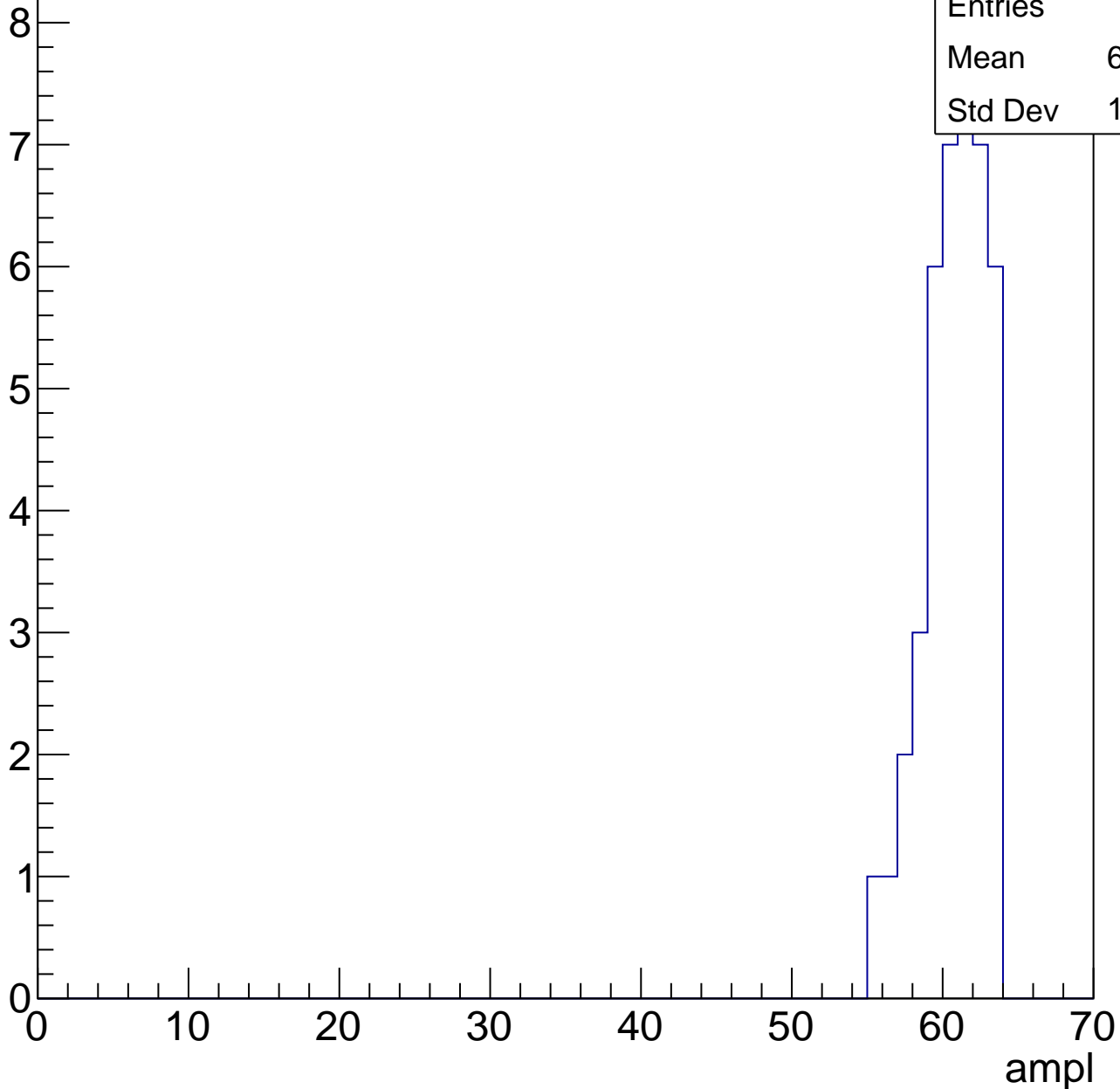


B1L101S, U23-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

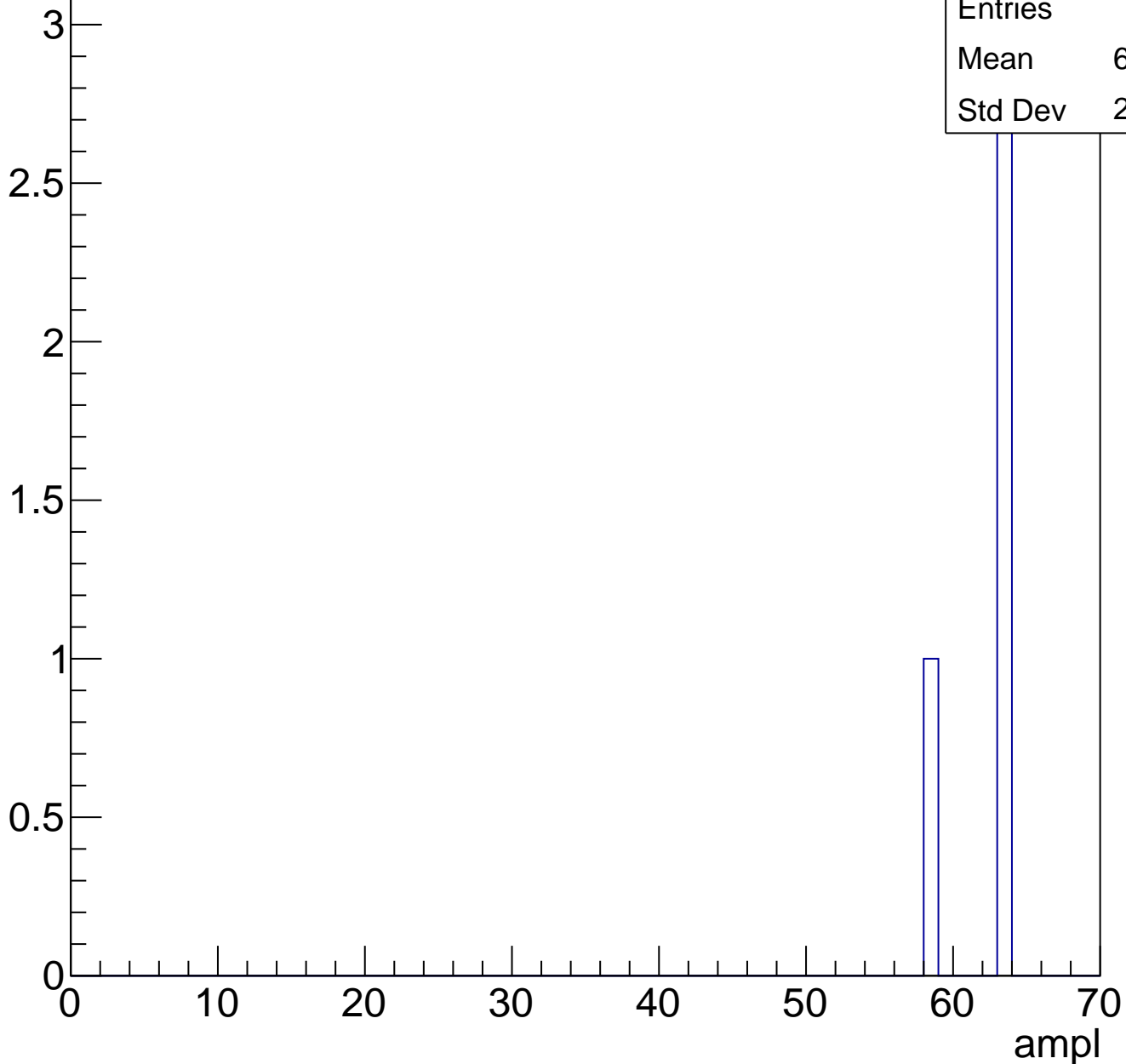
Entries	41
Mean	60.32
Std Dev	1.993



B1L101S, U23-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L101S, U23-ch15, adc0

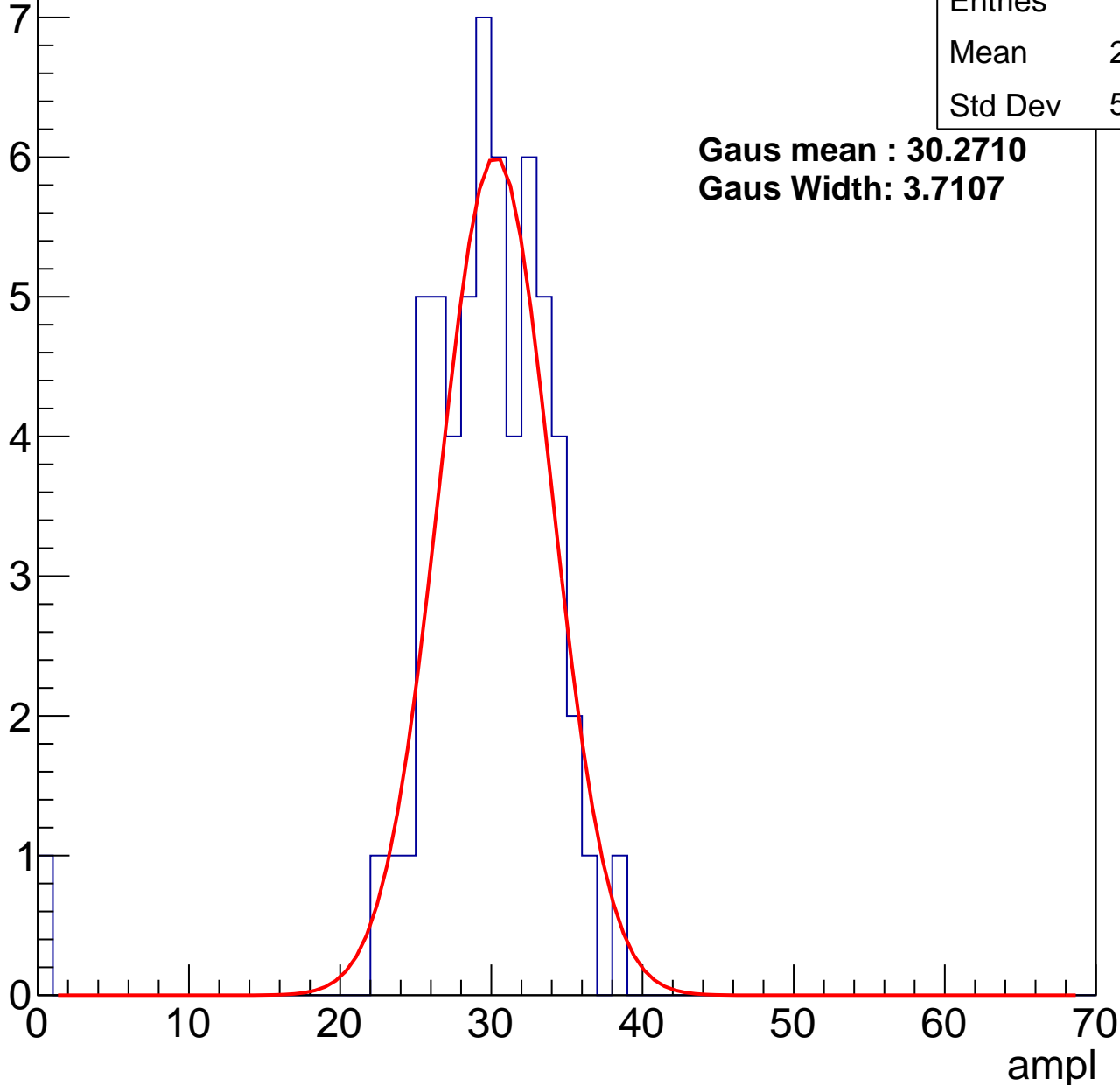
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.08
Std Dev	5.133

Gaus mean : 30.2710

Gaus Width: 3.7107



B1L101S, U23-ch15, adc1

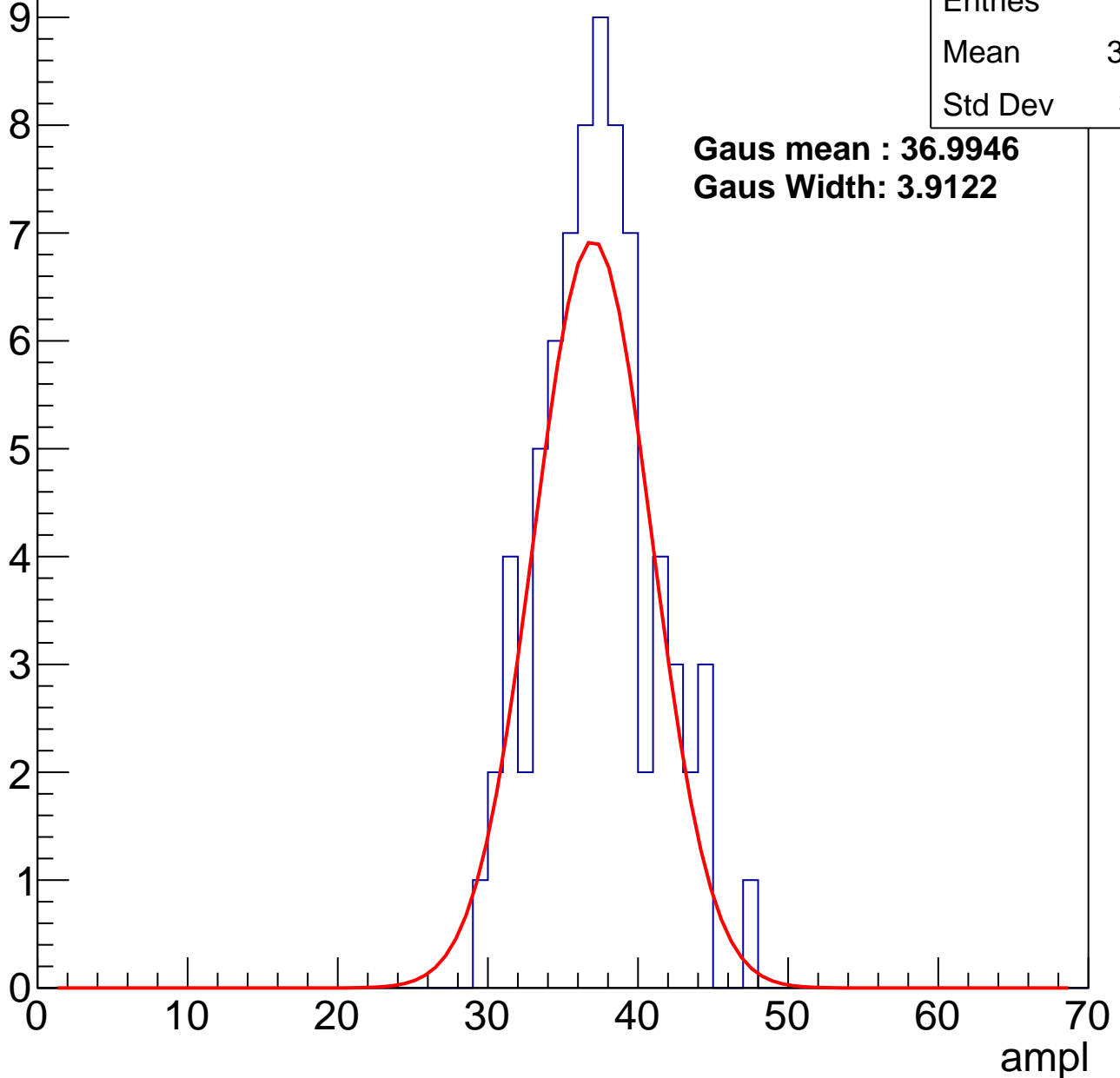
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.81
Std Dev	3.74

Gaus mean : 36.9946

Gaus Width: 3.9122



B1L101S, U23-ch15, adc2

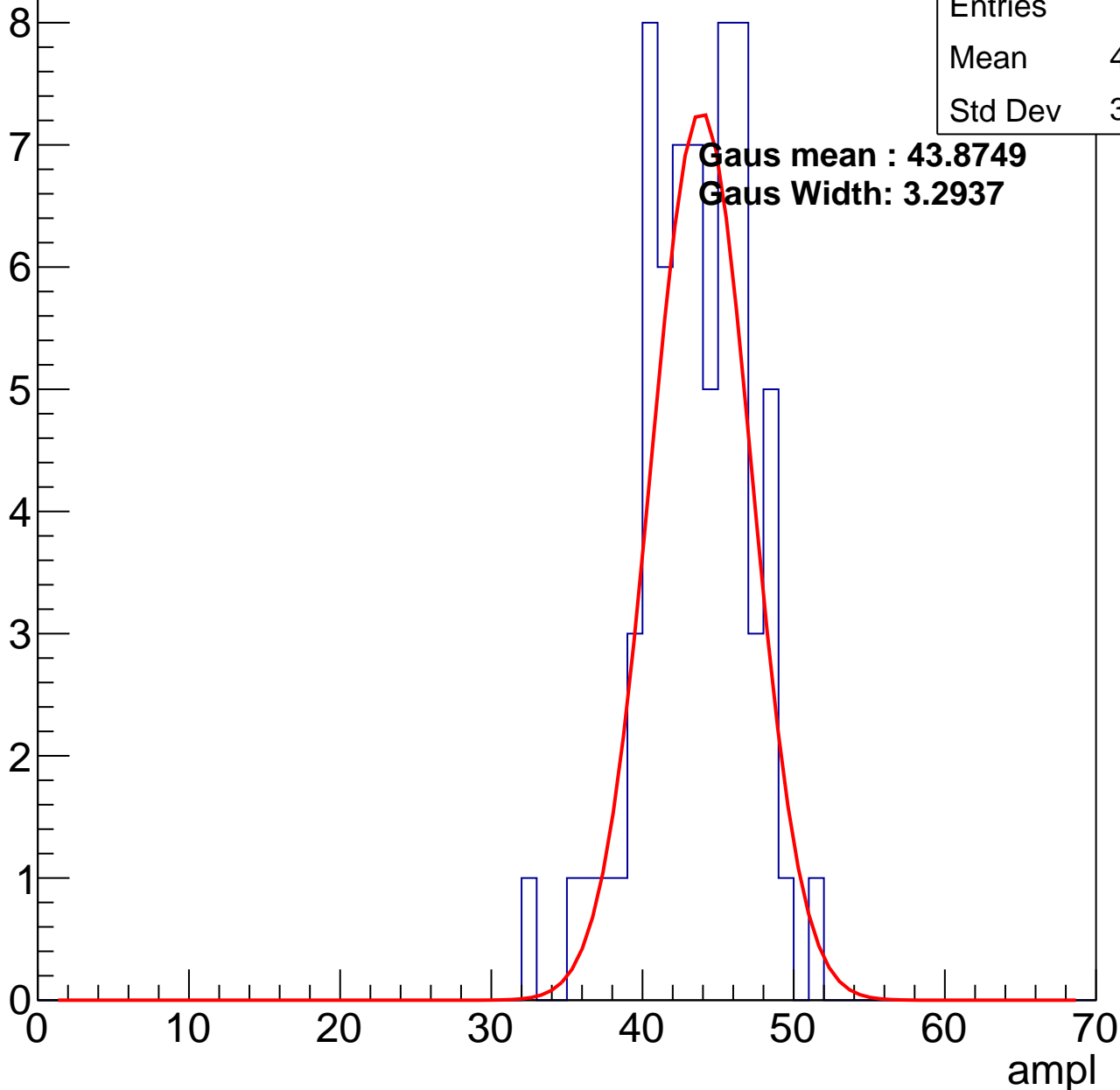
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.06
Std Dev	3.515

Gaus mean : 43.8749

Gaus Width: 3.2937

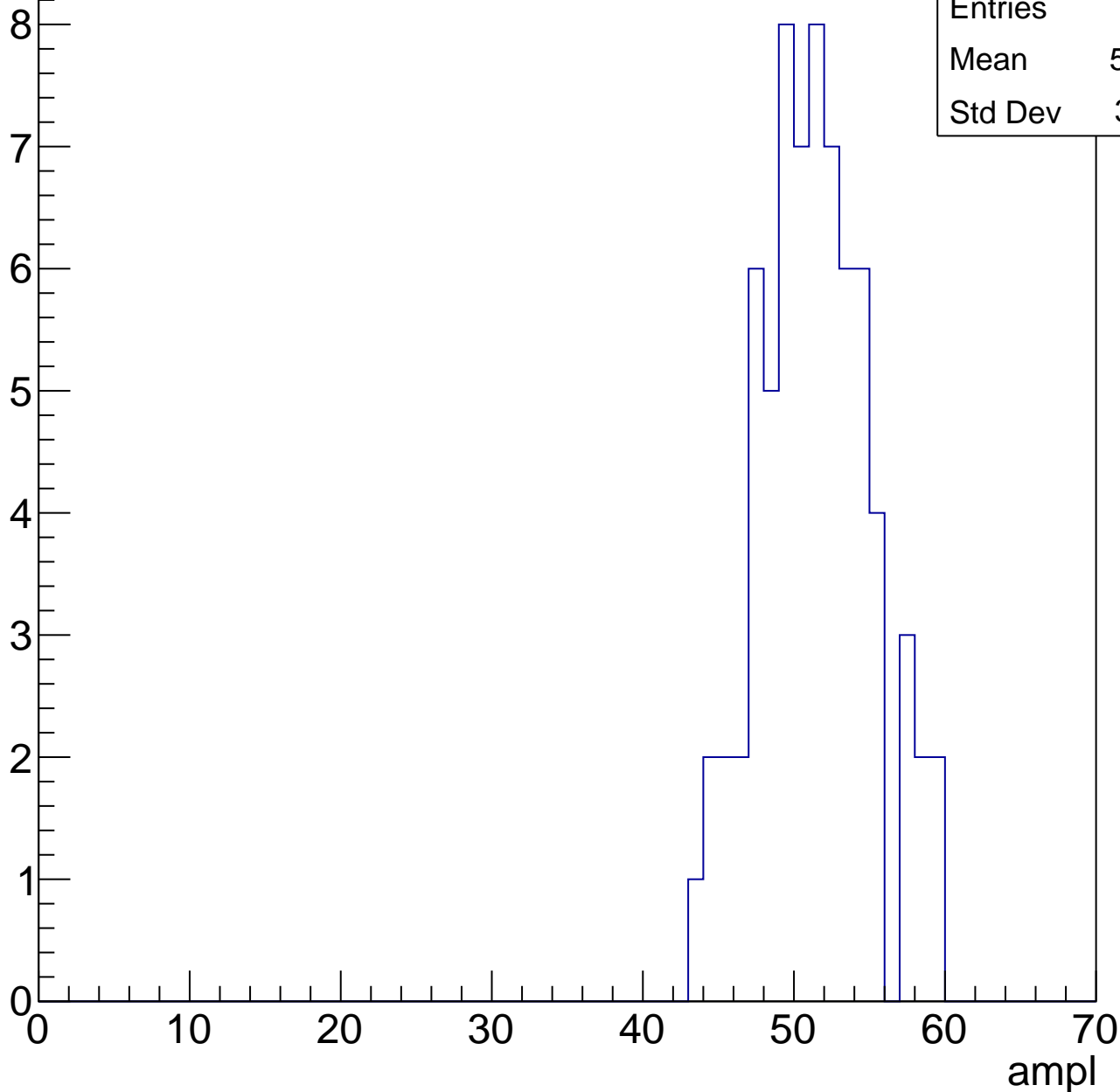


B1L101S, U23-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.93
Std Dev	3.651

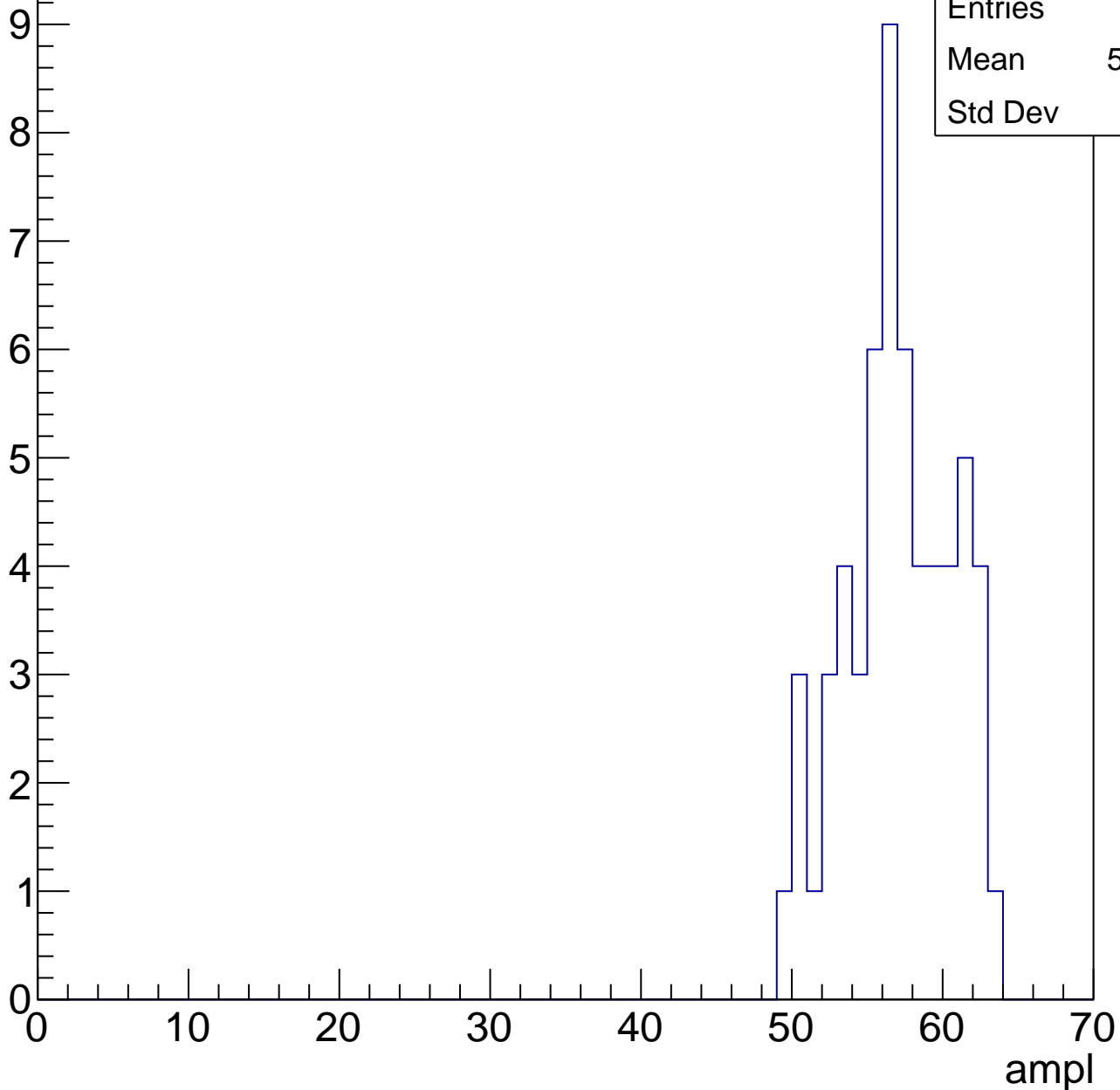


B1L101S, U23-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.55
Std Dev	3.49

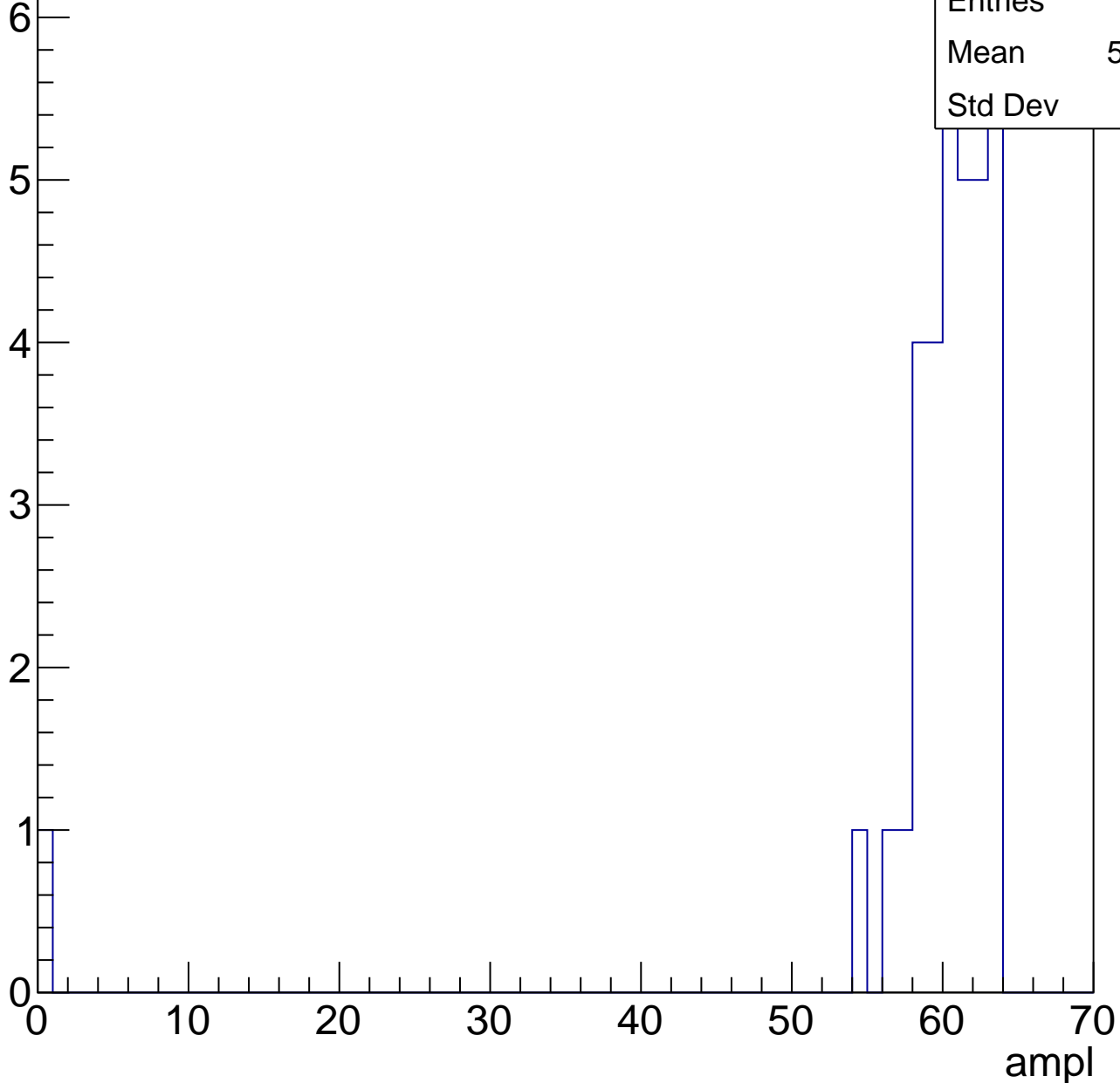


B1L101S, U23-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.47
Std Dev	10.4



B1L101S, U23-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

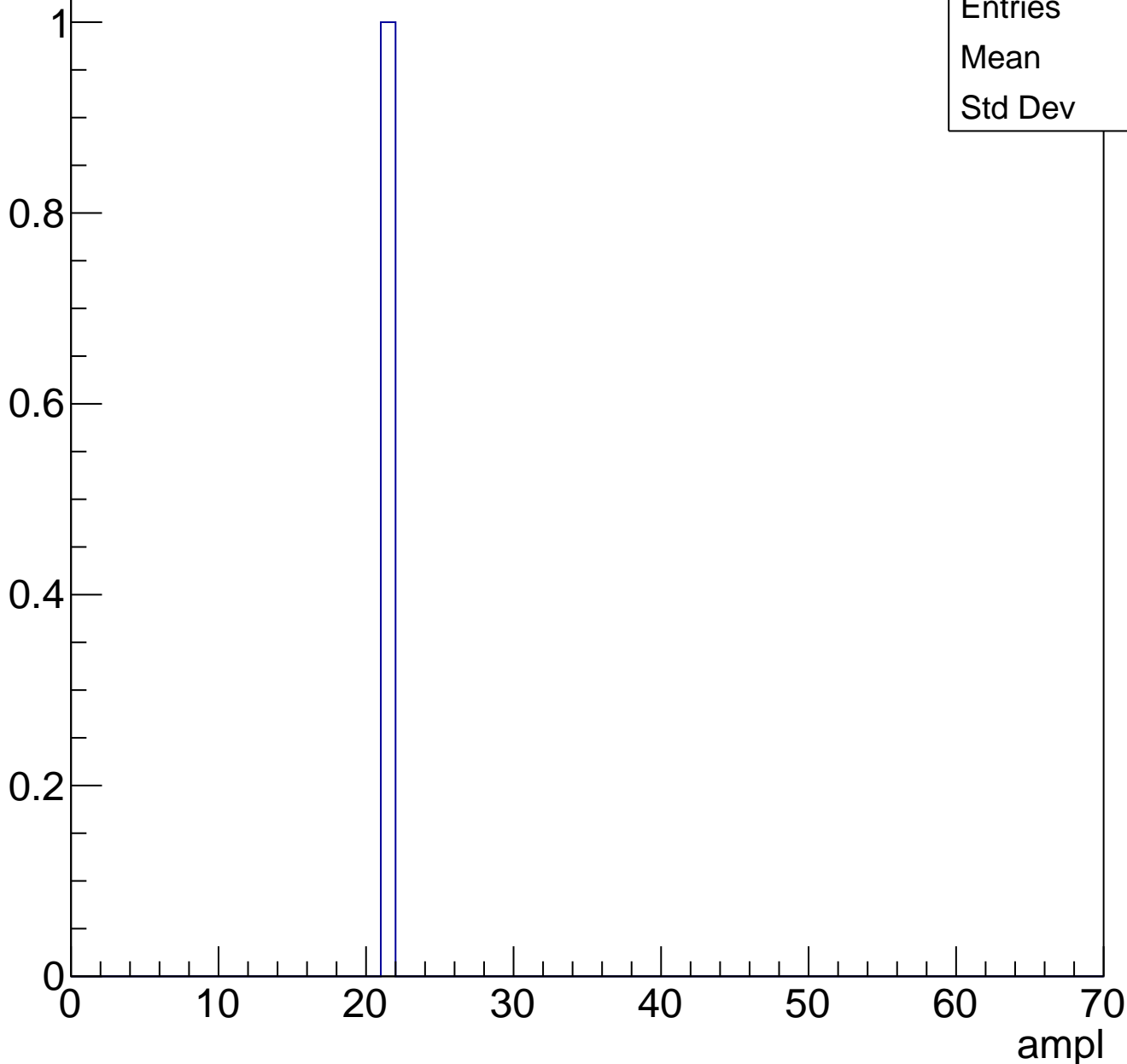
Entry



B1L101S, U23-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch16, adc0

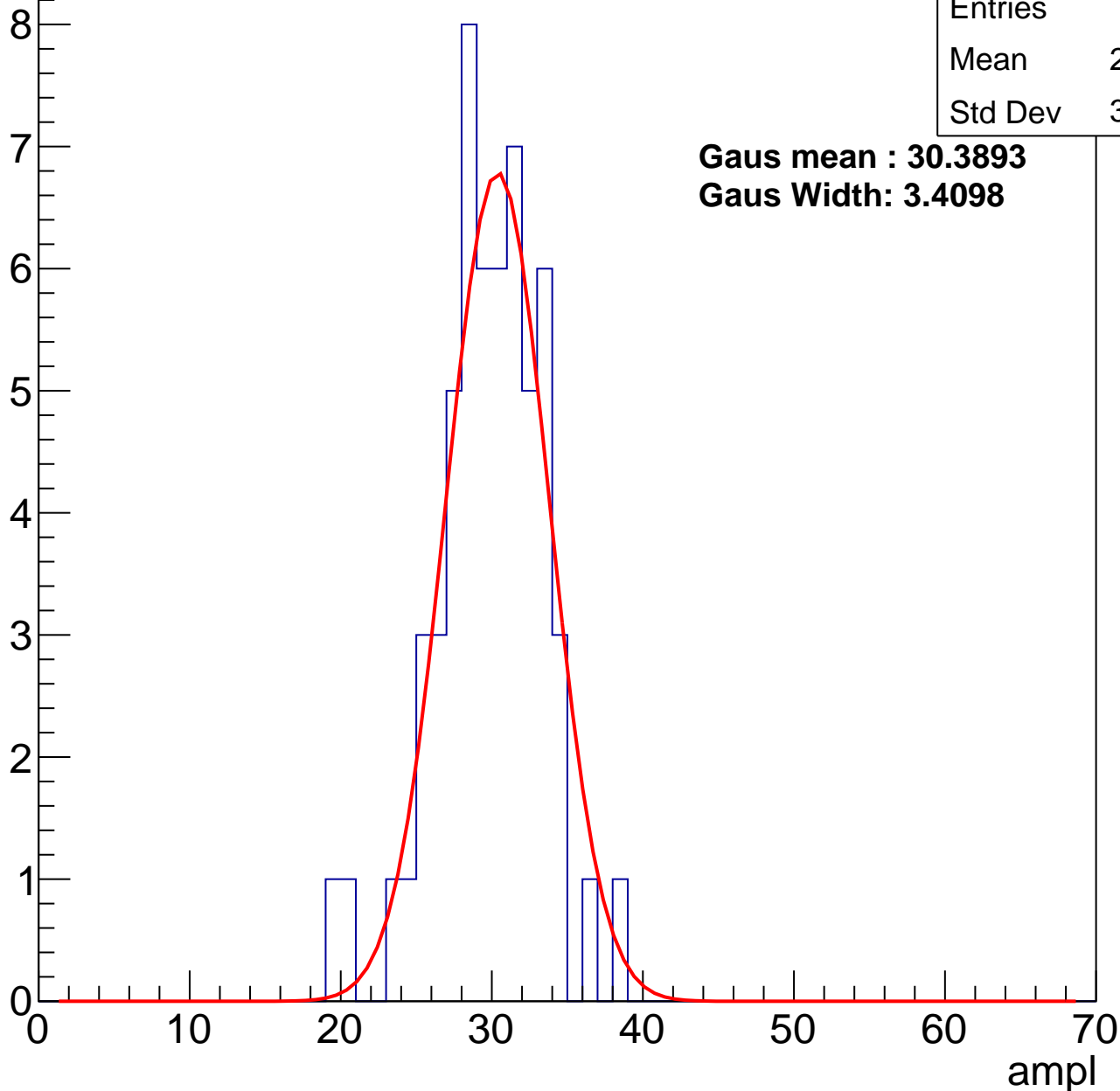
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.36
Std Dev	3.517

Gaus mean : 30.3893

Gaus Width: 3.4098



B1L101S, U23-ch16, adc1

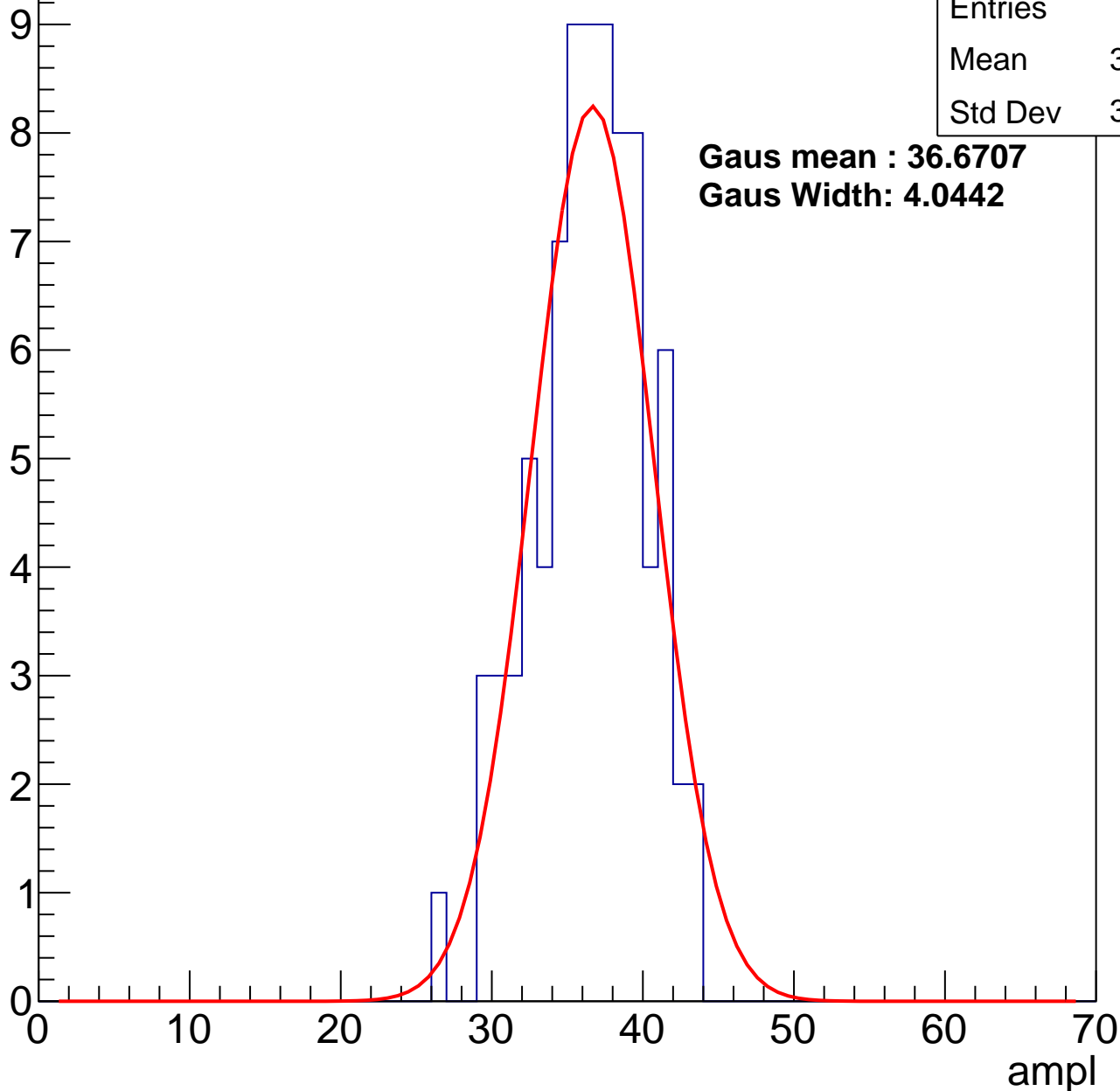
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.02
Std Dev	3.607

Gaus mean : 36.6707

Gaus Width: 4.0442



B1L101S, U23-ch16, adc2

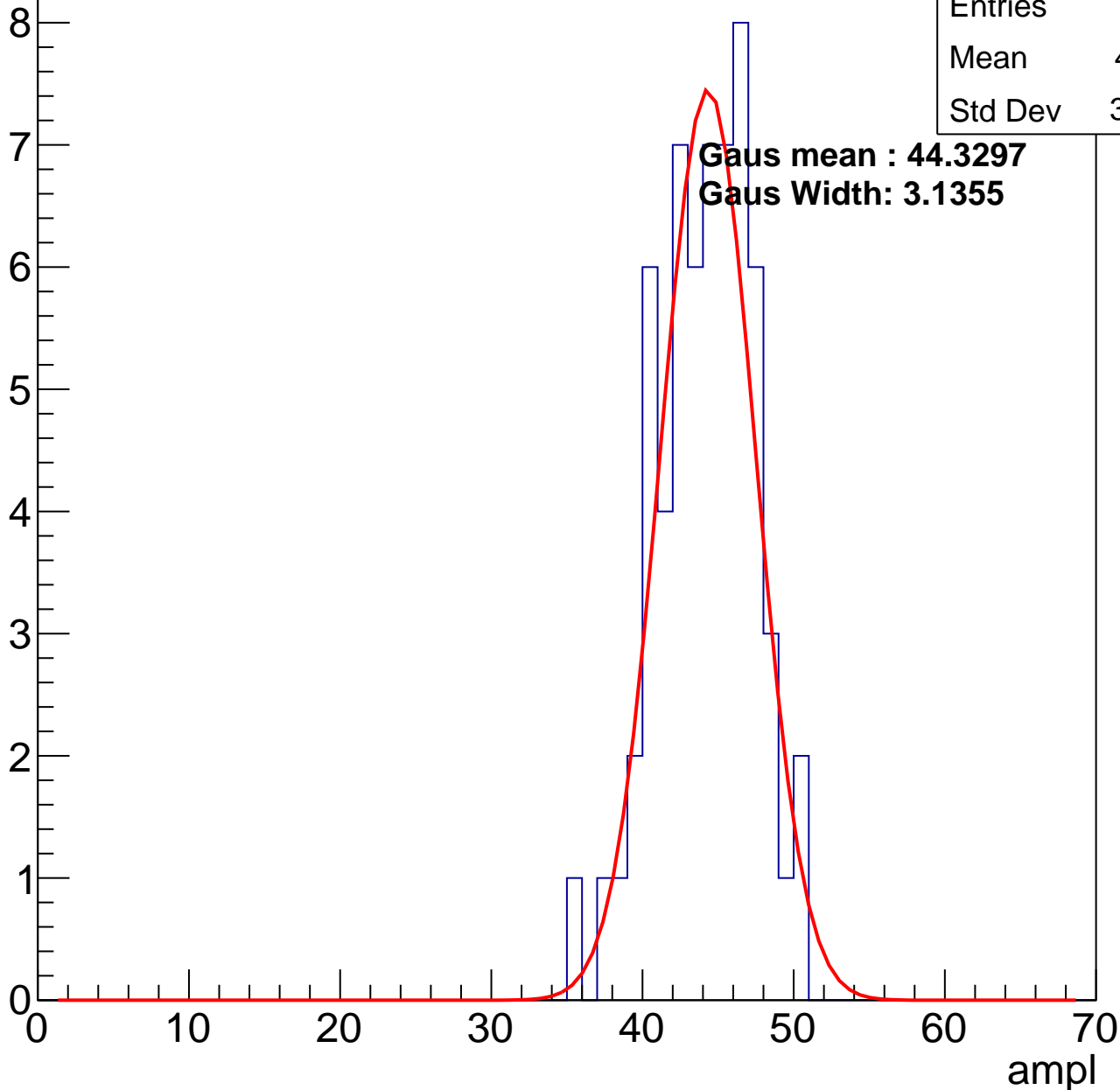
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.71
Std Dev	3.149

Gaus mean : 44.3297

Gaus Width: 3.1355

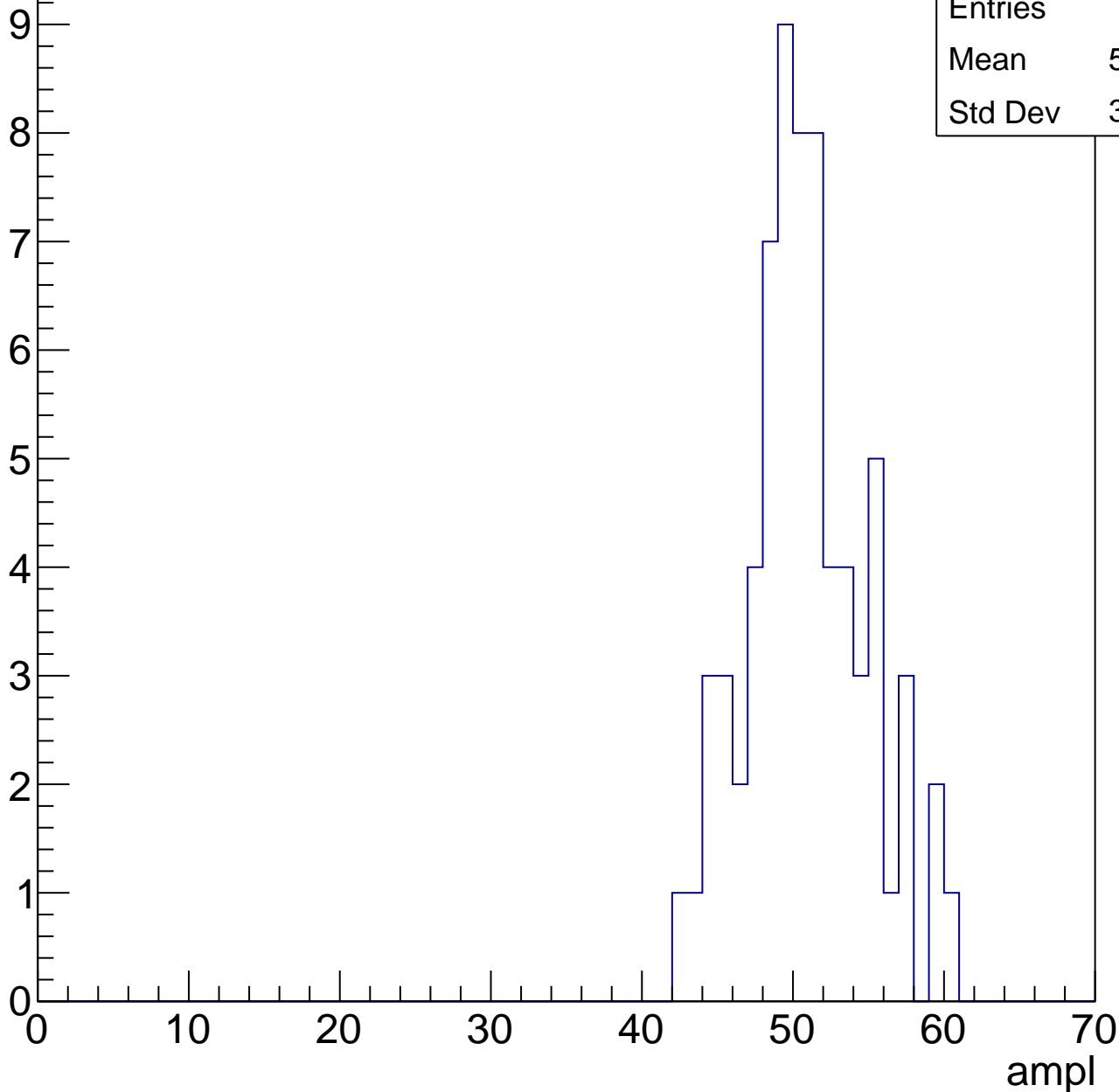


B1L101S, U23-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.42
Std Dev	3.932

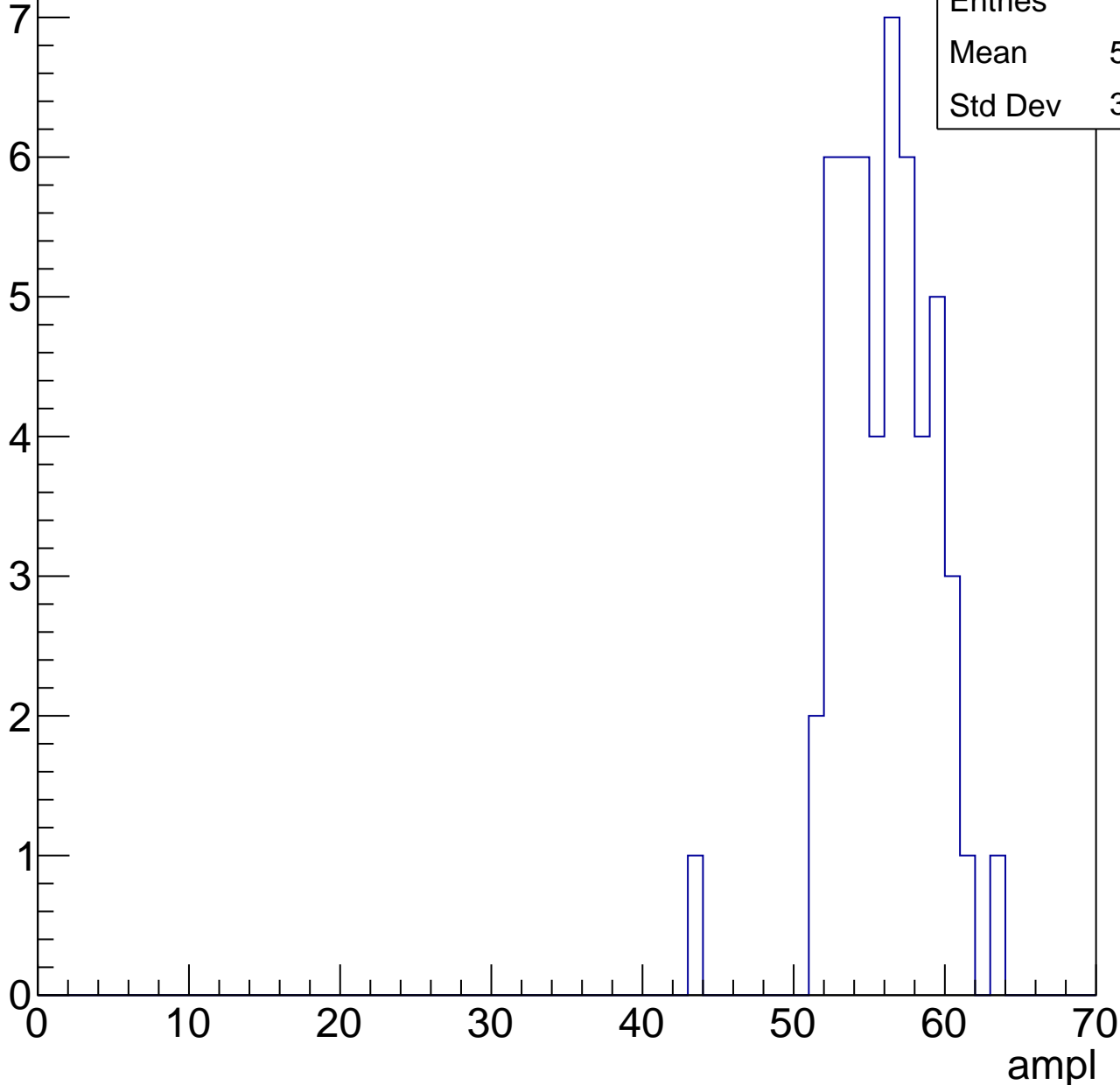


B1L101S, U23-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	55.46
Std Dev	3.314

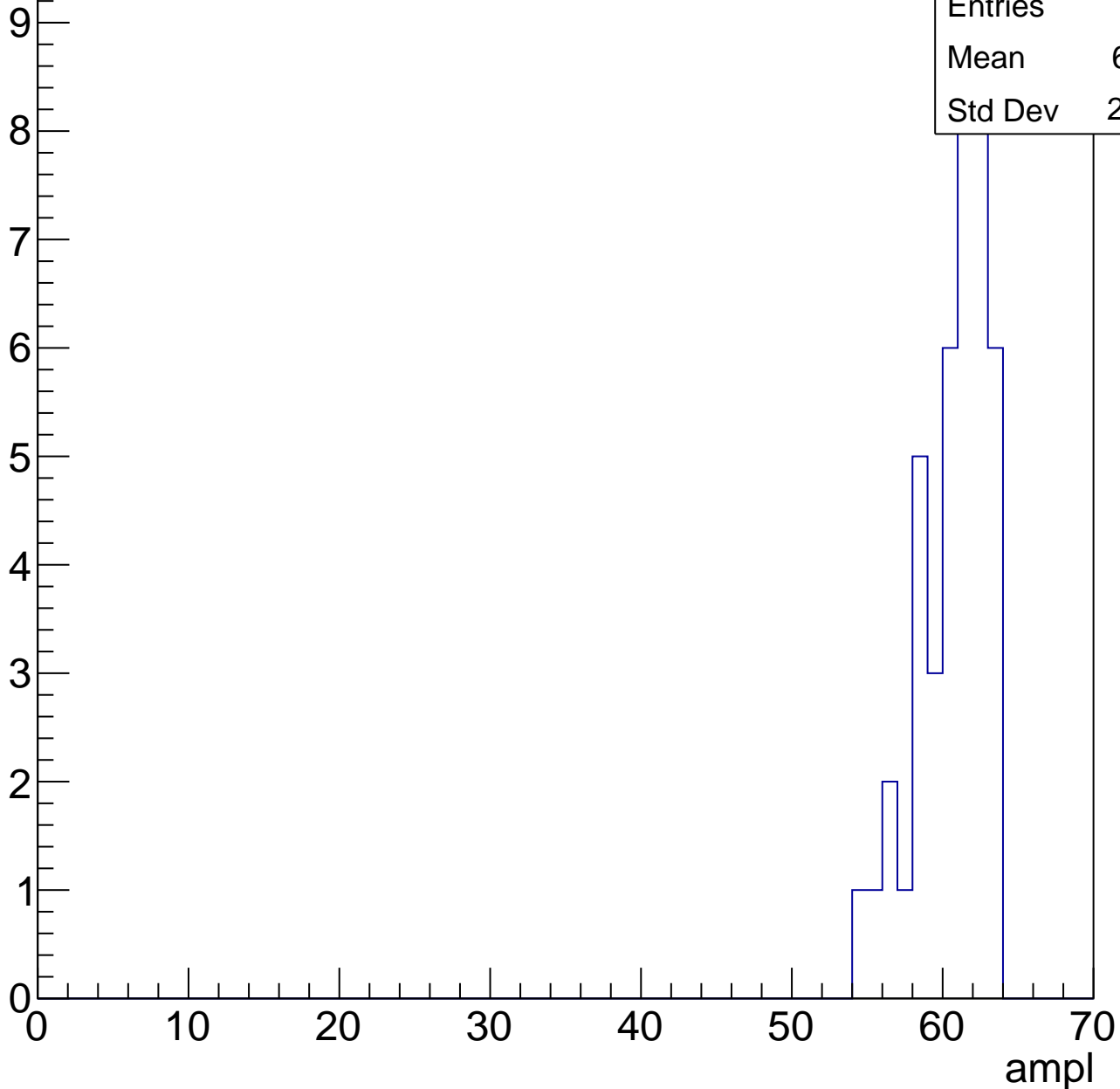


B1L101S, U23-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

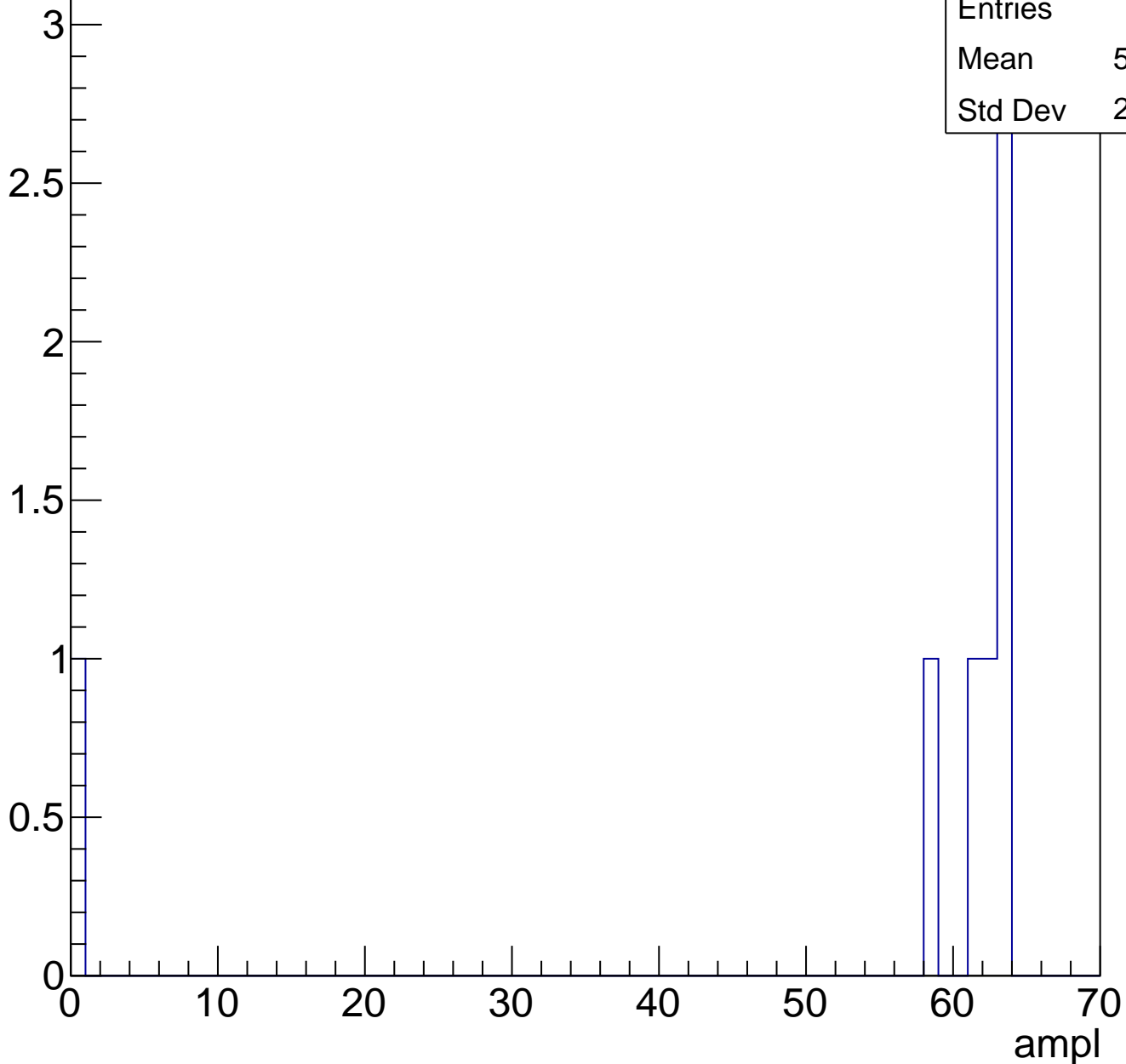
Entries	42
Mean	60.21
Std Dev	2.294



B1L101S, U23-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch17, adc0

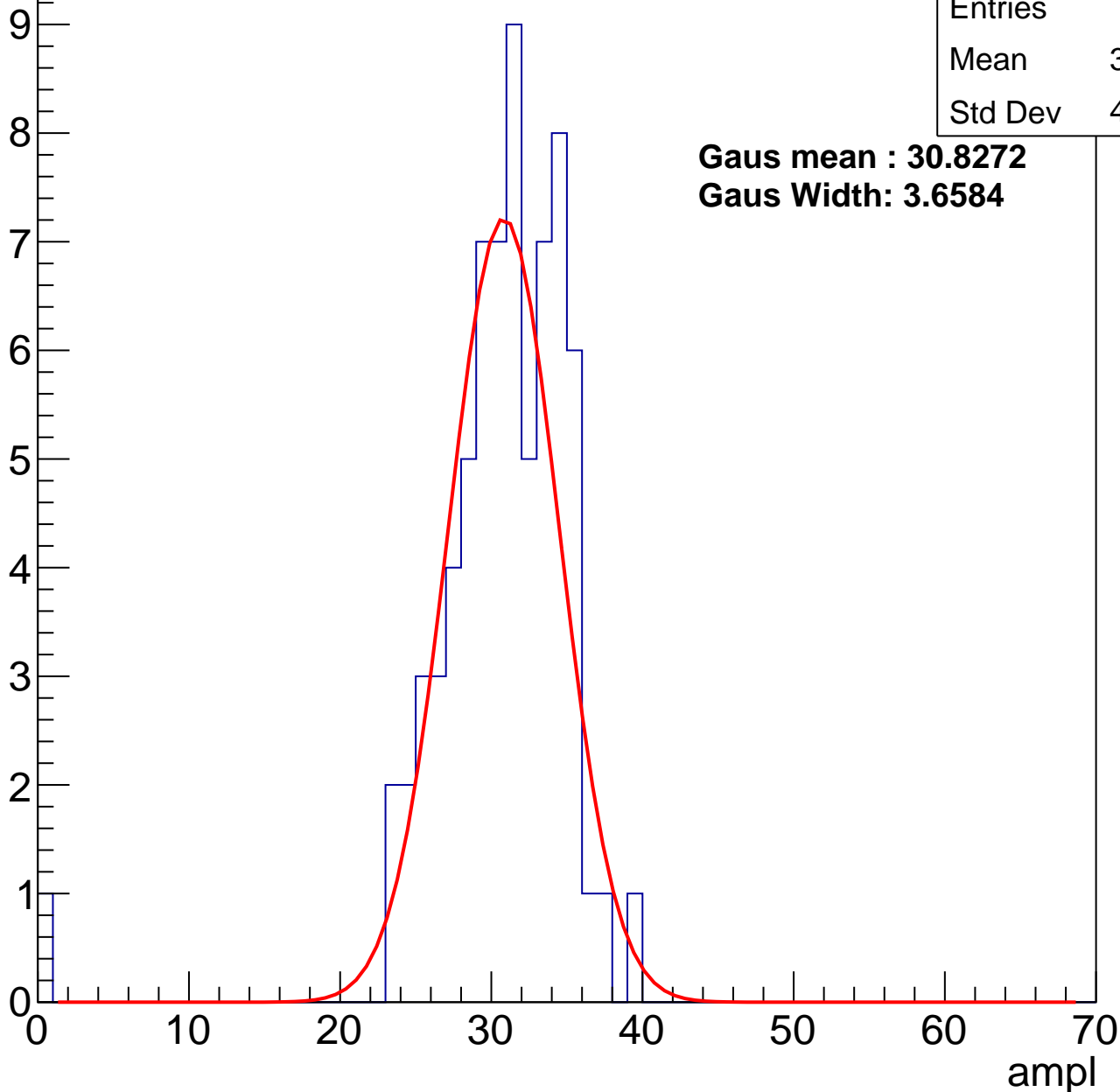
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.17
Std Dev	4.986

Gaus mean : 30.8272

Gaus Width: 3.6584



B1L101S, U23-ch17, adc1

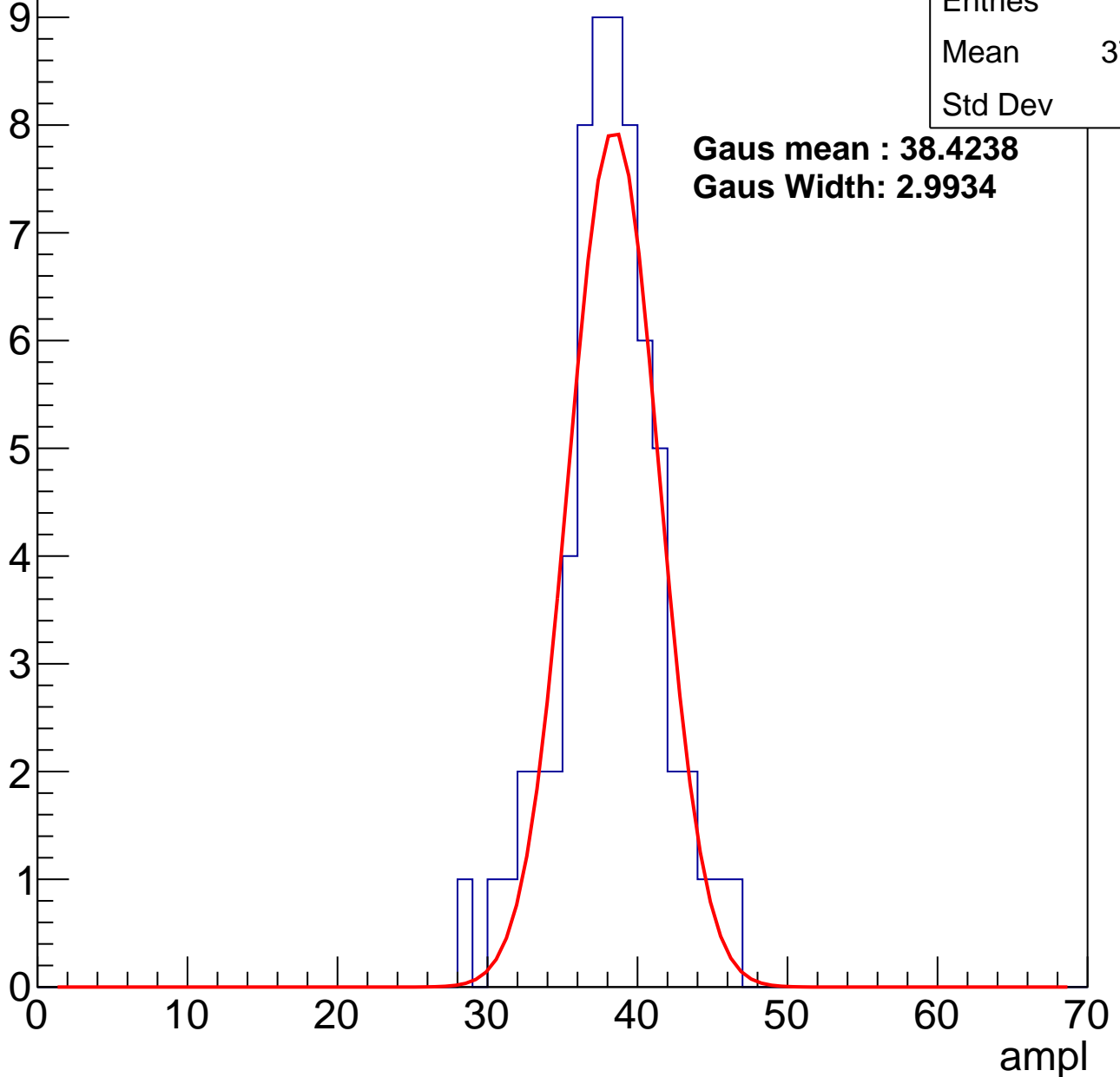
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.72
Std Dev	3.39

Gaus mean : 38.4238

Gaus Width: 2.9934



B1L101S, U23-ch17, adc2

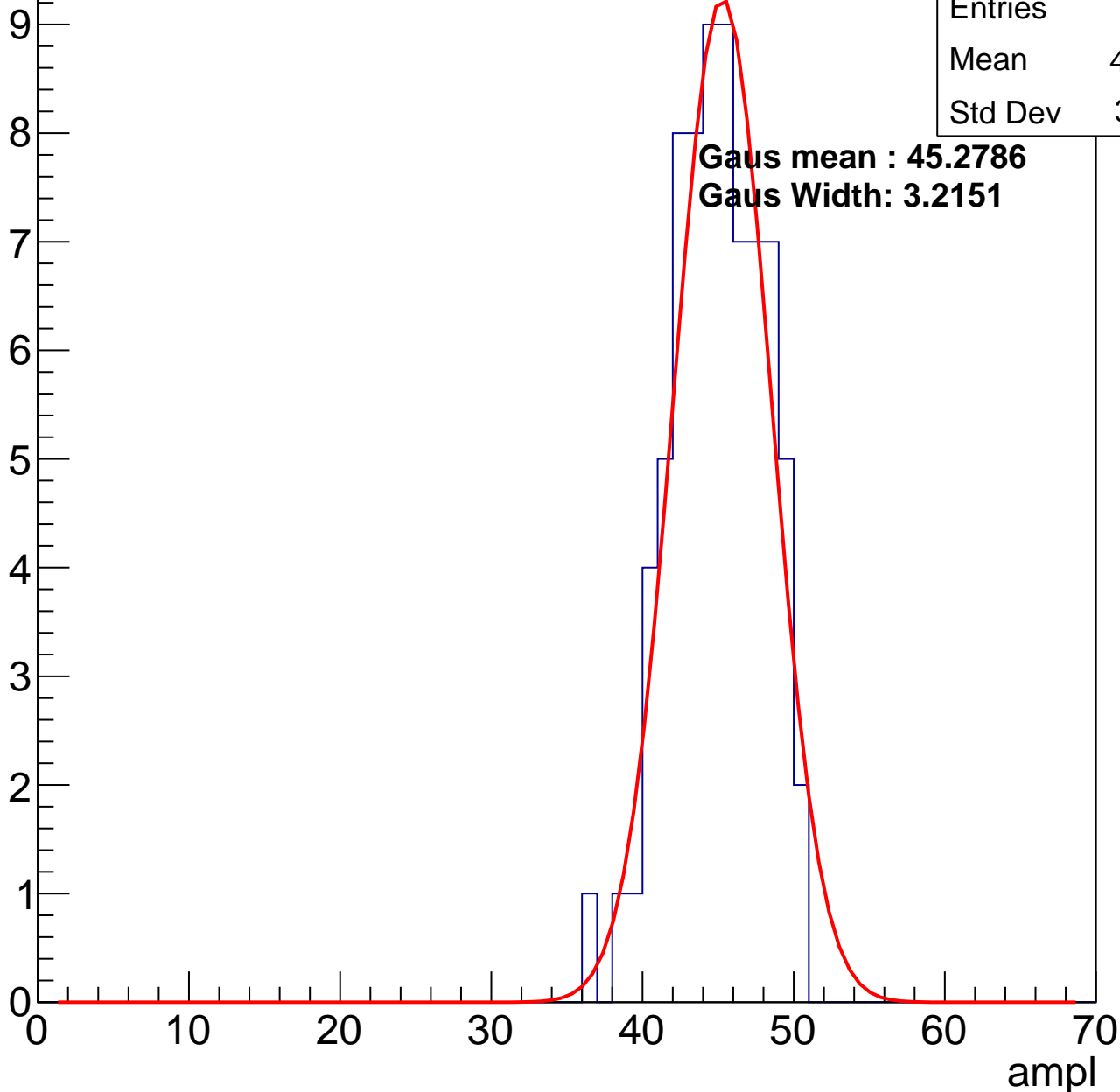
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.47
Std Dev	3.001

Gaus mean : 45.2786

Gaus Width: 3.2151

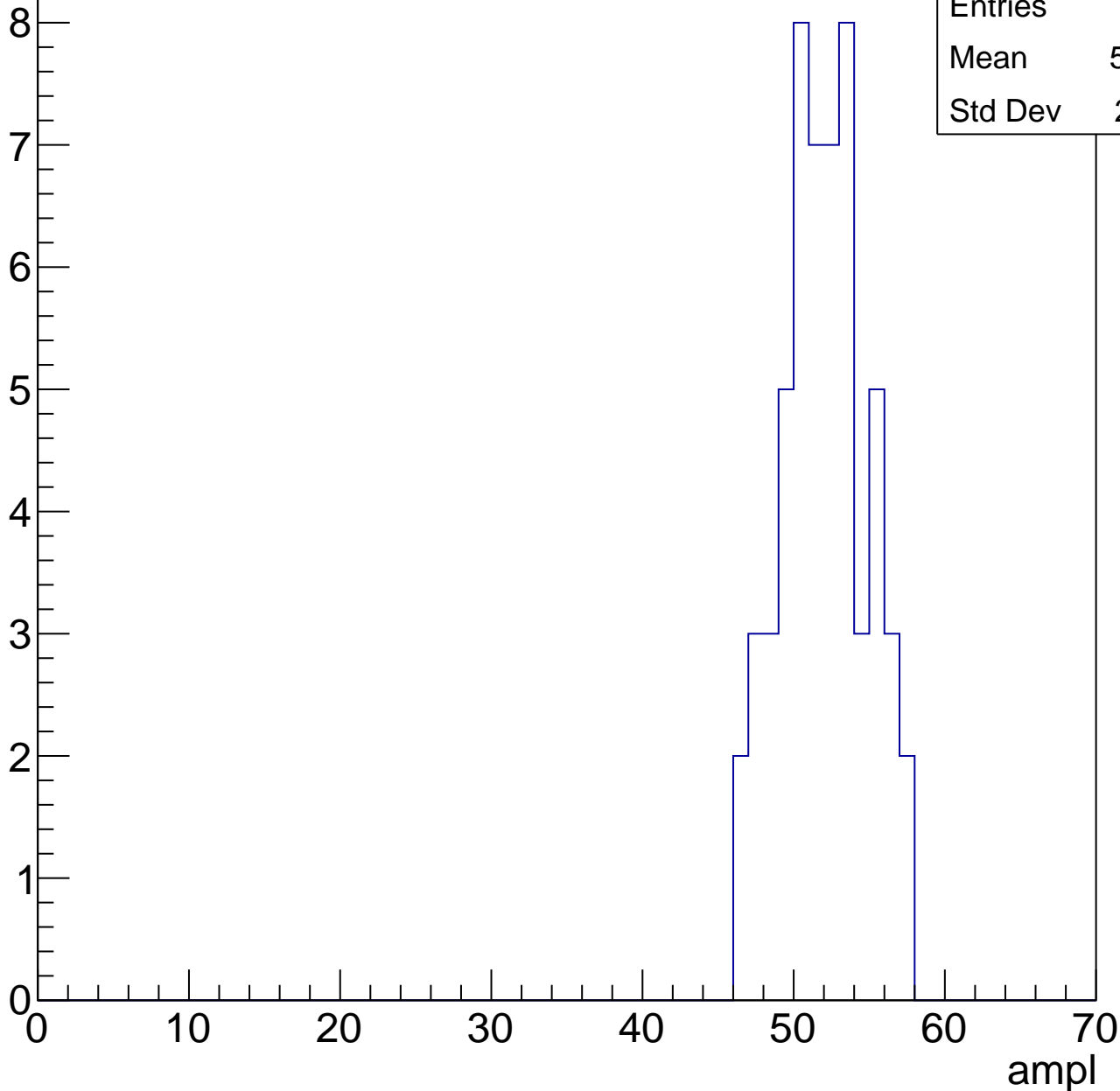


B1L101S, U23-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	51.54
Std Dev	2.771

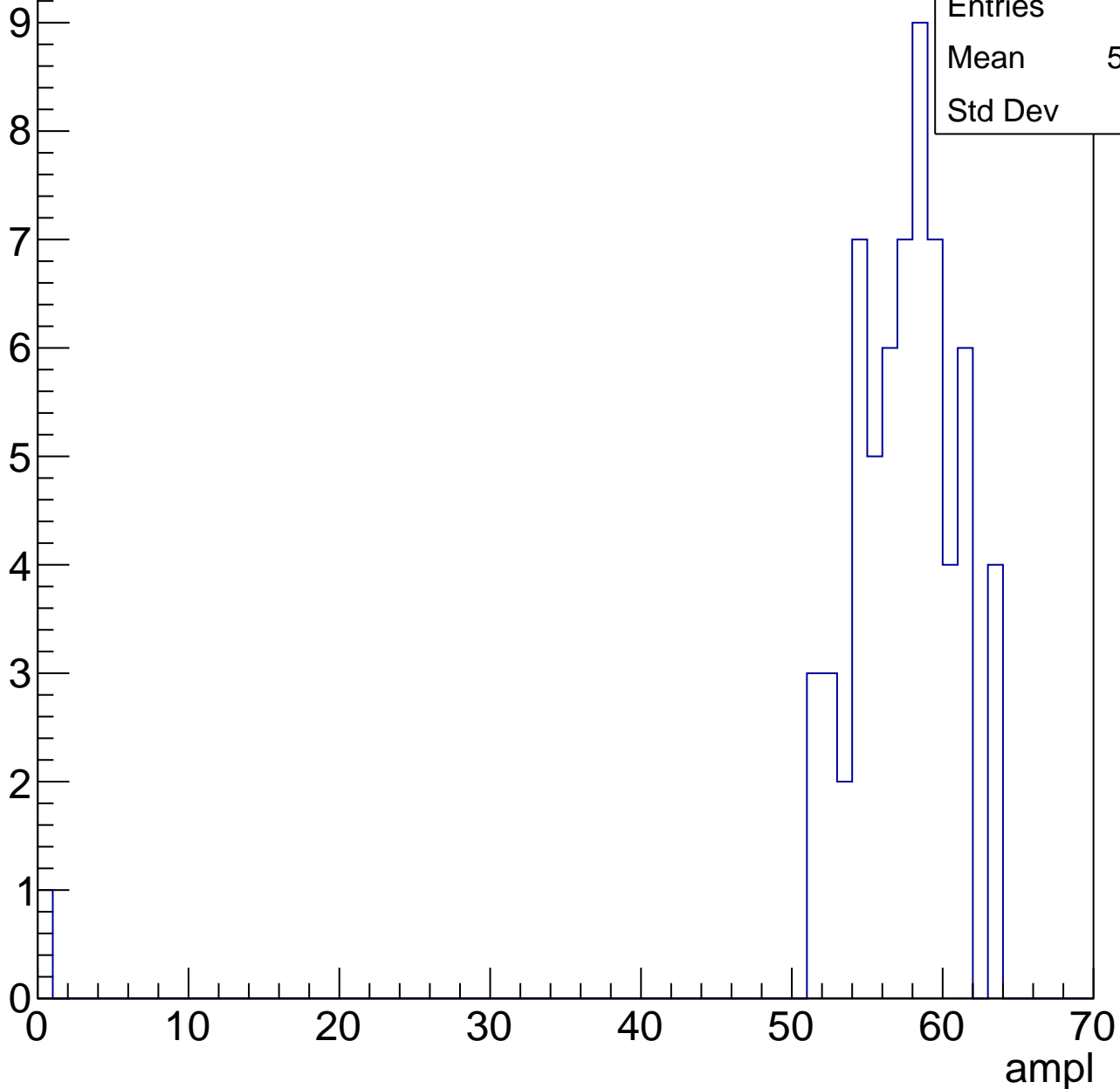


B1L101S, U23-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	56.19
Std Dev	7.73



B1L101S, U23-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	30
Mean	60.87
Std Dev	1.821

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L101S, U23-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch18, adc0

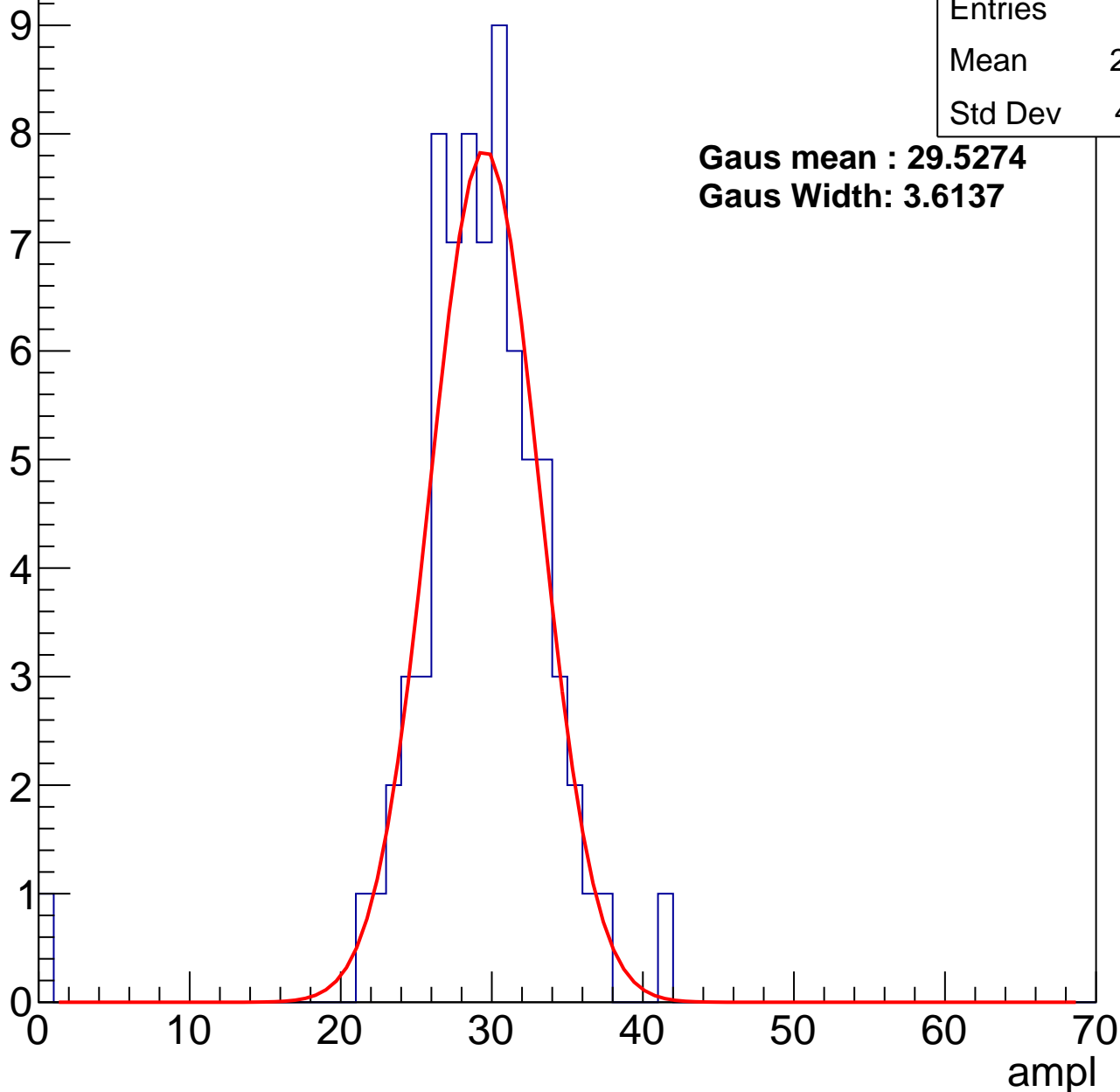
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.74
Std Dev	4.951

Gaus mean : 29.5274

Gaus Width: 3.6137



B1L101S, U23-ch18, adc1

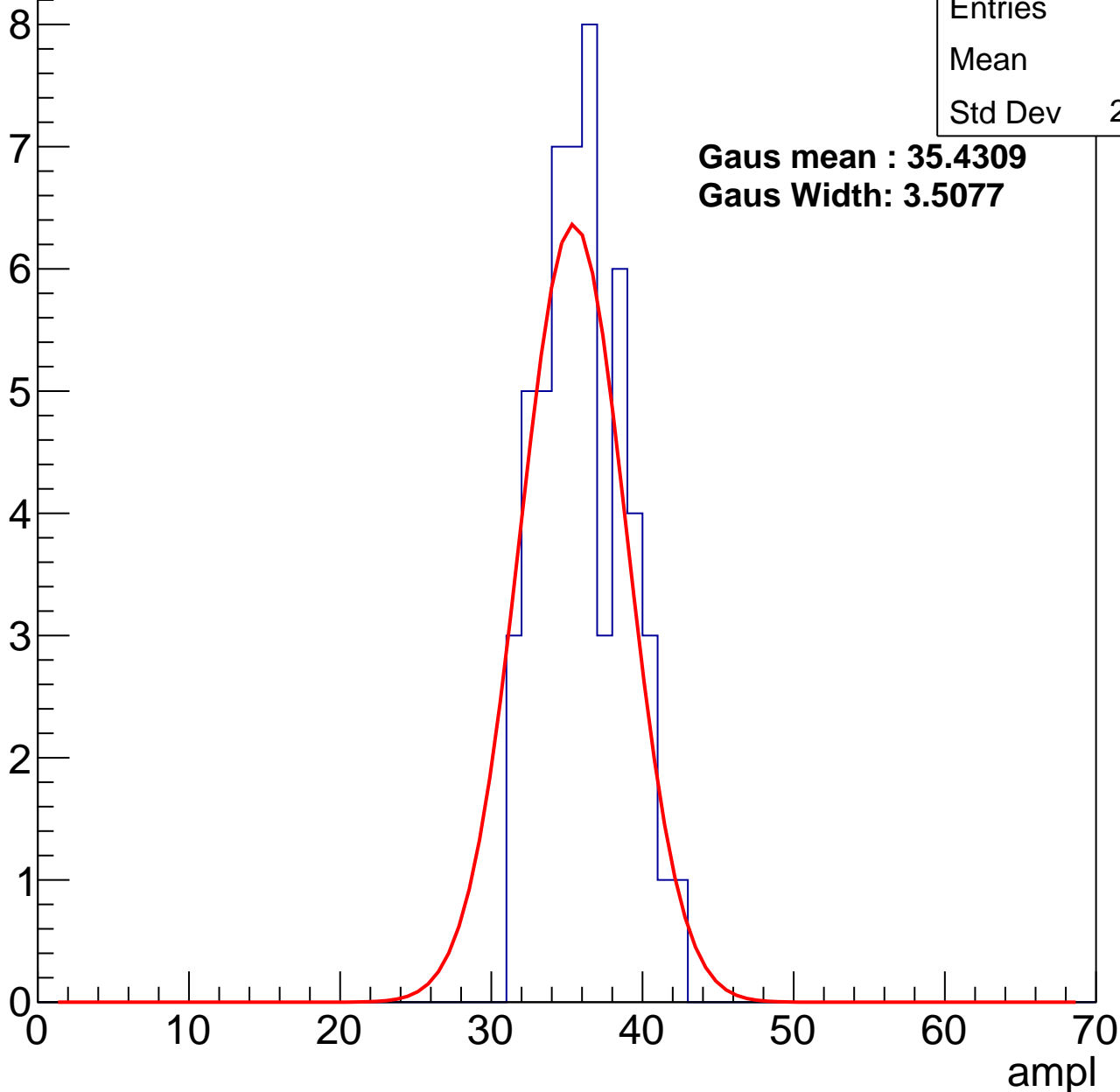
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	35.6
Std Dev	2.743

Gaus mean : 35.4309

Gaus Width: 3.5077



B1L101S, U23-ch18, adc2

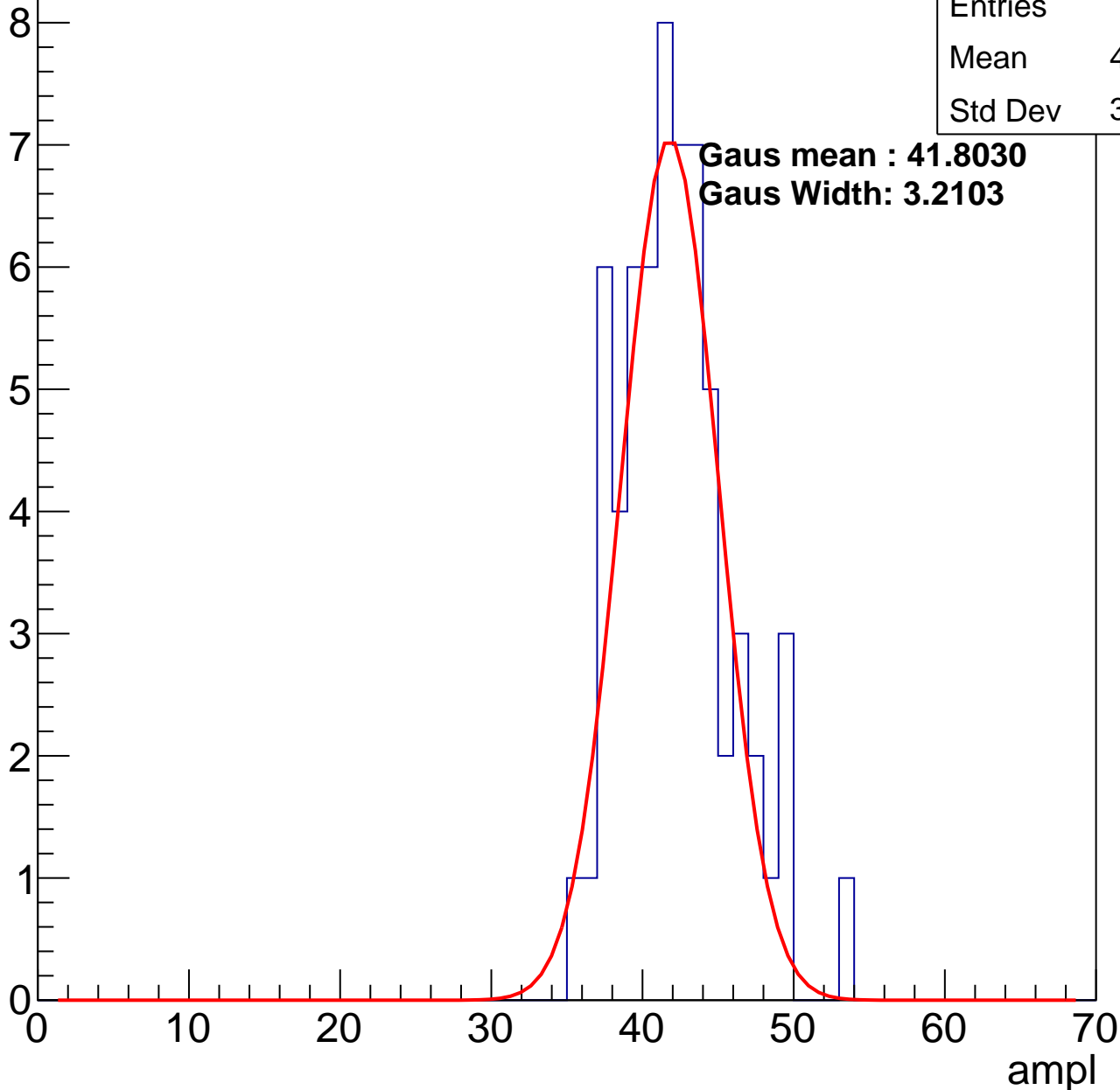
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	41.78
Std Dev	3.627

Gaus mean : 41.8030

Gaus Width: 3.2103

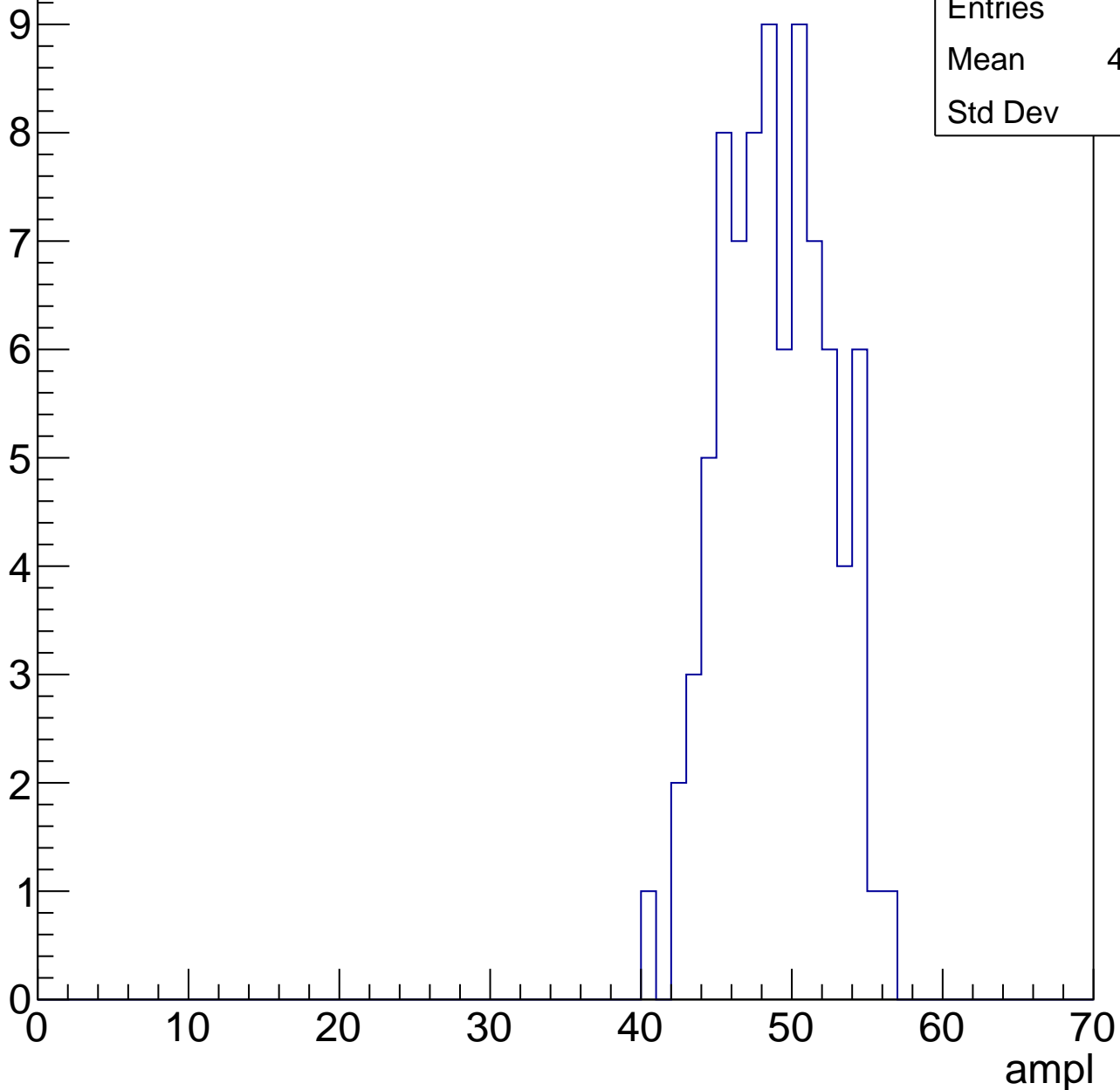


B1L101S, U23-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

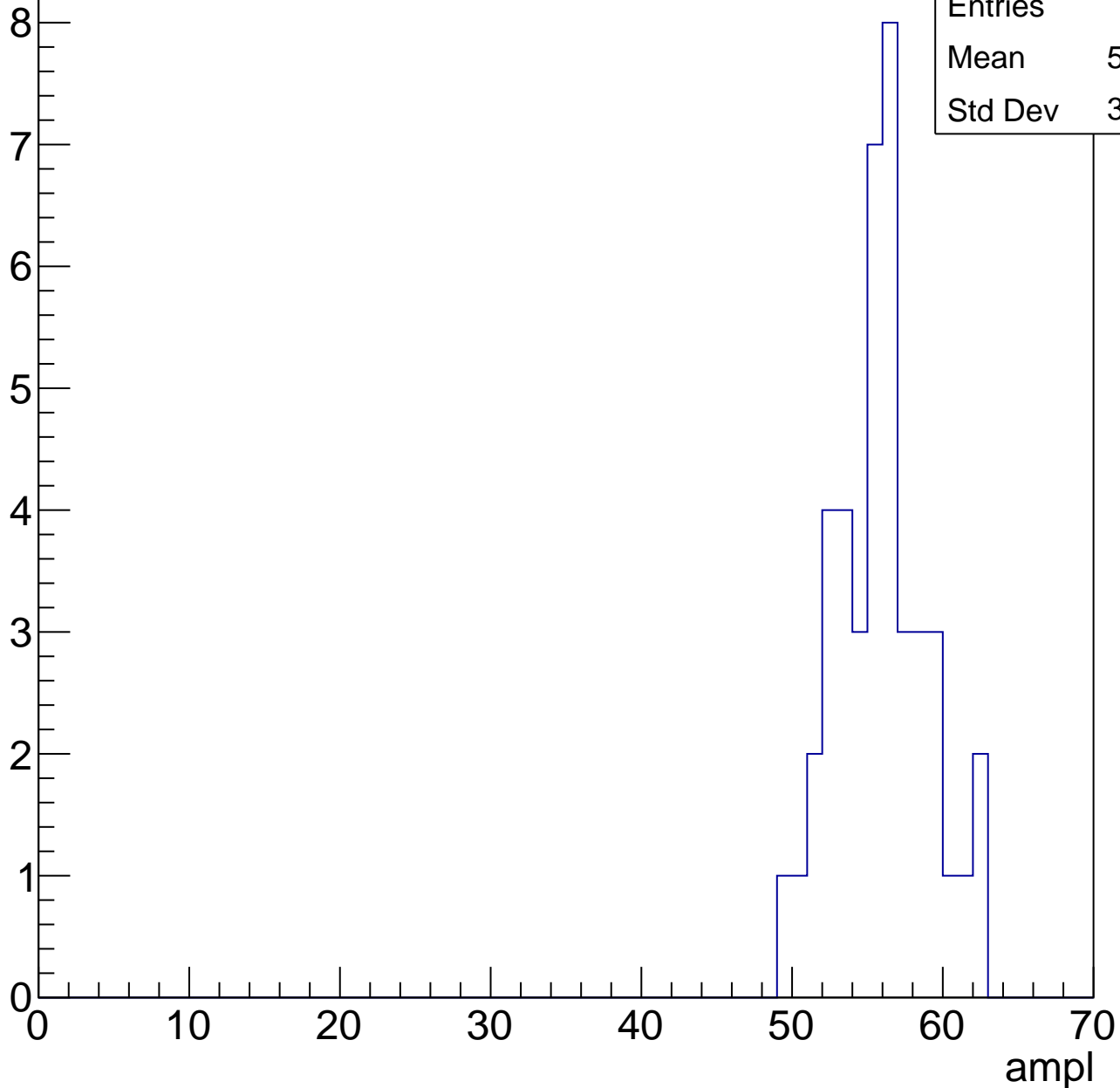
Entries	83
Mean	48.47
Std Dev	3.49



B1L101S, U23-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



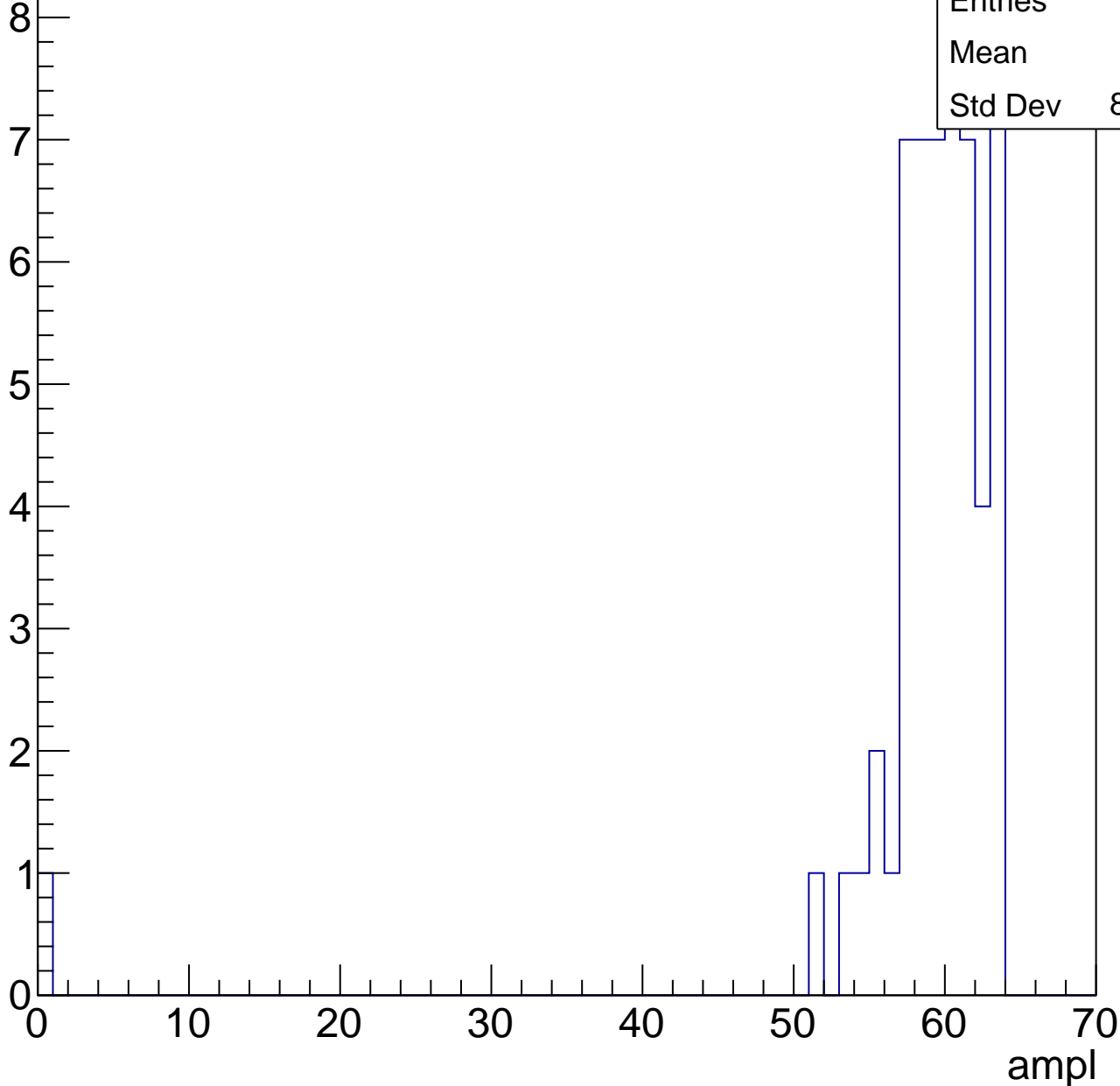
Entries	43
Mean	55.42
Std Dev	3.029

B1L101S, U23-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

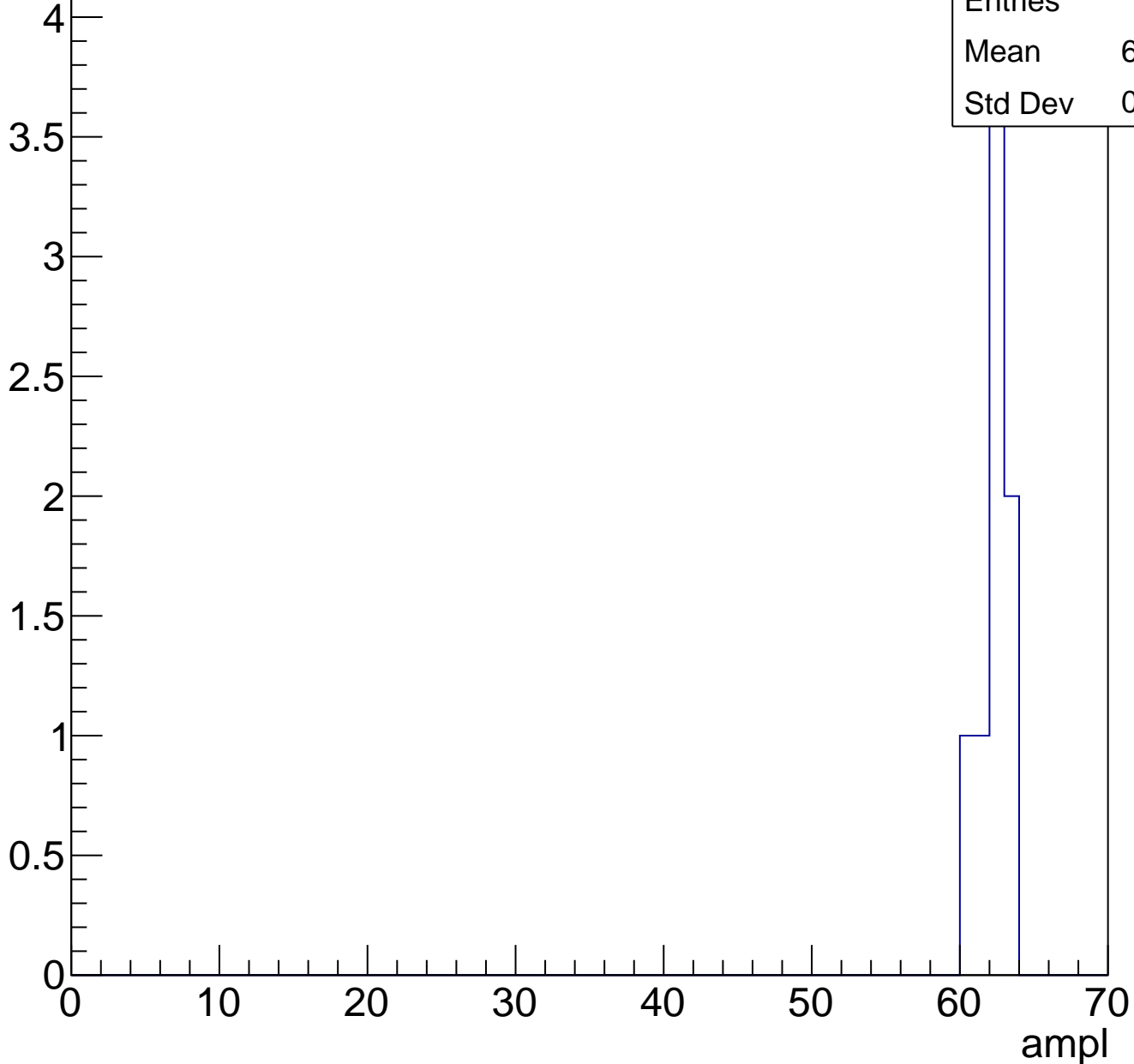
Entries	55
Mean	58.2
Std Dev	8.363



B1L101S, U23-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch19, adc0

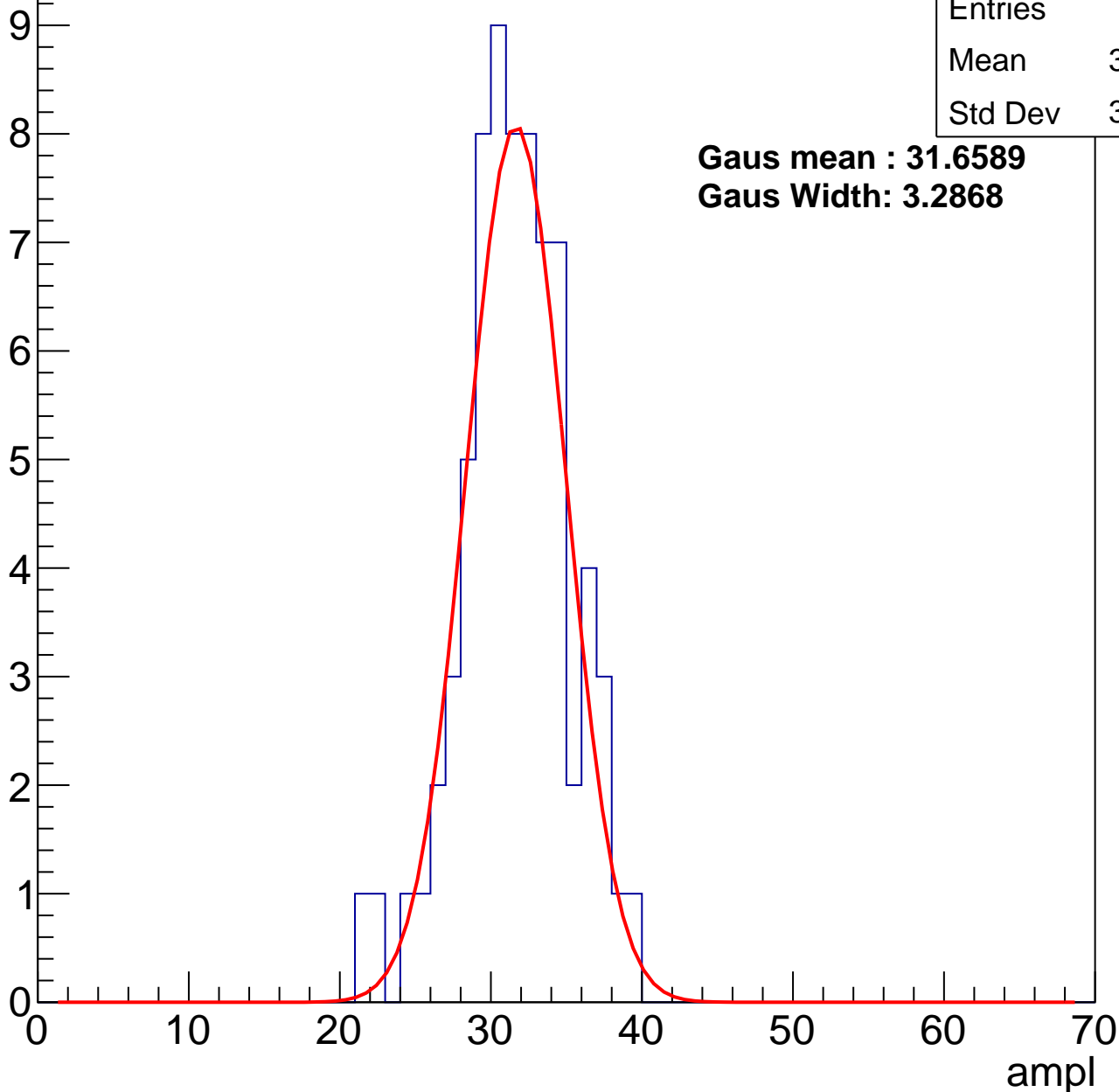
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	31.14
Std Dev	3.537

Gaus mean : 31.6589

Gaus Width: 3.2868



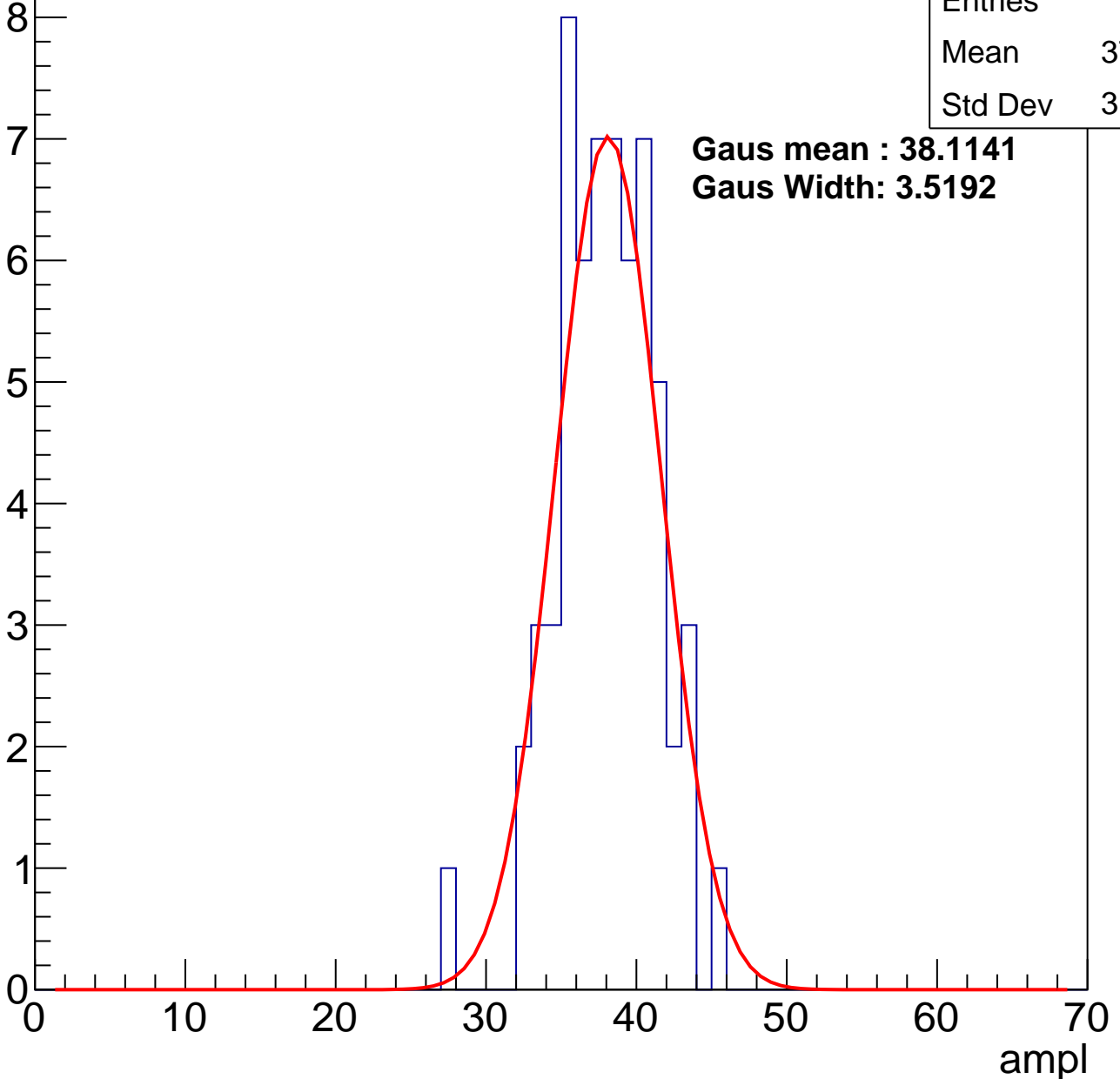
B1L101S, U23-ch19, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.54
Std Dev	3.242

Gaus mean : 38.1141
Gaus Width: 3.5192



B1L101S, U23-ch19, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.46
Std Dev	3.1

Gaus mean : 44.6375

Gaus Width: 2.9636

10

8

6

4

2

0

ampl

0

10

20

30

40

50

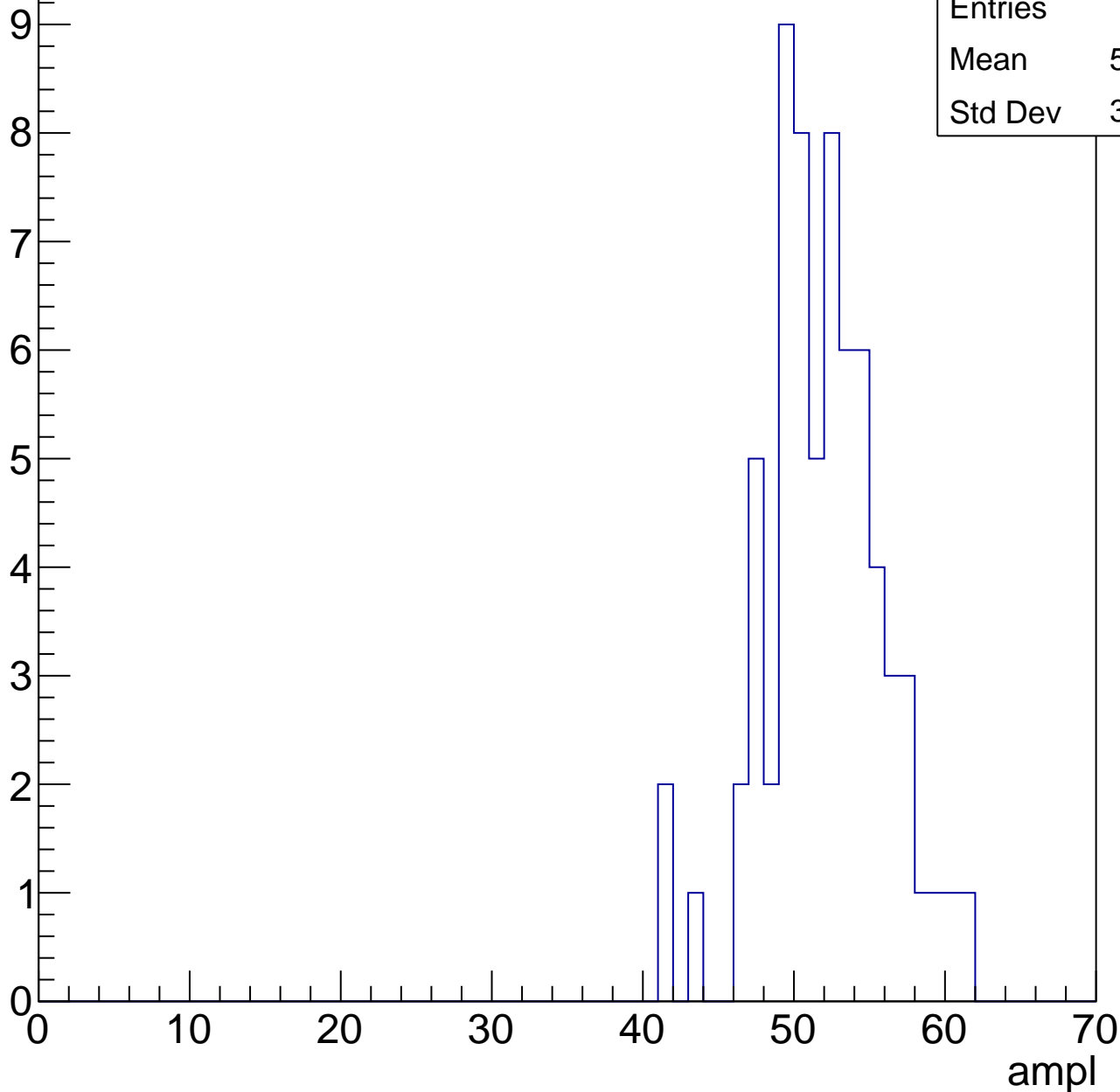
60

70

B1L101S, U23-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

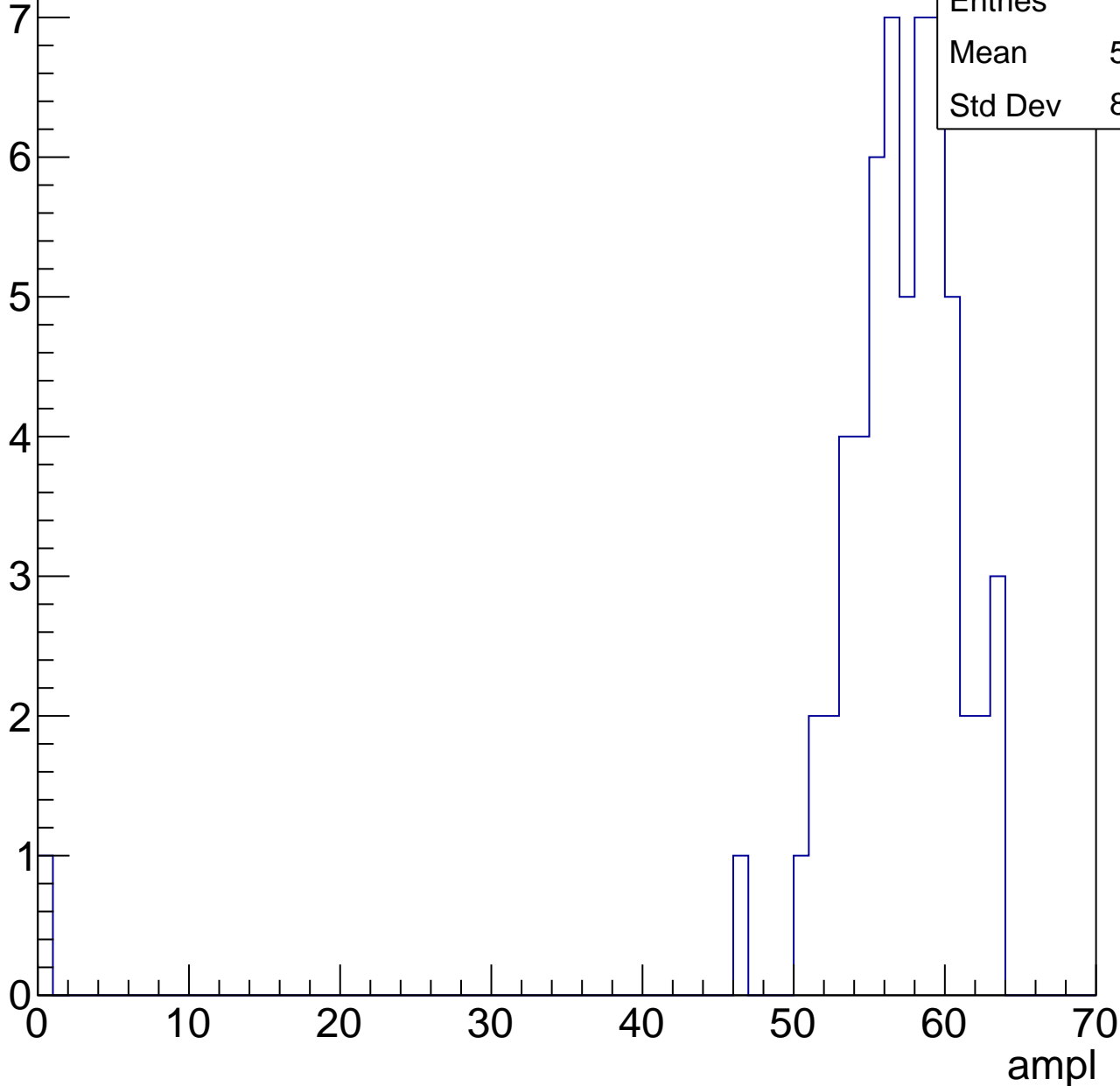


B1L101S, U23-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.78
Std Dev	8.082

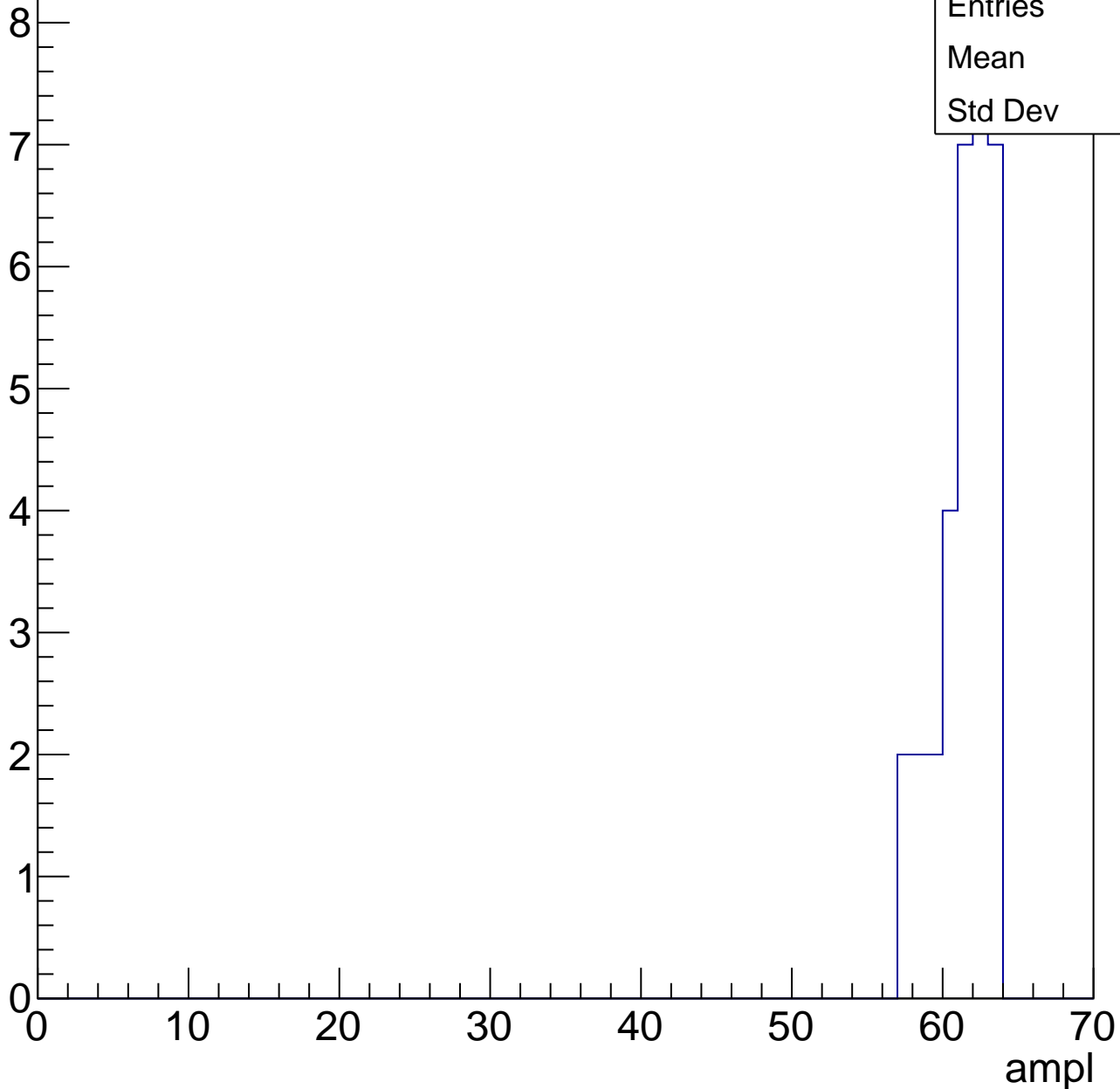


B1L101S, U23-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	61
Std Dev	1.75



B1L101S, U23-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch20, adc0

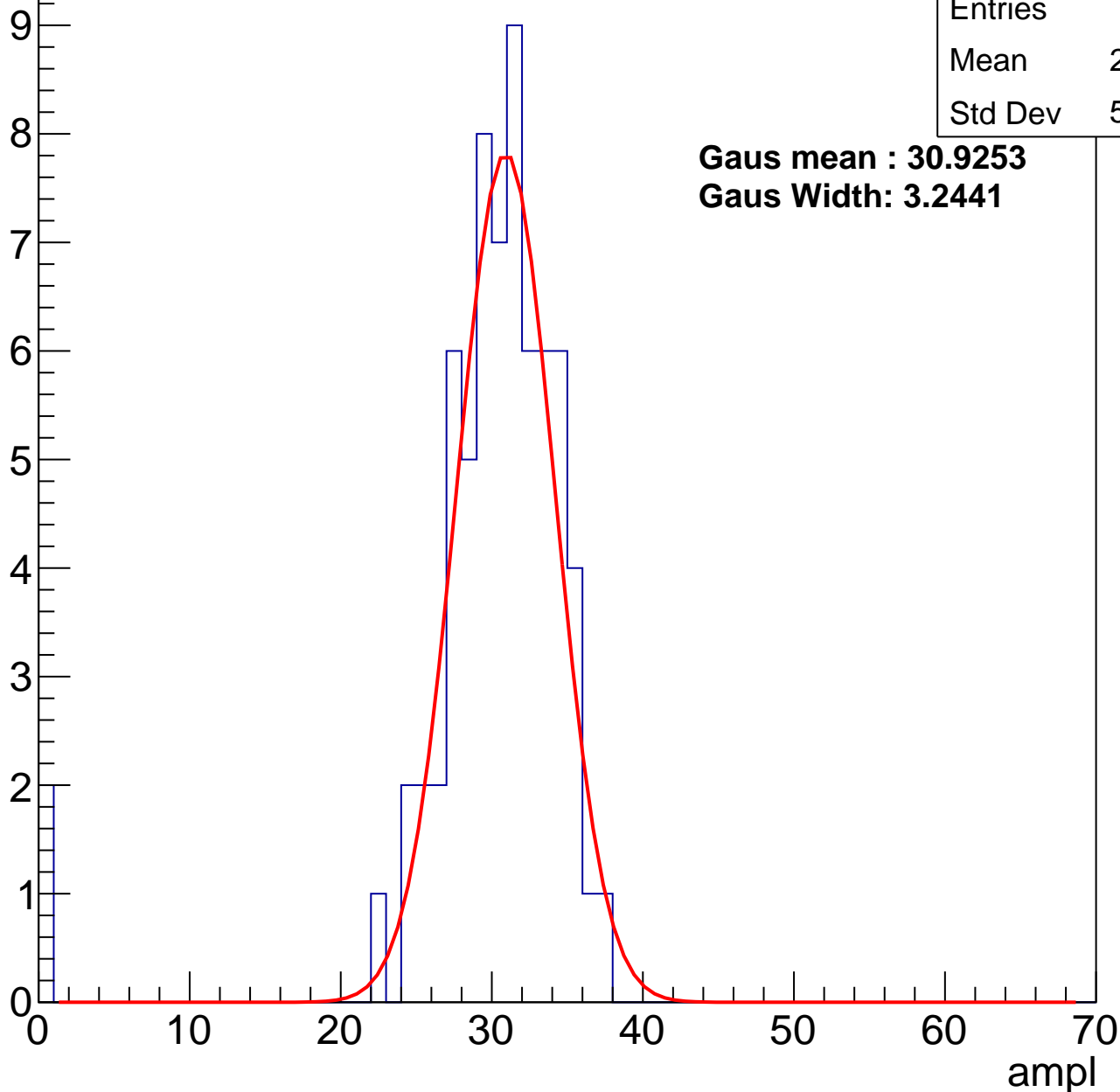
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.44
Std Dev	5.998

Gaus mean : 30.9253

Gaus Width: 3.2441



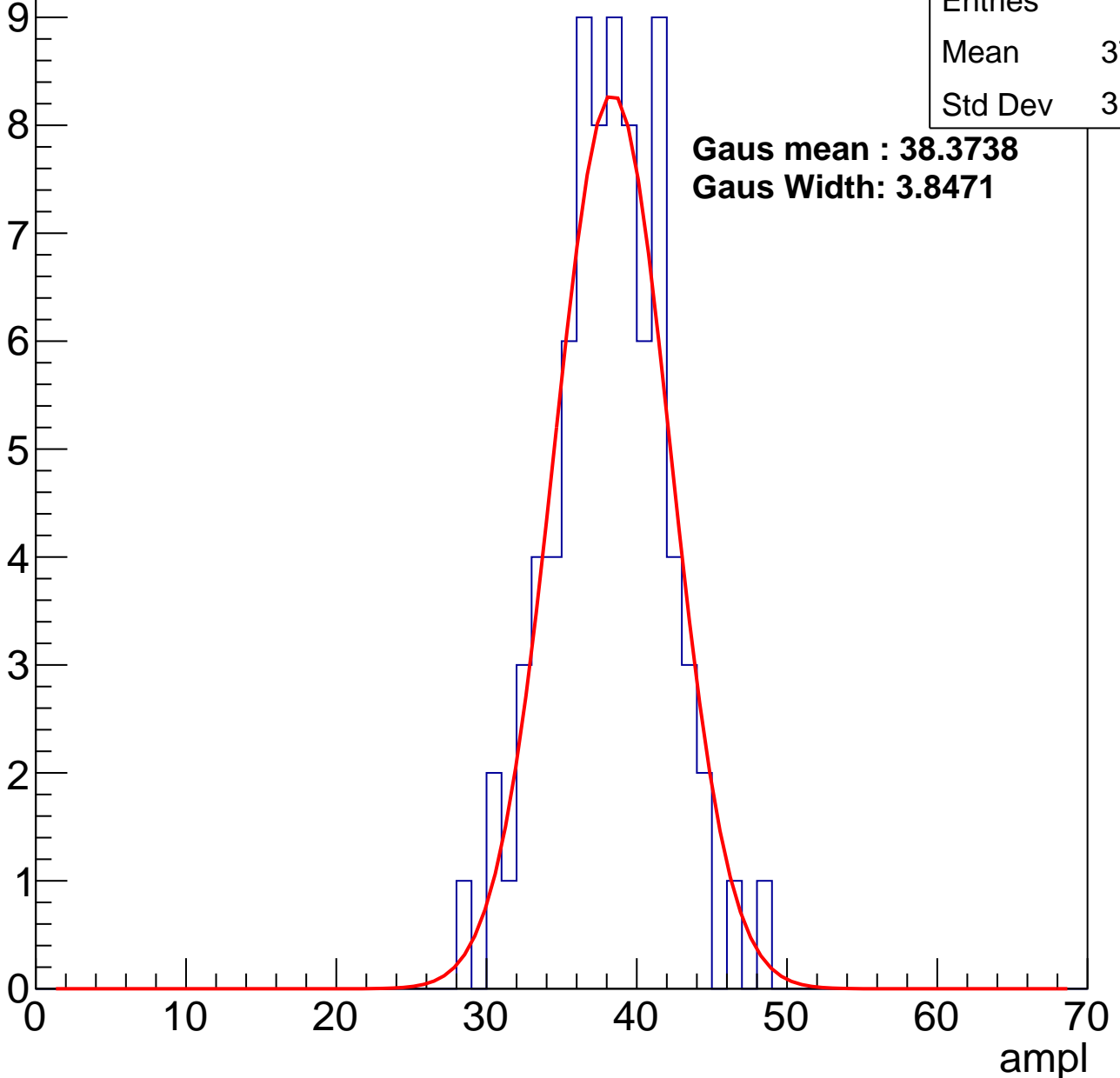
B1L101S, U23-ch20, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.72
Std Dev	3.733

Gaus mean : 38.3738
Gaus Width: 3.8471



B1L101S, U23-ch20, adc2

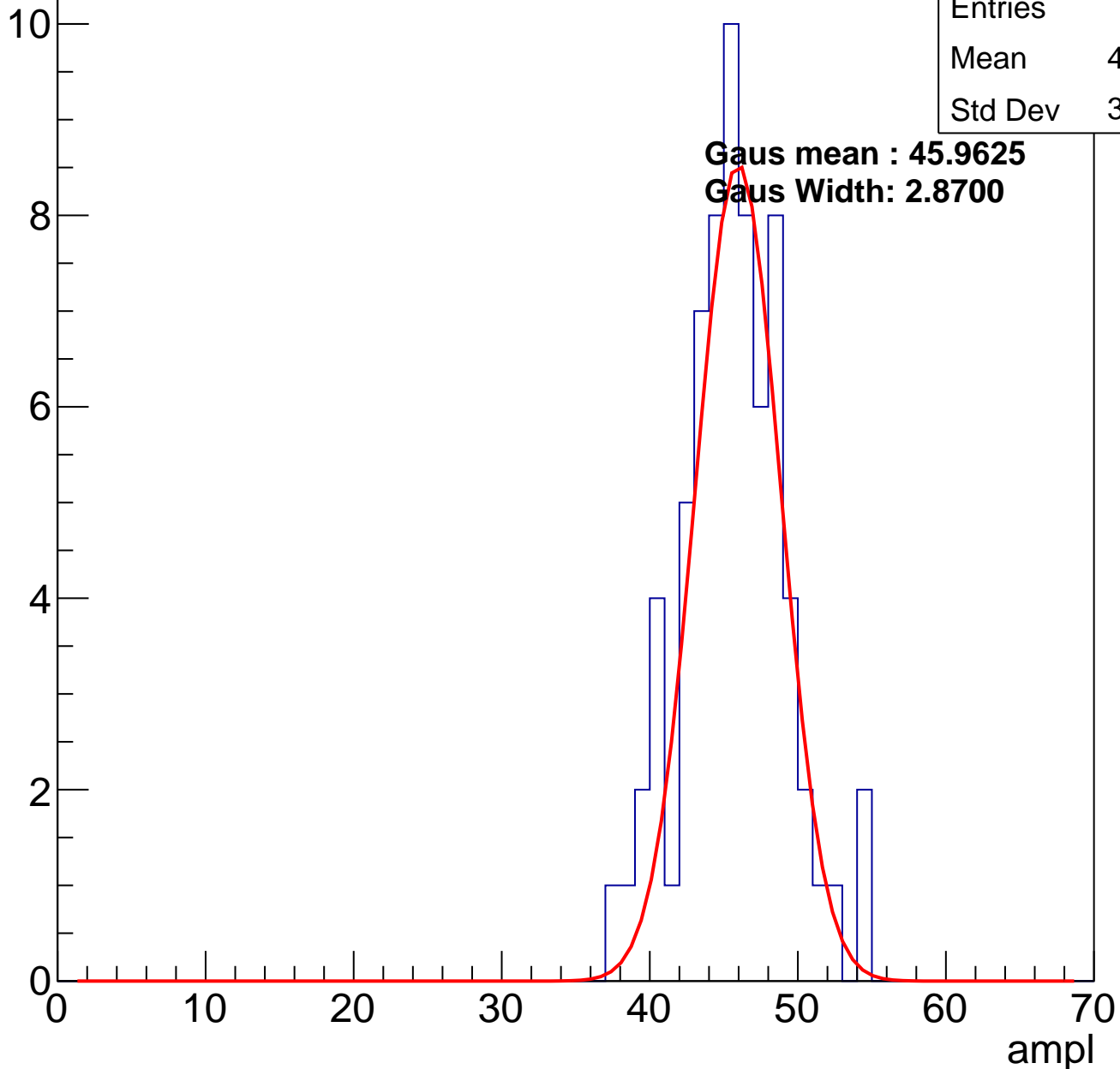
calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	45.18
Std Dev	3.449

Gaus mean : 45.9625

Gaus Width: 2.8700

Entry

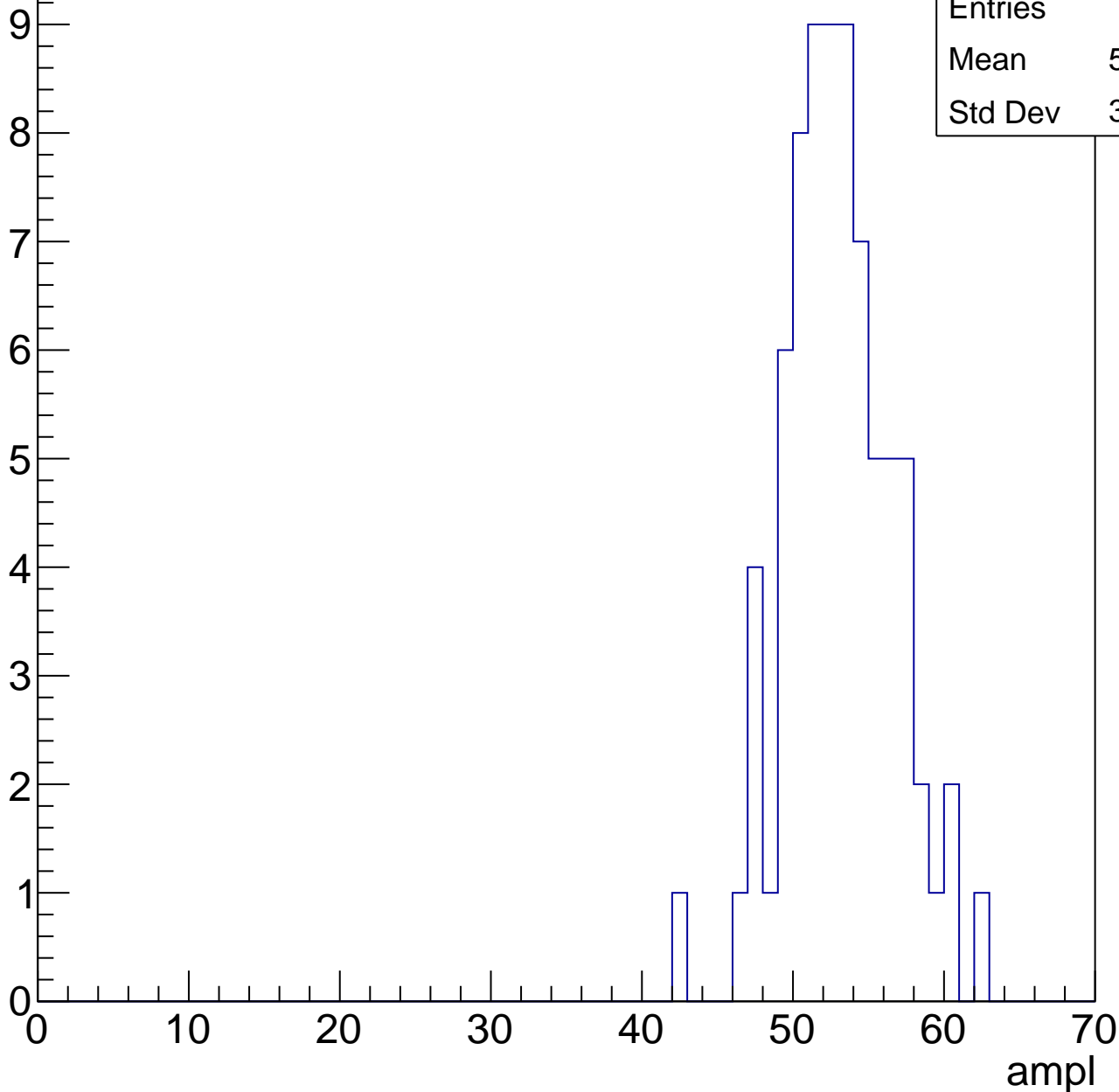


B1L101S, U23-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	52.59
Std Dev	3.547

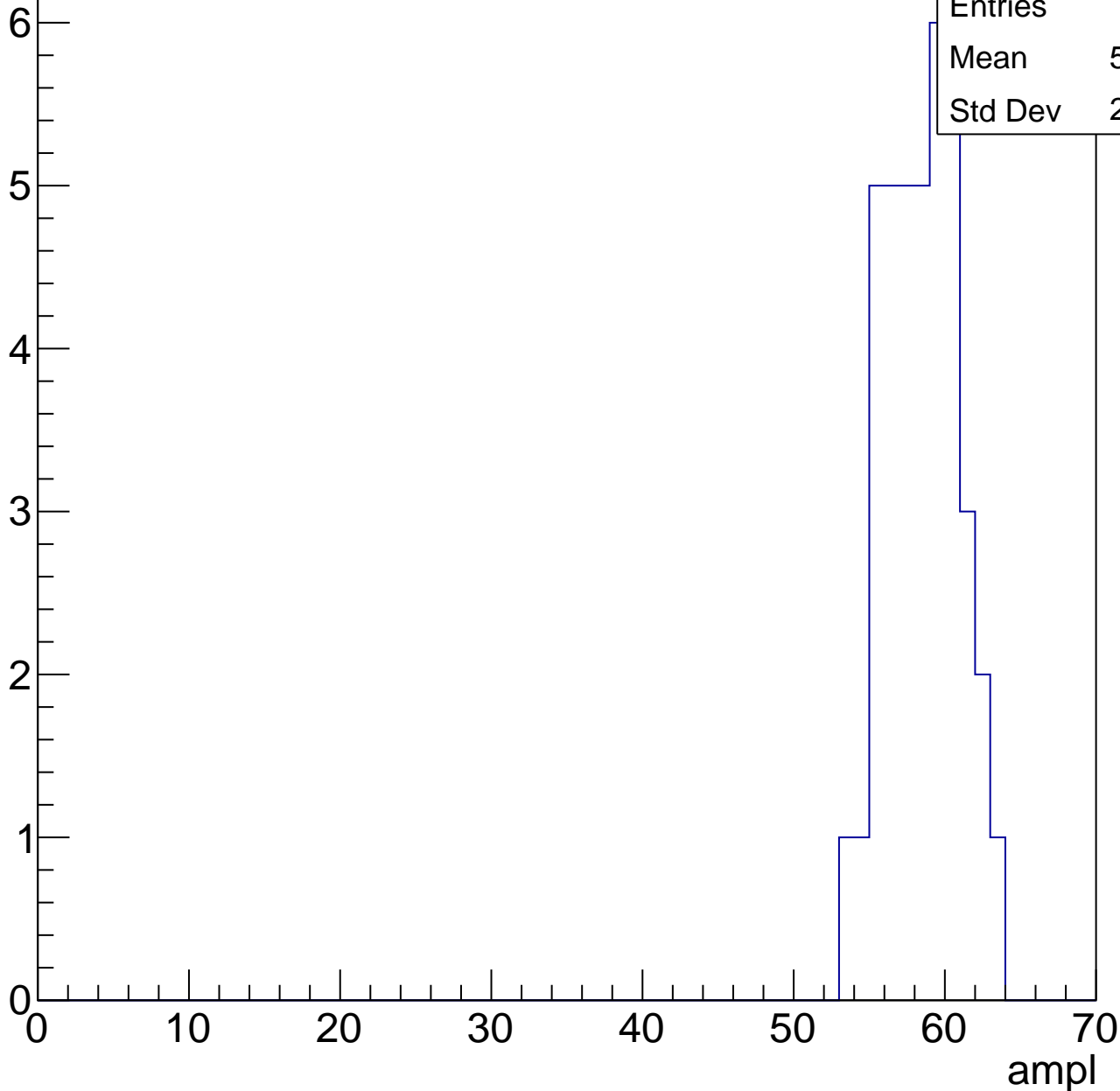


B1L101S, U23-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.02
Std Dev	2.372

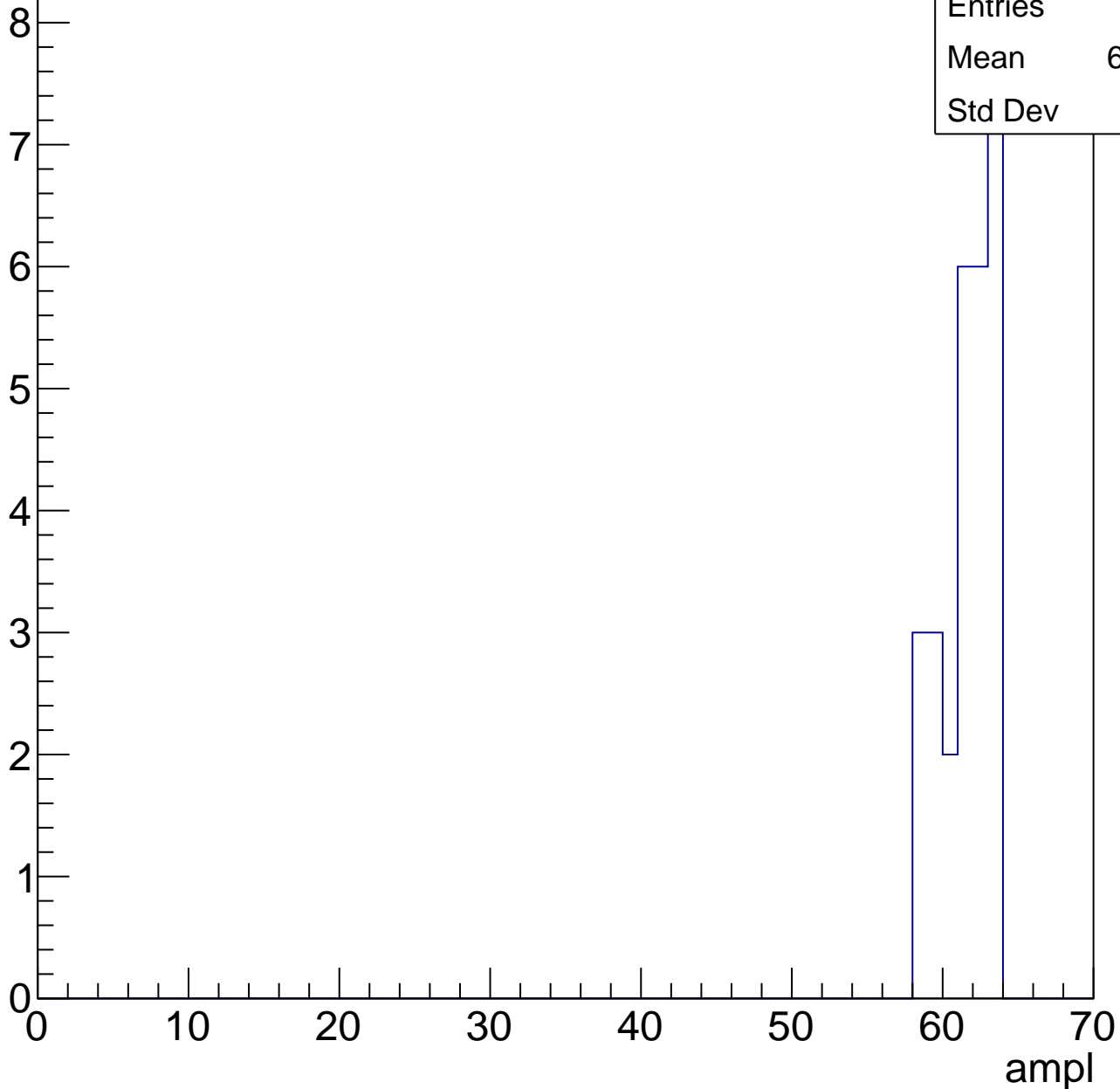


B1L101S, U23-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	28
Mean	61.18
Std Dev	1.67



B1L101S, U23-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch21, adc0

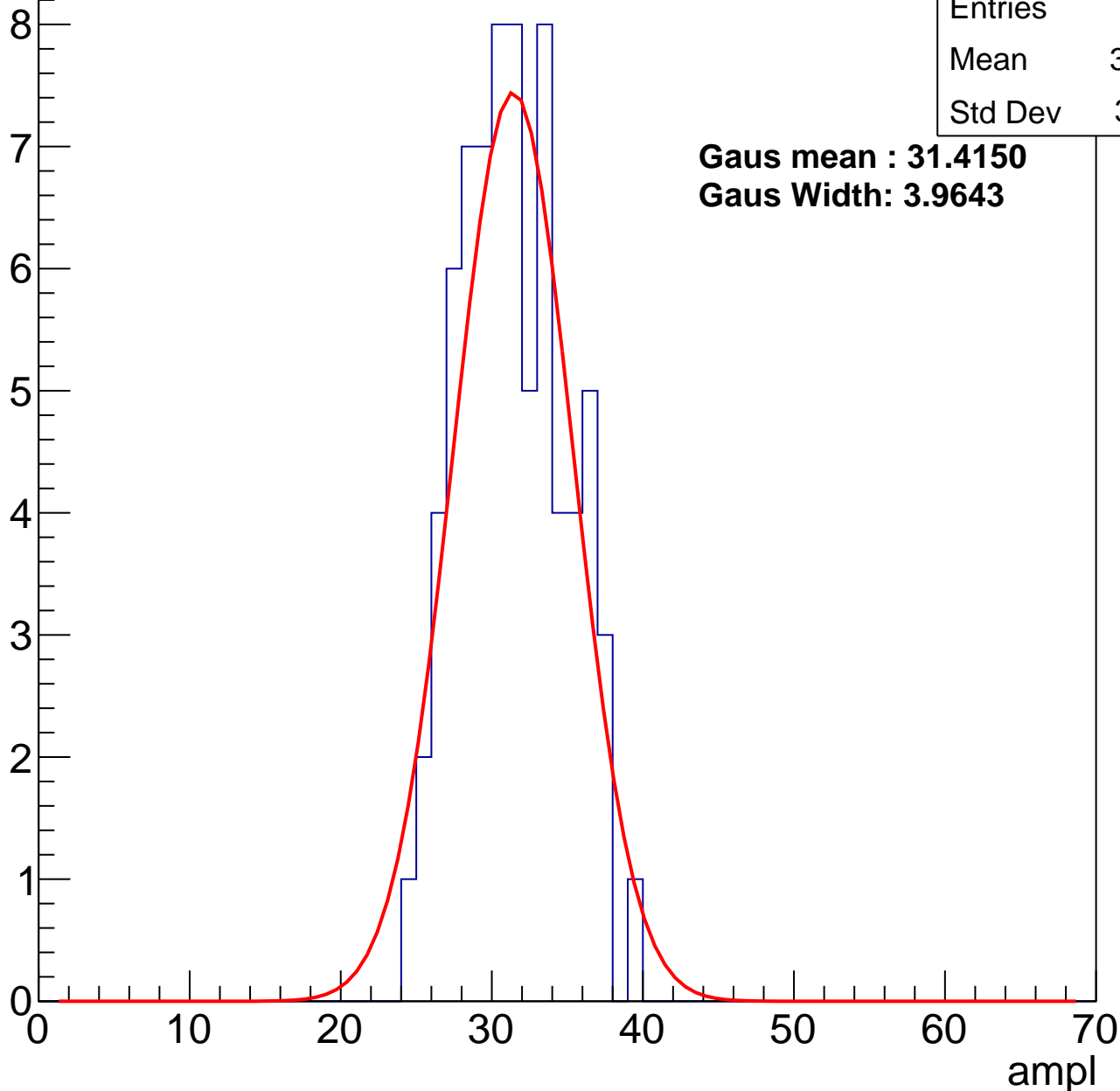
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.92
Std Dev	3.411

Gaus mean : 31.4150

Gaus Width: 3.9643



B1L101S, U23-ch21, adc1

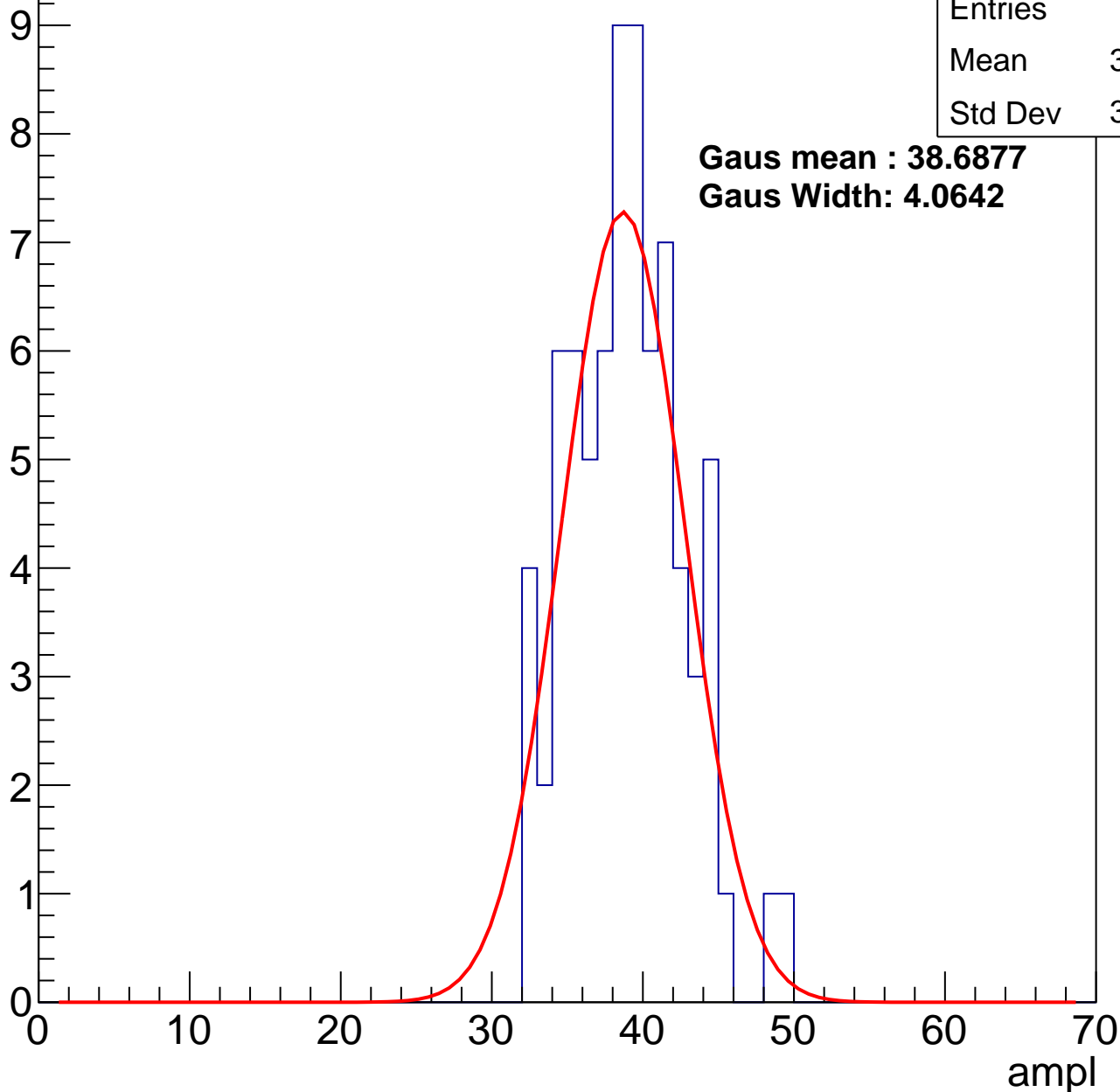
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	38.52
Std Dev	3.714

Gaus mean : 38.6877

Gaus Width: 4.0642



B1L101S, U23-ch21, adc2

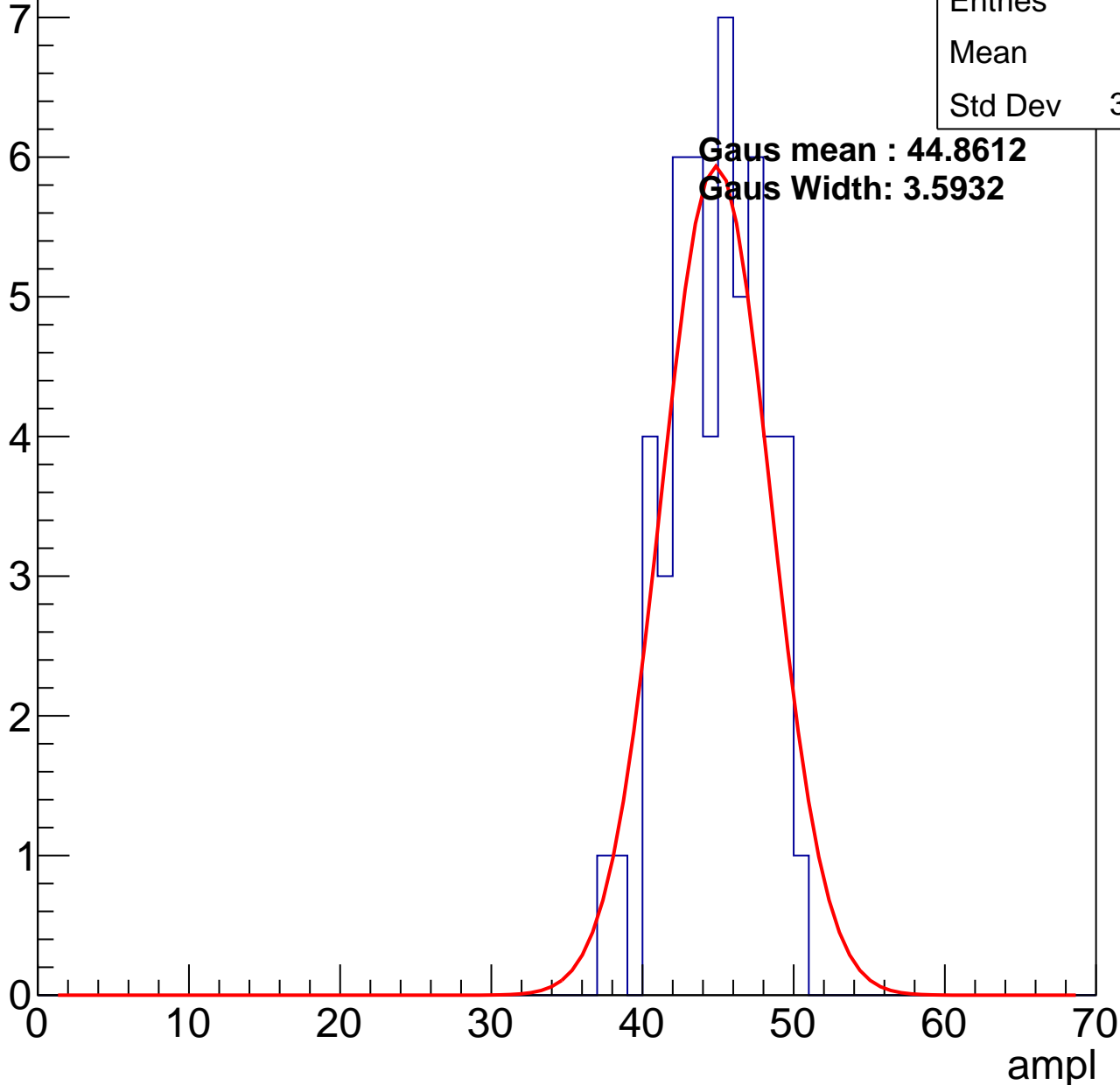
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.4
Std Dev	3.034

Gaus mean : 44.8612

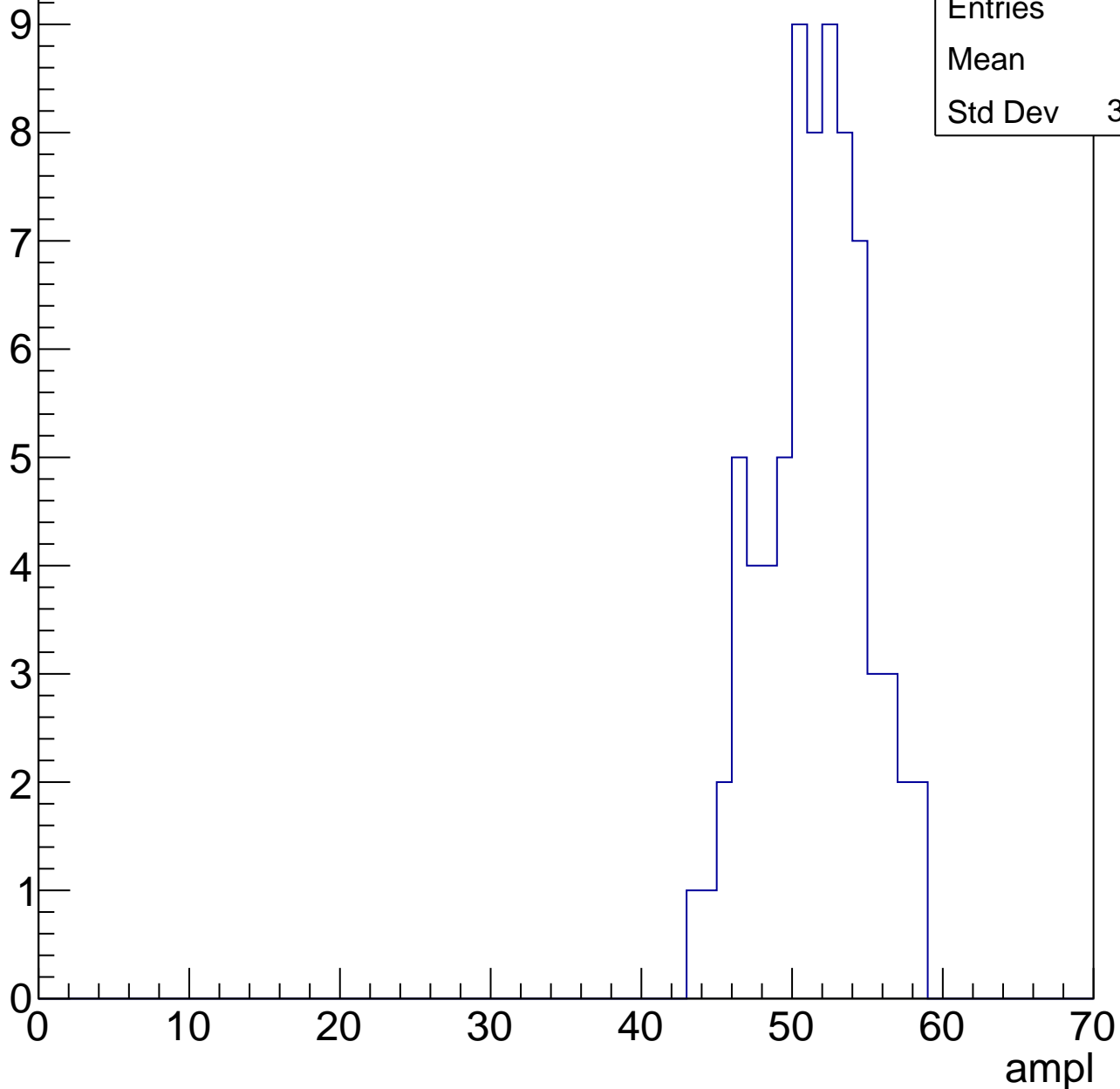
Gaus Width: 3.5932



B1L101S, U23-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

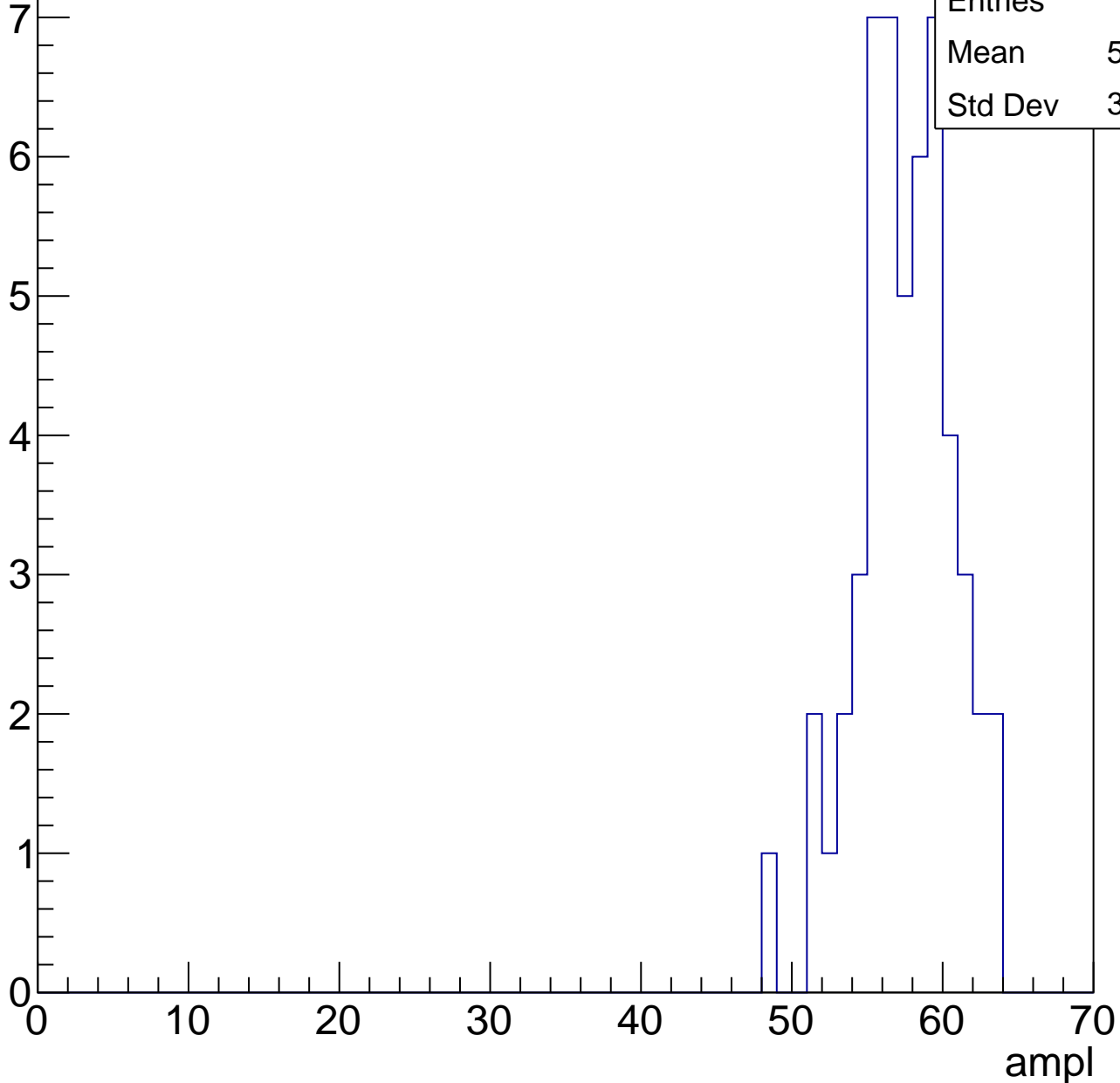


B1L101S, U23-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.04
Std Dev	3.138

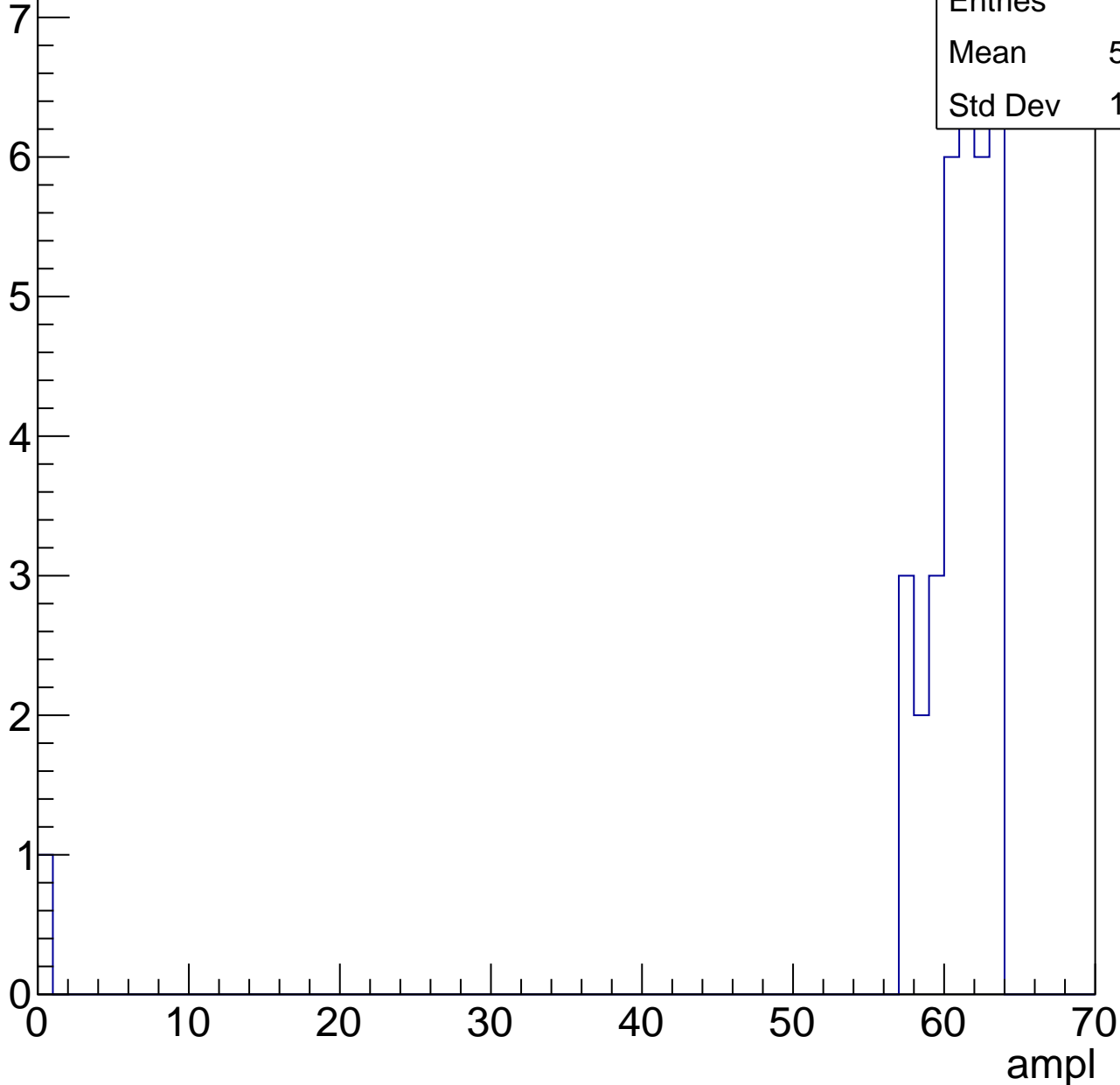


B1L101S, U23-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.97
Std Dev	10.27



B1L101S, U23-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L101S, U23-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L101S, U23-ch22, adc0

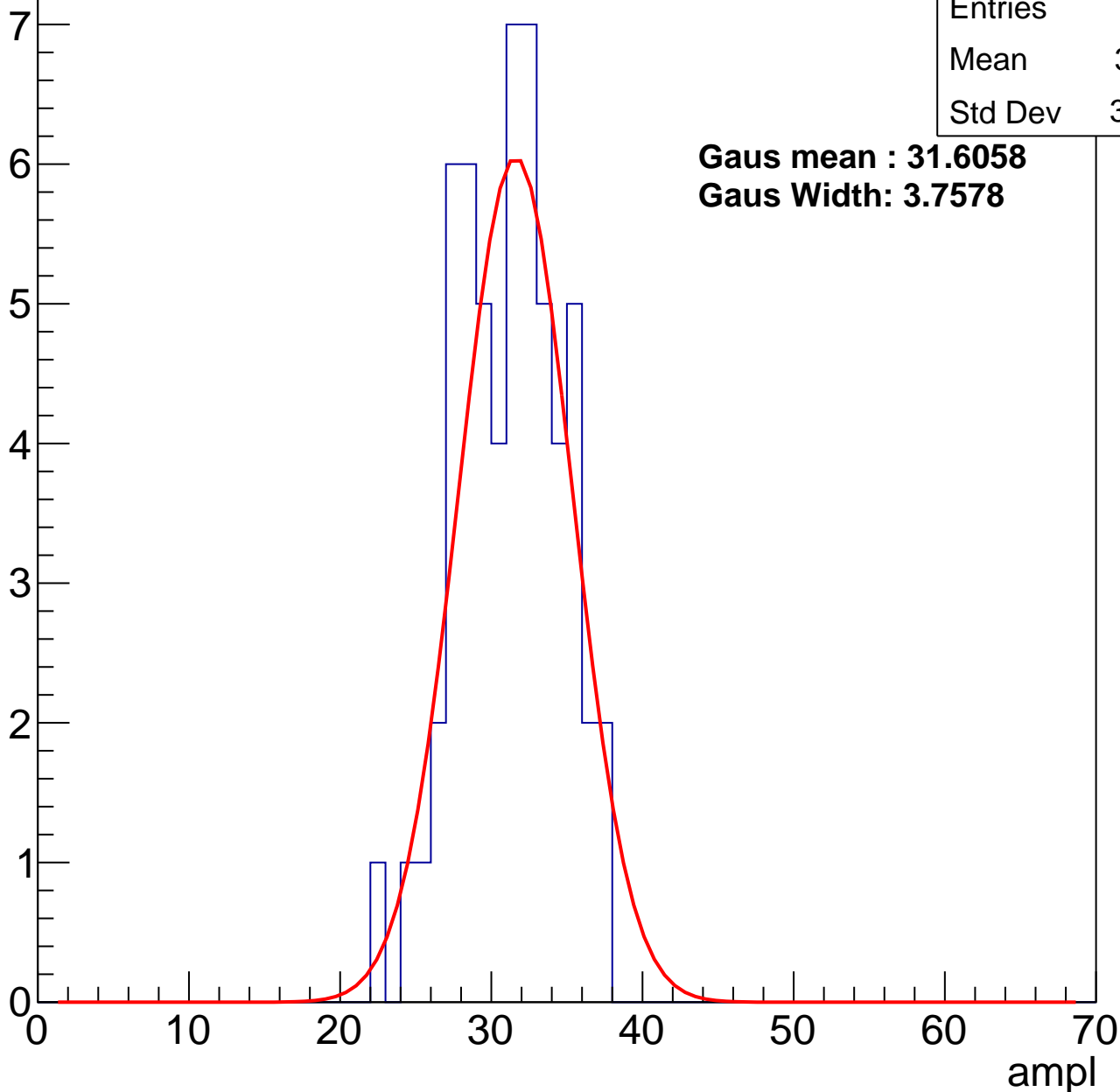
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	30.71
Std Dev	3.348

Gaus mean : 31.6058

Gaus Width: 3.7578



B1L101S, U23-ch22, adc1

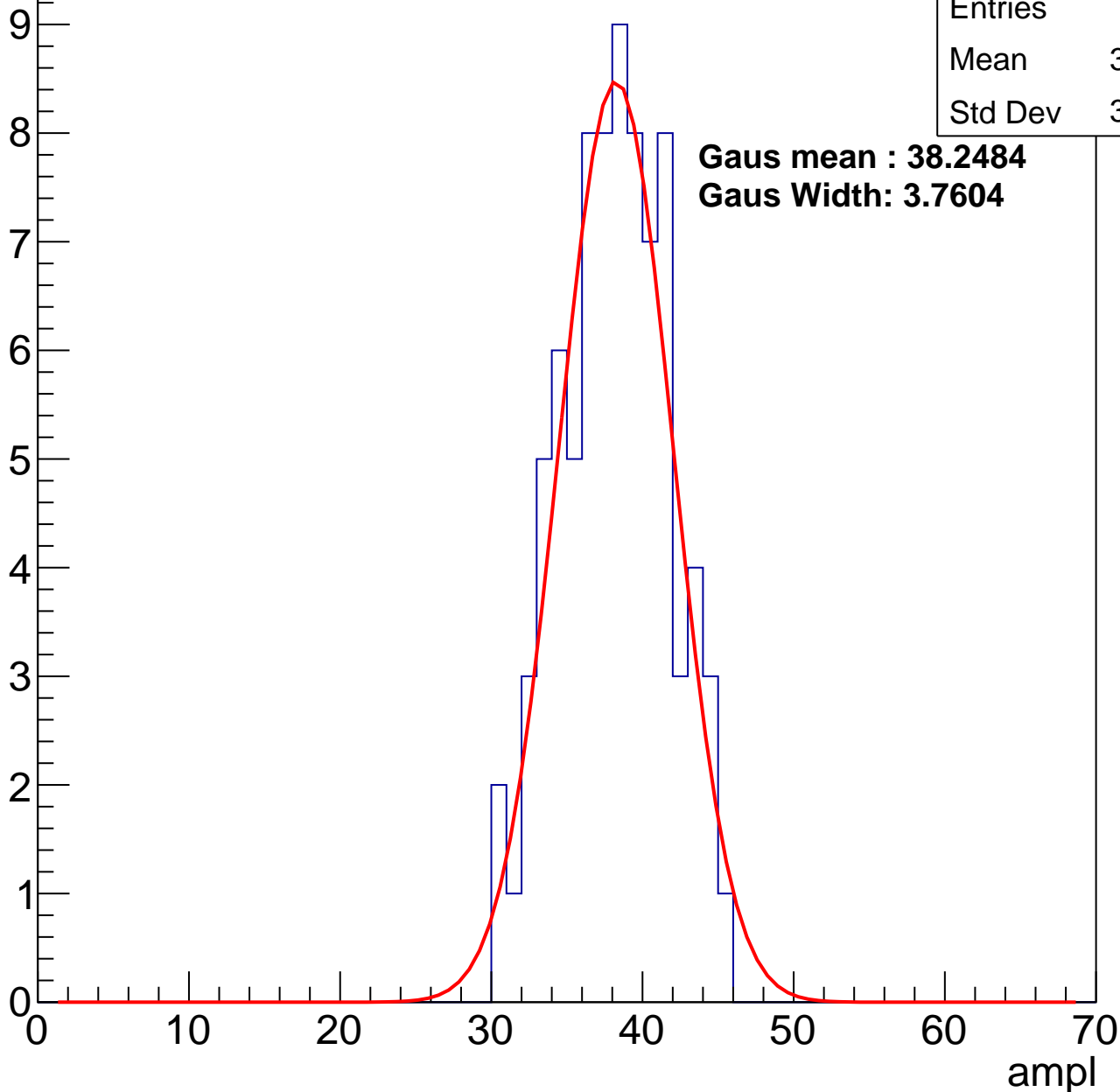
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	37.68
Std Dev	3.506

Gaus mean : 38.2484

Gaus Width: 3.7604



B1L101S, U23-ch22, adc2

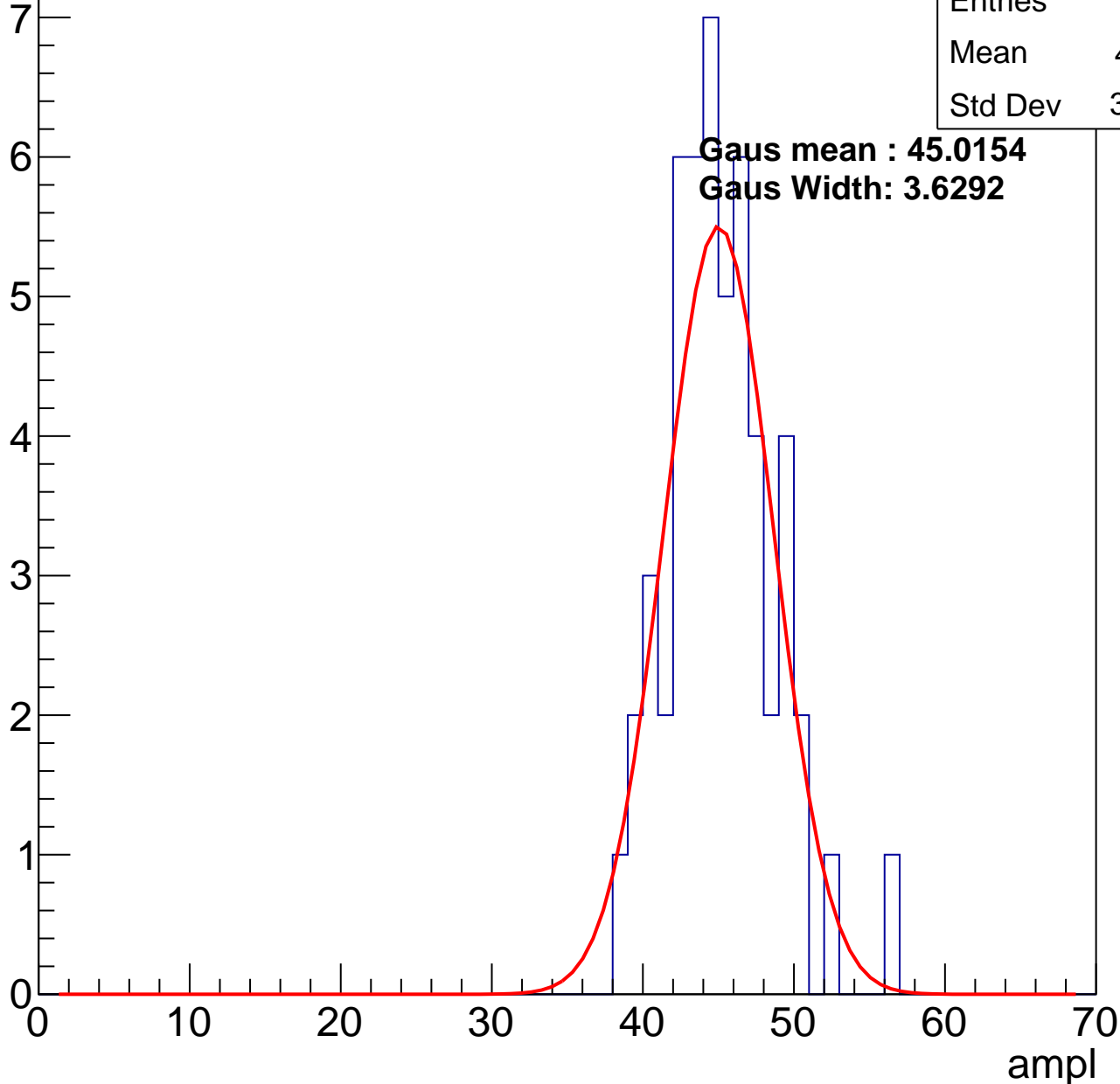
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.71
Std Dev	3.488

Gaus mean : 45.0154

Gaus Width: 3.6292

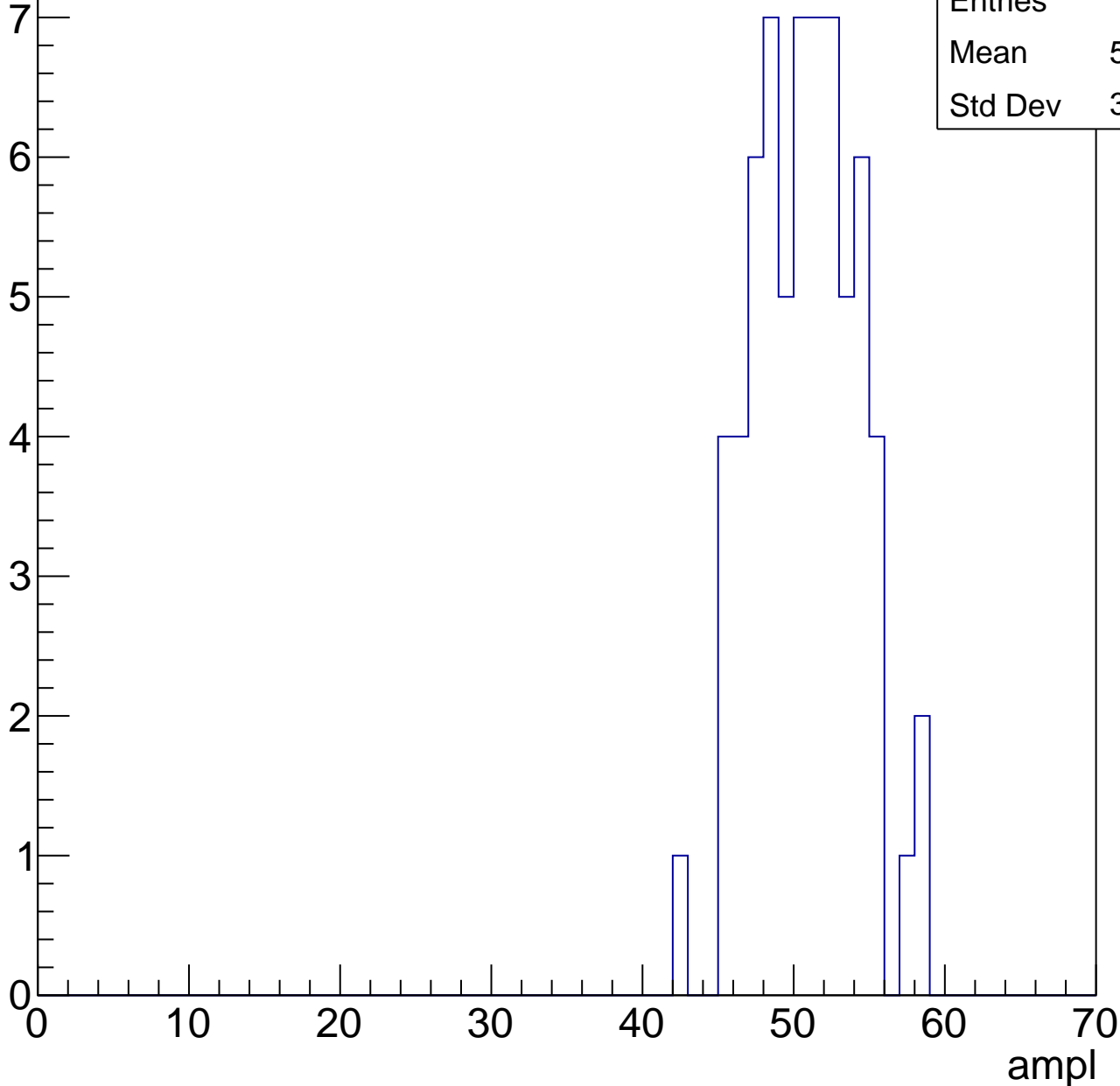


B1L101S, U23-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.33
Std Dev	3.395

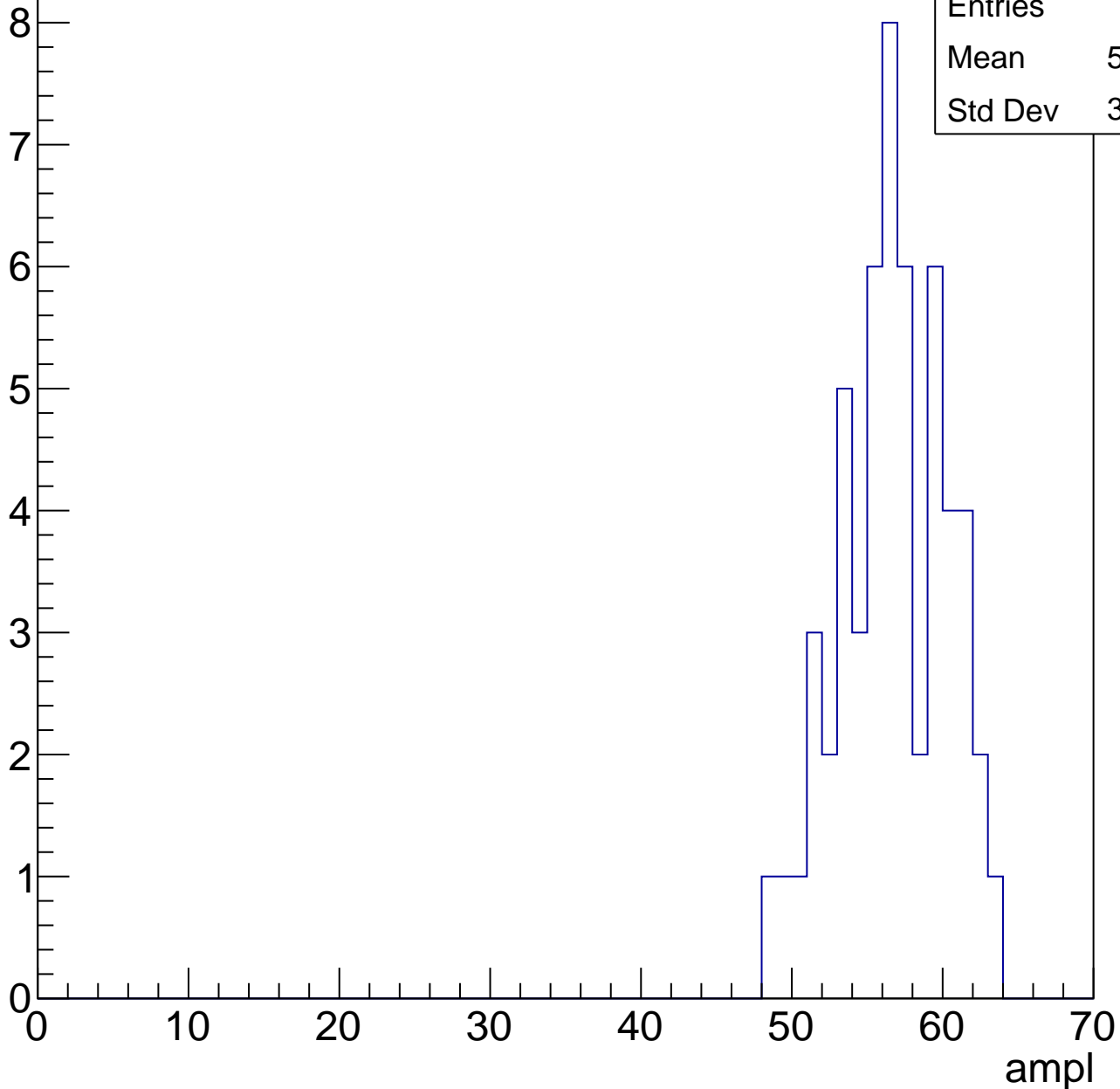


B1L101S, U23-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.22
Std Dev	3.489

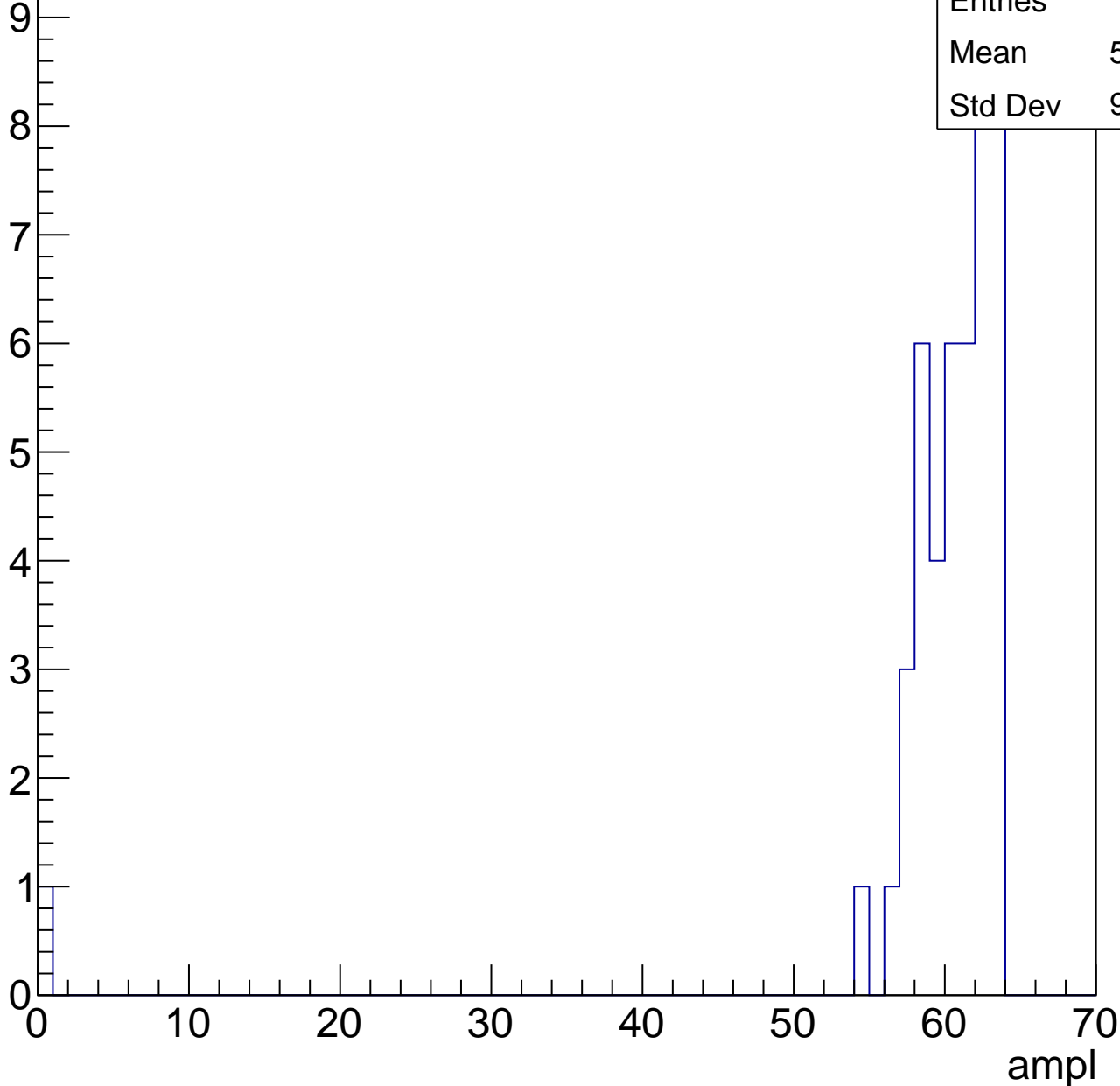


B1L101S, U23-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.98
Std Dev	9.164



B1L101S, U23-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch23, adc0

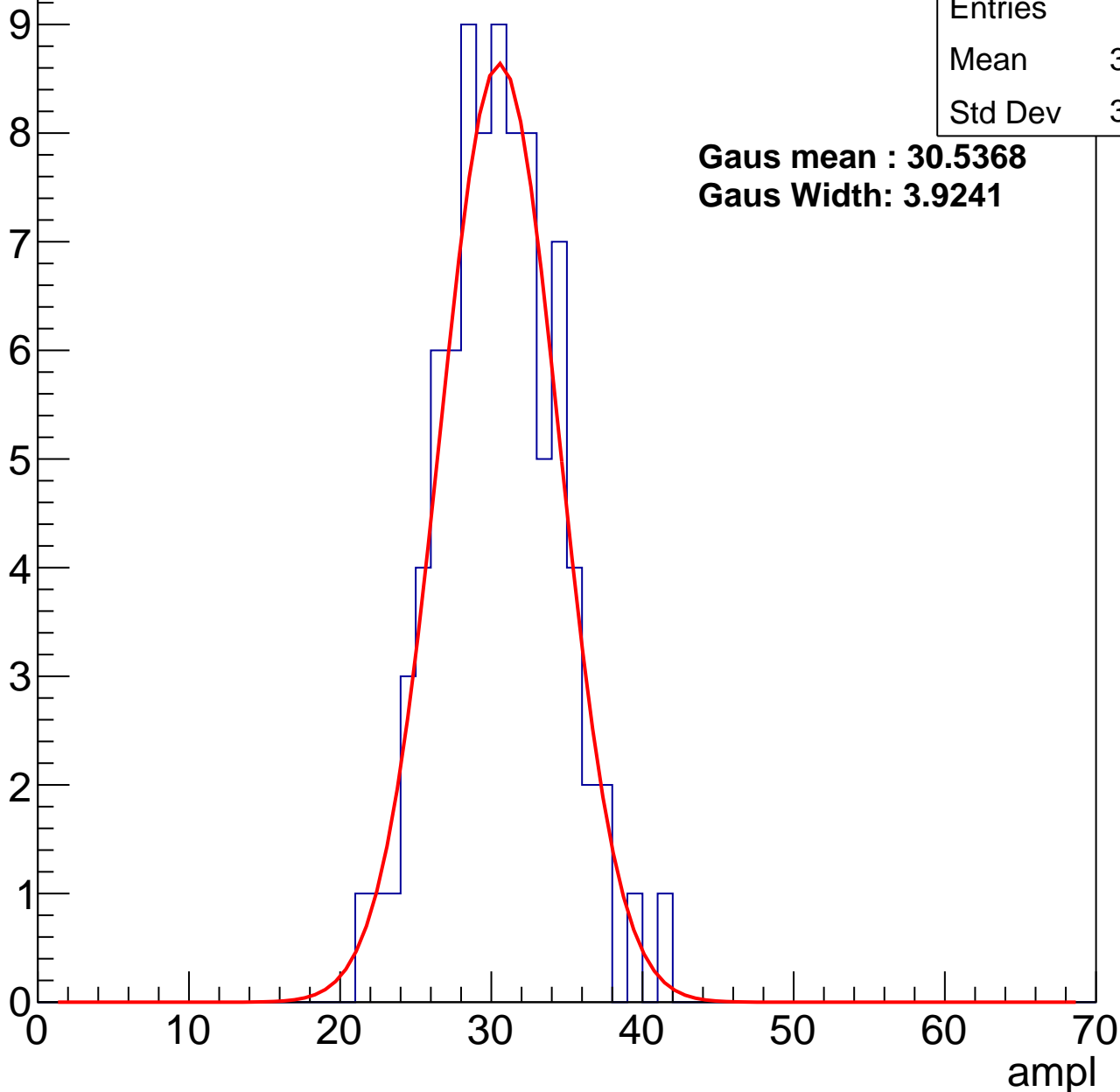
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	30.03
Std Dev	3.835

Gaus mean : 30.5368

Gaus Width: 3.9241



B1L101S, U23-ch23, adc1

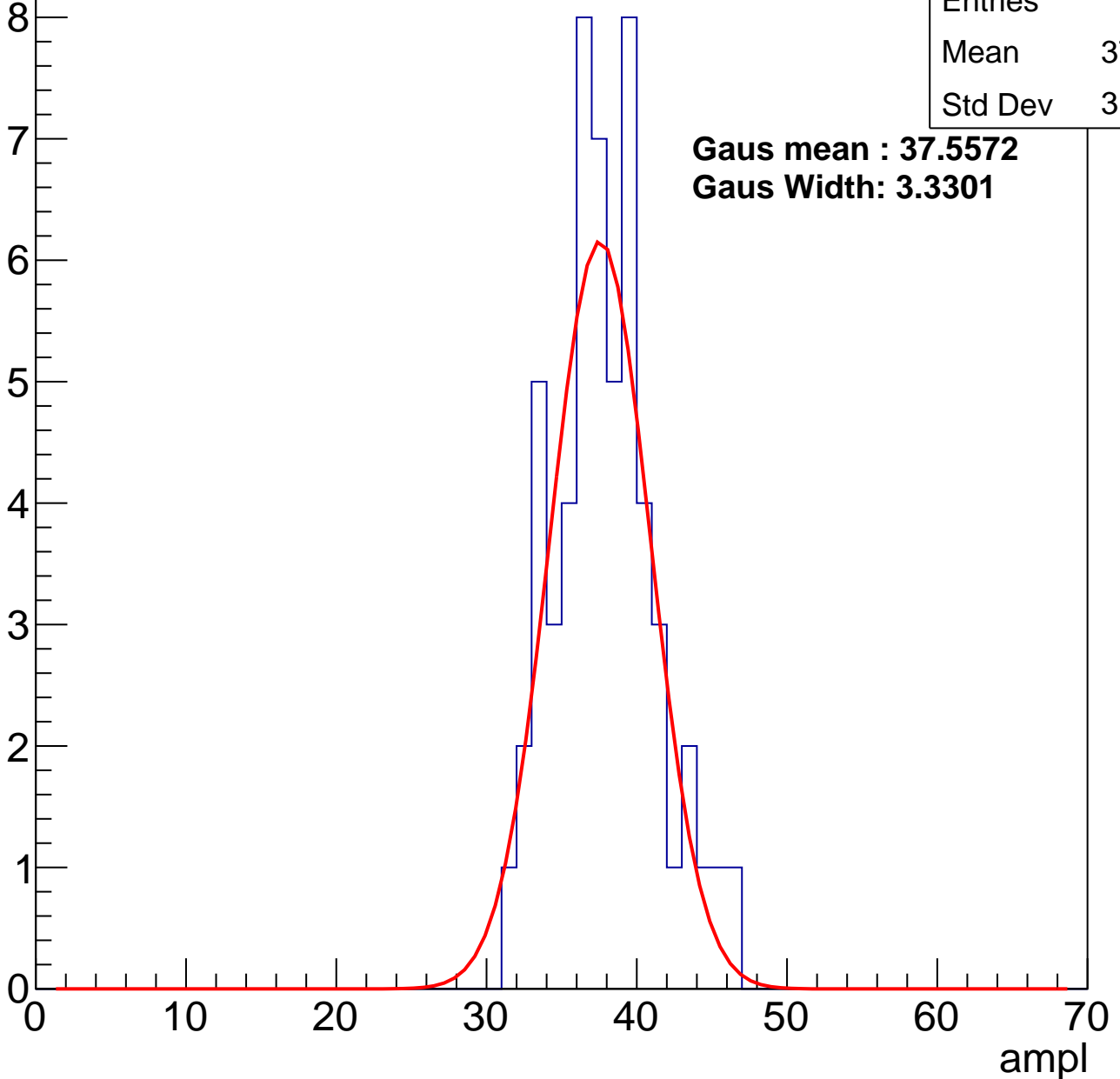
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	37.45
Std Dev	3.322

Gaus mean : 37.5572

Gaus Width: 3.3301



B1L101S, U23-ch23, adc2

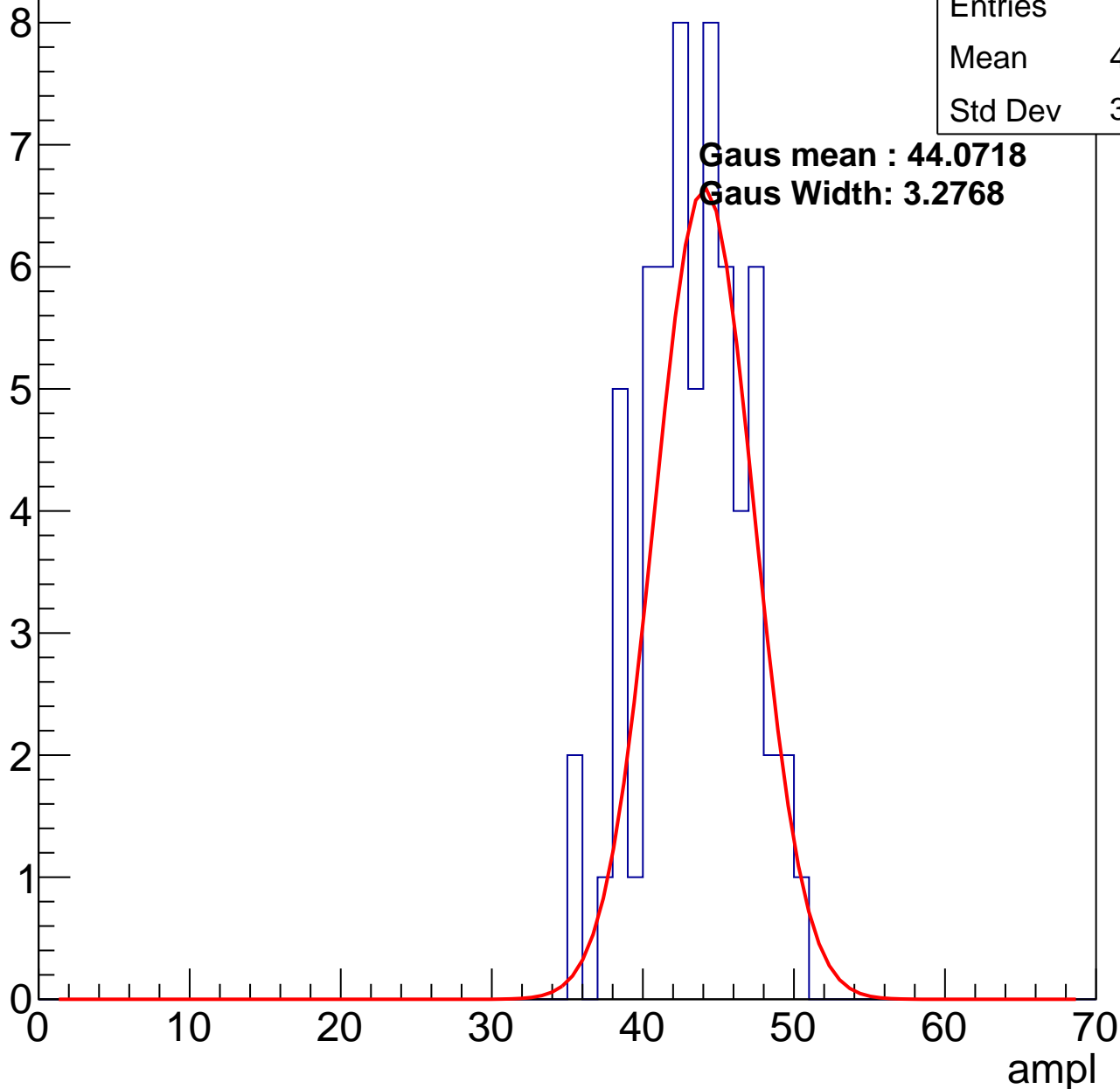
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	42.94
Std Dev	3.394

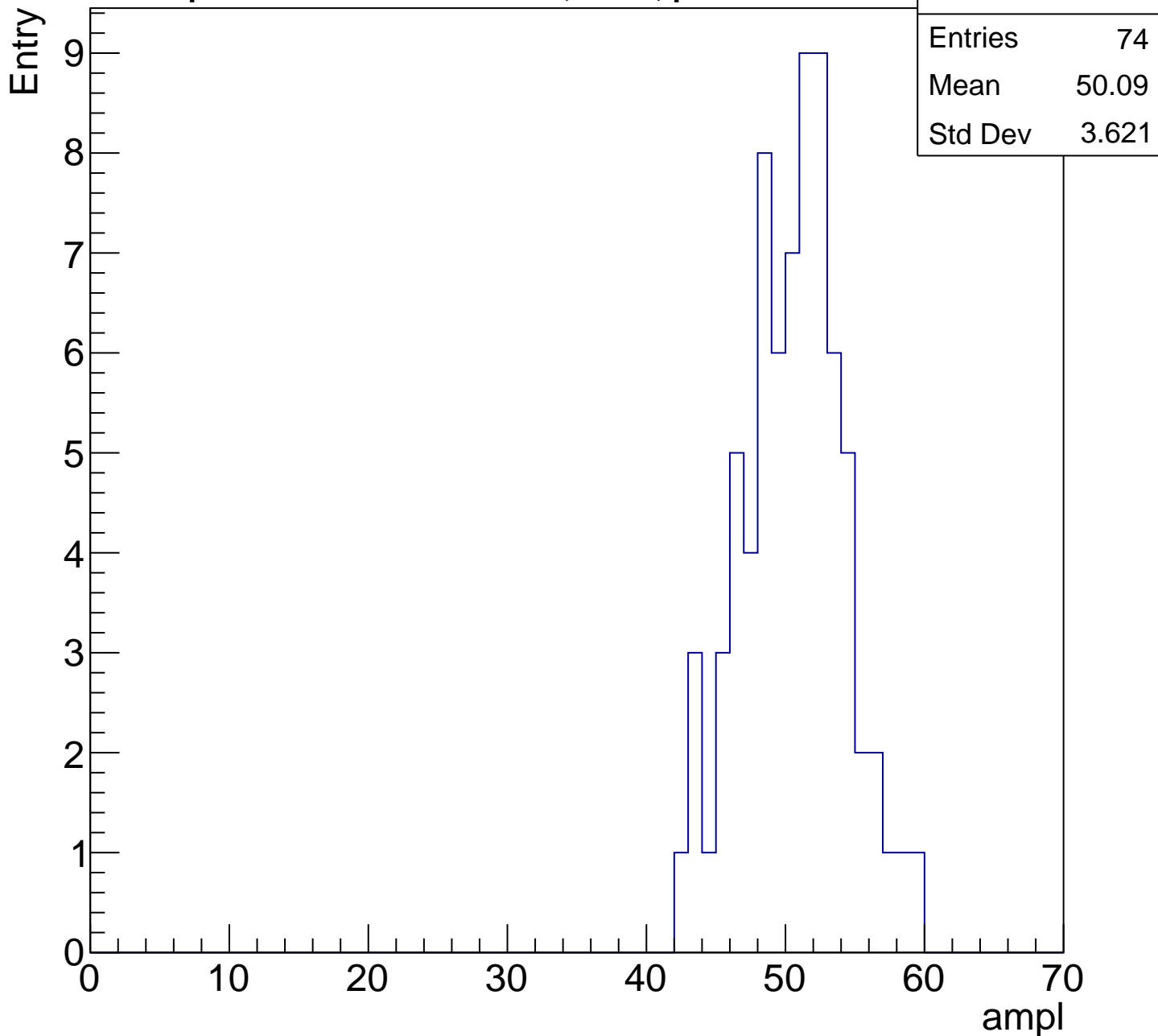
Gaus mean : 44.0718

Gaus Width: 3.2768



B1L101S, U23-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

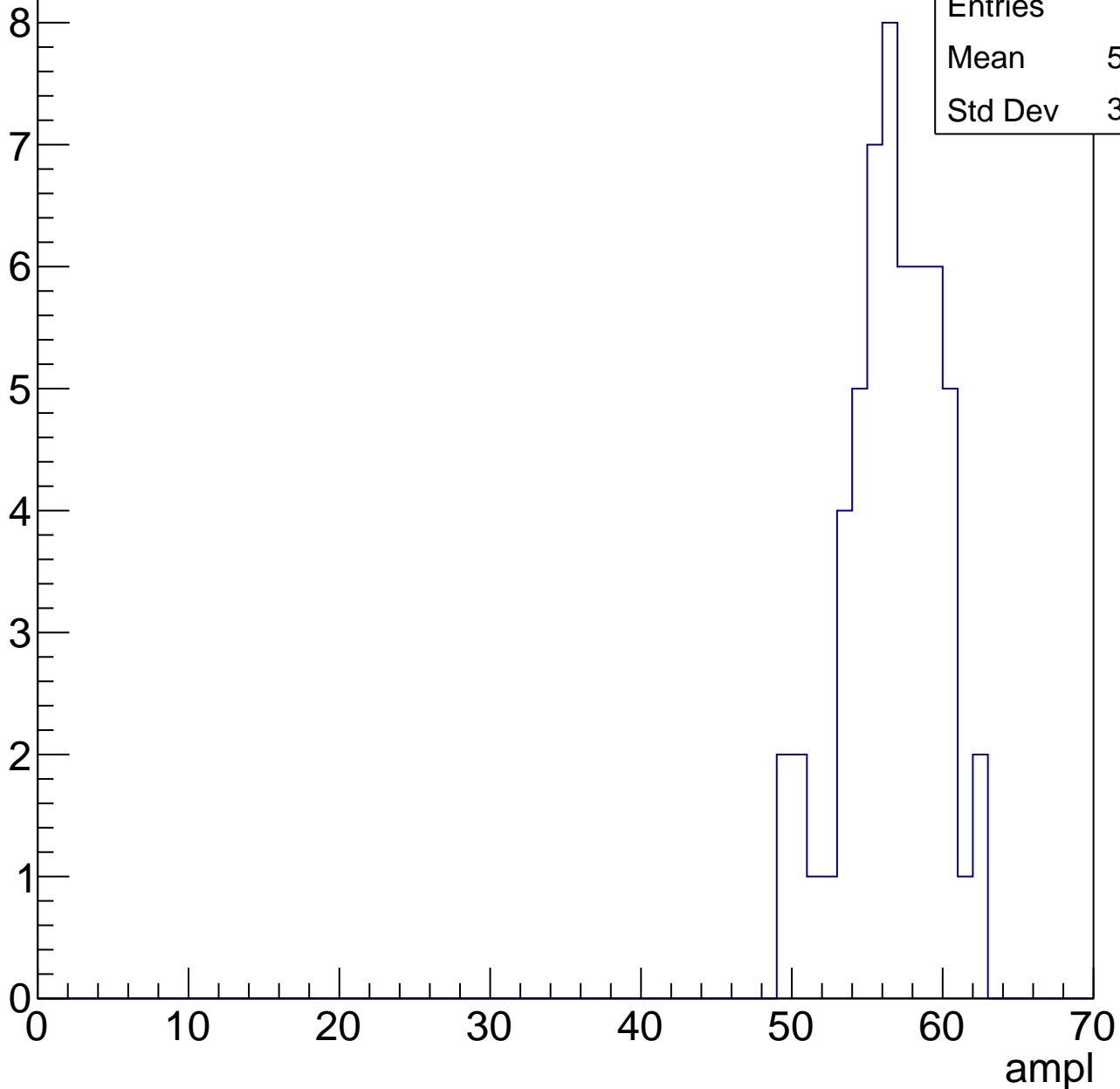


B1L101S, U23-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.16
Std Dev	3.087

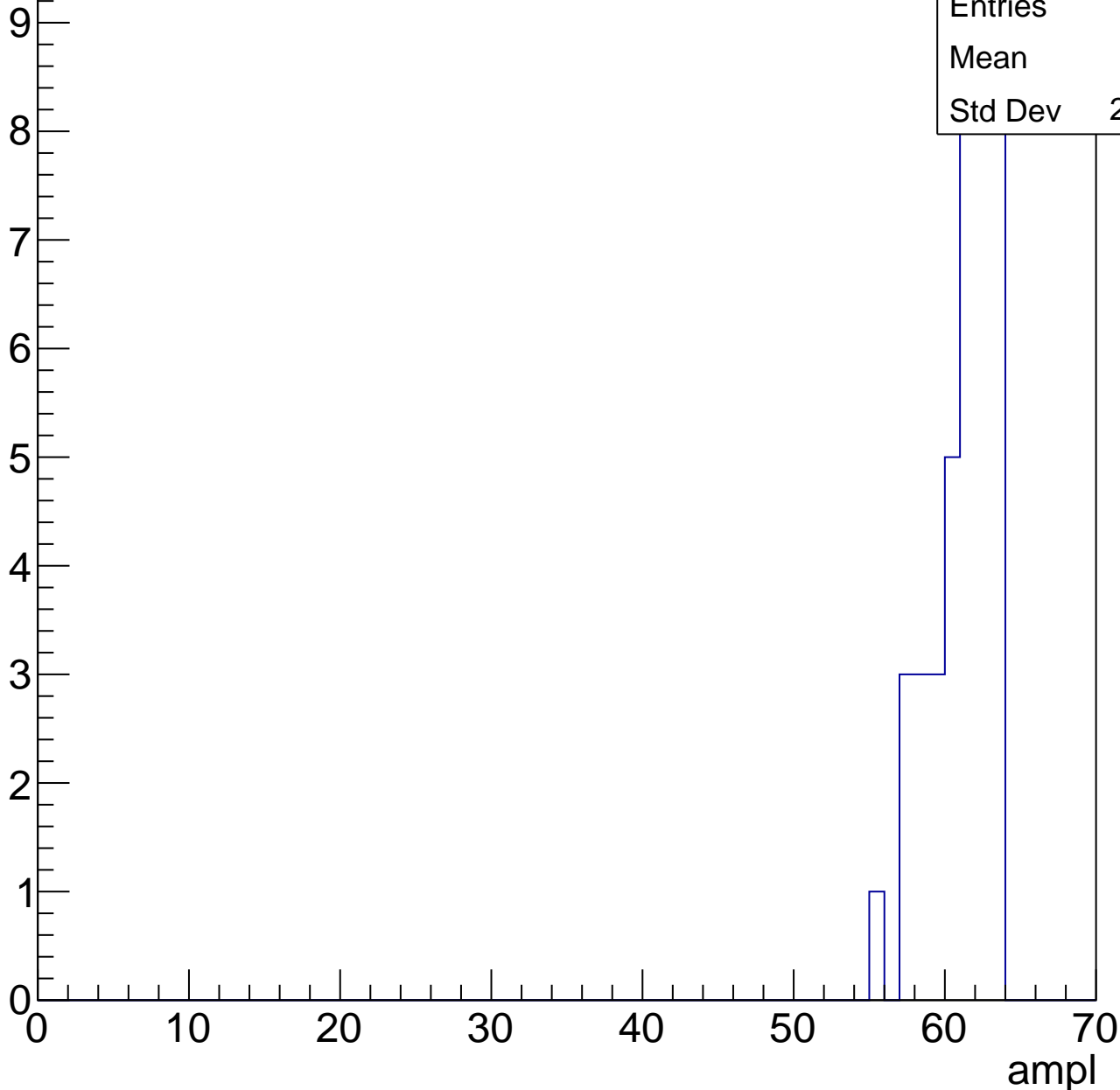


B1L101S, U23-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

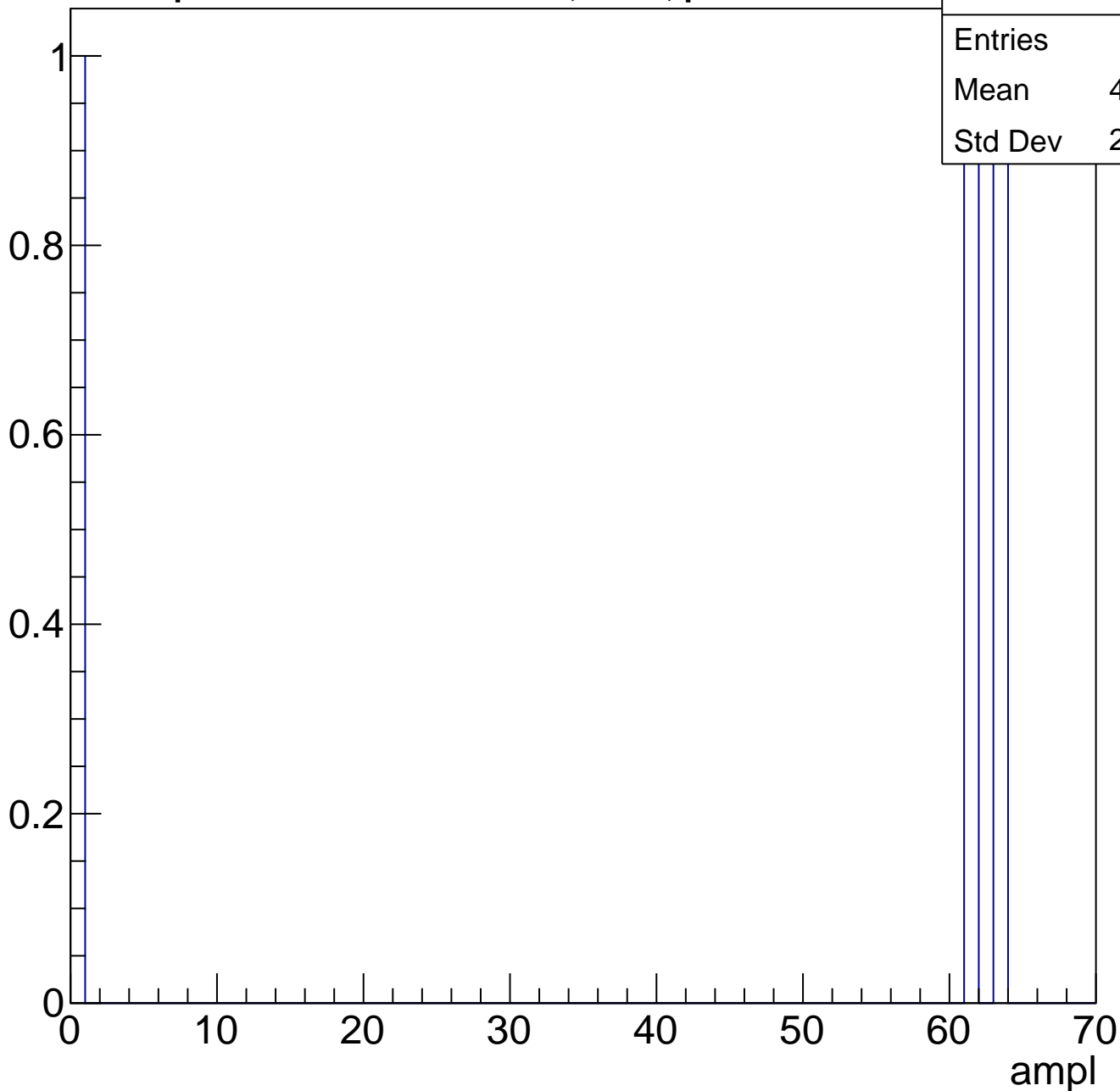
Entries	40
Mean	60.7
Std Dev	2.052



B1L101S, U23-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

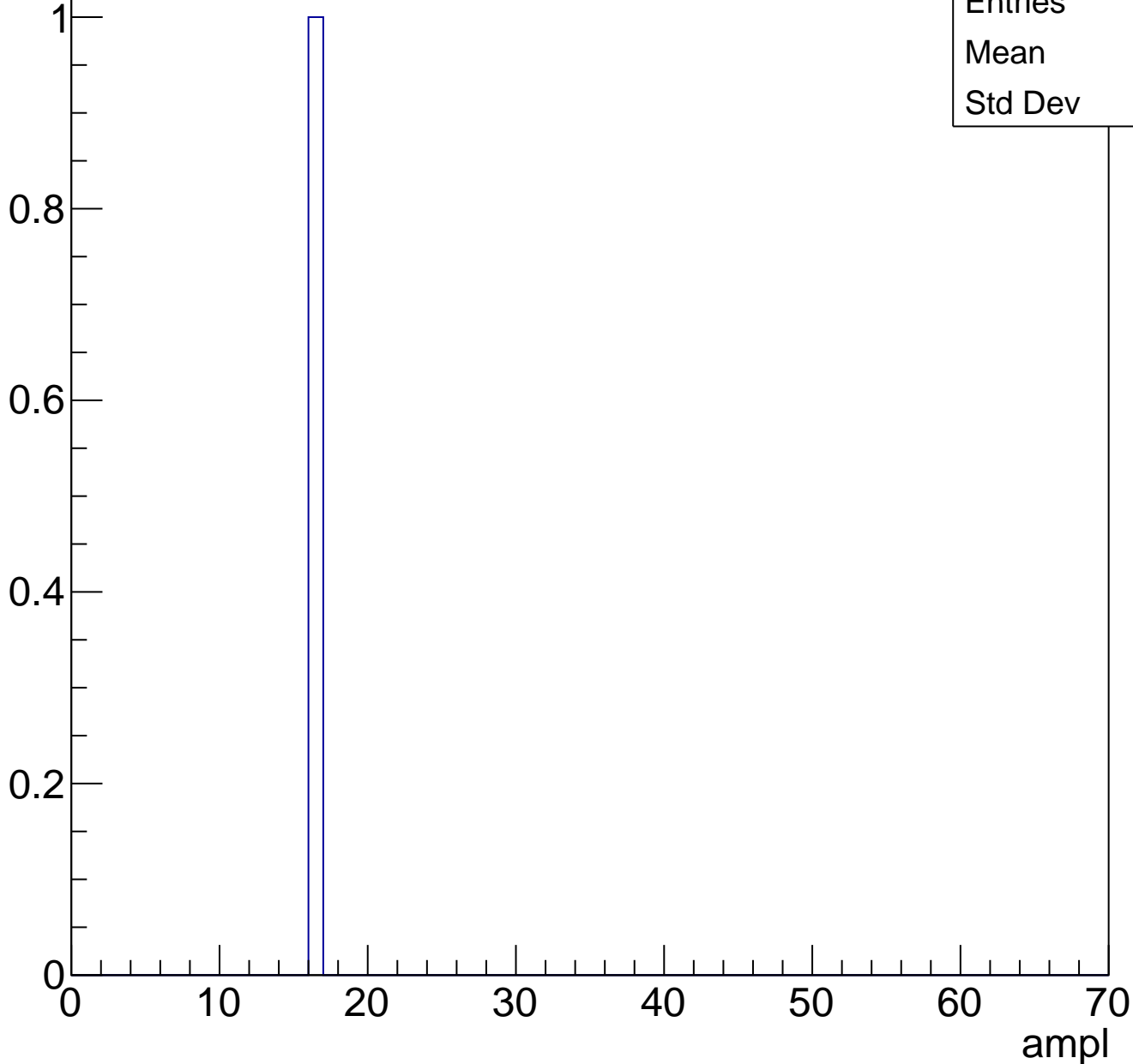
Entry



B1L101S, U23-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	16
Std Dev	0

B1L101S, U23-ch24, adc0

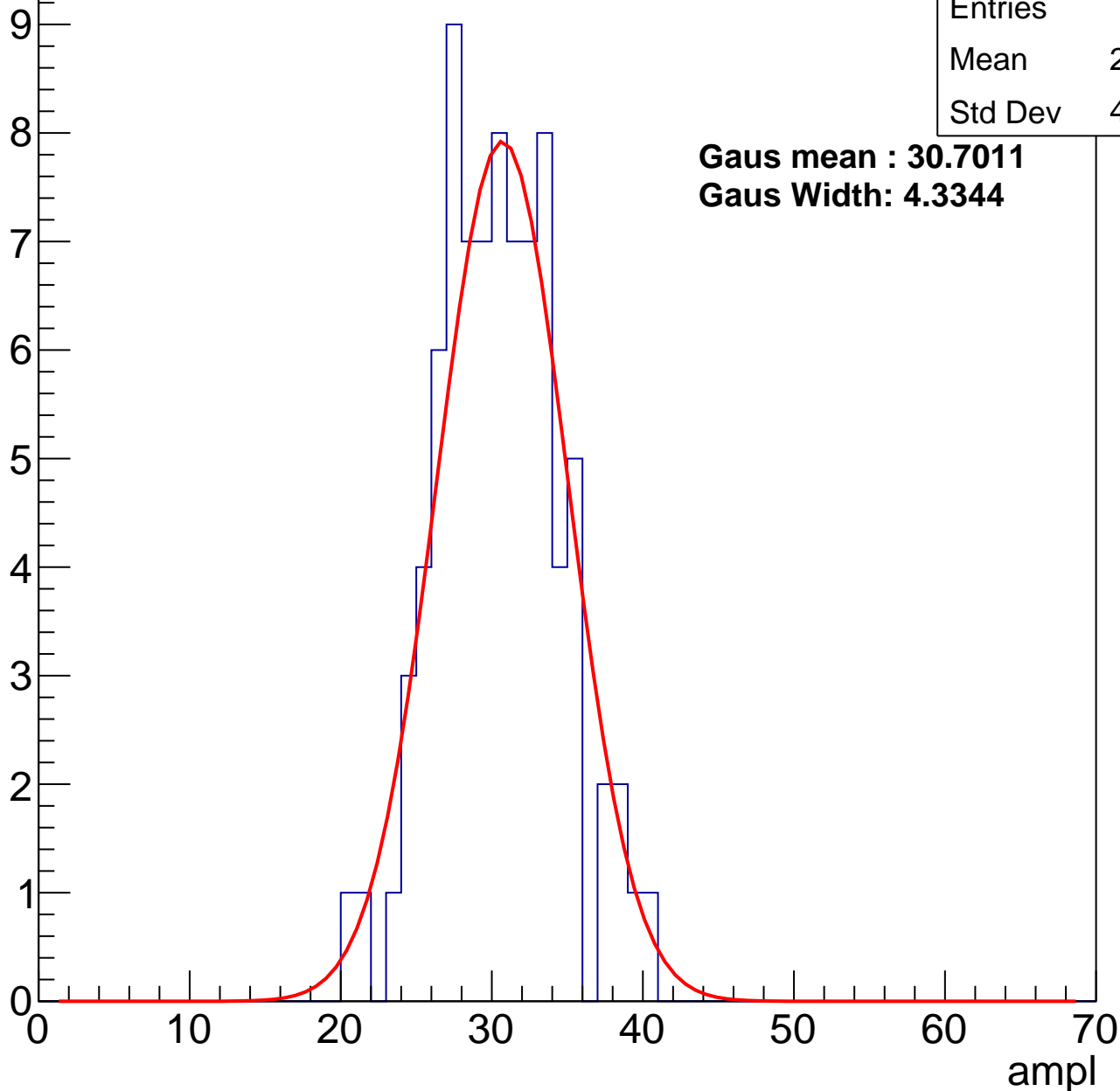
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.99
Std Dev	4.013

Gaus mean : 30.7011

Gaus Width: 4.3344



B1L101S, U23-ch24, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	37.79
Std Dev	3.711

Gaus mean : 38.2925

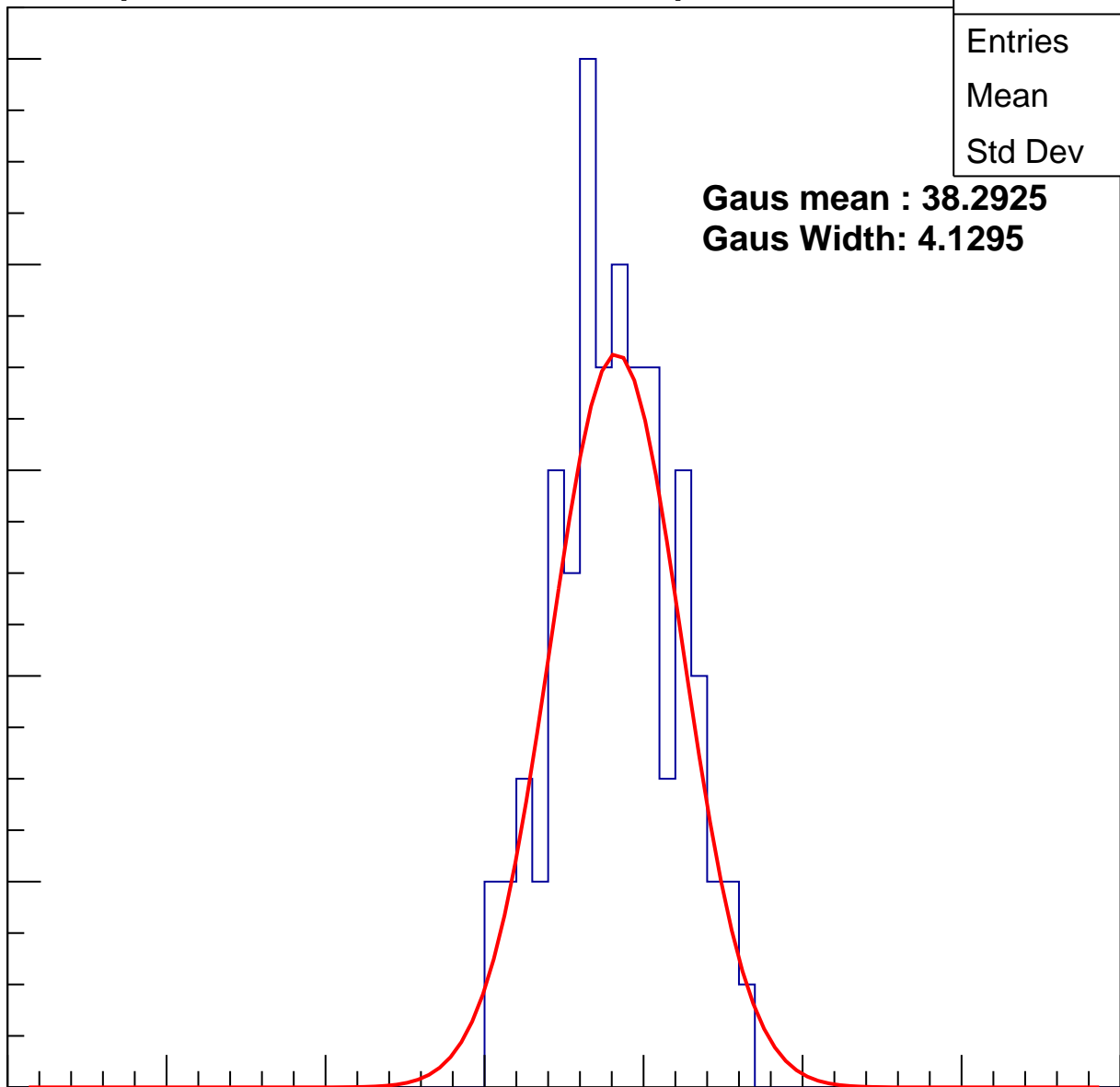
Gaus Width: 4.1295

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch24, adc2

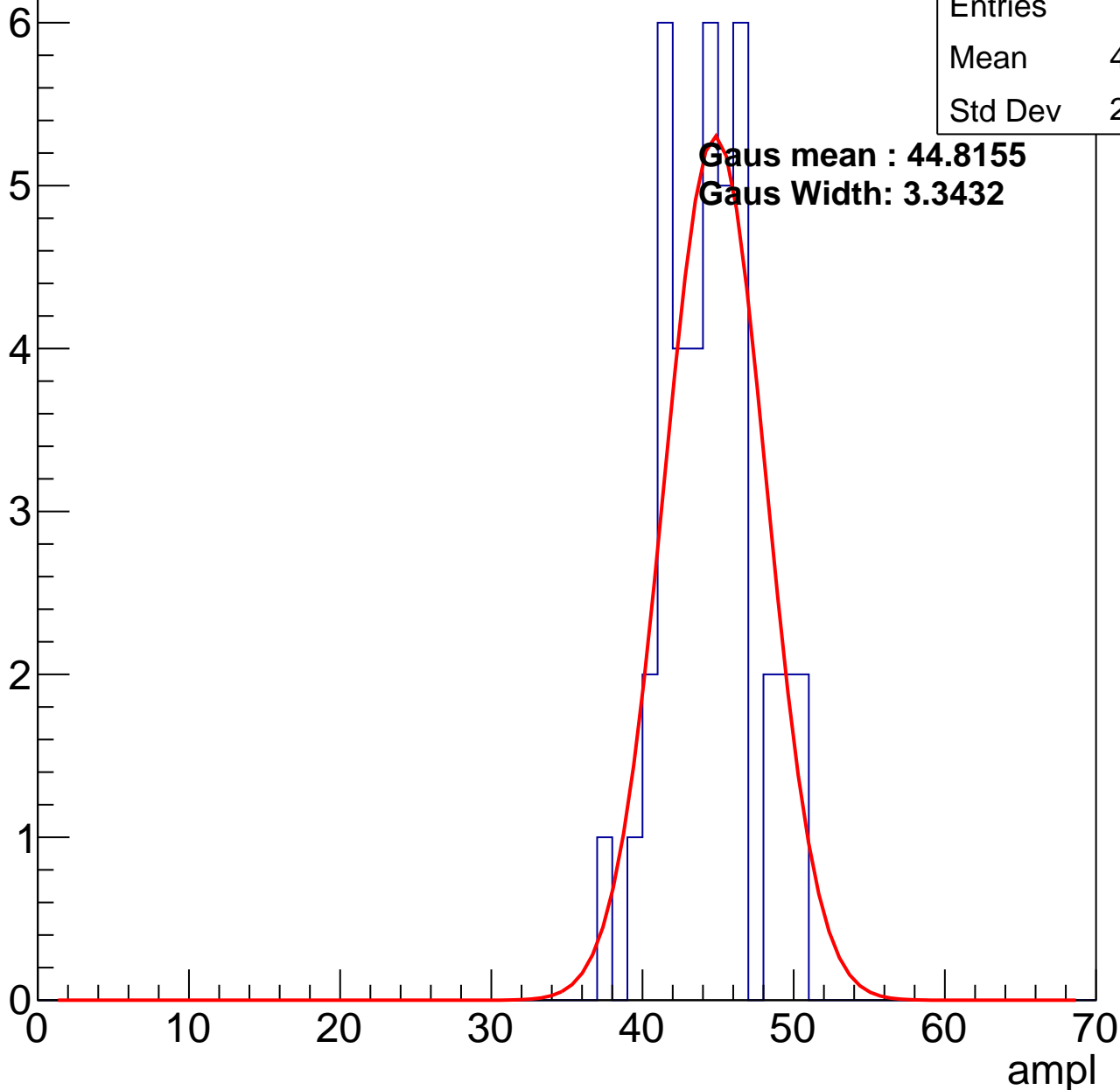
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	43.93
Std Dev	2.975

Gaus mean : 44.8155

Gaus Width: 3.3432

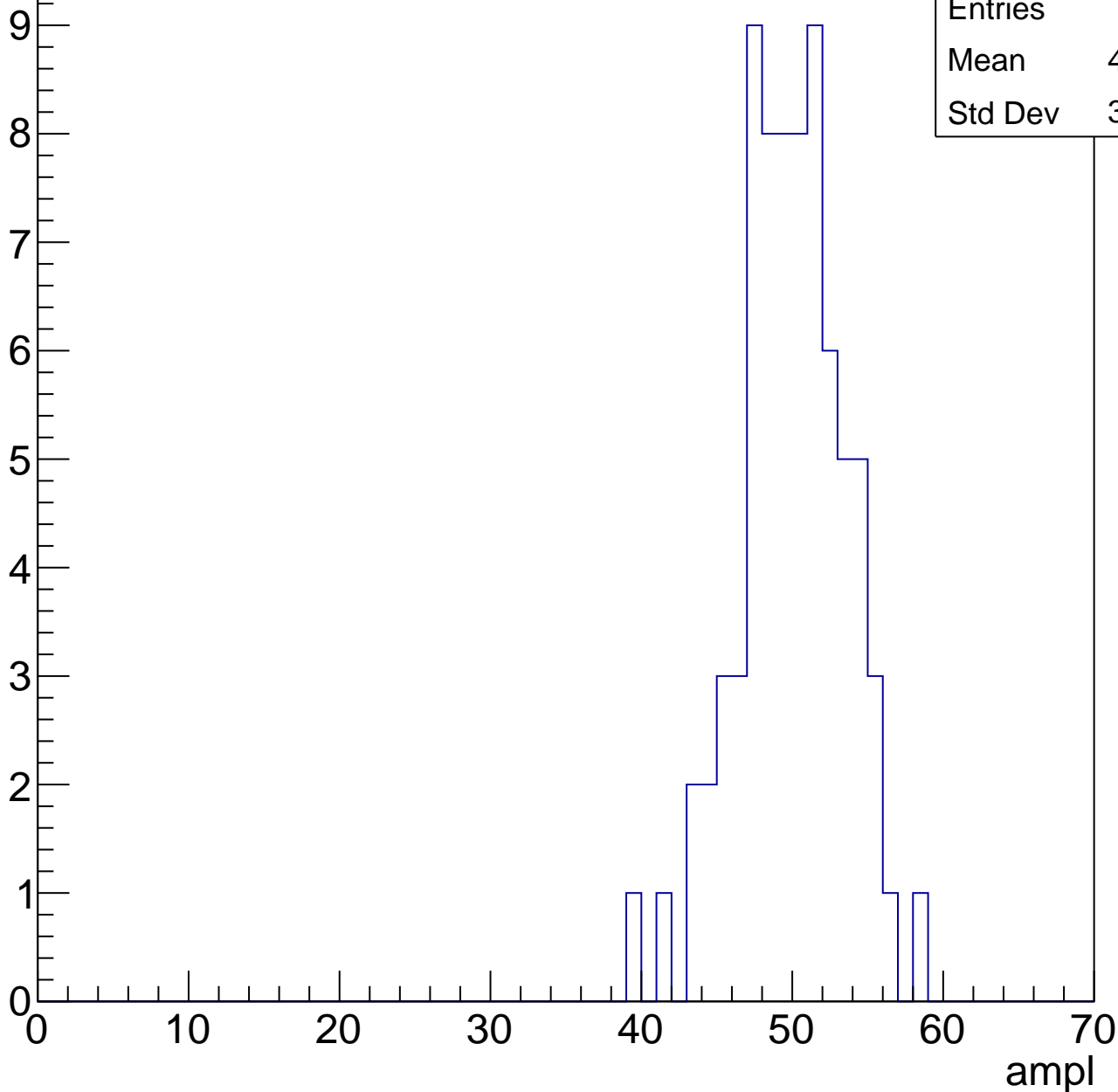


B1L101S, U23-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	49.48
Std Dev	3.519

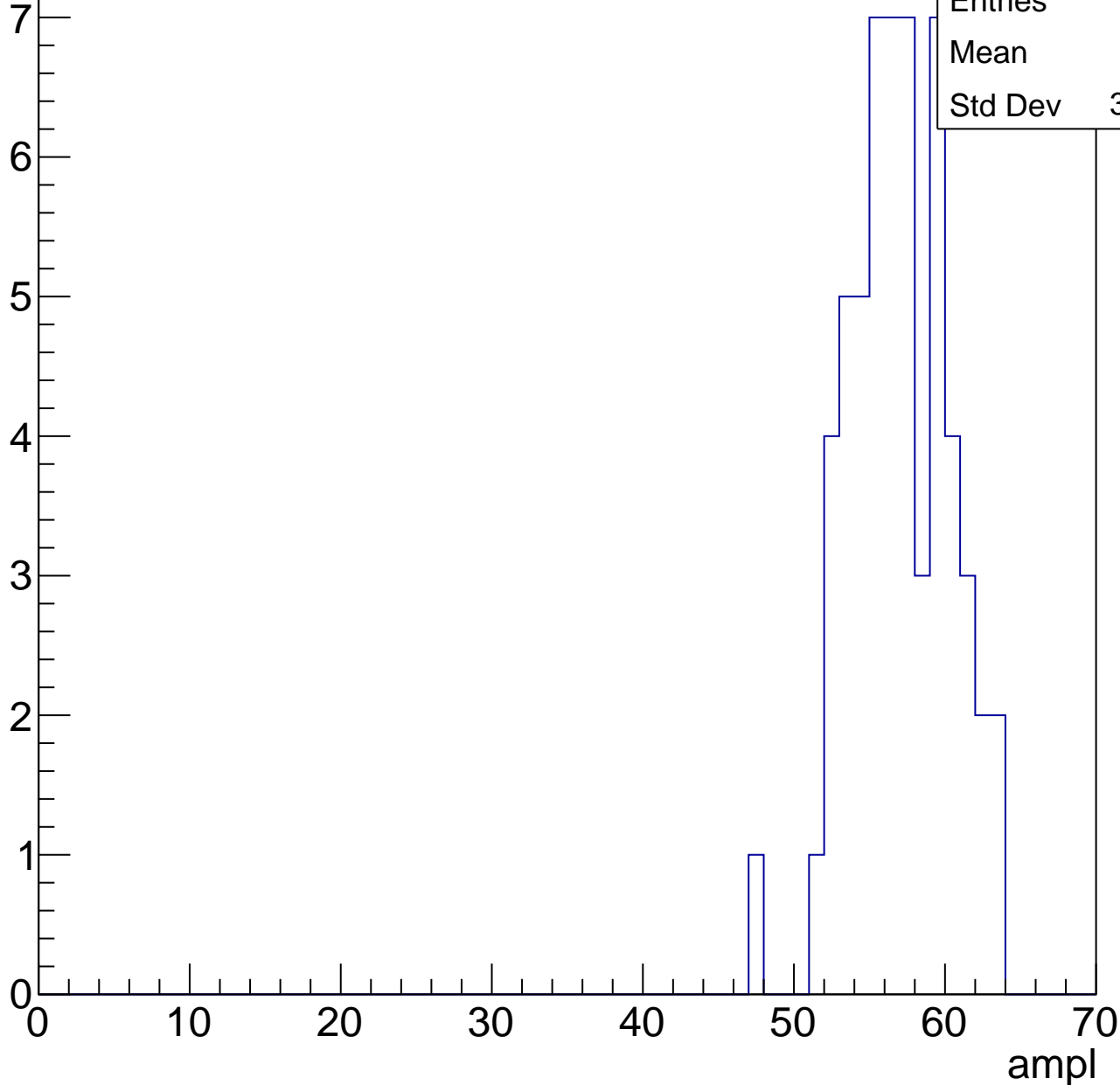


B1L101S, U23-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.5
Std Dev	3.266

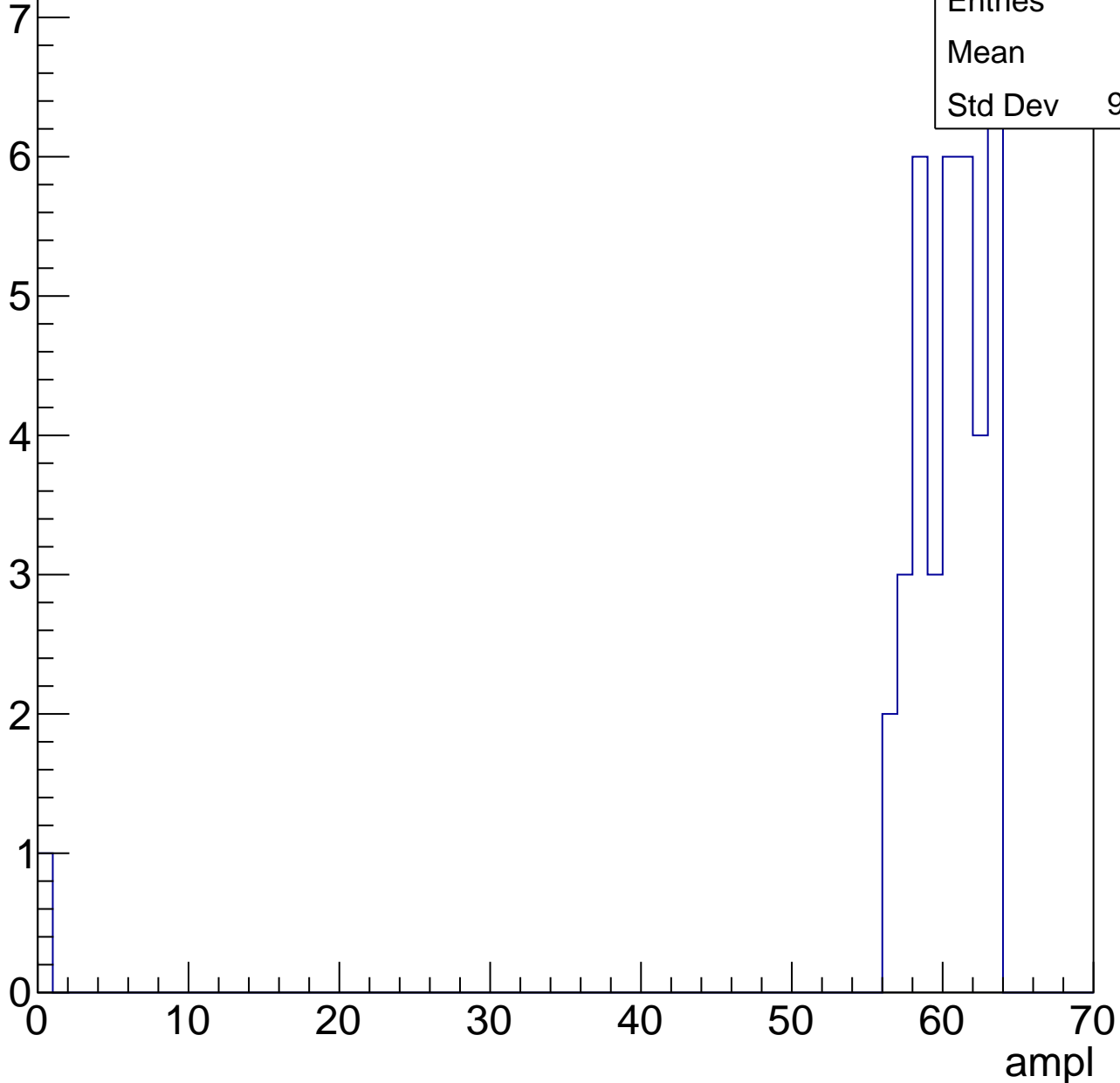


B1L101S, U23-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

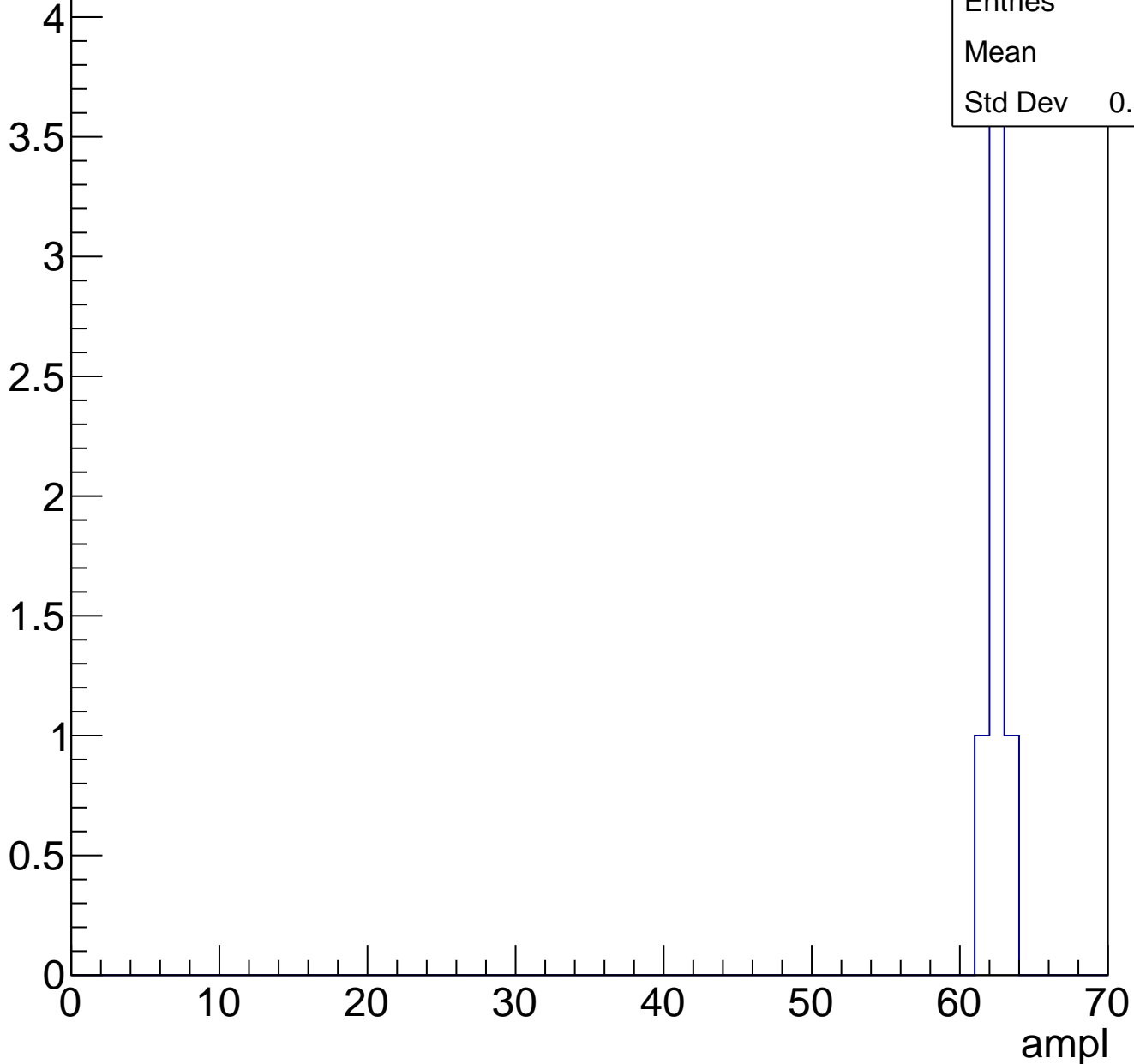
Entries	38
Mean	58.5
Std Dev	9.848



B1L101S, U23-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch25, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	30.38
Std Dev	3.354

Gaus mean : 30.7438

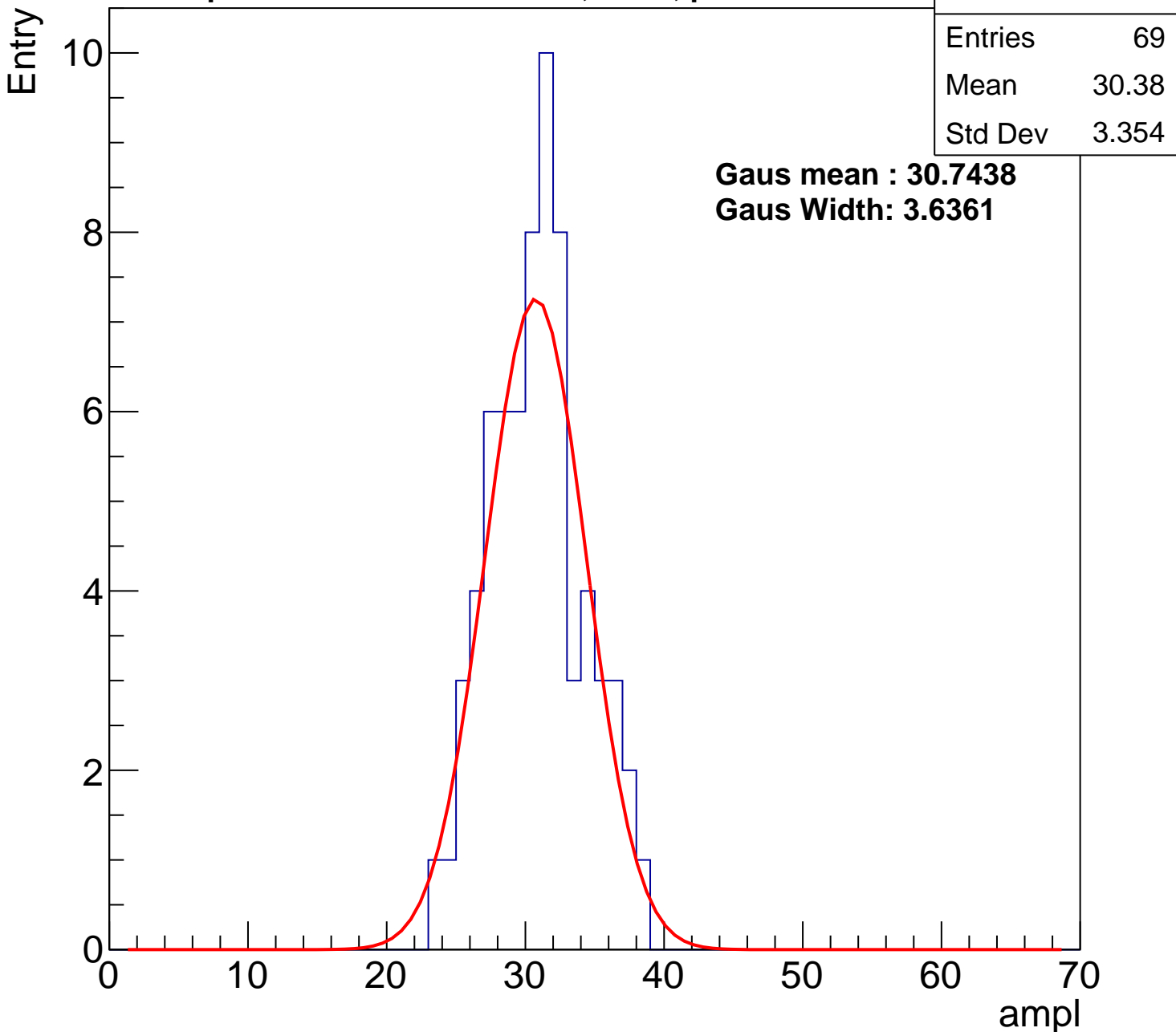
Gaus Width: 3.6361

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch25, adc1

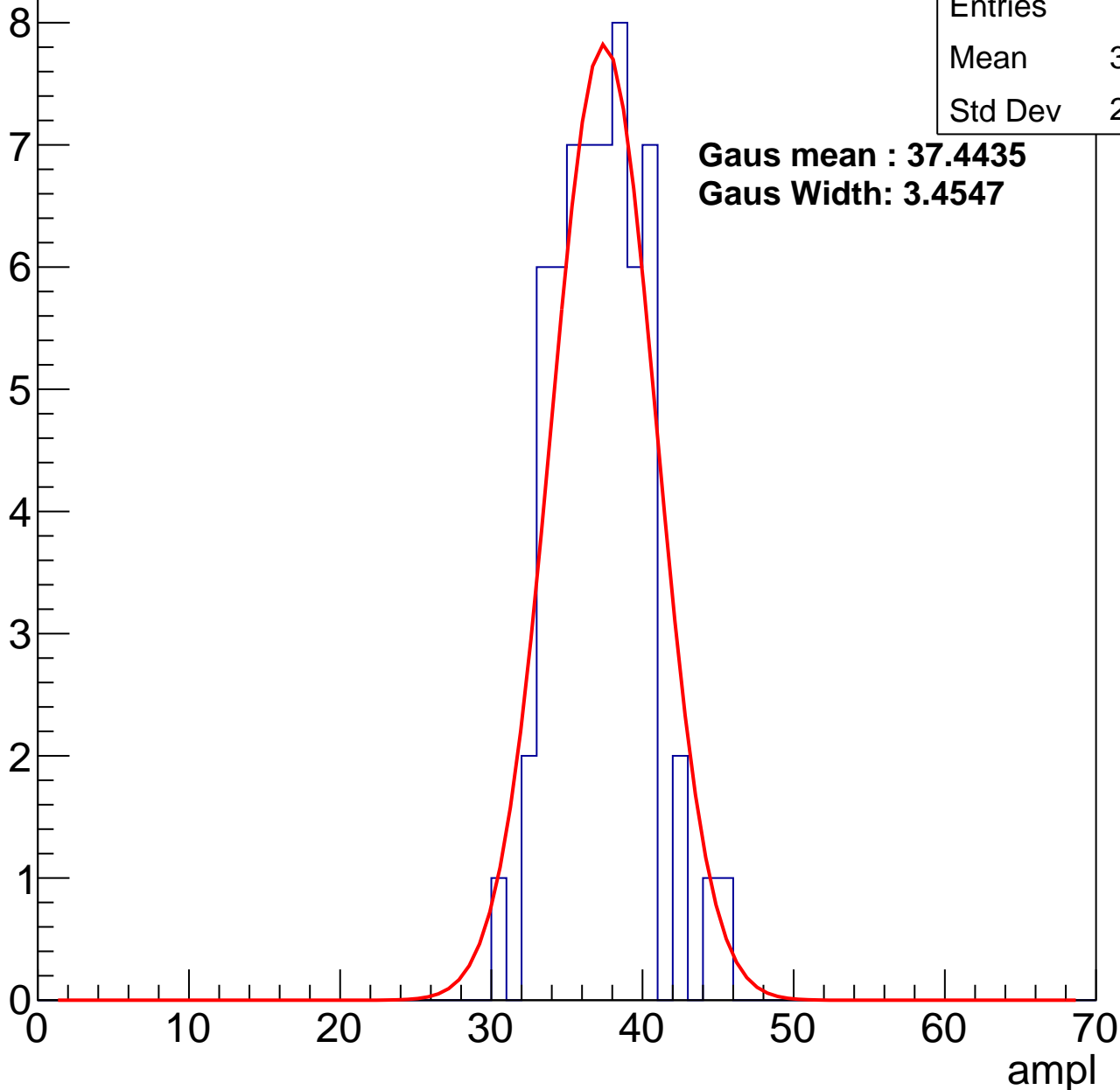
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.77
Std Dev	2.972

Gaus mean : 37.4435

Gaus Width: 3.4547



B1L101S, U23-ch25, adc2

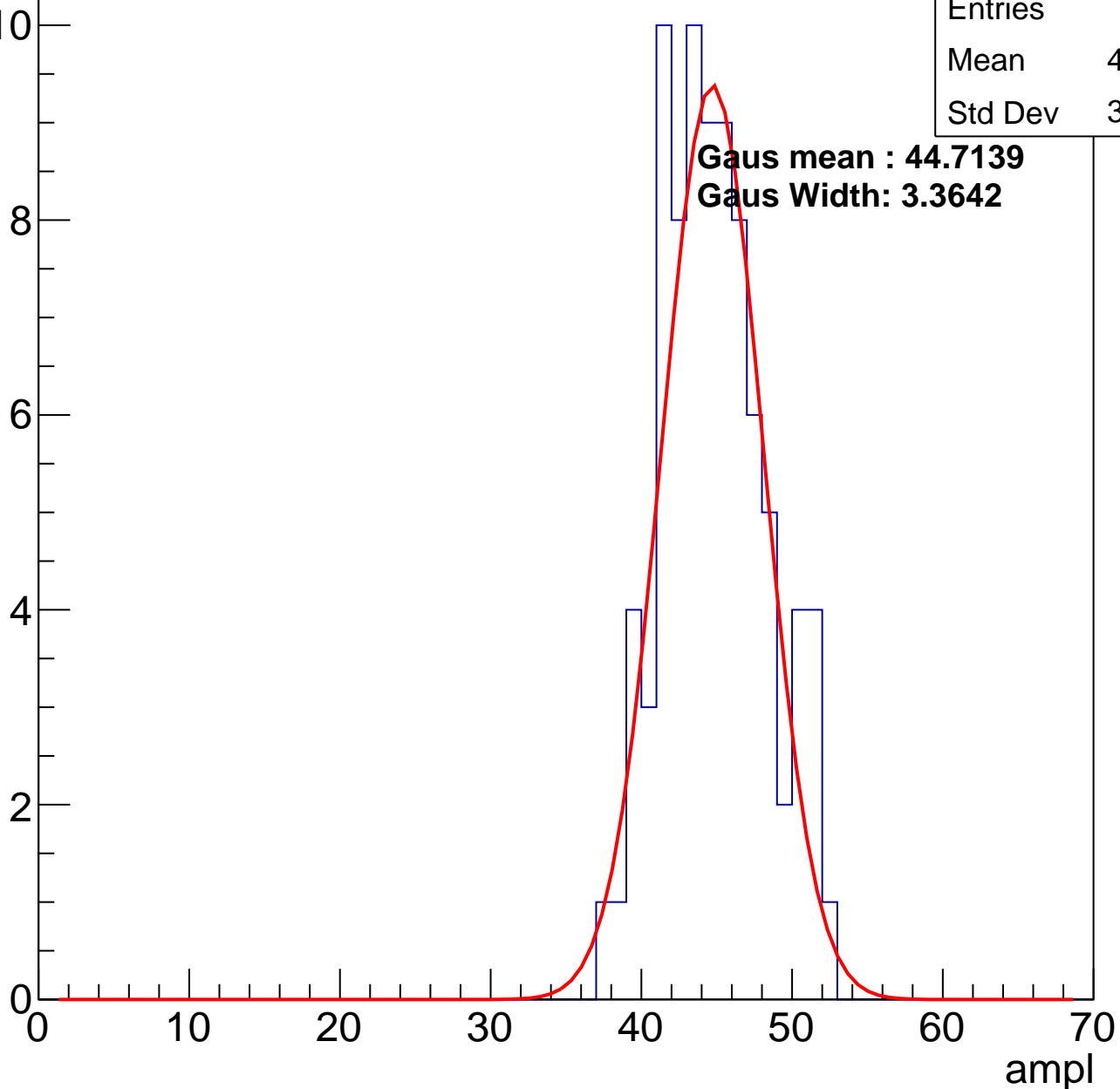
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	44.38
Std Dev	3.399

Gaus mean : 44.7139

Gaus Width: 3.3642

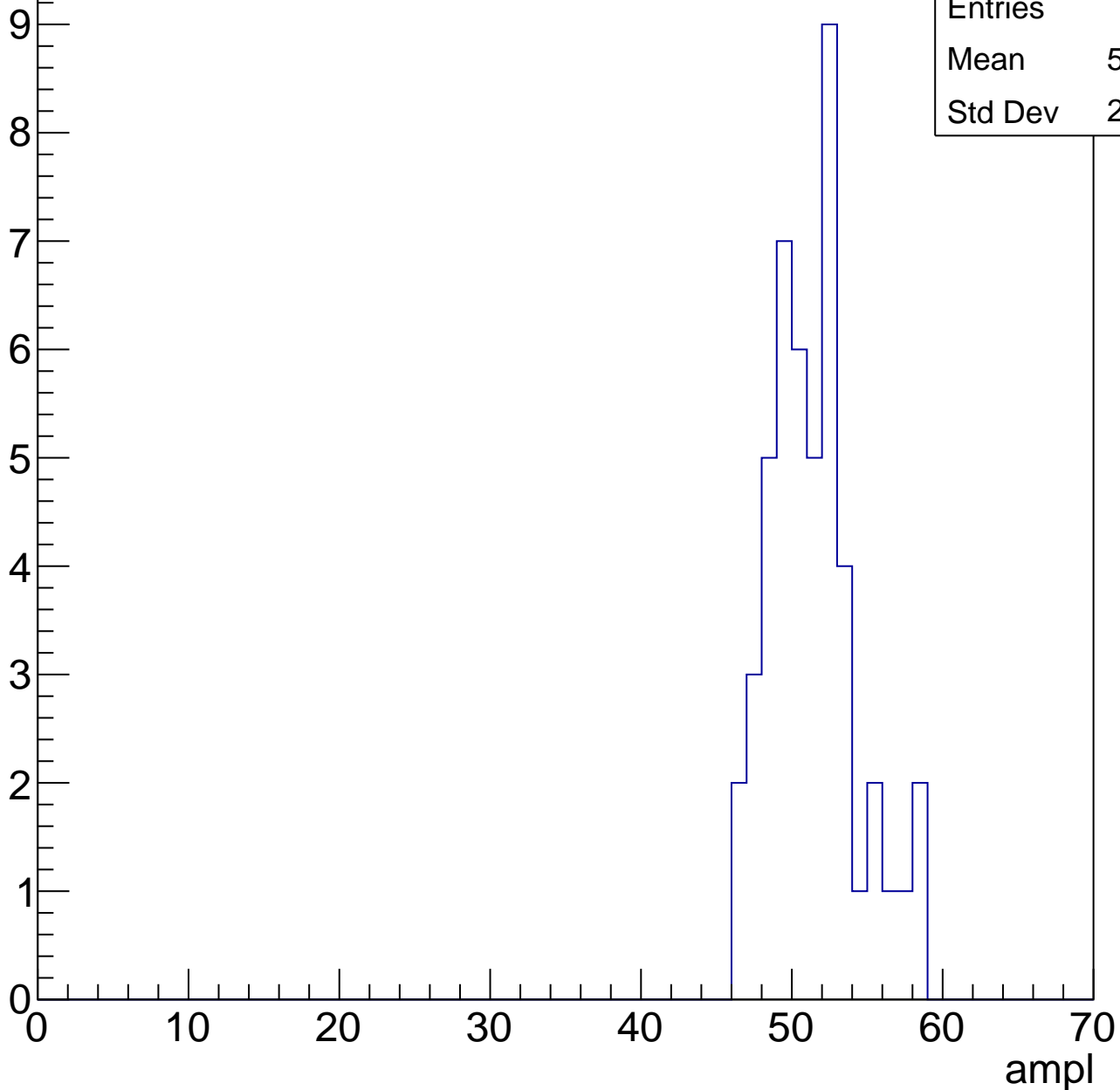


B1L101S, U23-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	50.92
Std Dev	2.893

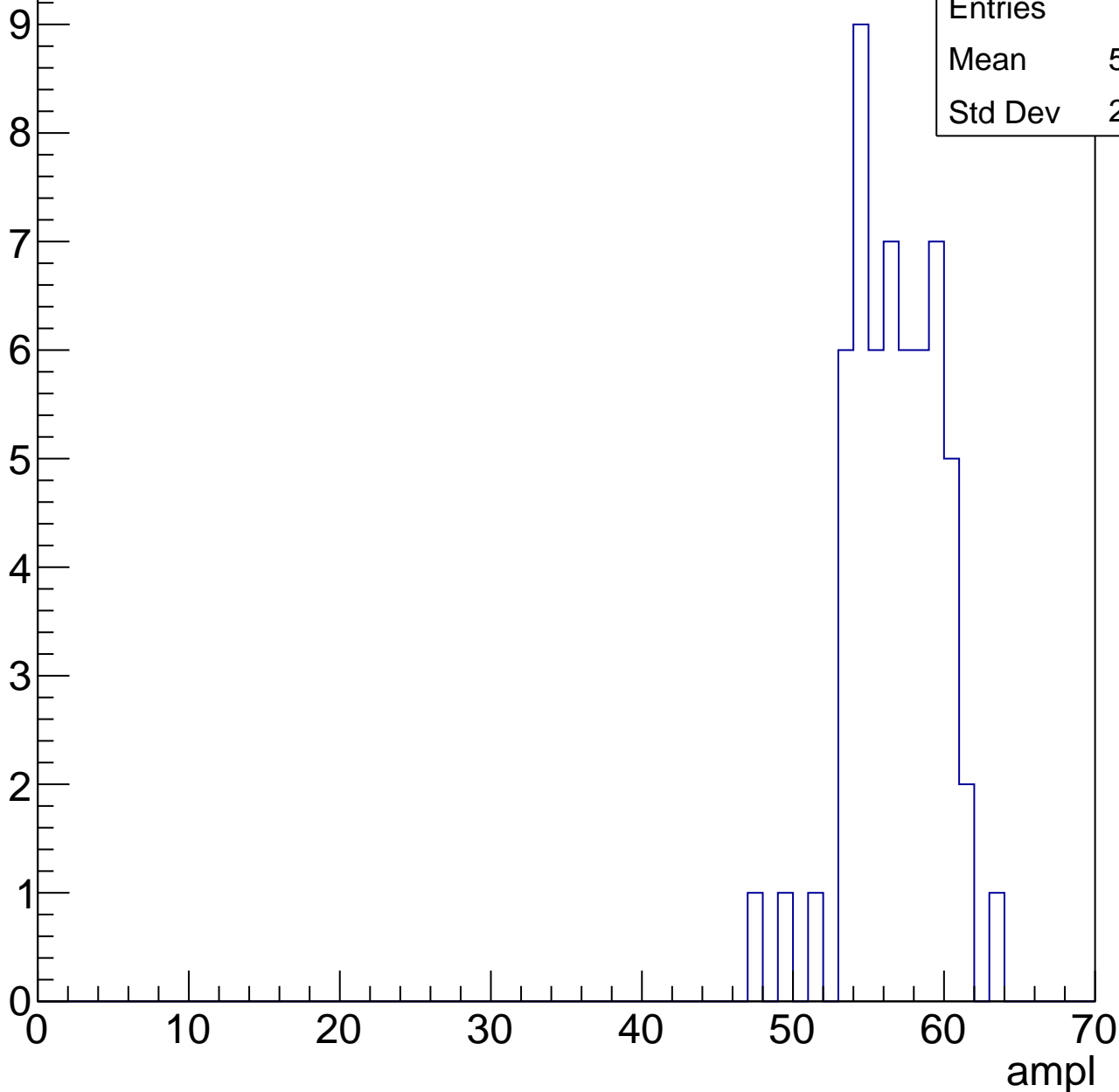


B1L101S, U23-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.22
Std Dev	2.994



B1L101S, U23-ch25, adc5

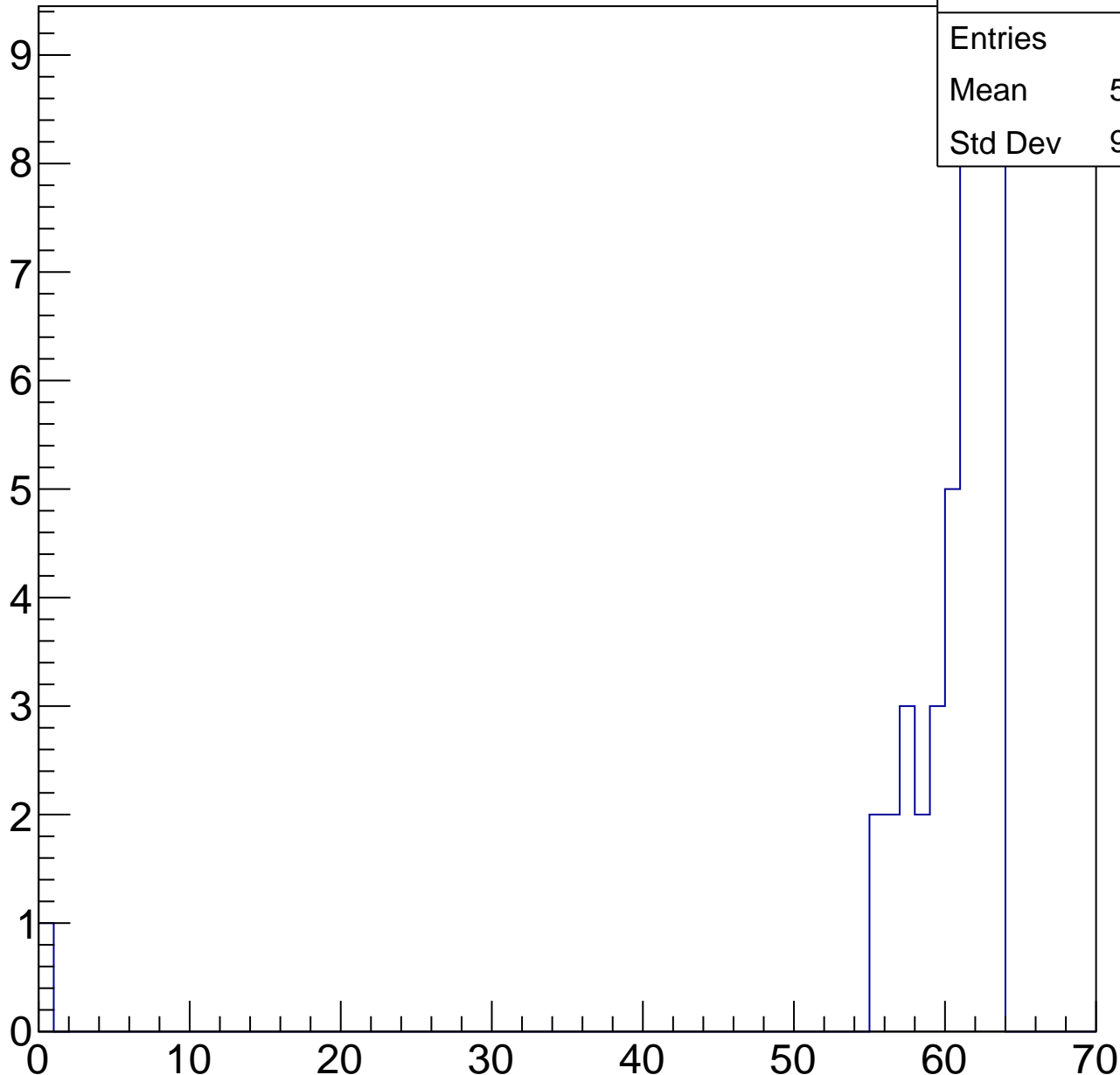
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.98
Std Dev	9.389

ampl



B1L101S, U23-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

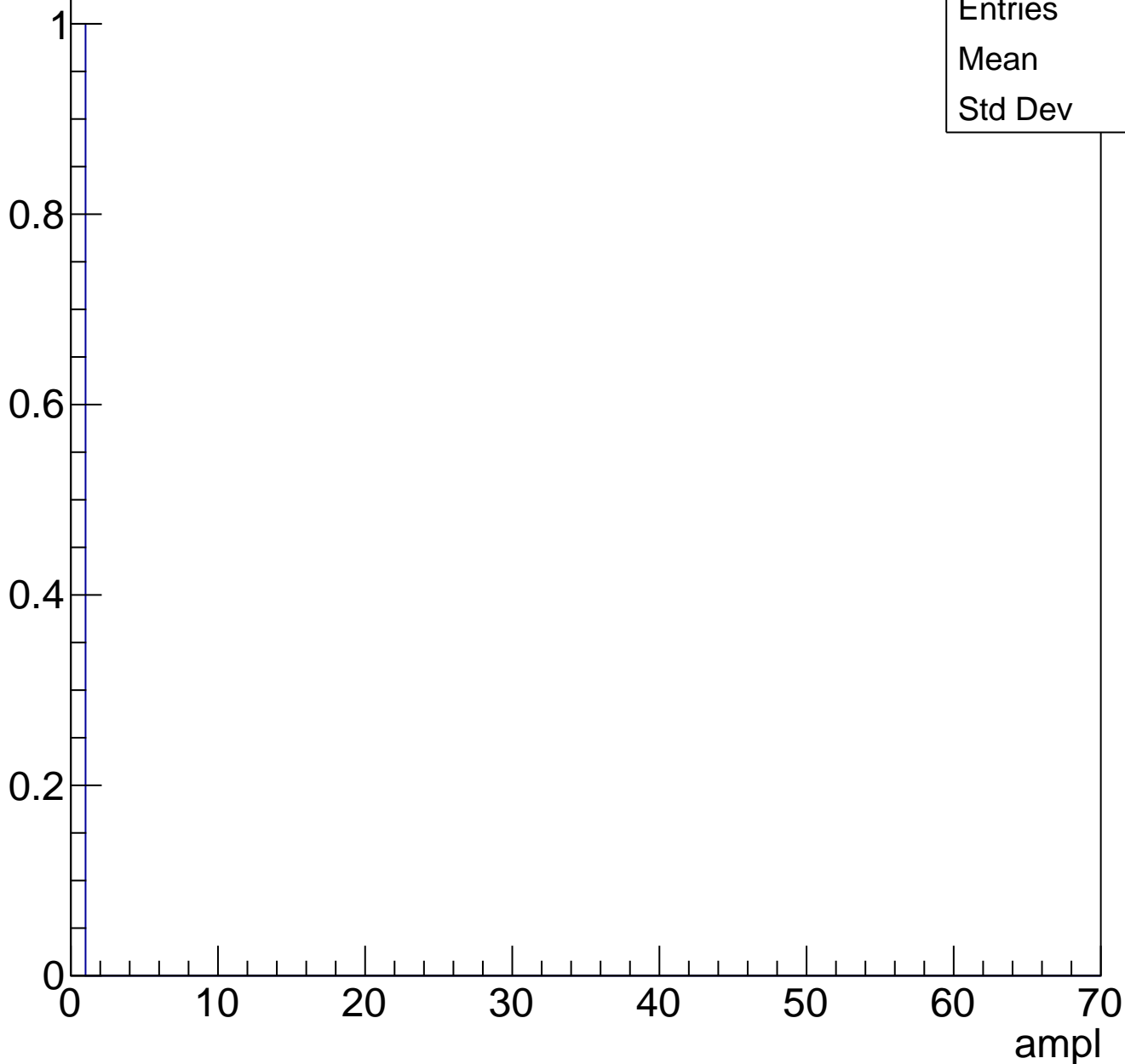
Entry



B1L101S, U23-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch26, adc0

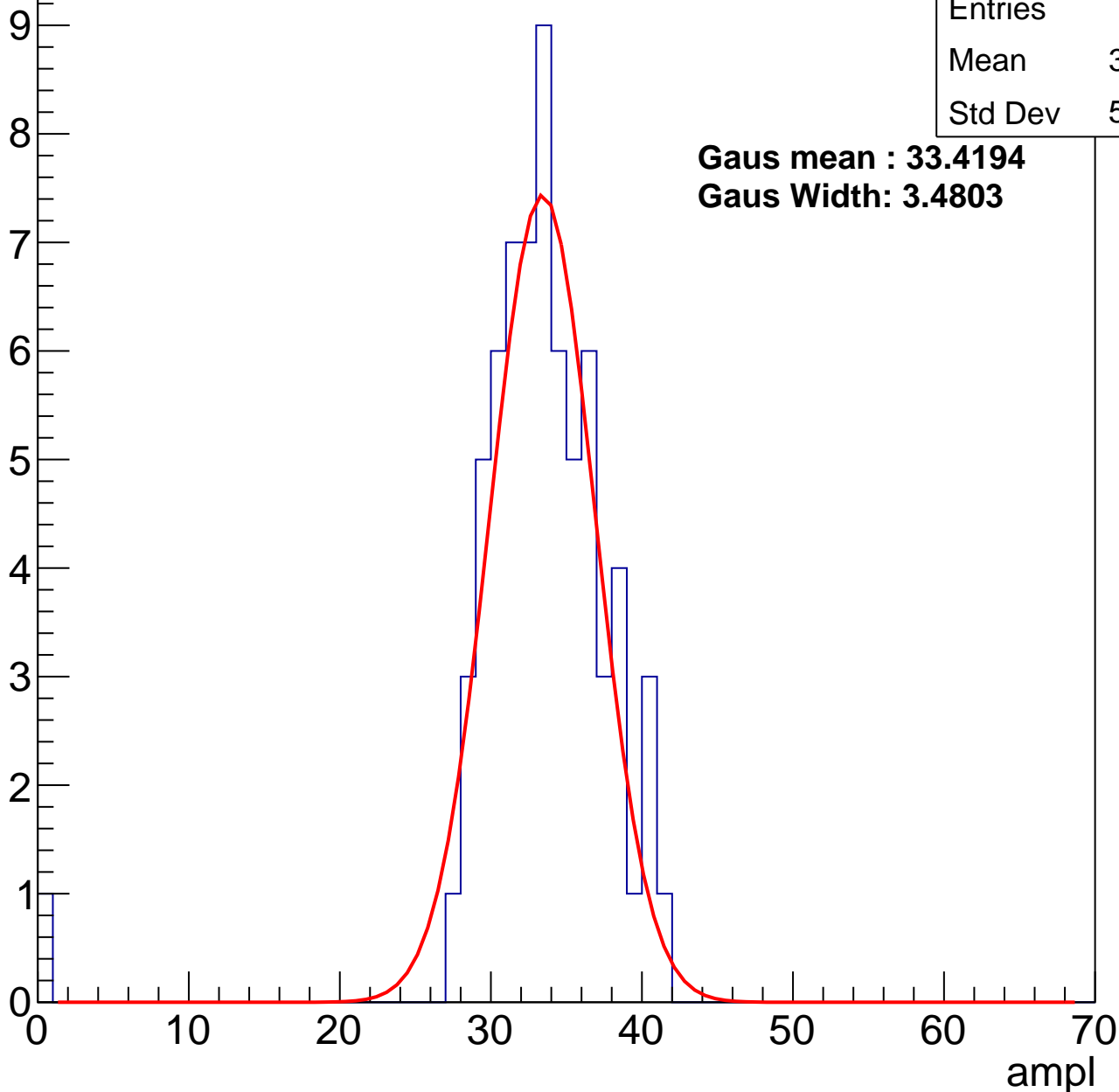
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	32.82
Std Dev	5.204

Gaus mean : 33.4194

Gaus Width: 3.4803



B1L101S, U23-ch26, adc1

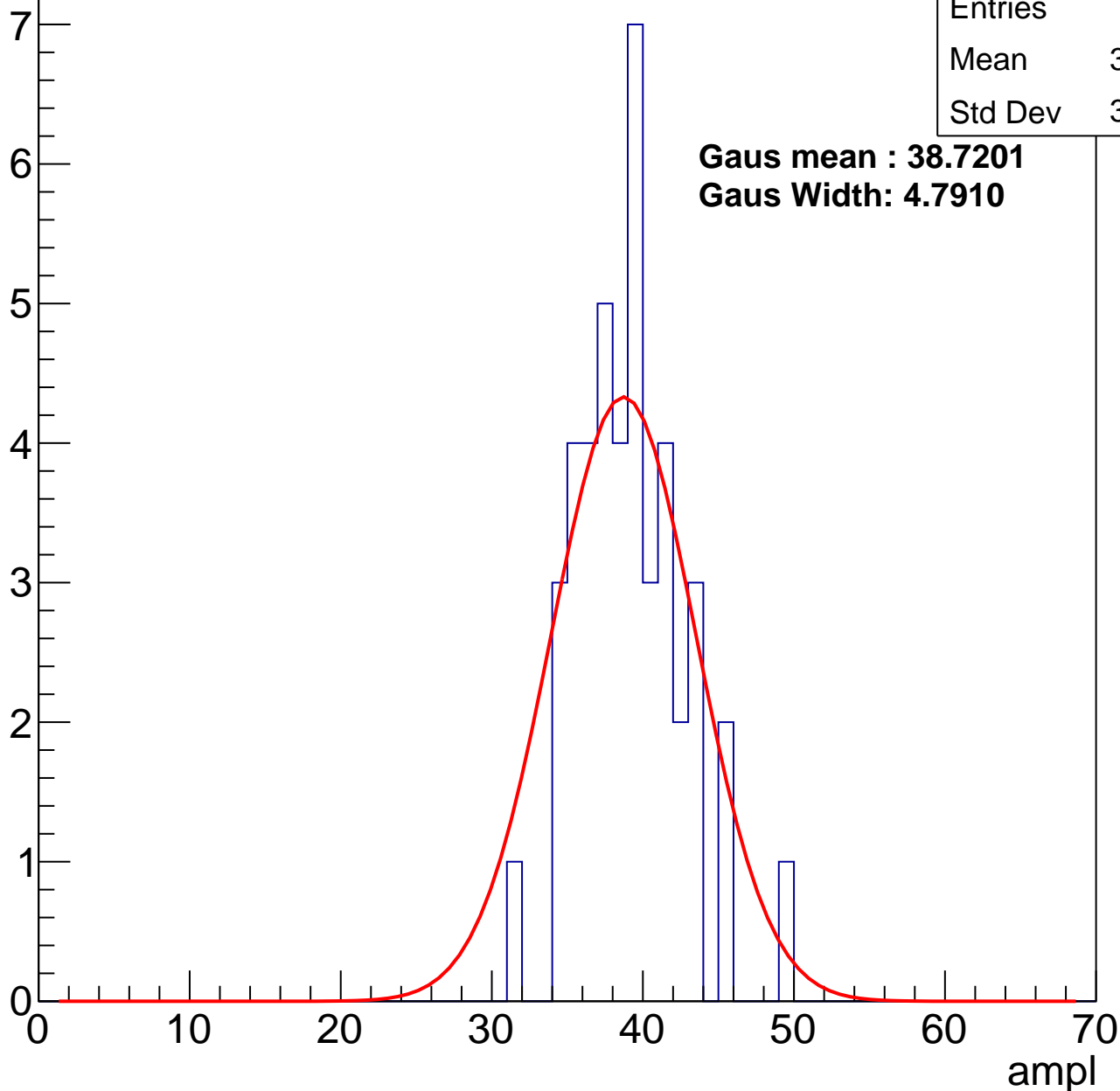
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	38.67
Std Dev	3.462

Gaus mean : 38.7201

Gaus Width: 4.7910



B1L101S, U23-ch26, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	44.7
Std Dev	3.576

Gaus mean : 45.2446

Gaus Width: 3.9857

Entry

10

8

6

4

2

0

0

10

20

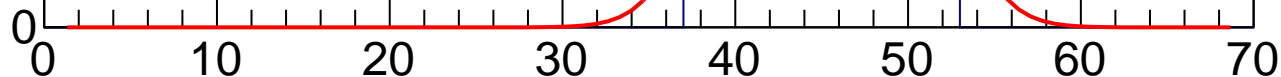
30

40

50

60

ampl

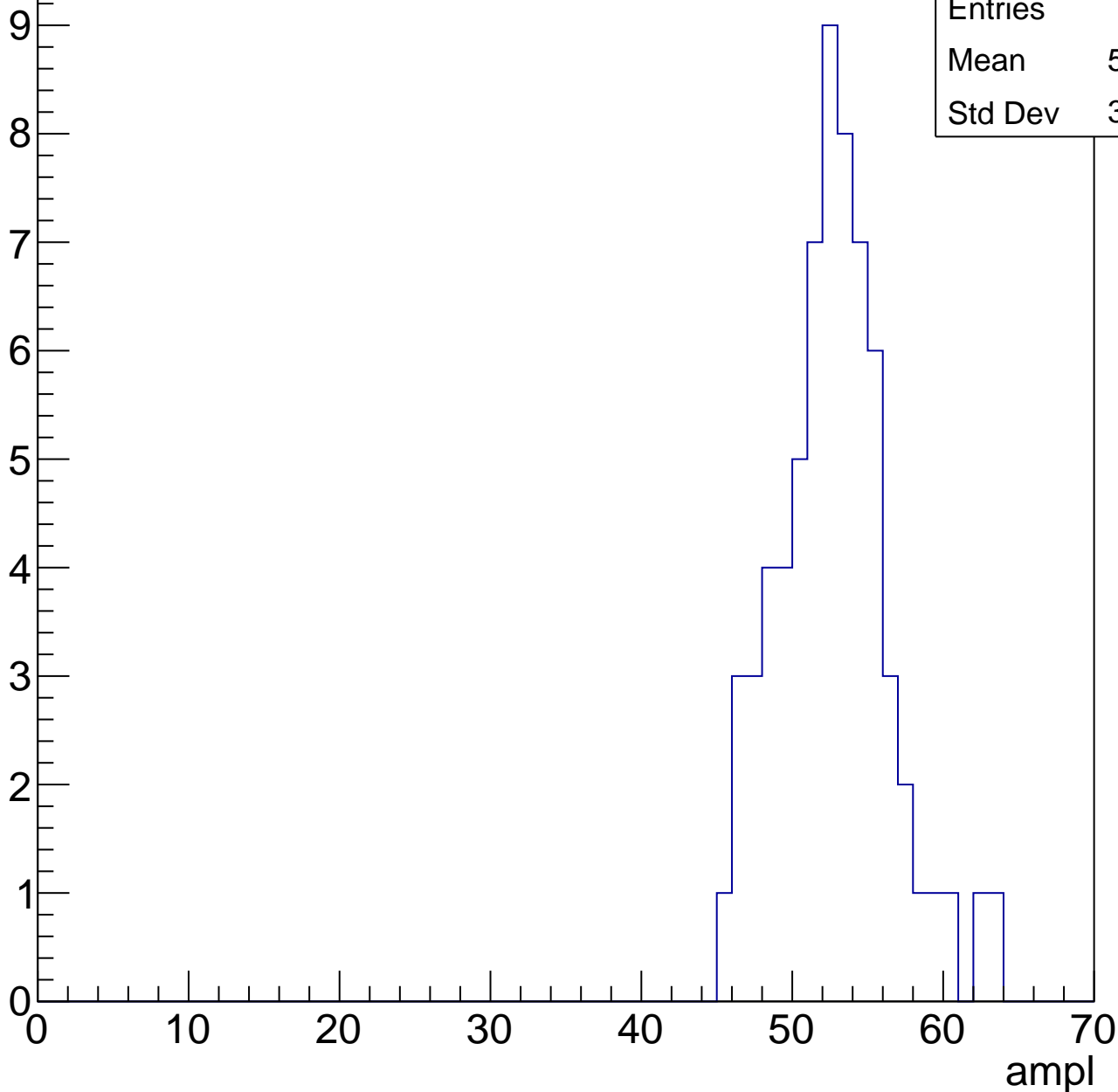


B1L101S, U23-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	52.28
Std Dev	3.689

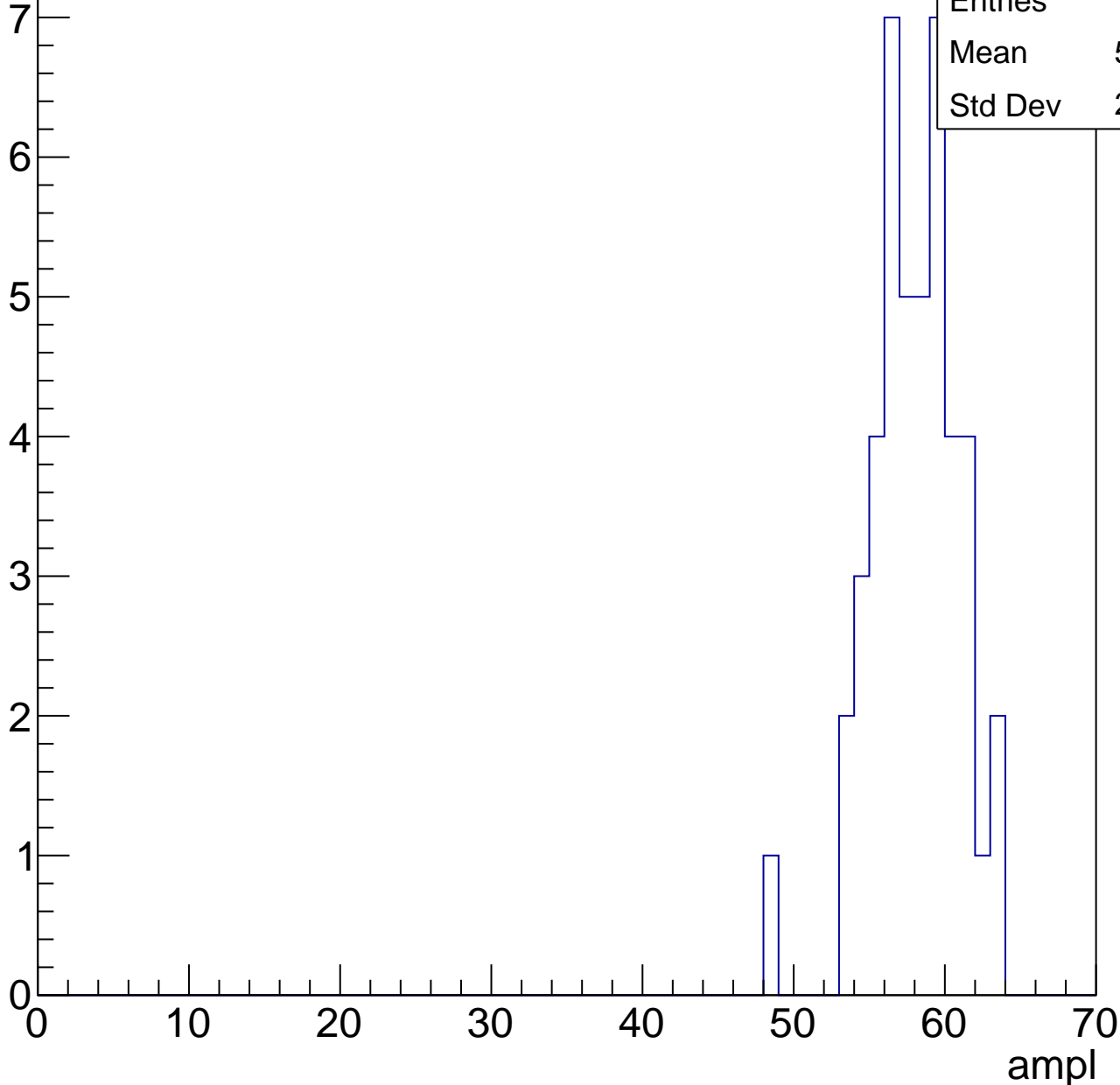


B1L101S, U23-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

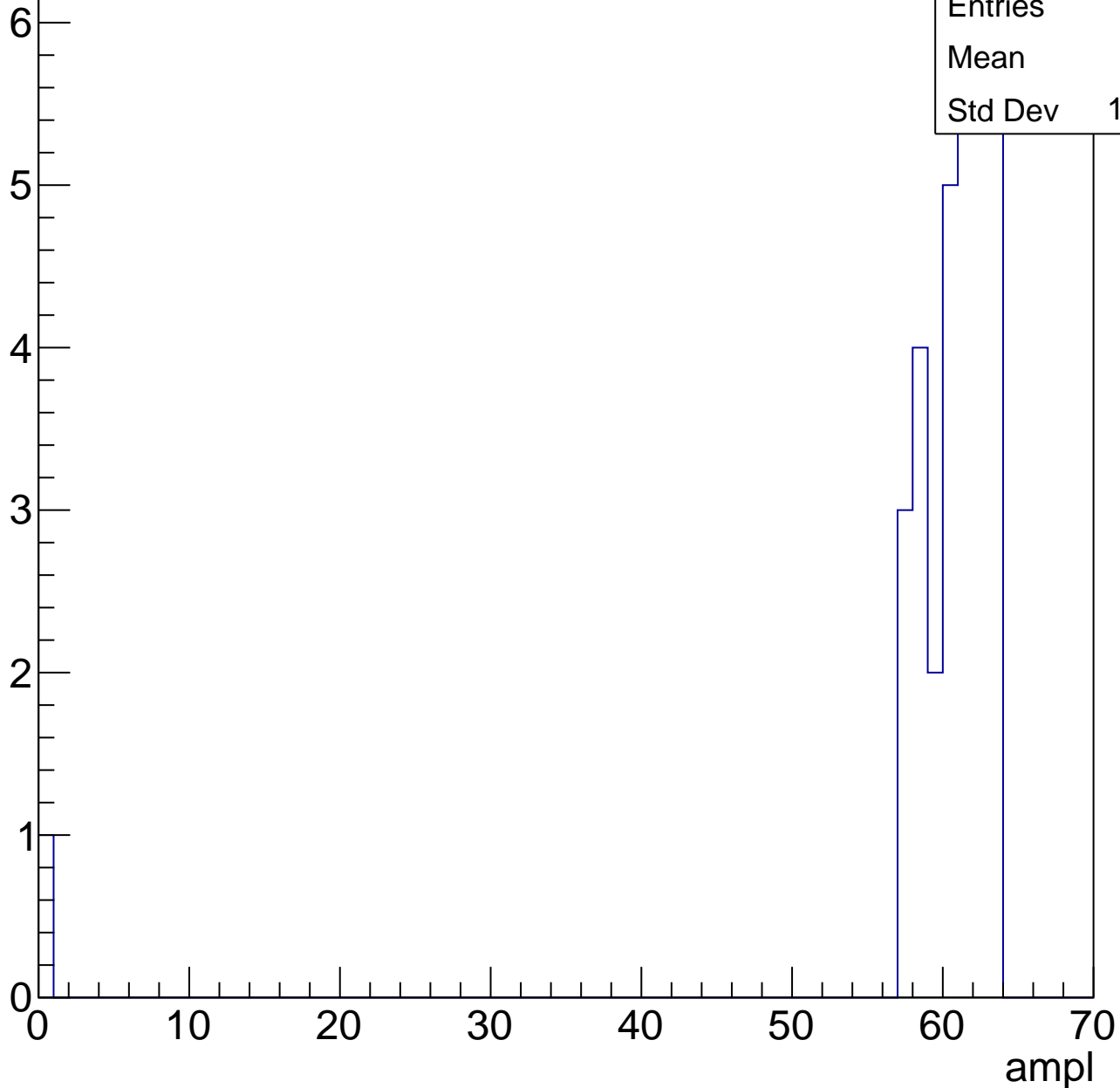
Entries	45
Mean	57.51
Std Dev	2.911



B1L101S, U23-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L101S, U23-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B1L101S, U23-ch27, adc0

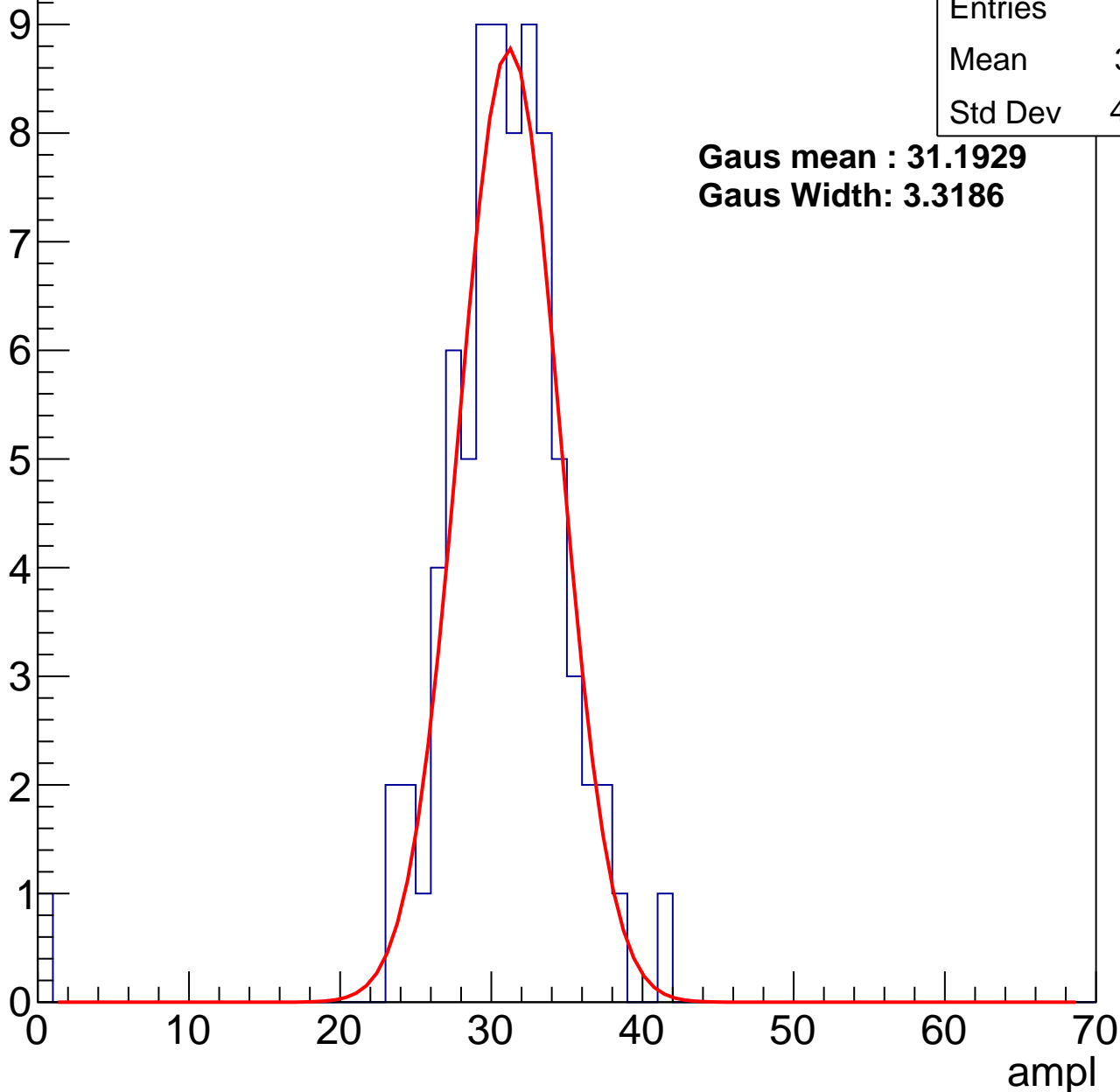
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.21
Std Dev	4.889

Gaus mean : 31.1929

Gaus Width: 3.3186



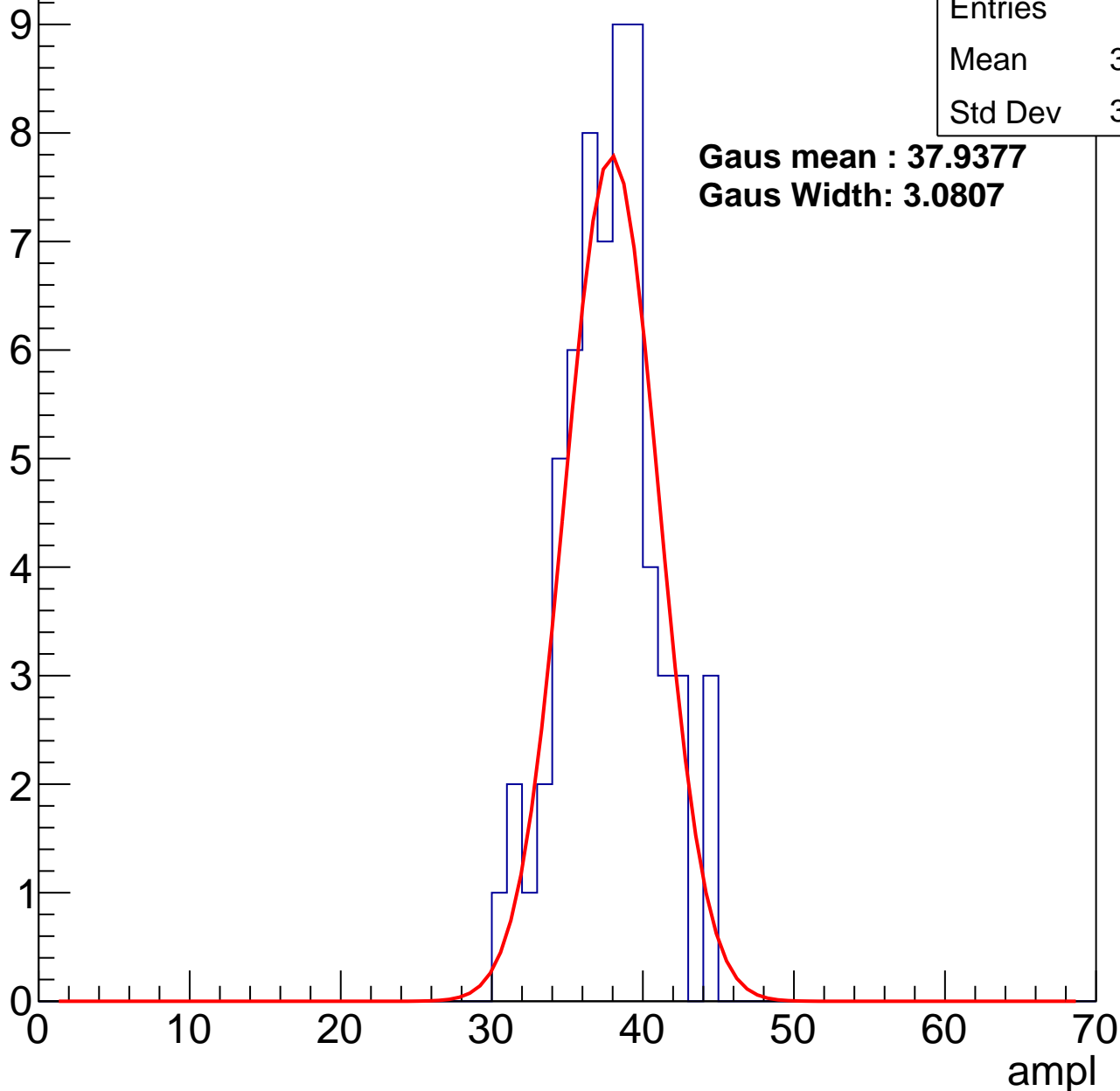
B1L101S, U23-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.32
Std Dev	3.085

Gaus mean : 37.9377
Gaus Width: 3.0807



B1L101S, U23-ch27, adc2

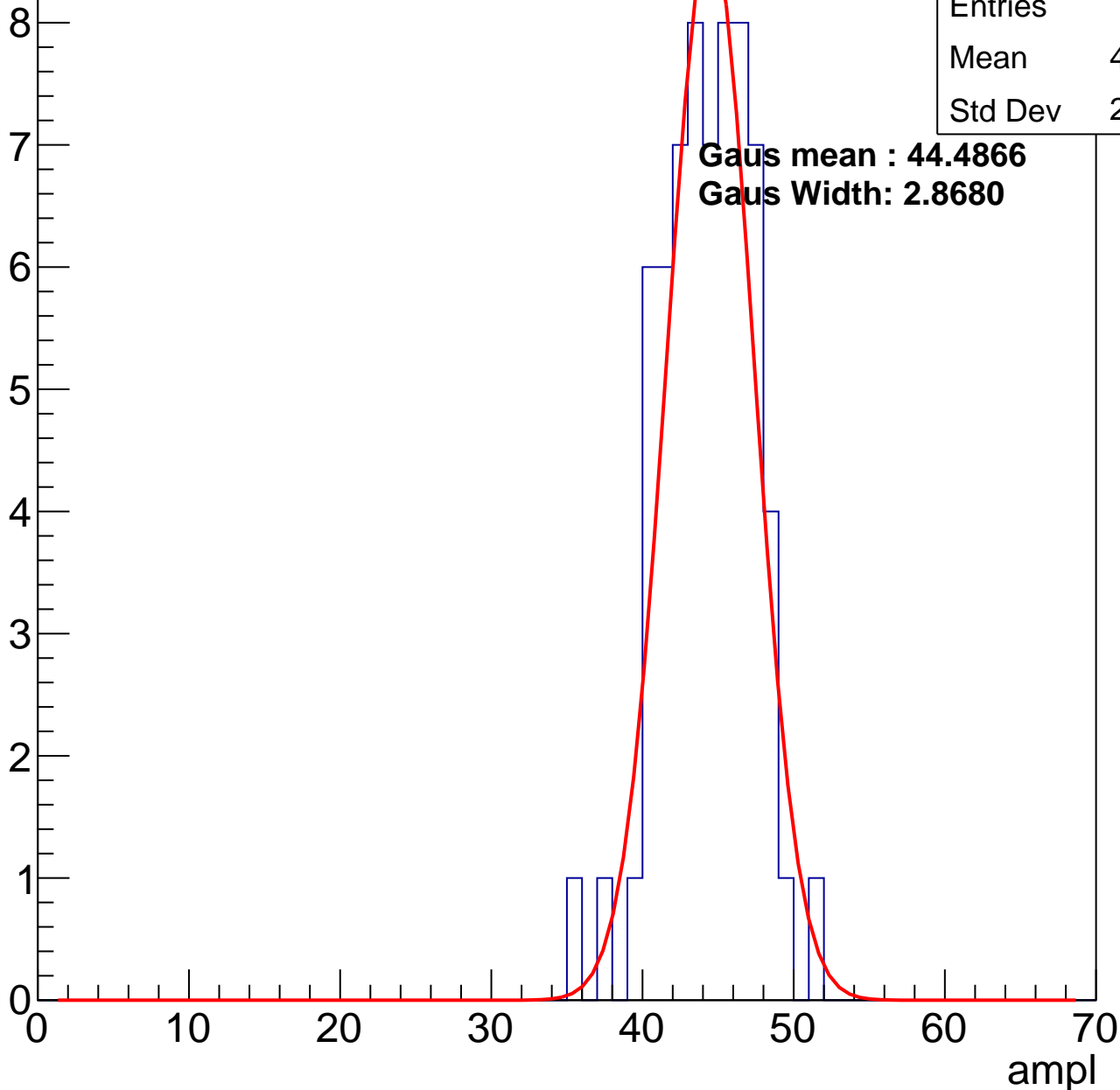
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.82
Std Dev	2.964

Gaus mean : 44.4866

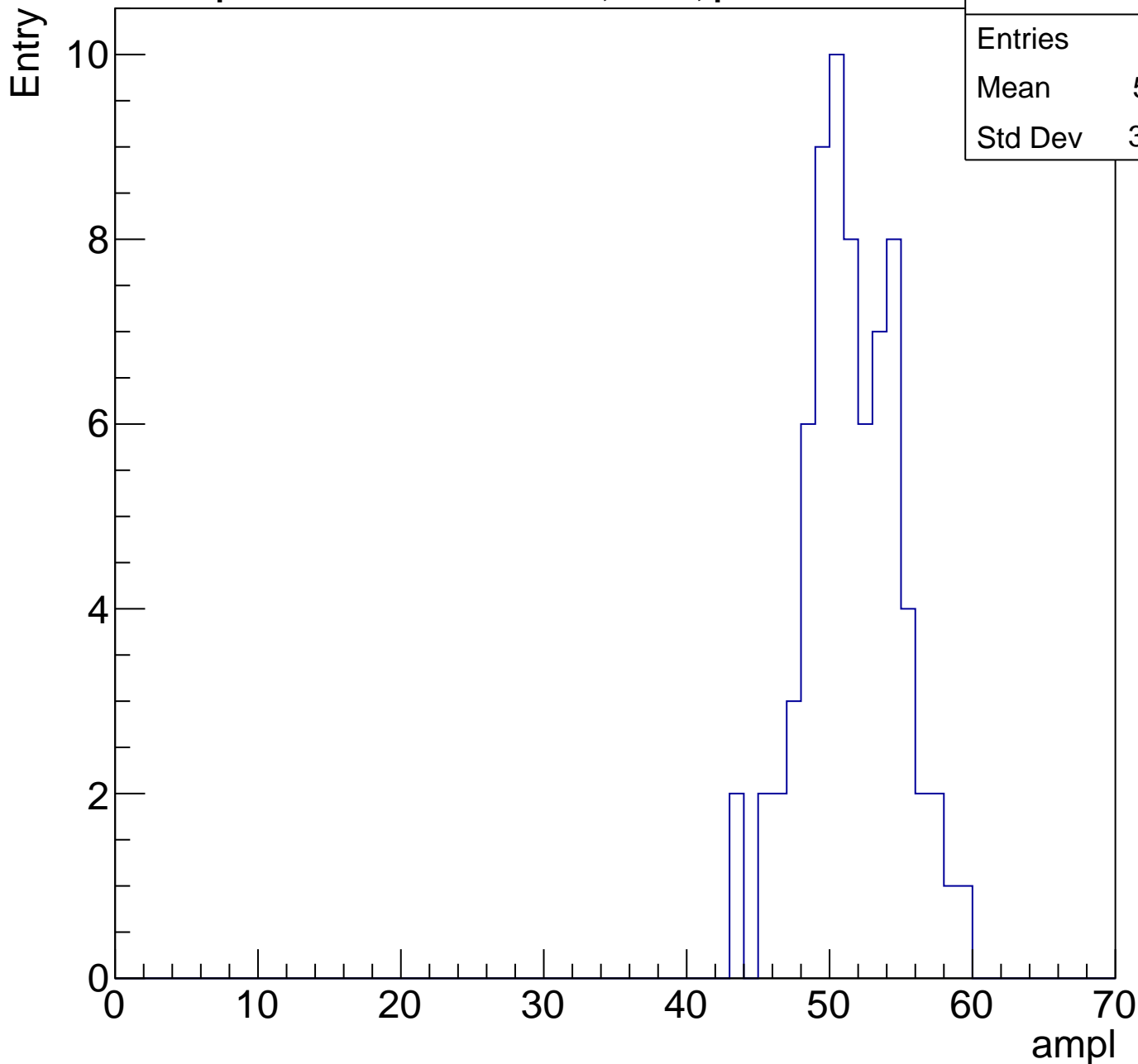
Gaus Width: 2.8680



B1L101S, U23-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	51.01
Std Dev	3.317

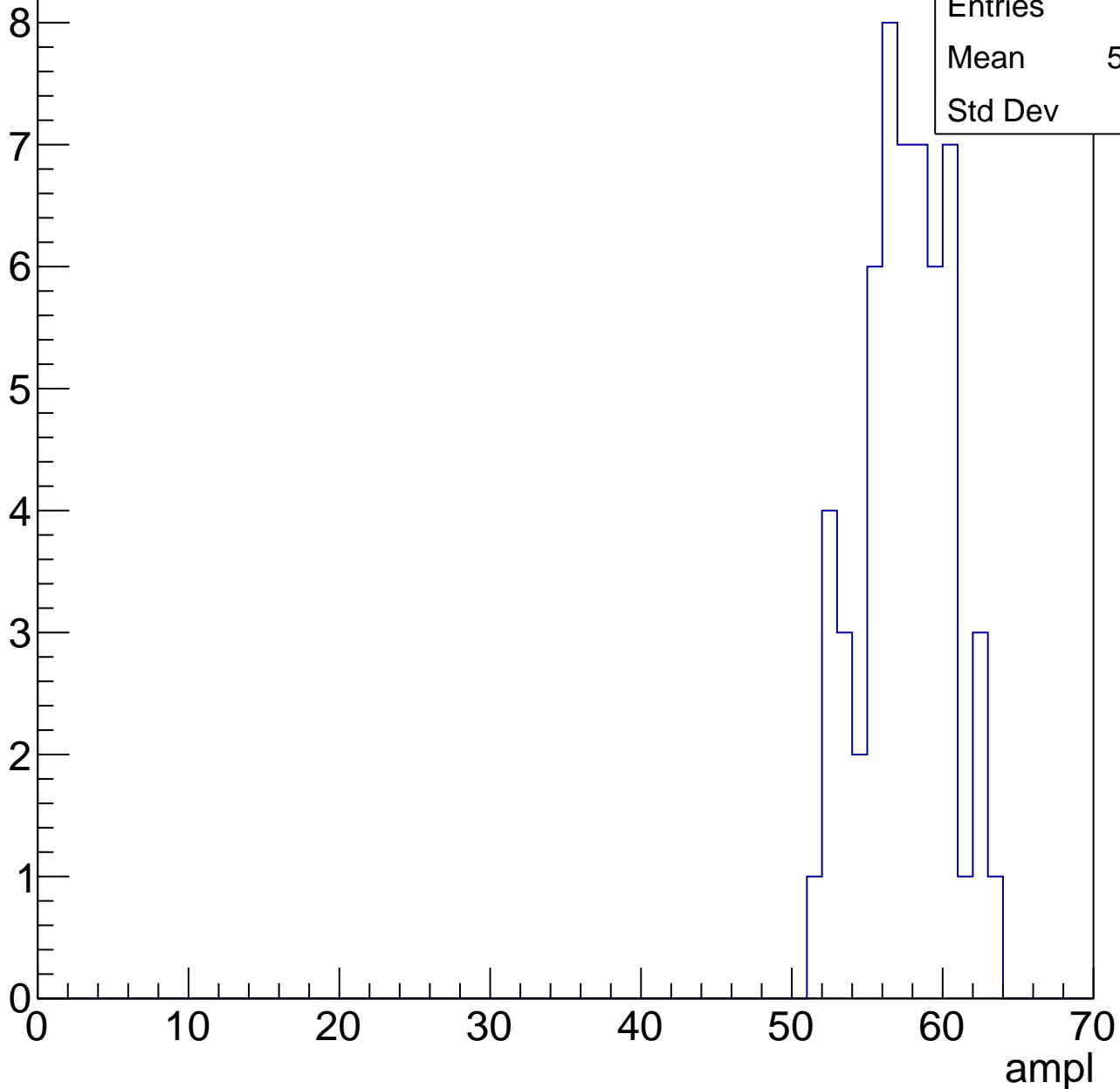


B1L101S, U23-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

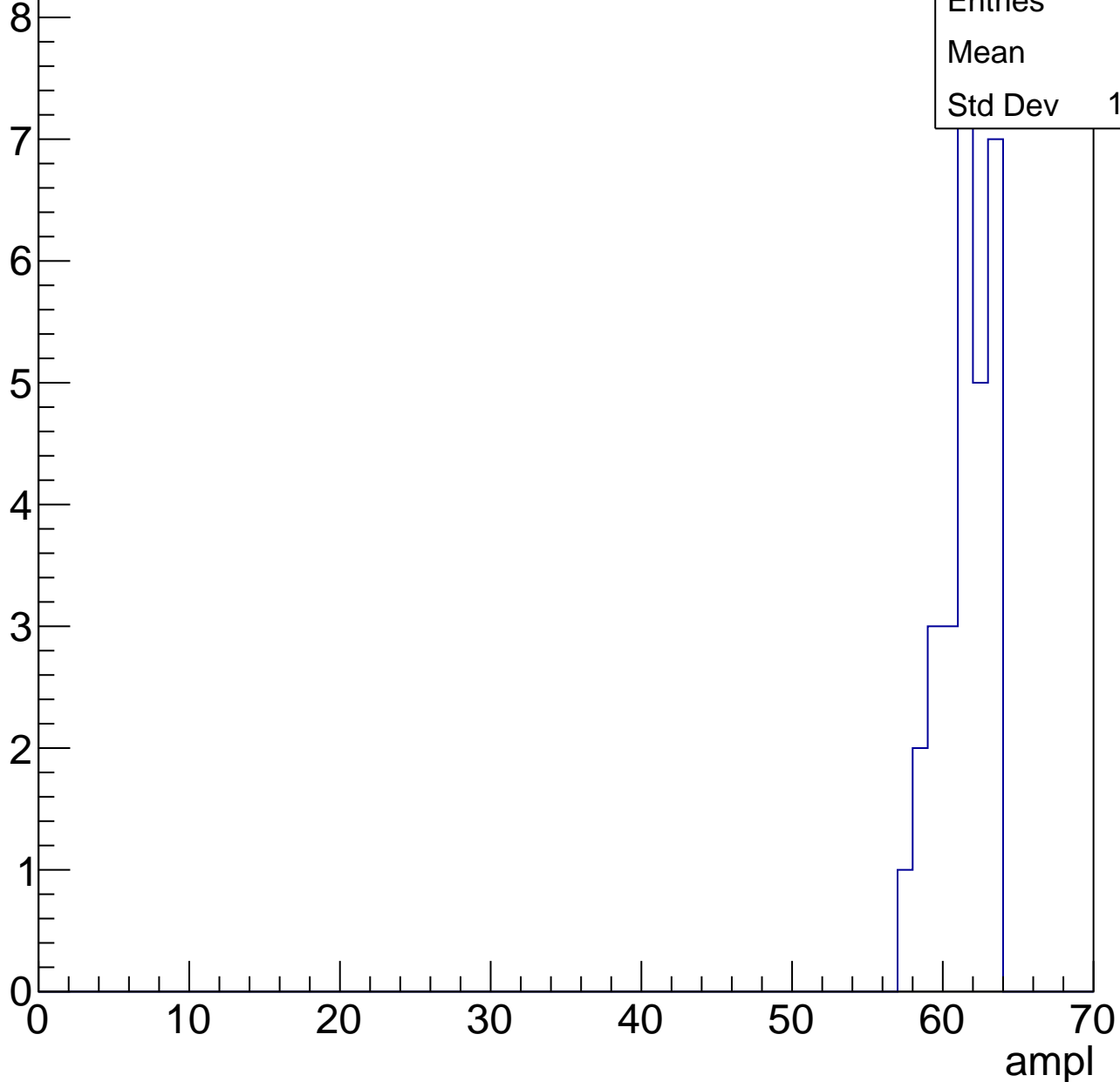
Entries	56
Mean	57.02
Std Dev	2.85



B1L101S, U23-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

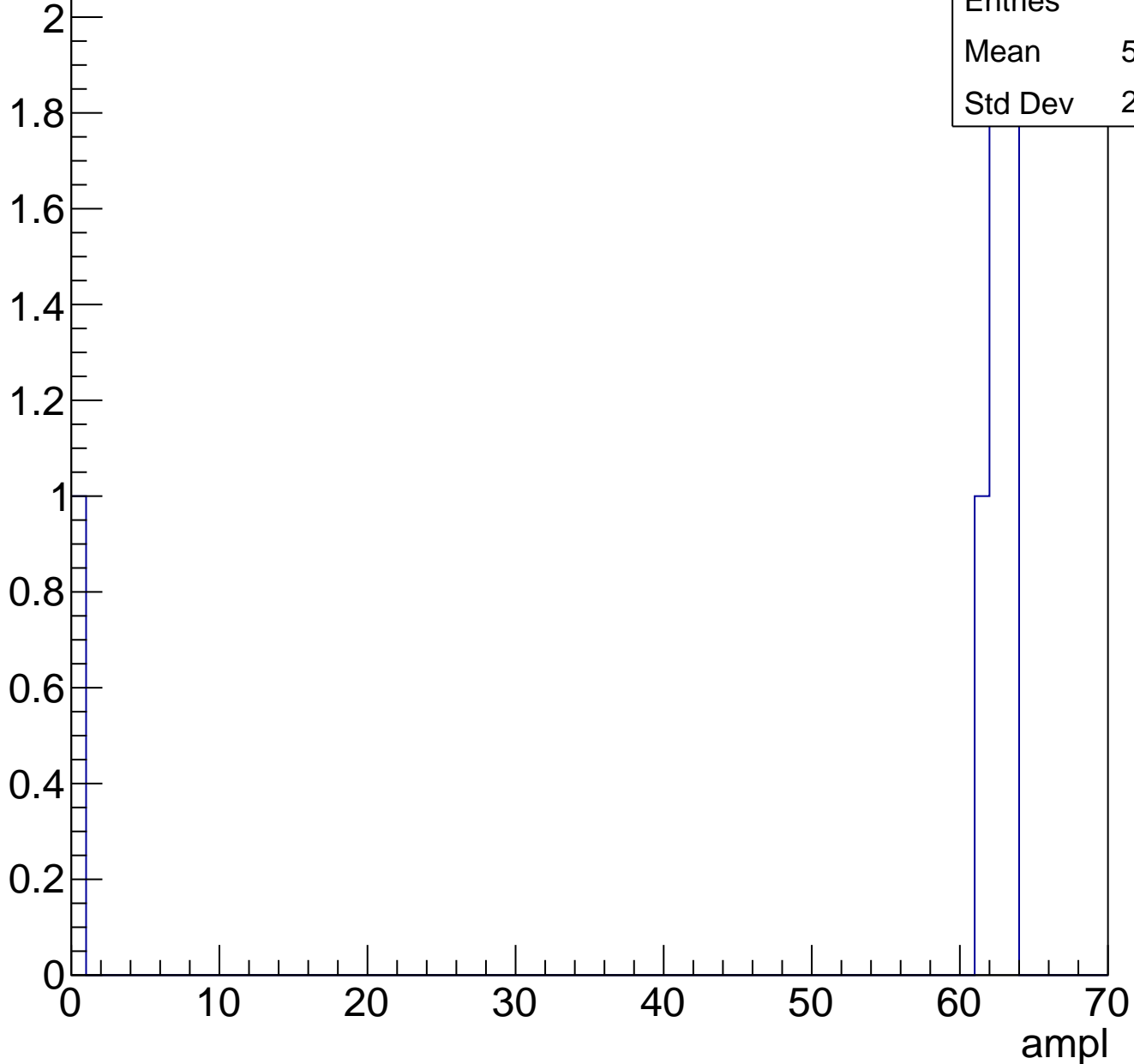
Entry



B1L101S, U23-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch28, adc0

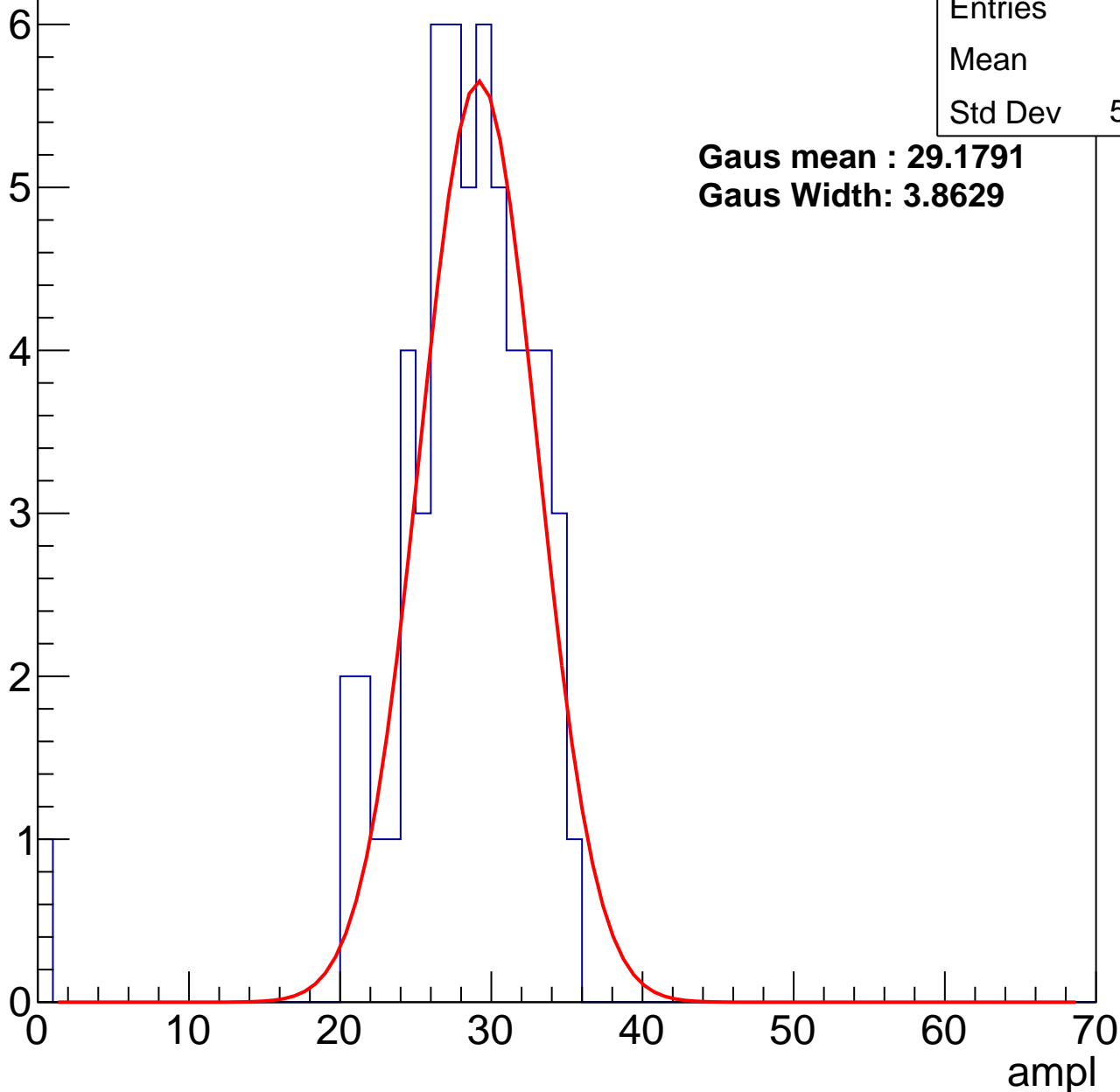
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	27.6
Std Dev	5.196

Gaus mean : 29.1791

Gaus Width: 3.8629



B1L101S, U23-ch28, adc1

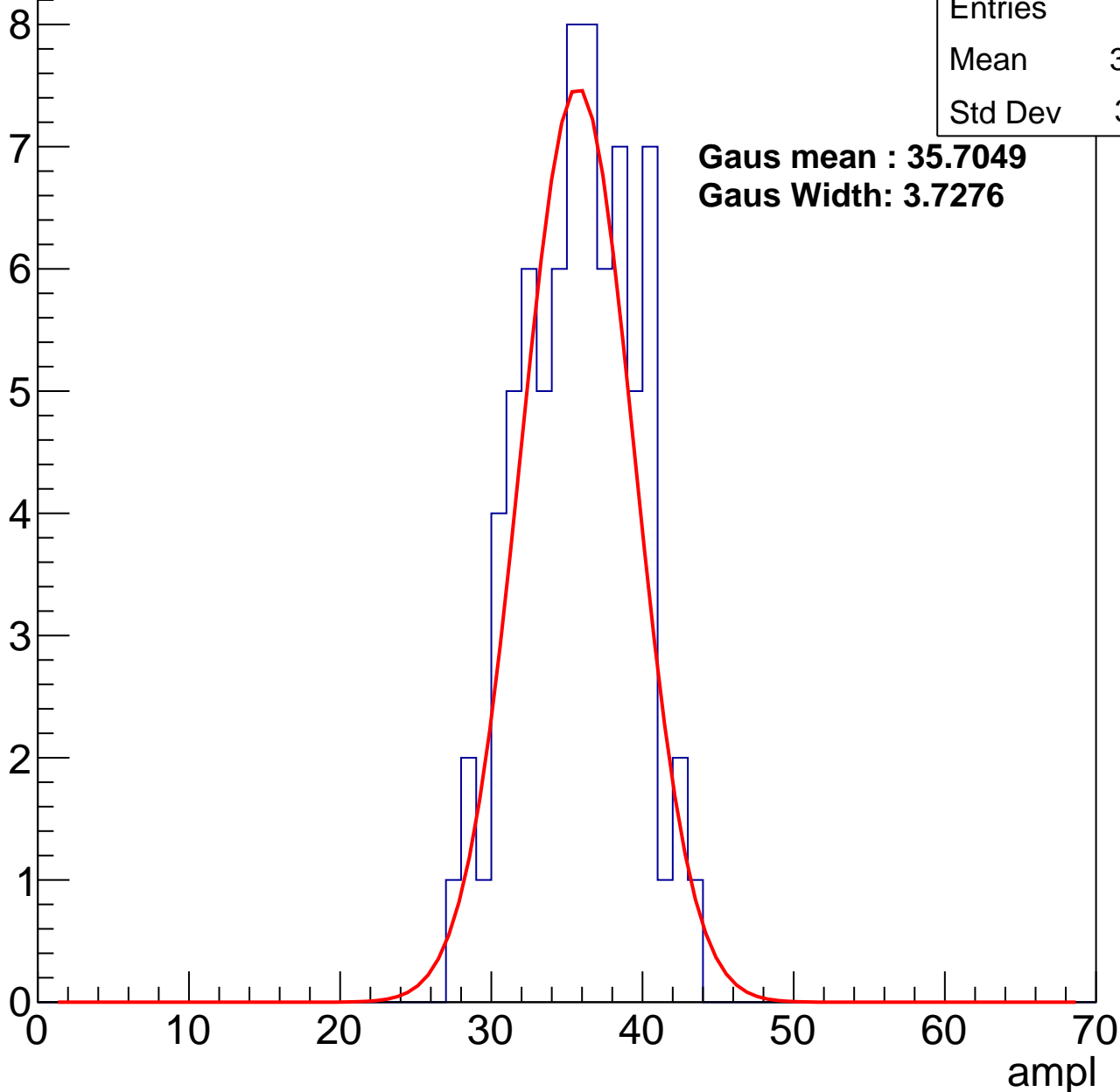
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.29
Std Dev	3.651

Gaus mean : 35.7049

Gaus Width: 3.7276



B1L101S, U23-ch28, adc2

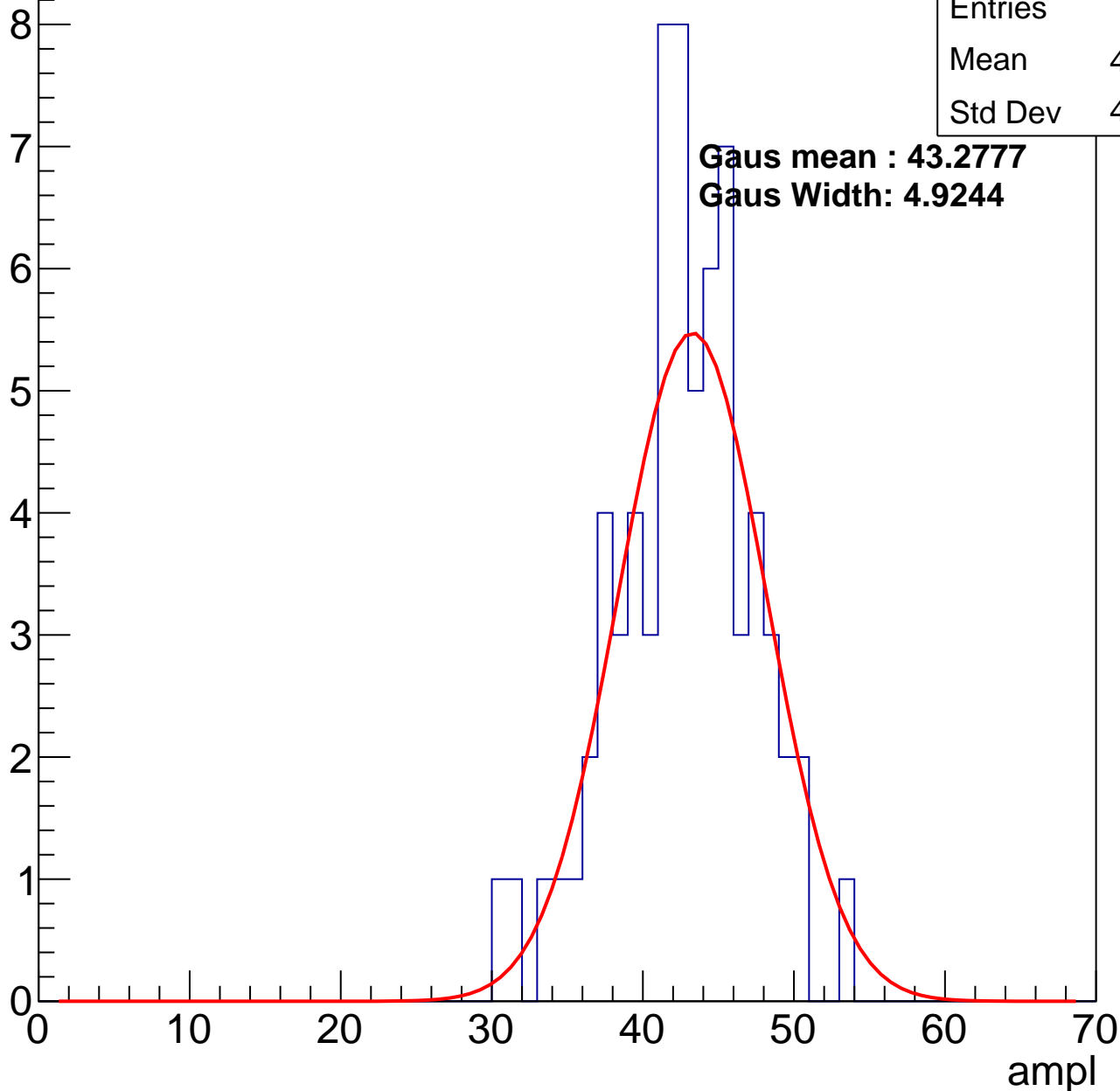
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.17
Std Dev	4.517

Gaus mean : 43.2777

Gaus Width: 4.9244

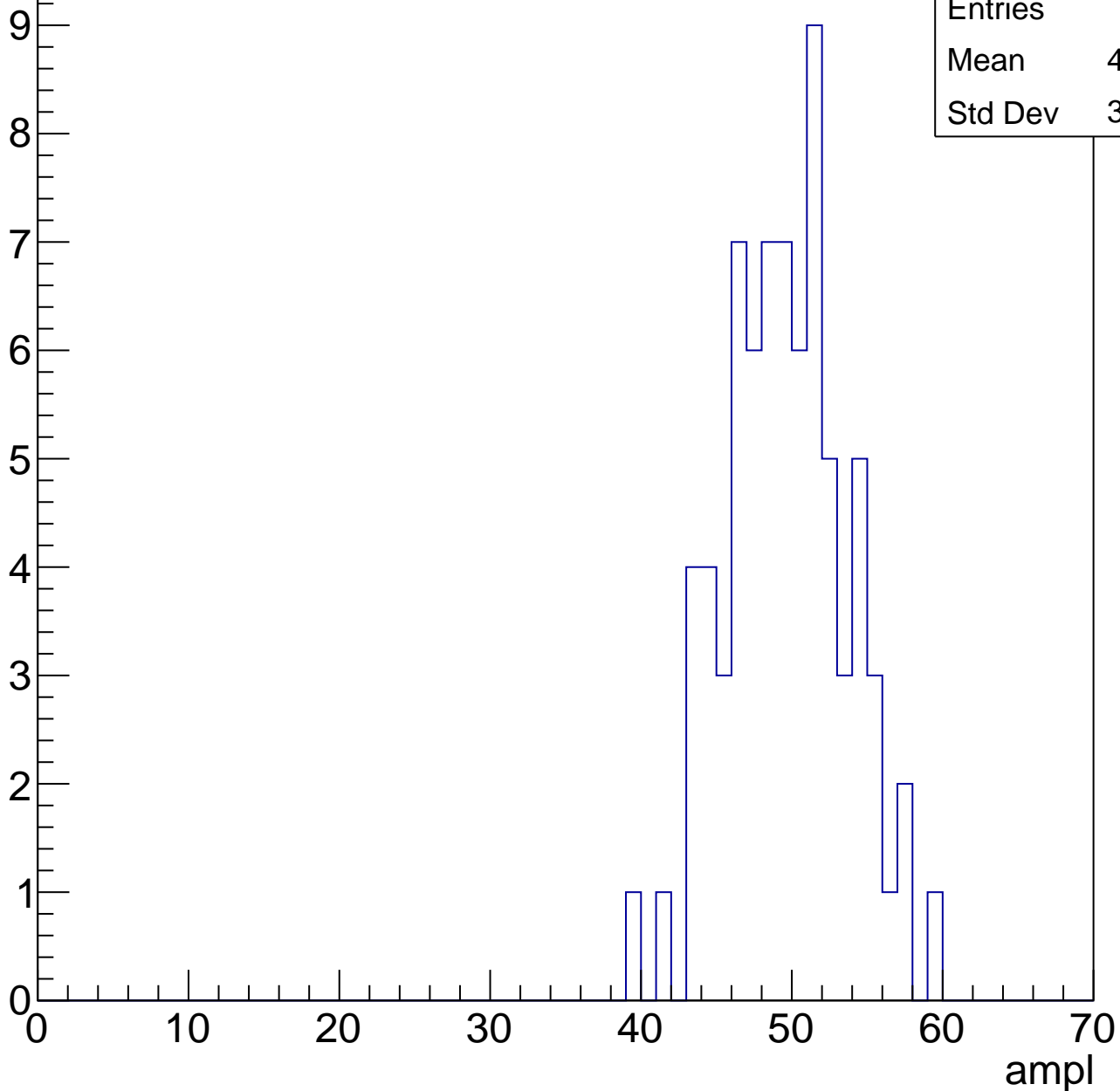


B1L101S, U23-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

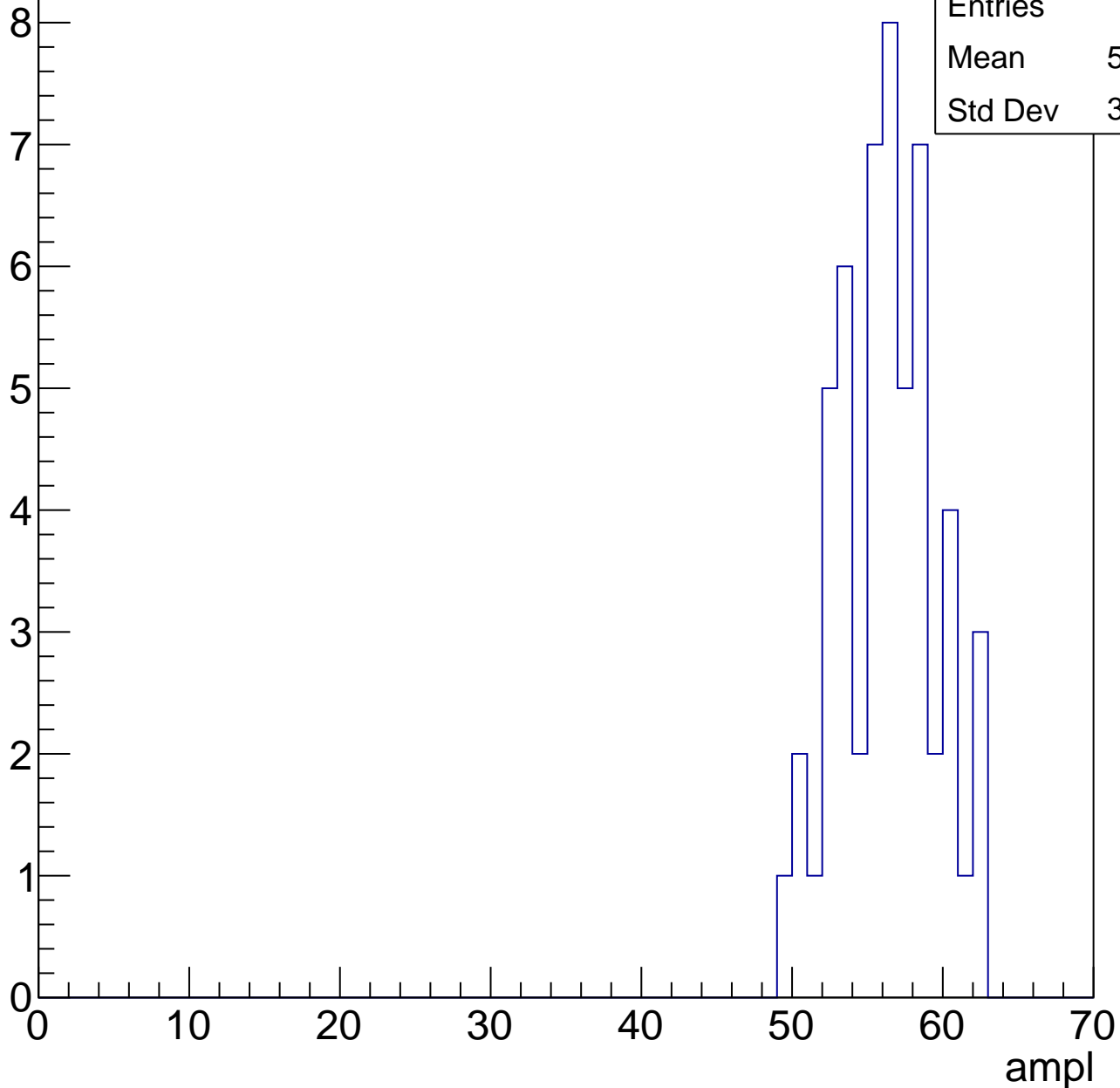
Entries	75
Mean	49.17
Std Dev	3.988



B1L101S, U23-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

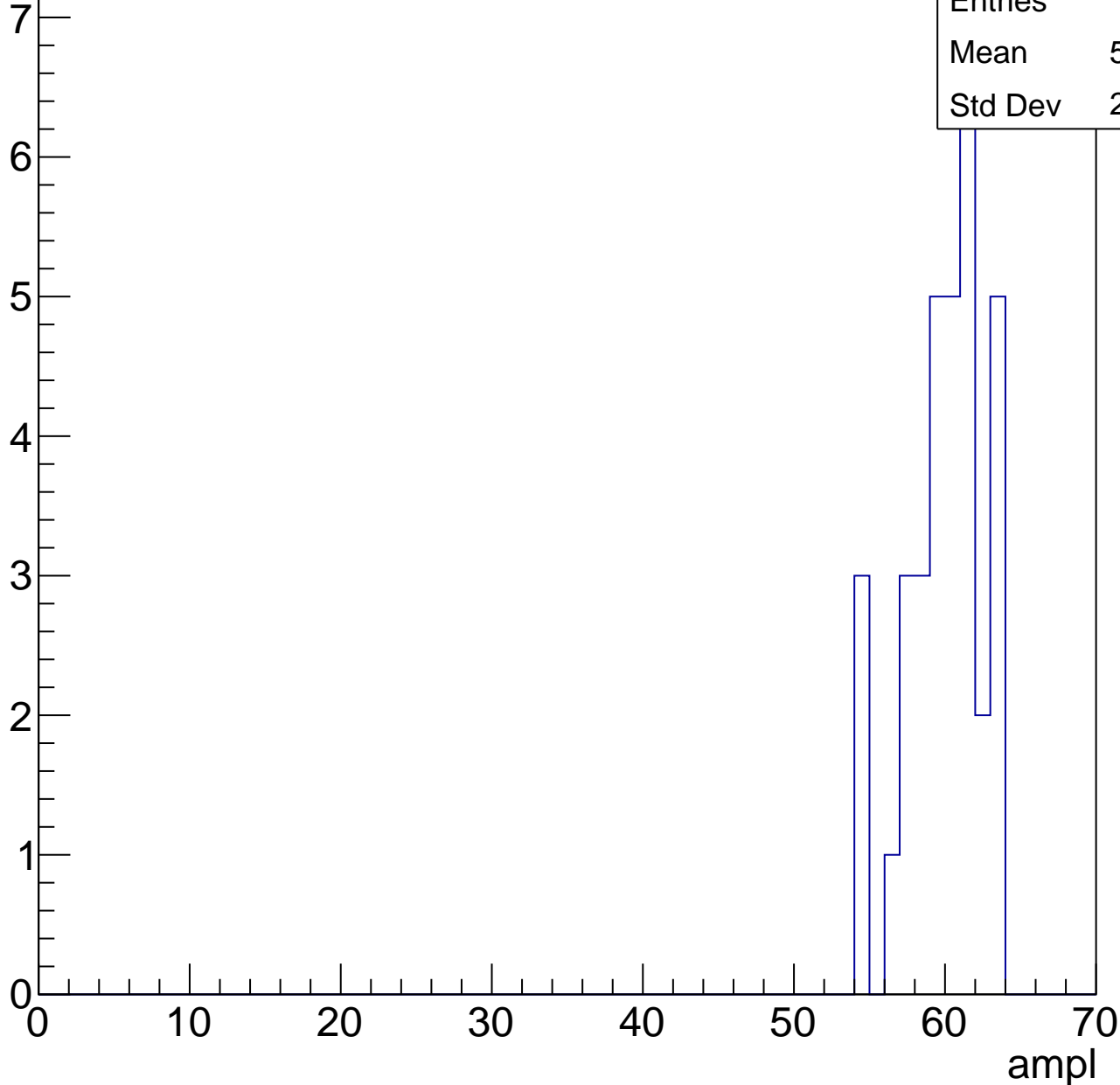


B1L101S, U23-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	59.53
Std Dev	2.546

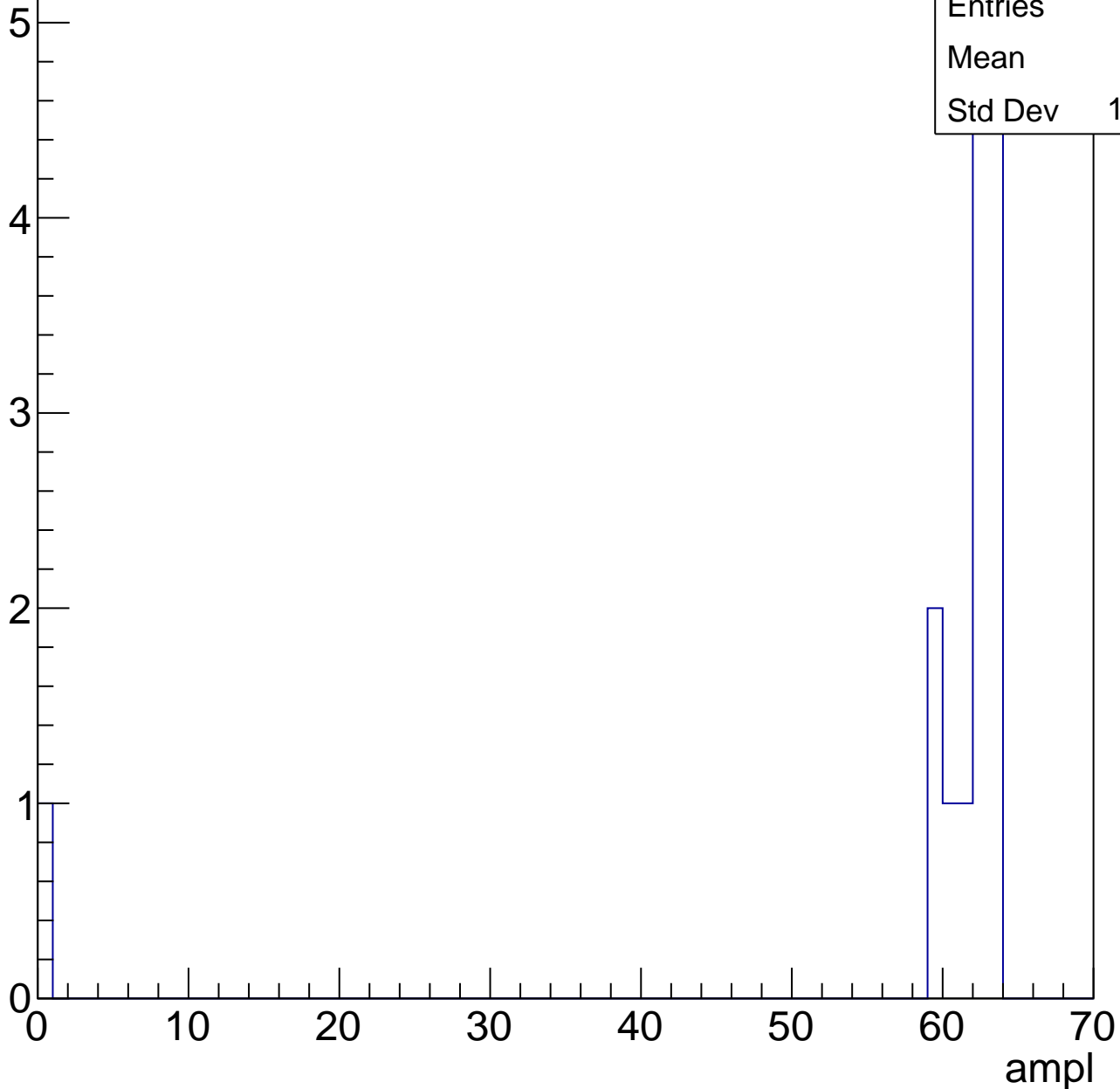


B1L101S, U23-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	57.6
Std Dev	15.45



B1L101S, U23-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch29, adc0

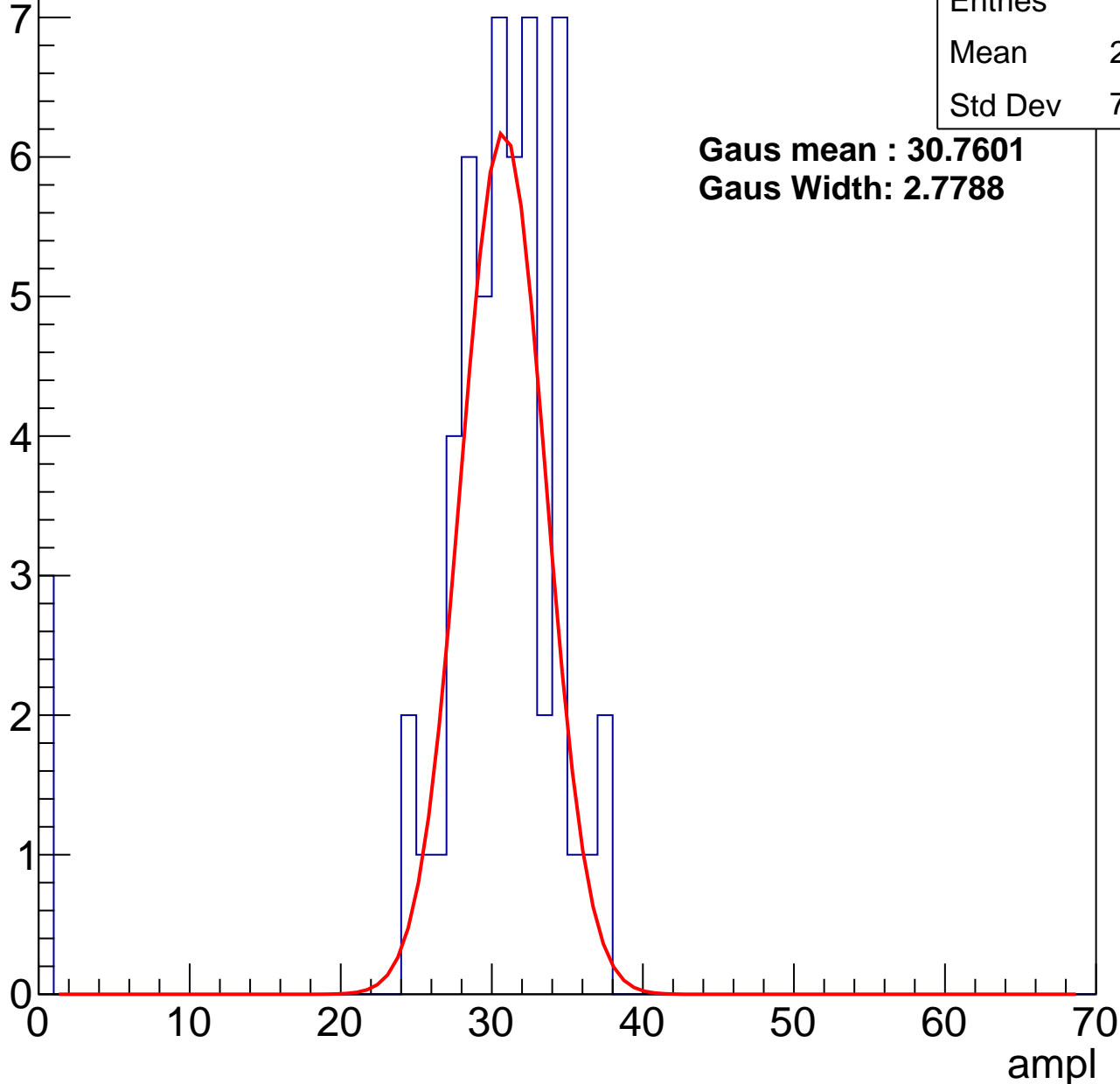
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.89
Std Dev	7.548

Gaus mean : 30.7601

Gaus Width: 2.7788



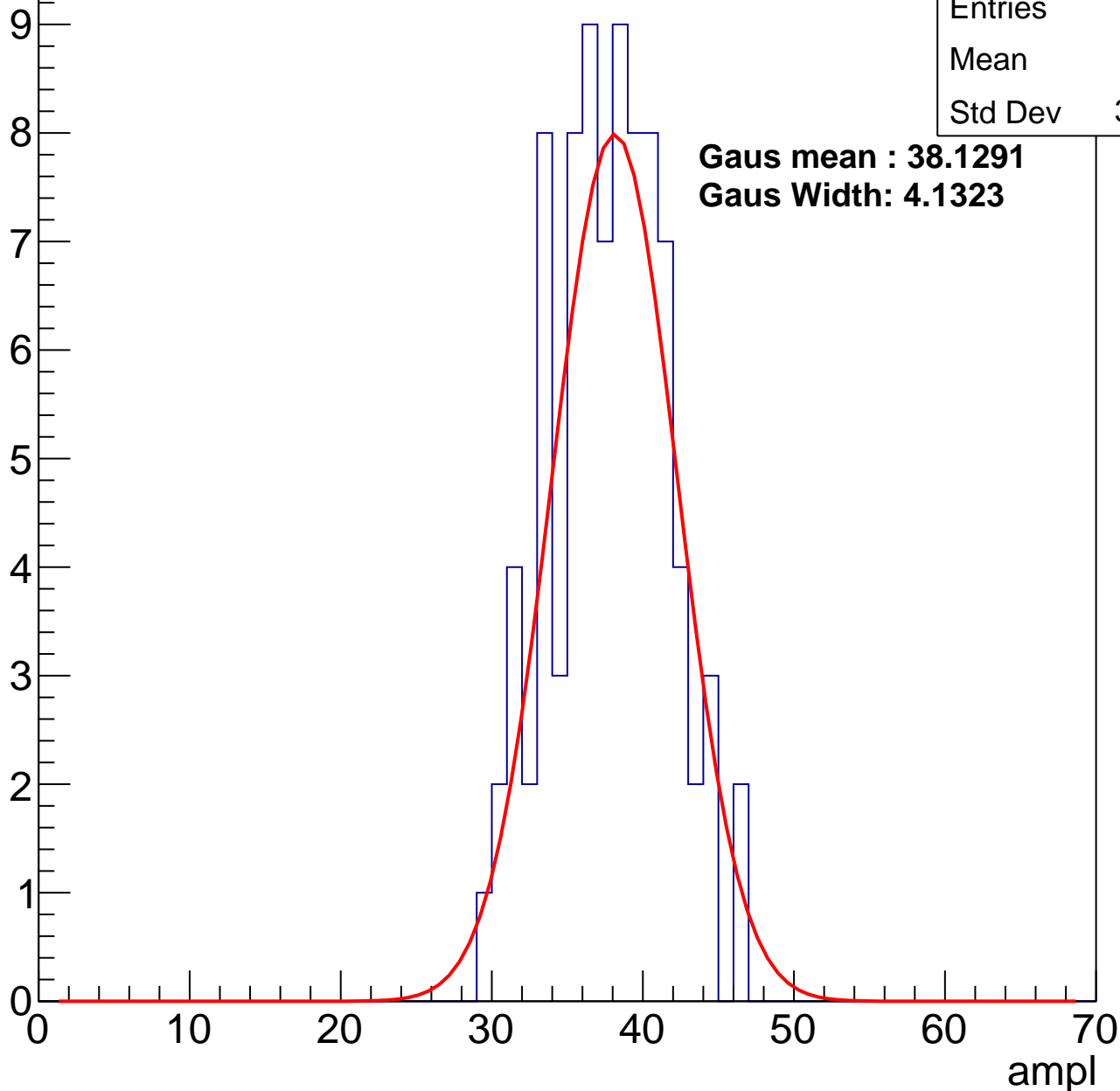
B1L101S, U23-ch29, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	37.3
Std Dev	3.791

Gaus mean : 38.1291
Gaus Width: 4.1323

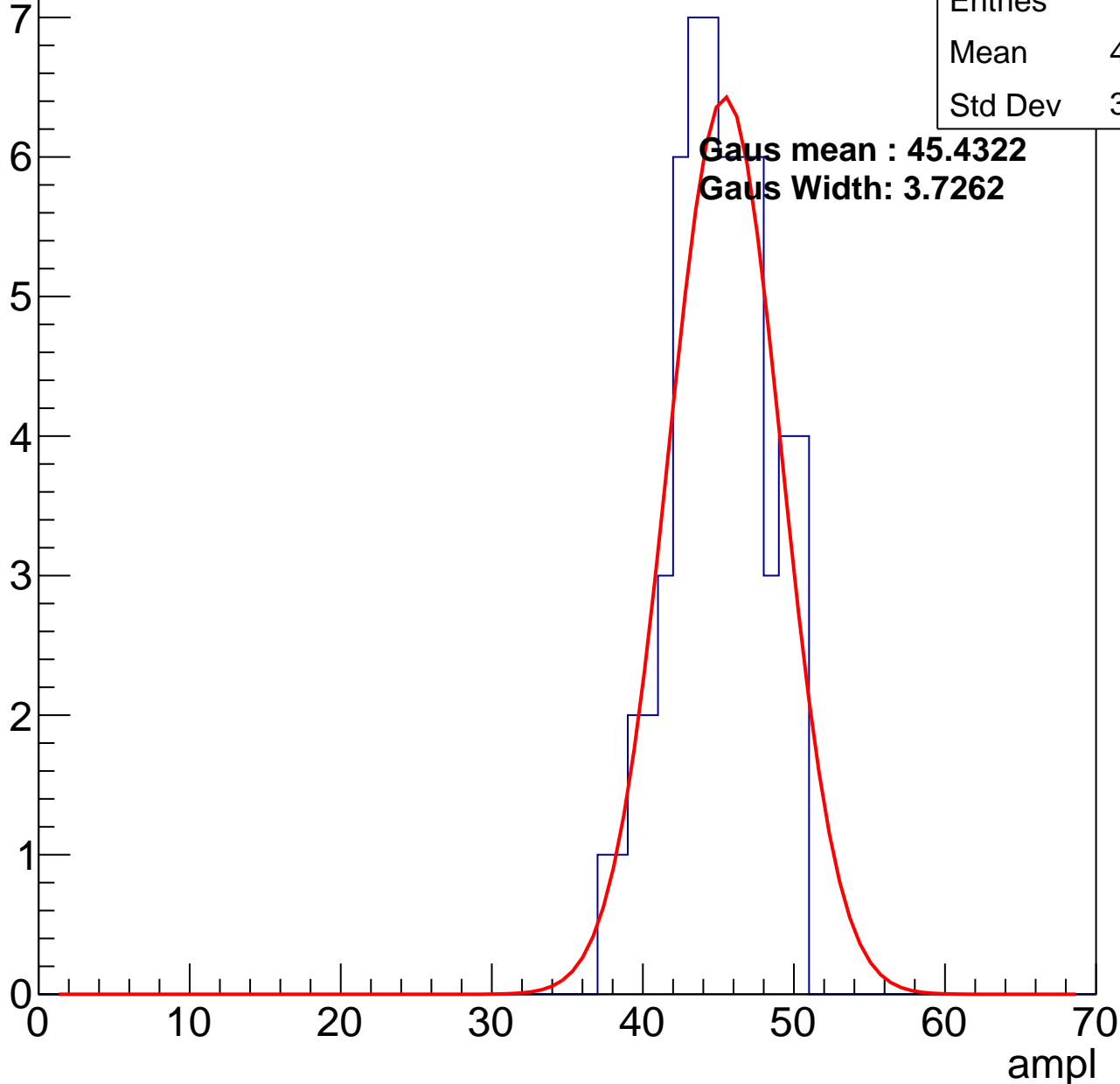


B1L101S, U23-ch29, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.57
Std Dev	3.168



B1L101S, U23-ch29, adc3

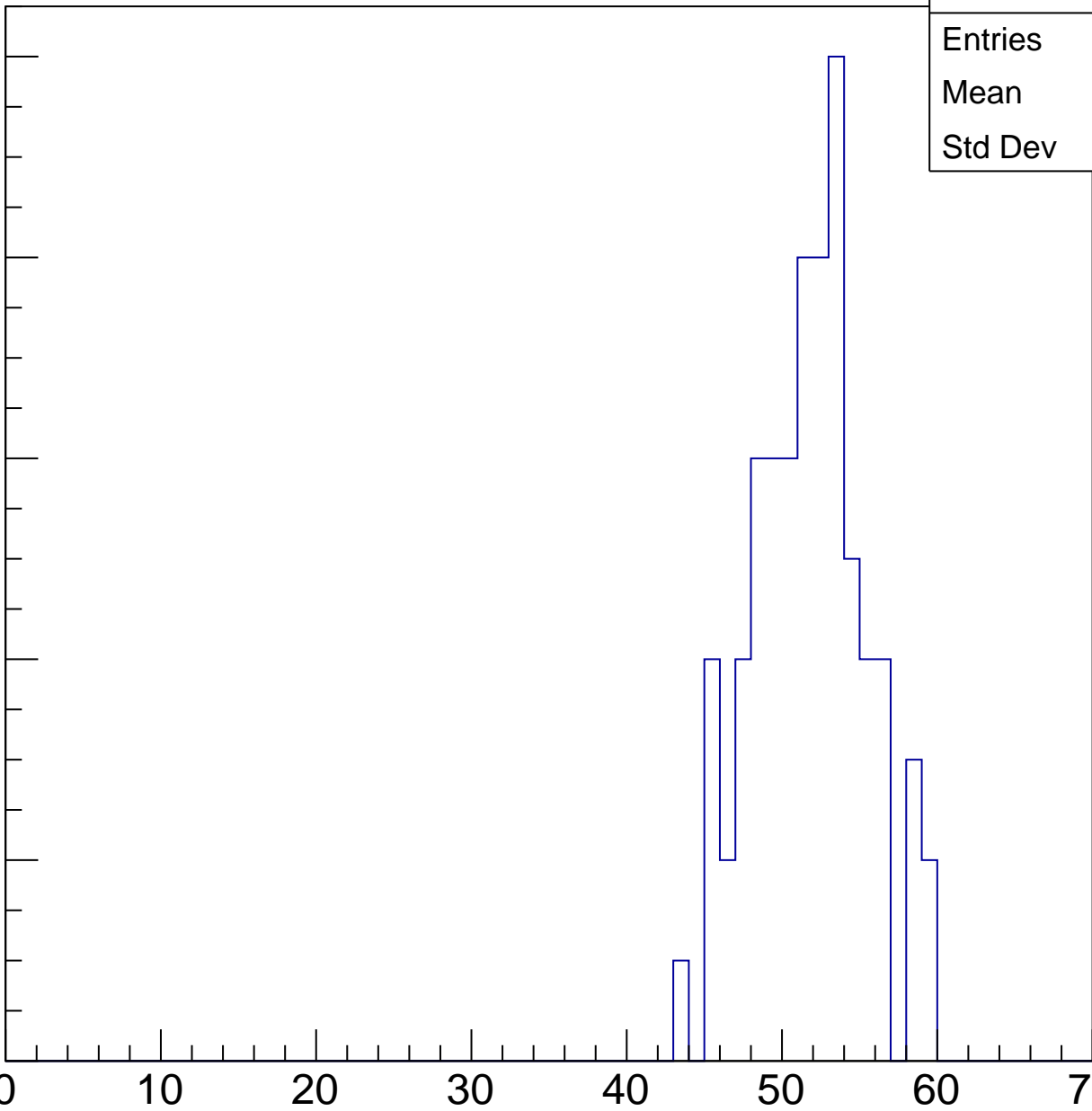
calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	51.3
Std Dev	3.572

Entry

10
8
6
4
2
0

ampl



B1L101S, U23-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	53
Mean	57.15
Std Dev	3.098

Entry

10

8

6

4

2

0

0

10

20

30

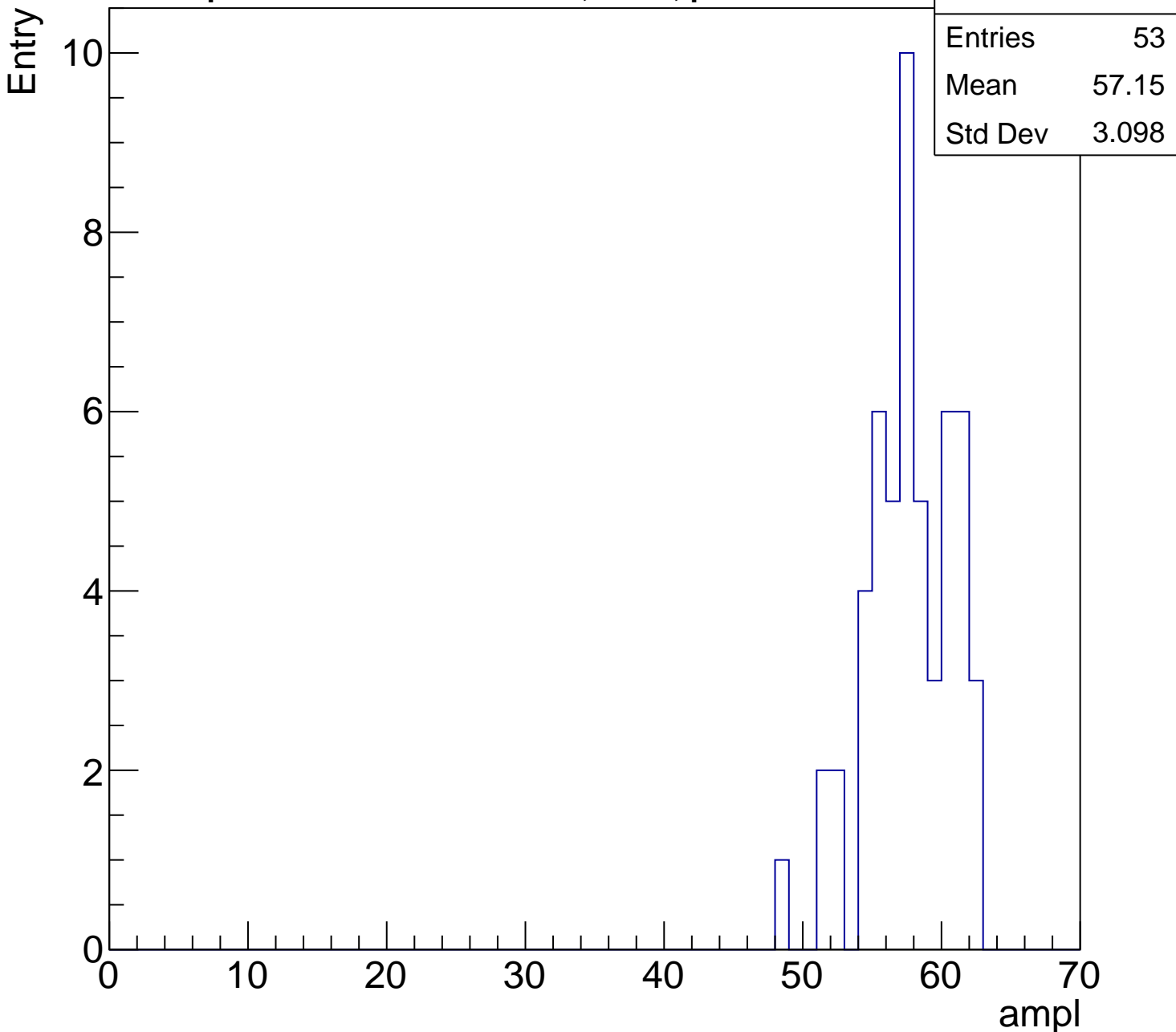
40

50

60

ampl

70

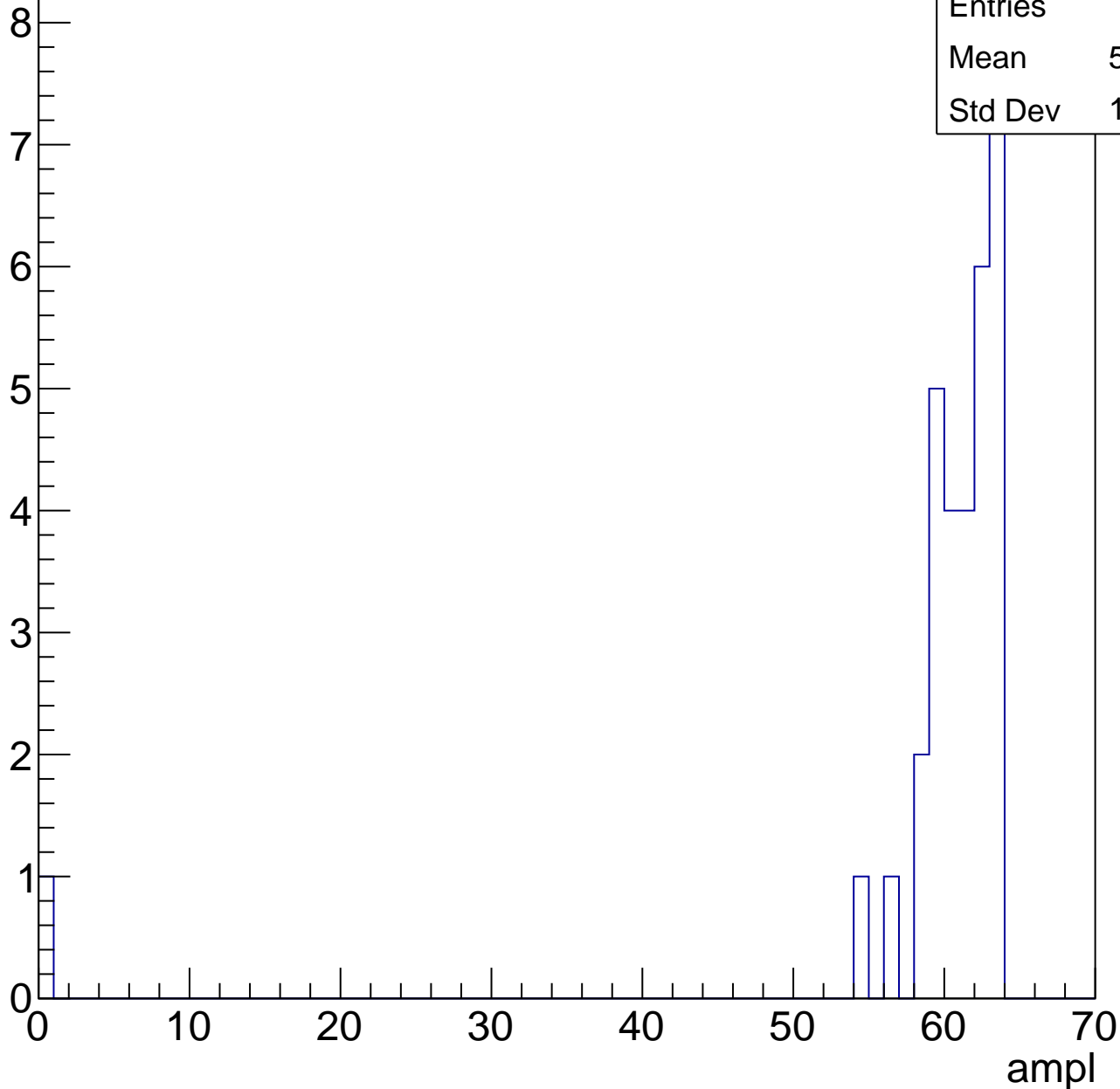


B1L101S, U23-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.78
Std Dev	10.78



B1L101S, U23-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch30, adc0

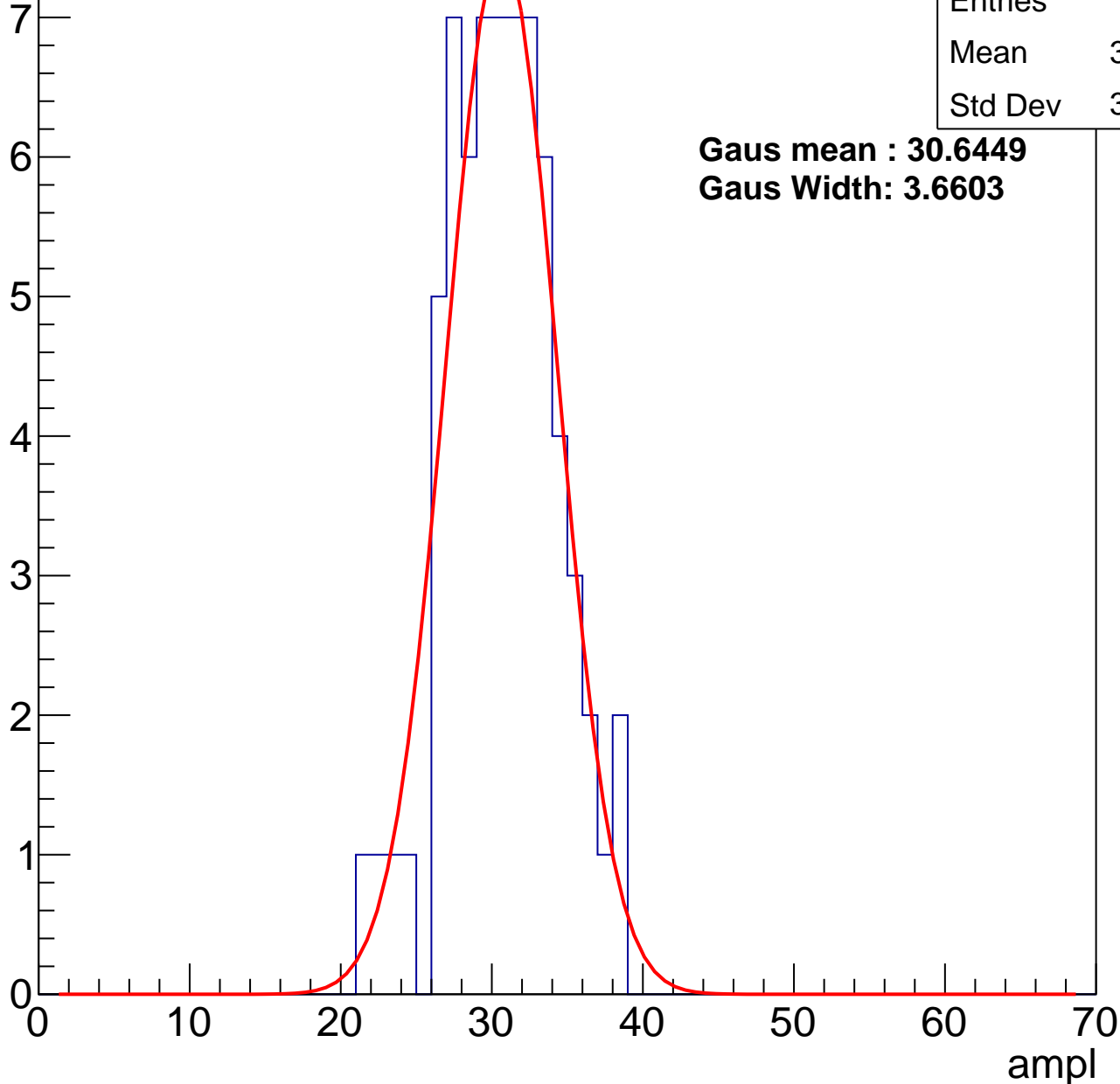
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.22
Std Dev	3.593

Gaus mean : 30.6449

Gaus Width: 3.6603



B1L101S, U23-ch30, adc1

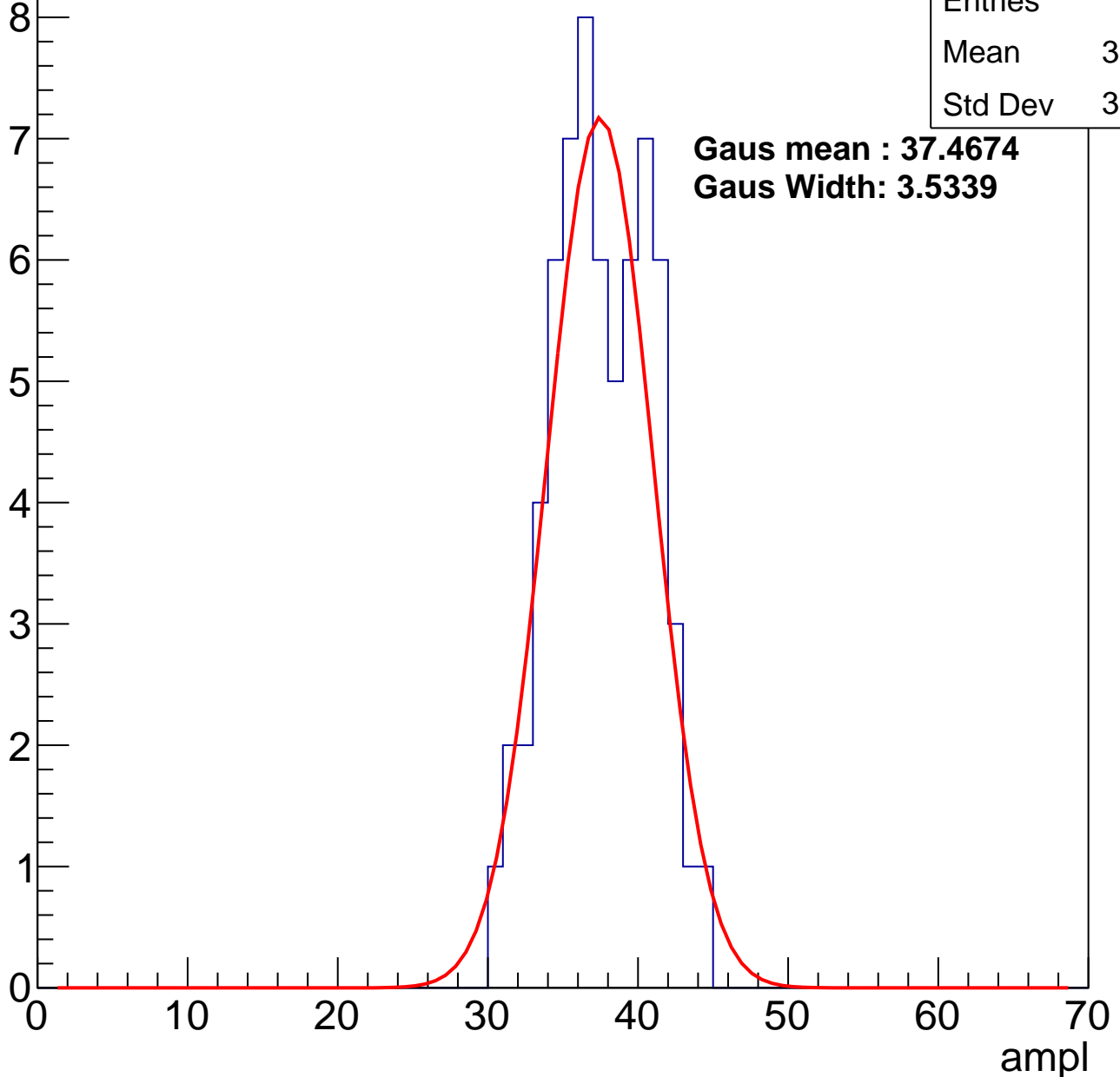
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.08
Std Dev	3.217

Gaus mean : 37.4674

Gaus Width: 3.5339



B1L101S, U23-ch30, adc2

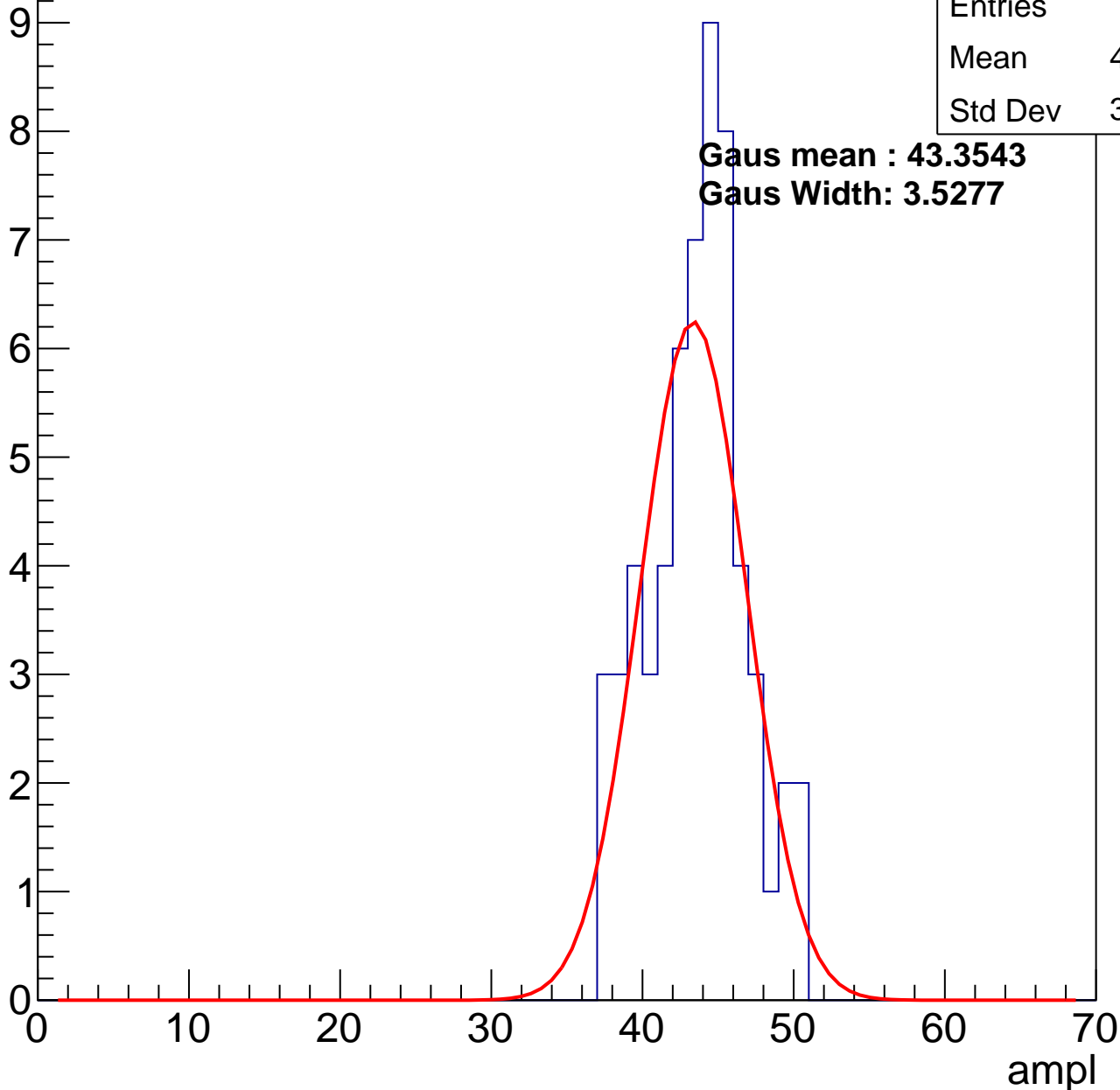
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.14
Std Dev	3.228

Gaus mean : 43.3543

Gaus Width: 3.5277

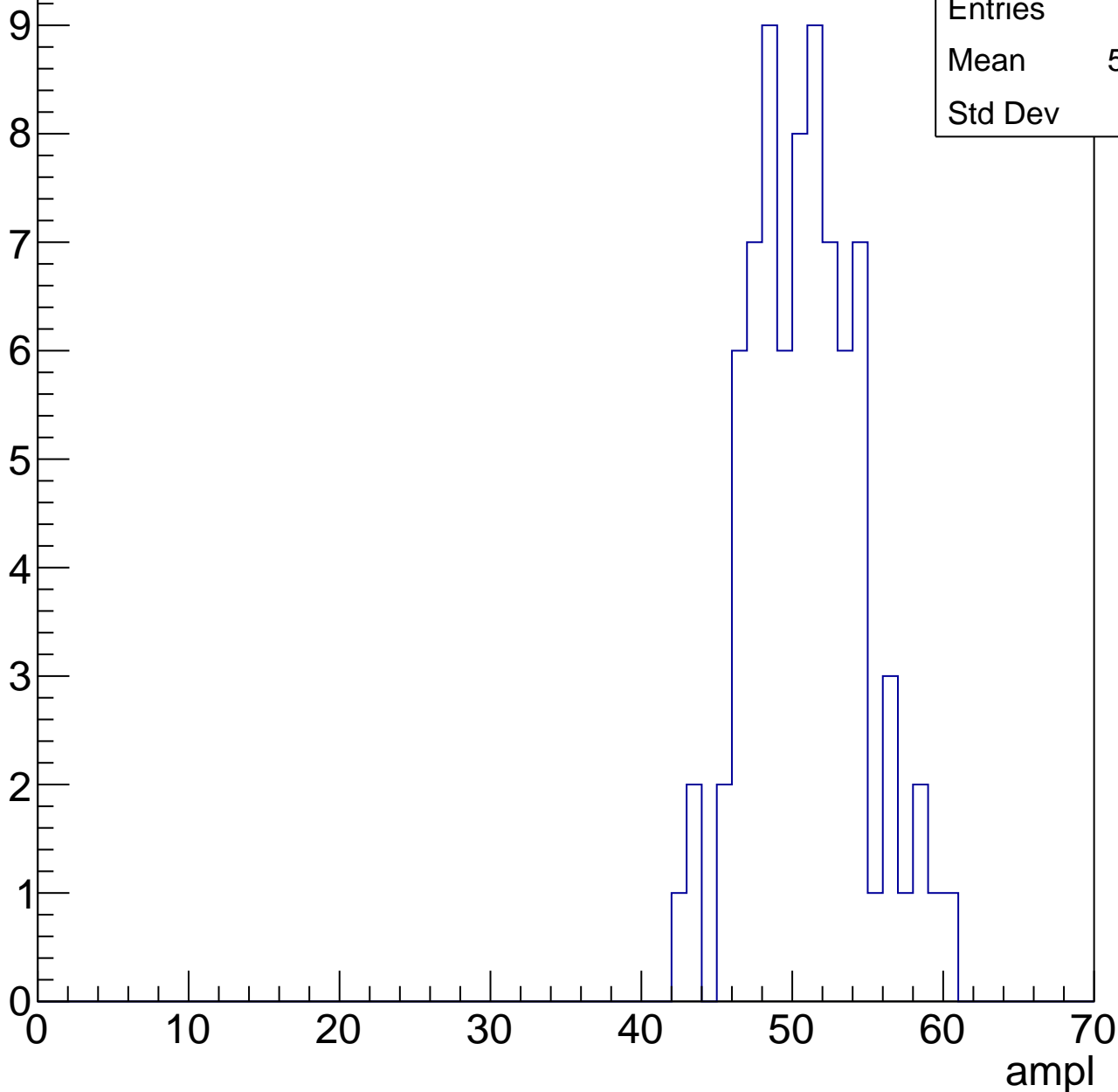


B1L101S, U23-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

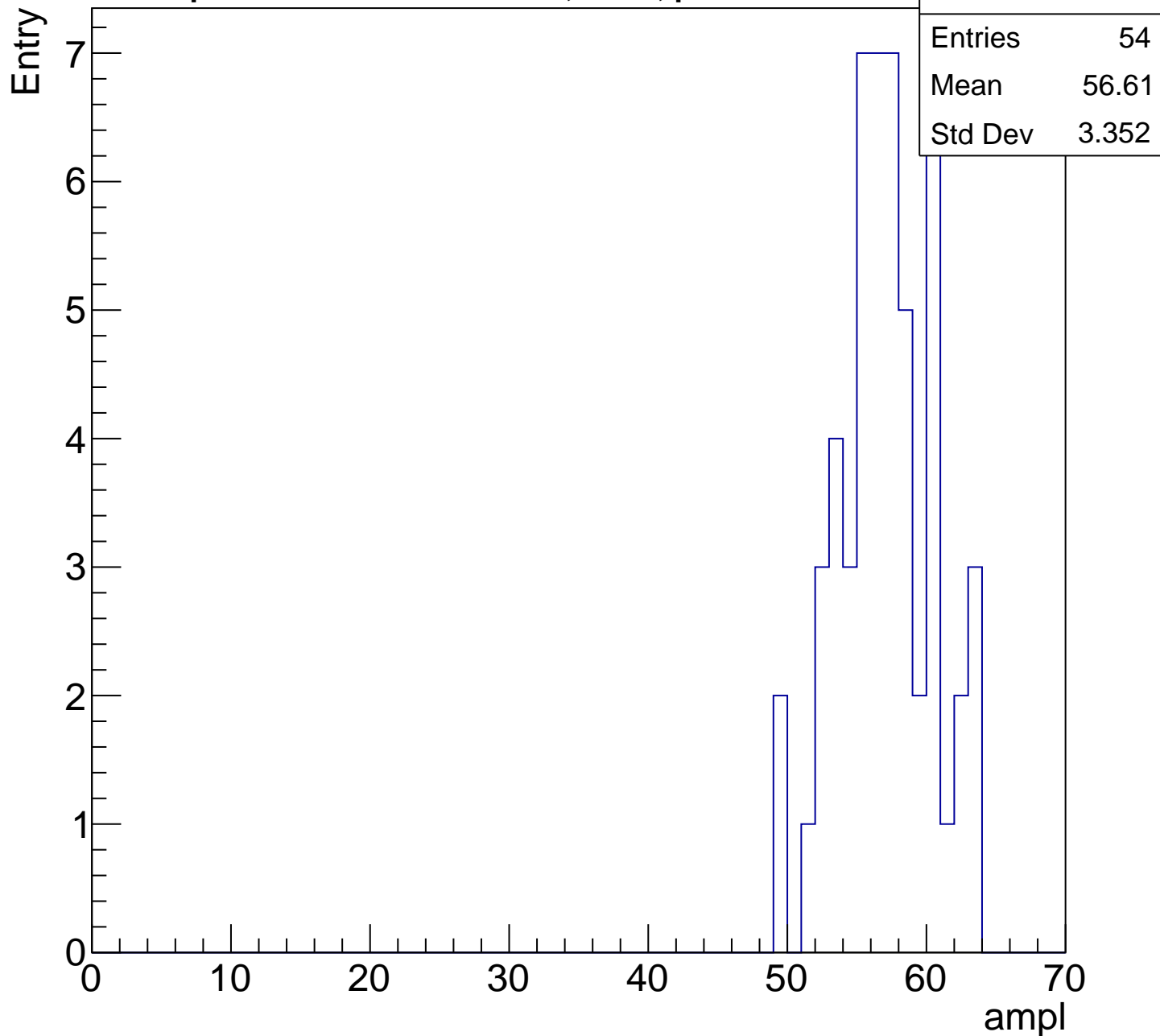
Entry

Entries	79
Mean	50.42
Std Dev	3.72



B1L101S, U23-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

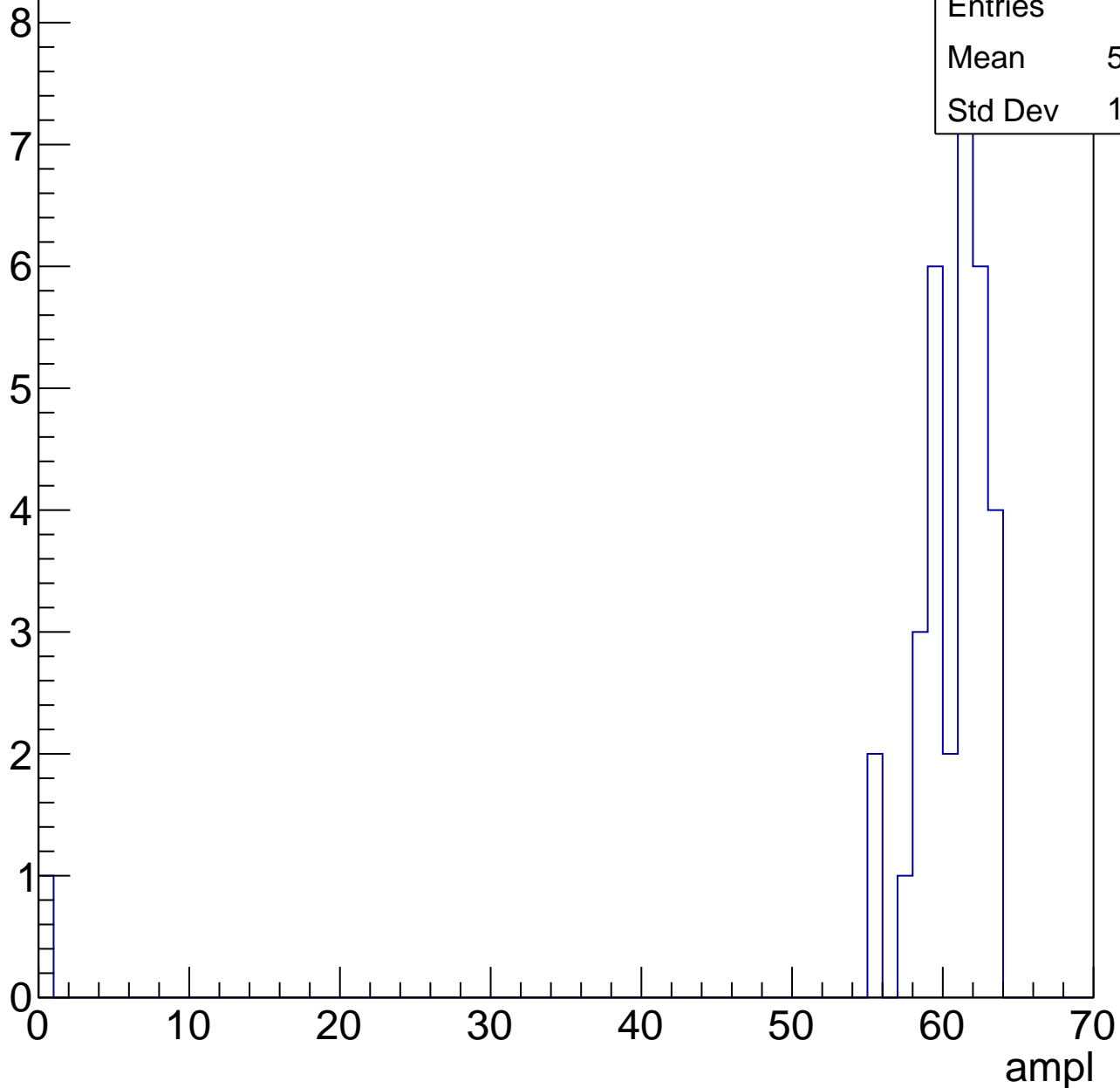


B1L101S, U23-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

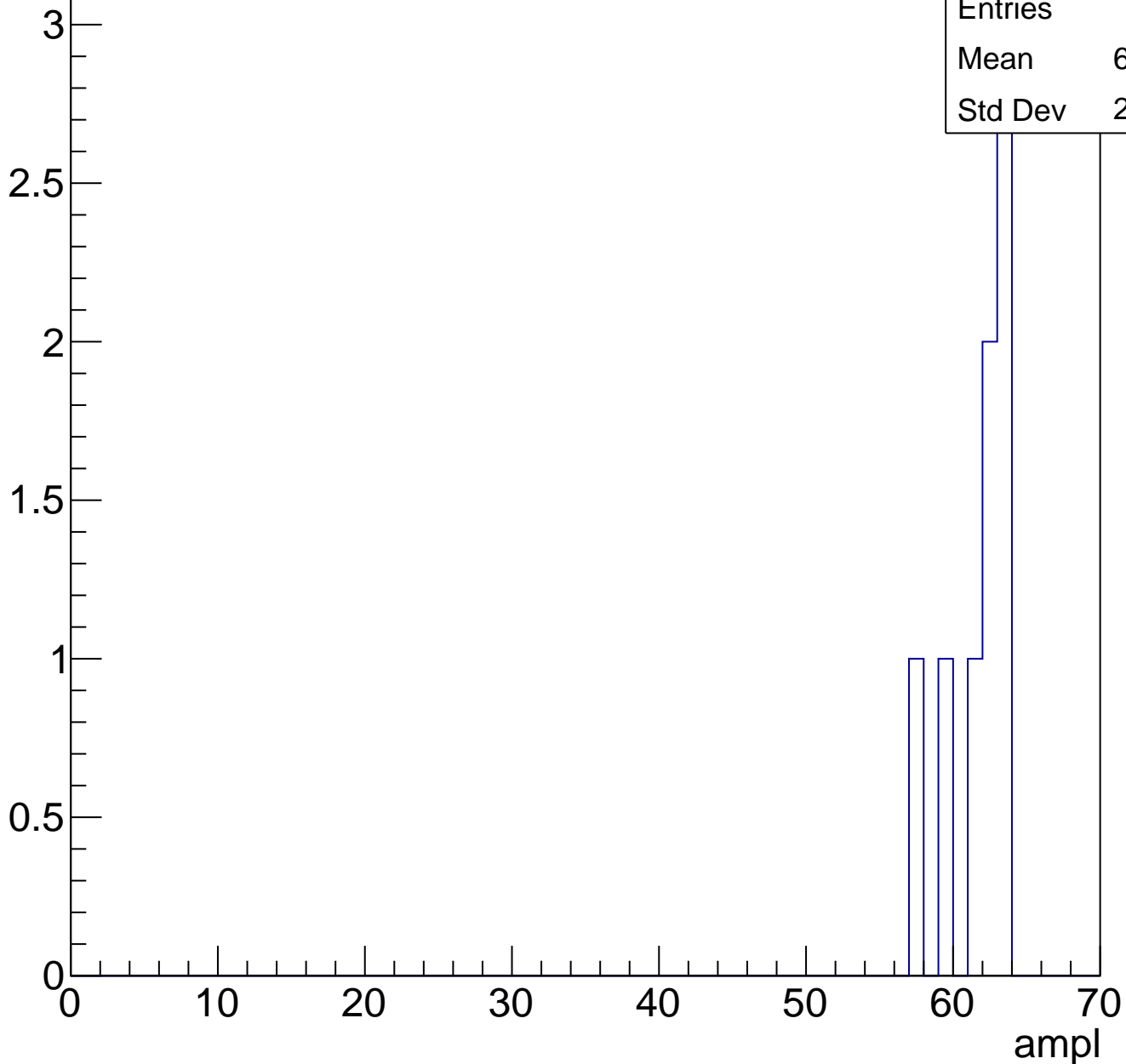
Entries	33
Mean	58.39
Std Dev	10.53



B1L101S, U23-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch31, adc0

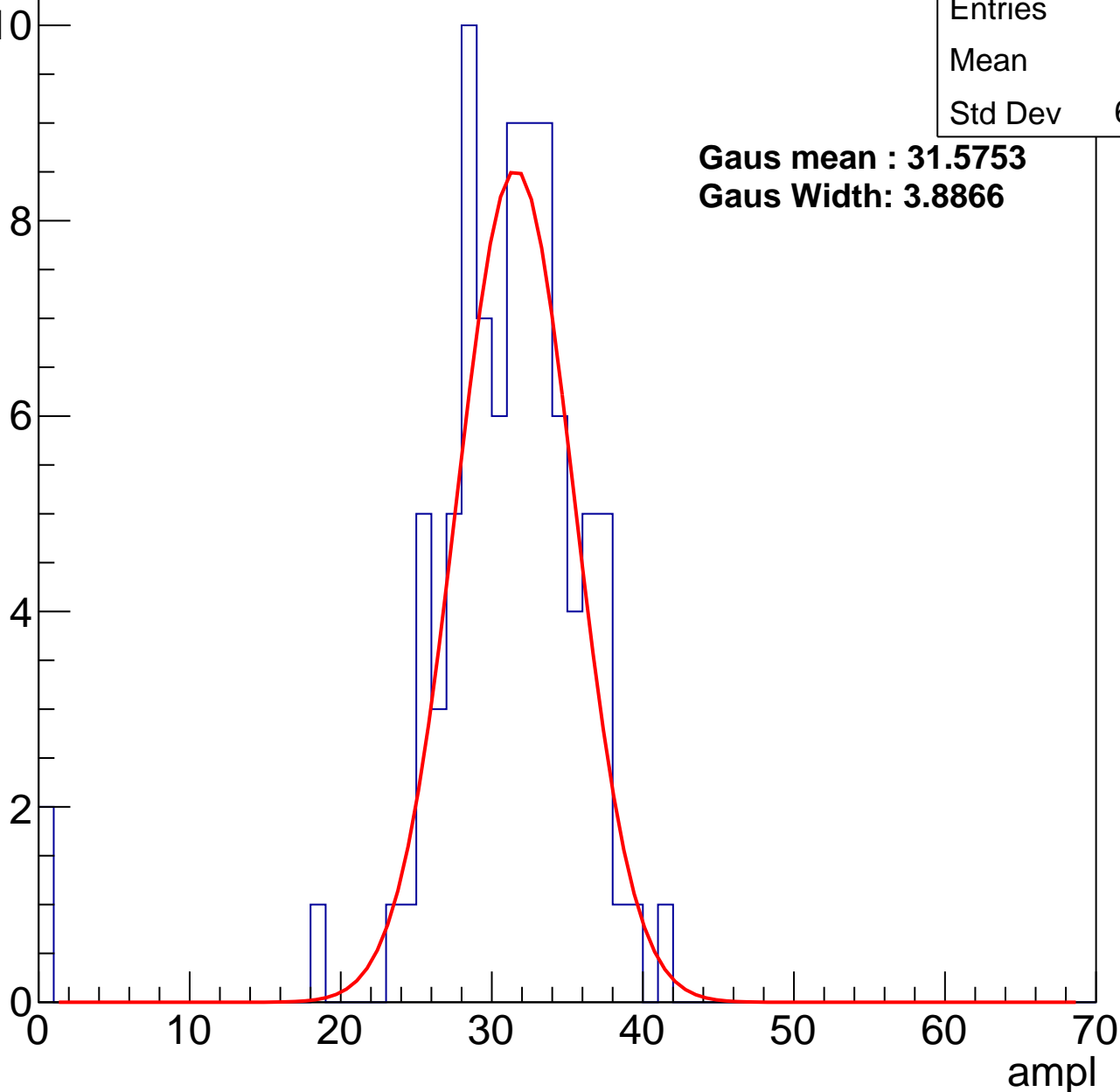
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	91
Mean	30.3
Std Dev	6.021

Gaus mean : 31.5753

Gaus Width: 3.8866



B1L101S, U23-ch31, adc1

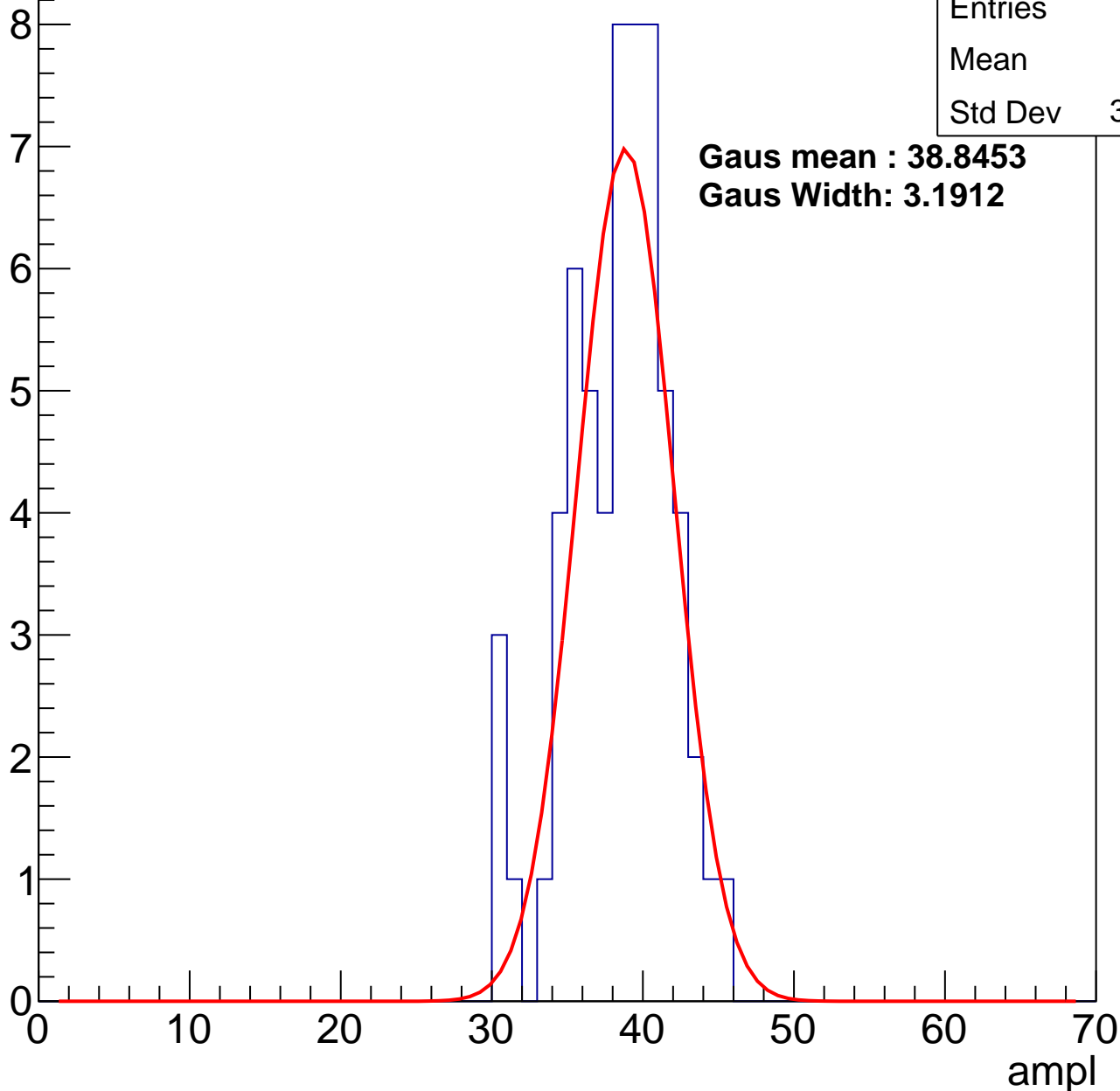
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.9
Std Dev	3.367

Gaus mean : 38.8453

Gaus Width: 3.1912



B1L101S, U23-ch31, adc2

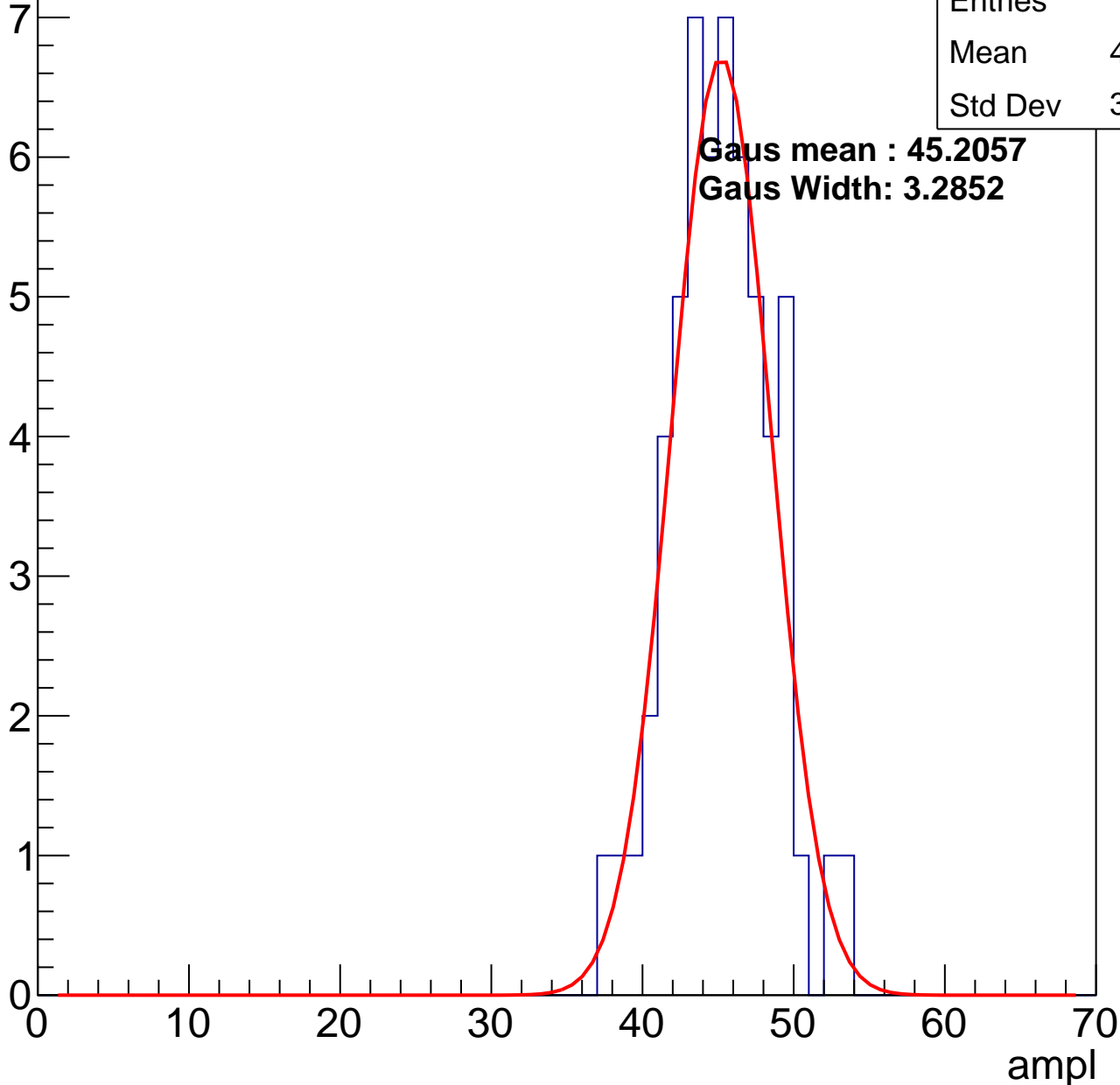
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.75
Std Dev	3.294

Gaus mean : 45.2057

Gaus Width: 3.2852

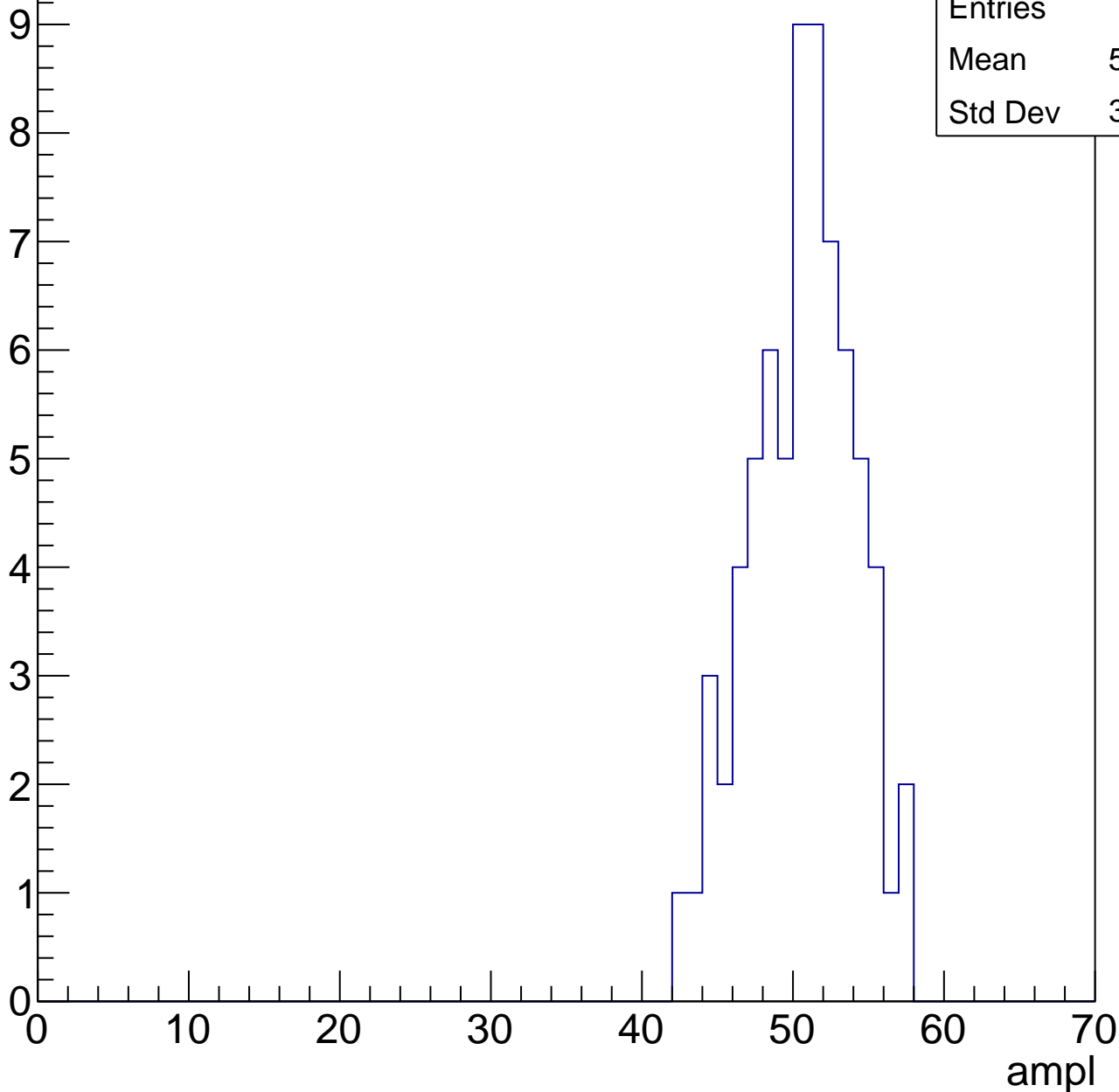


B1L101S, U23-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

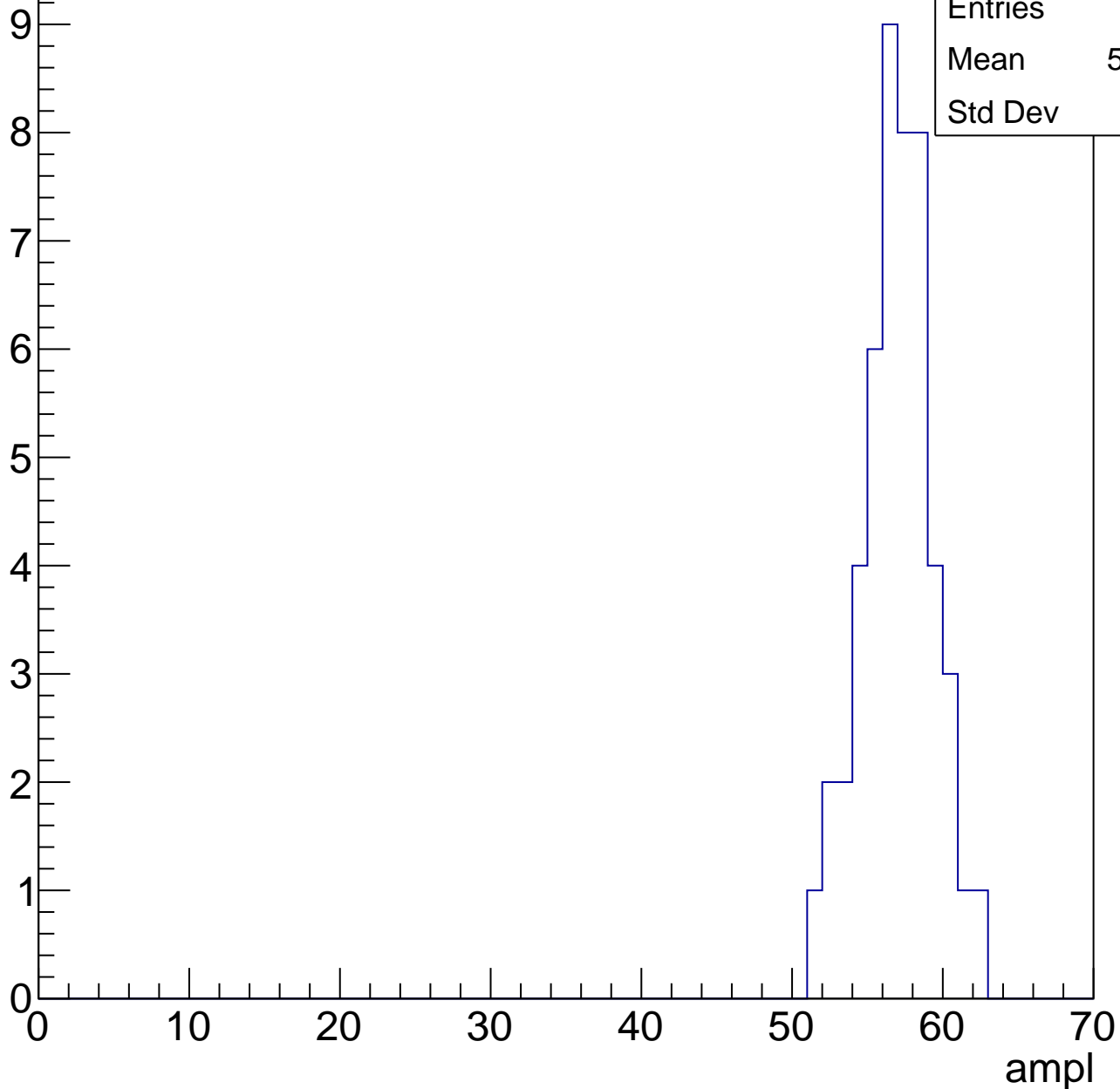
Entries	70
Mean	50.14
Std Dev	3.407



B1L101S, U23-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



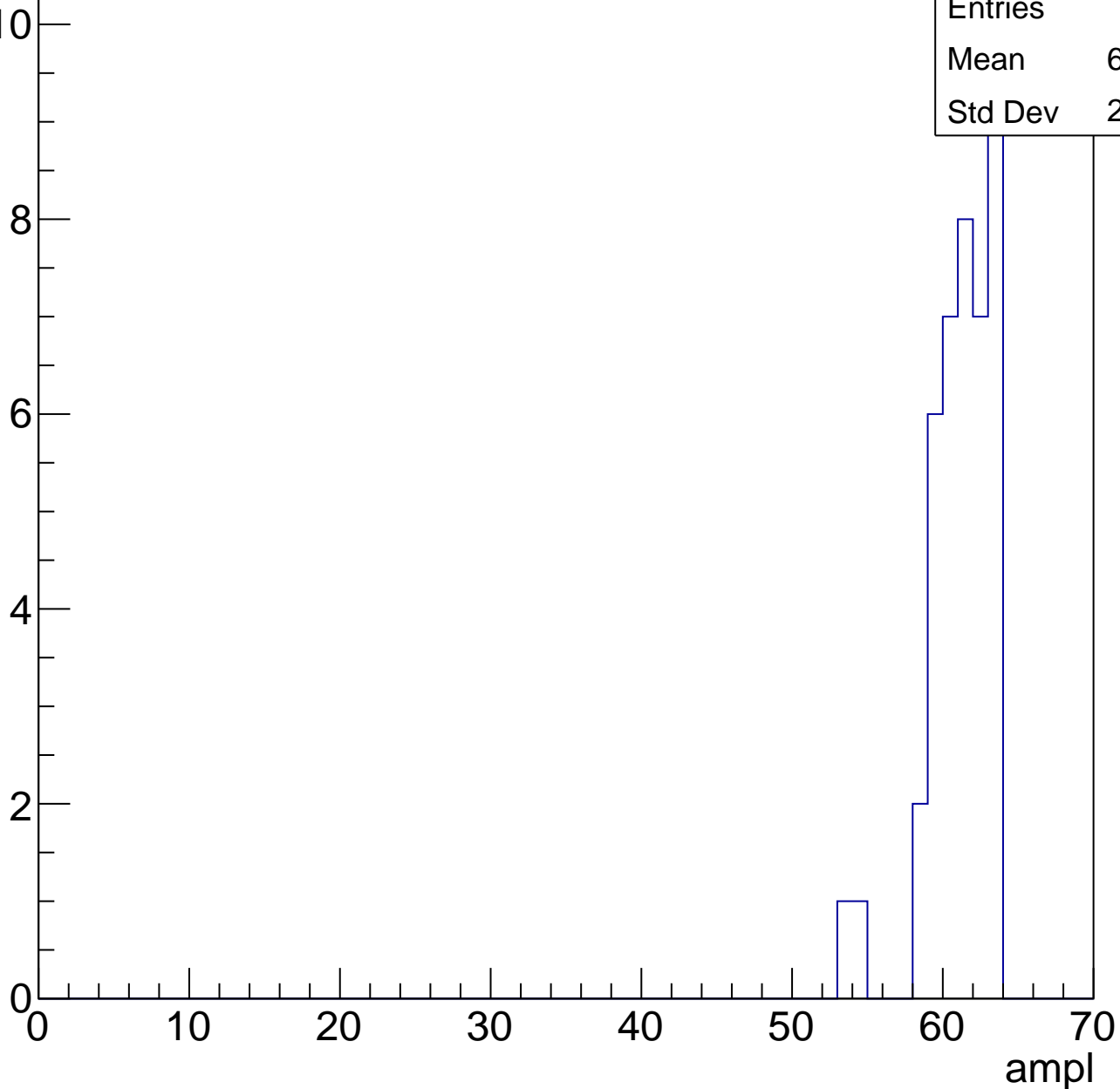
Entries	49
Mean	56.53
Std Dev	2.34

B1L101S, U23-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

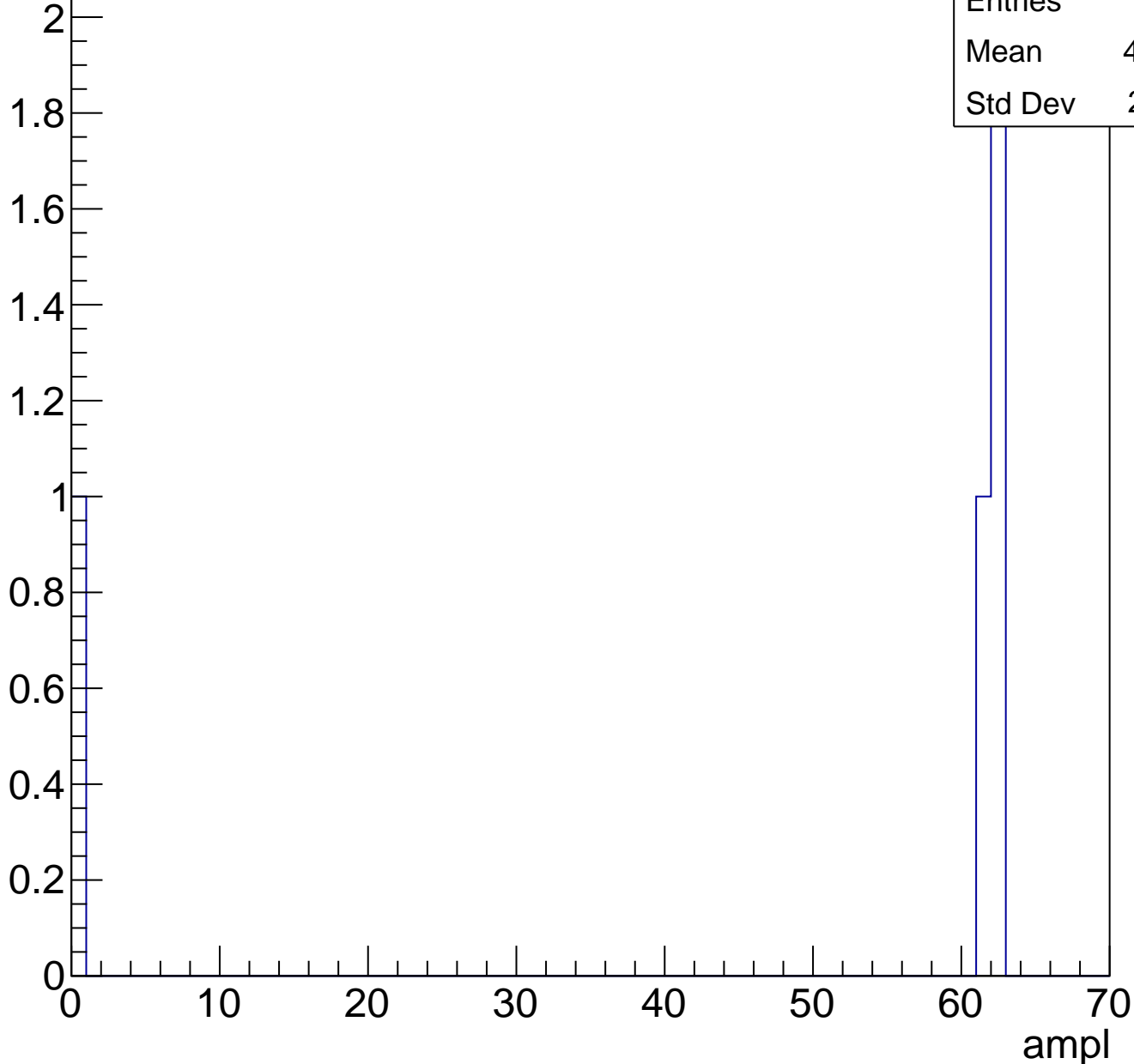
Entries	42
Mean	60.69
Std Dev	2.209



B1L101S, U23-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch32, adc0

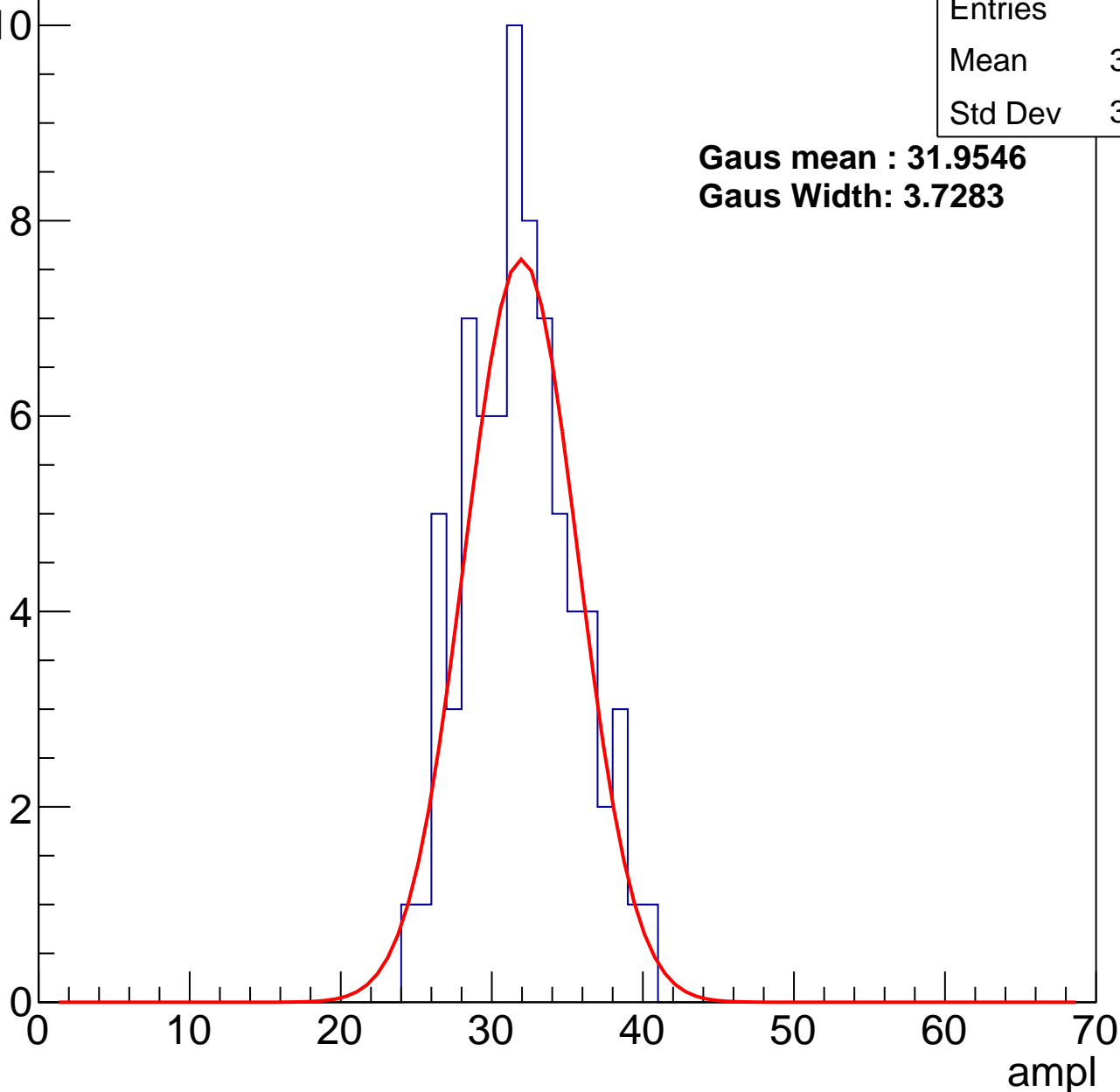
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	31.46
Std Dev	3.572

Gaus mean : 31.9546

Gaus Width: 3.7283



B1L101S, U23-ch32, adc1

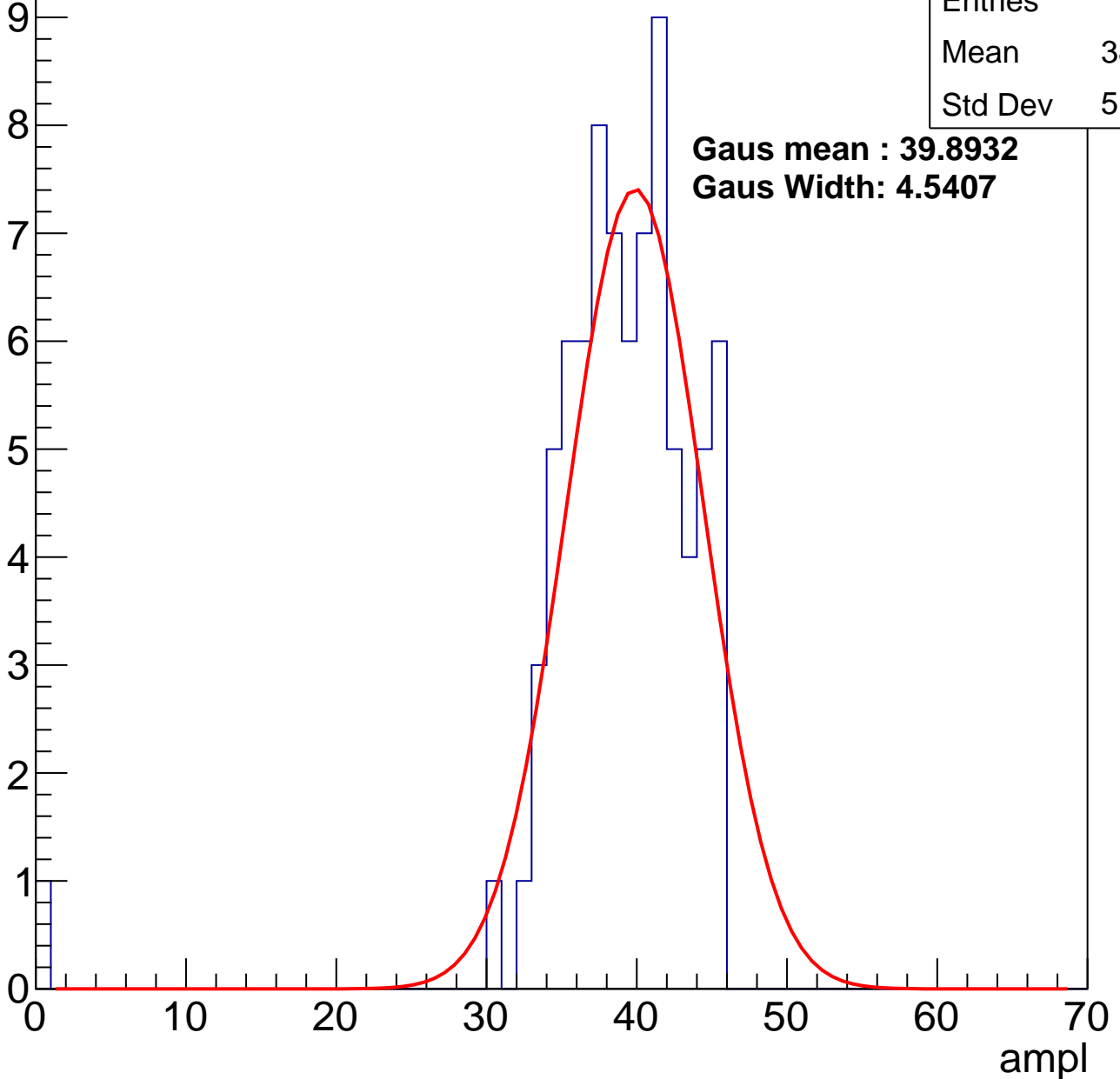
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	38.42
Std Dev	5.634

Gaus mean : 39.8932

Gaus Width: 4.5407



B1L101S, U23-ch32, adc2

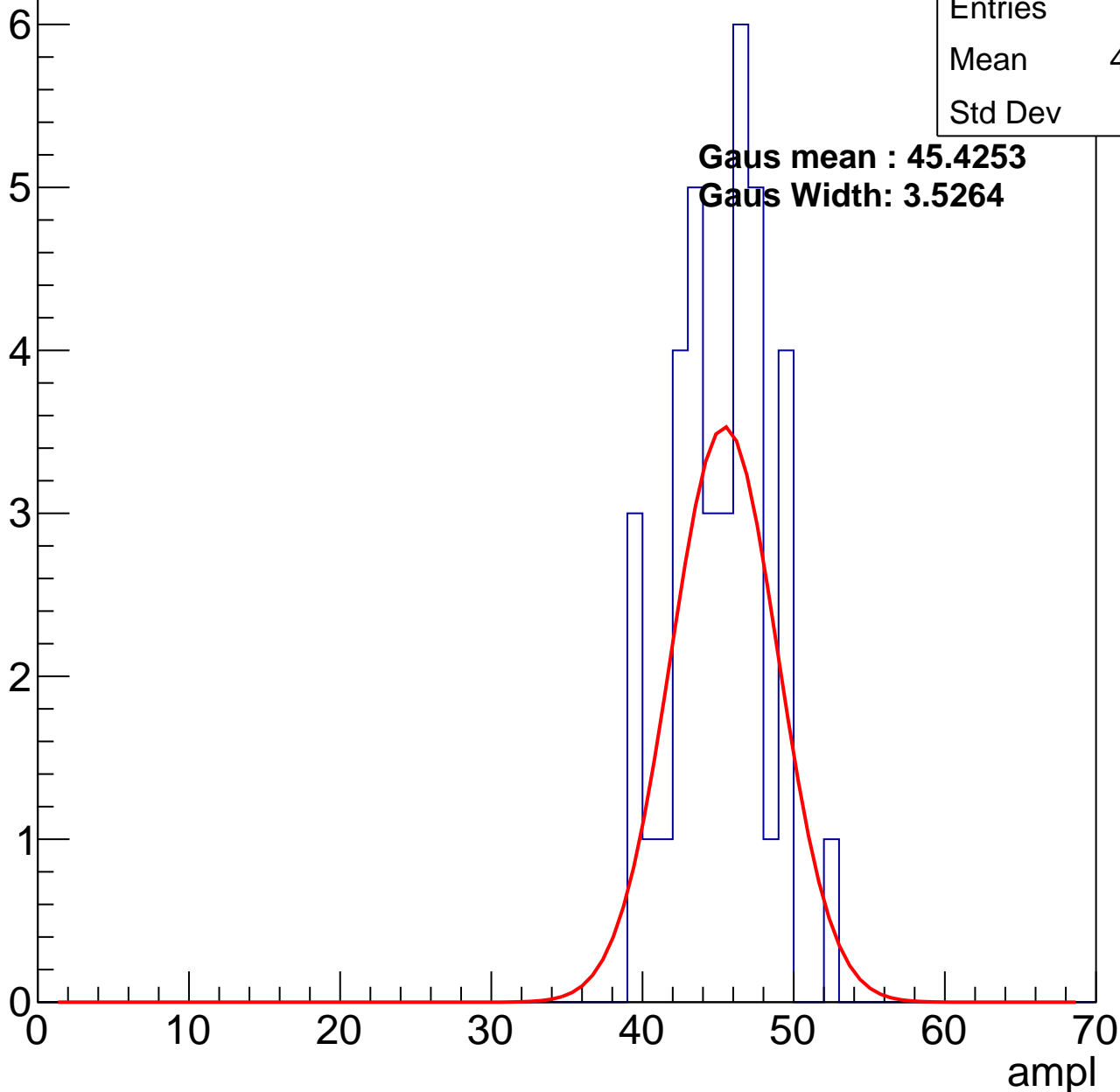
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	44.73
Std Dev	3.09

Gaus mean : 45.4253

Gaus Width: 3.5264

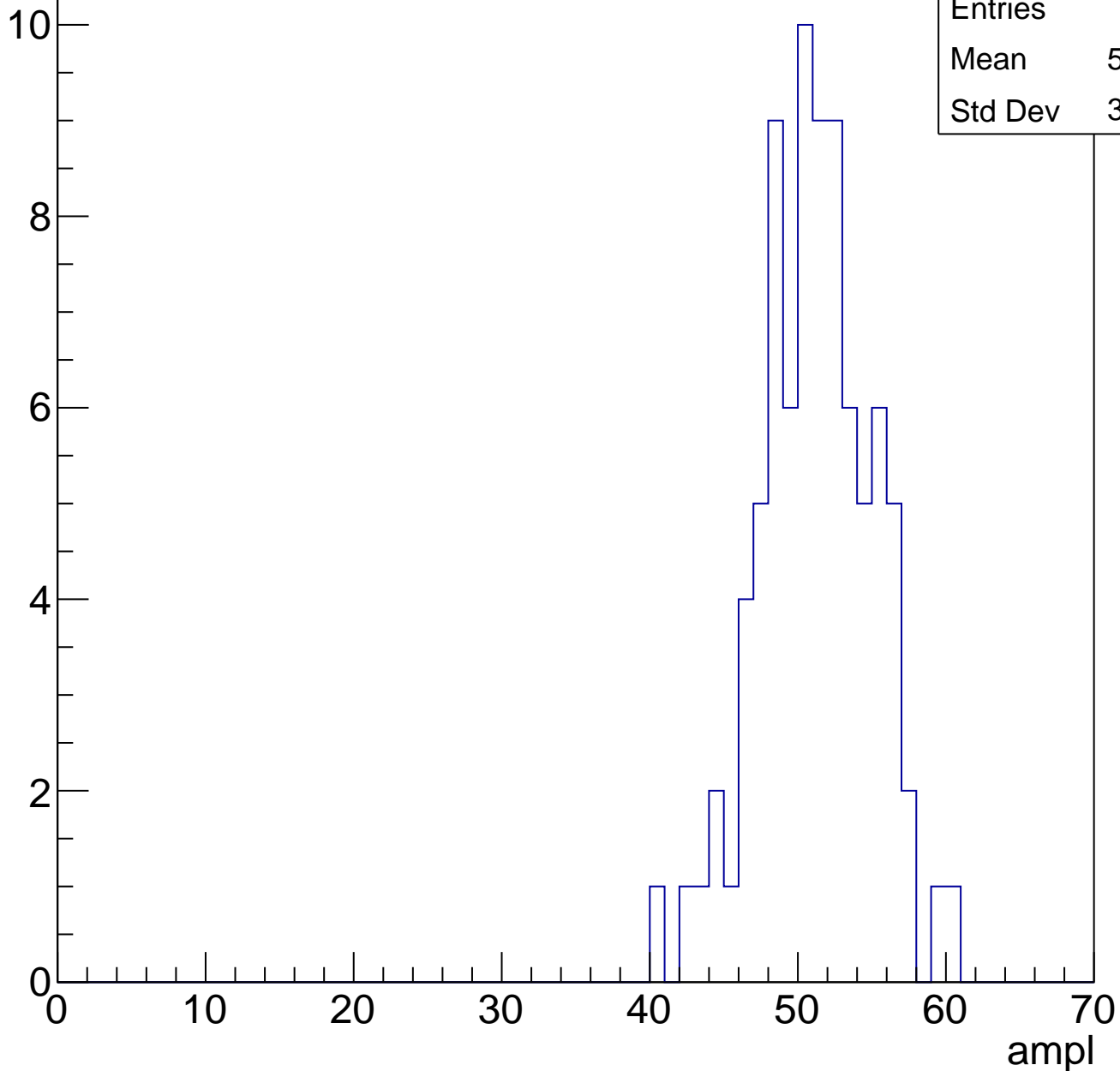


B1L101S, U23-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	50.73
Std Dev	3.787

Entry

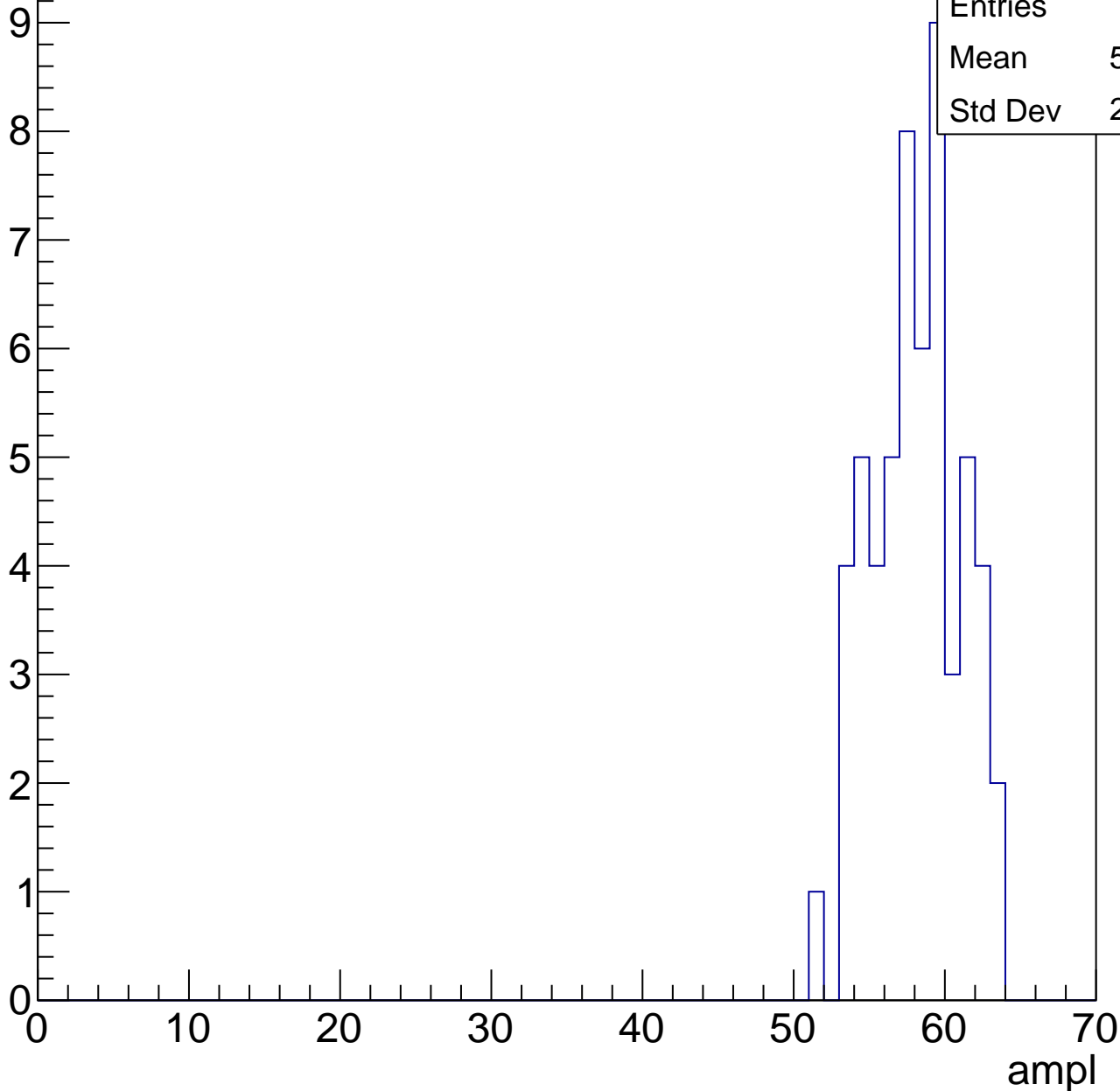


B1L101S, U23-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.62
Std Dev	2.882

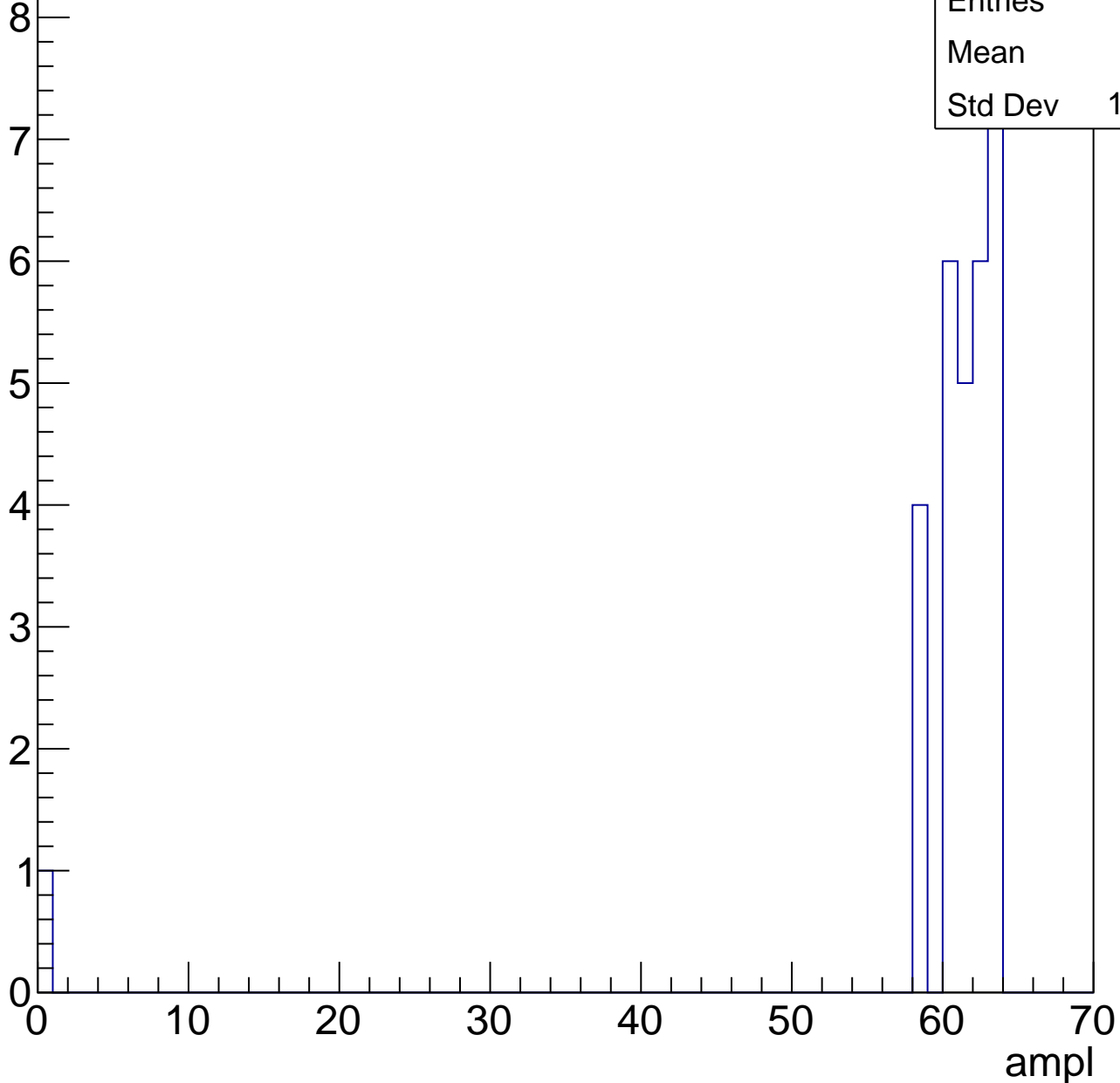


B1L101S, U23-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	59.1
Std Dev	11.09



B1L101S, U23-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

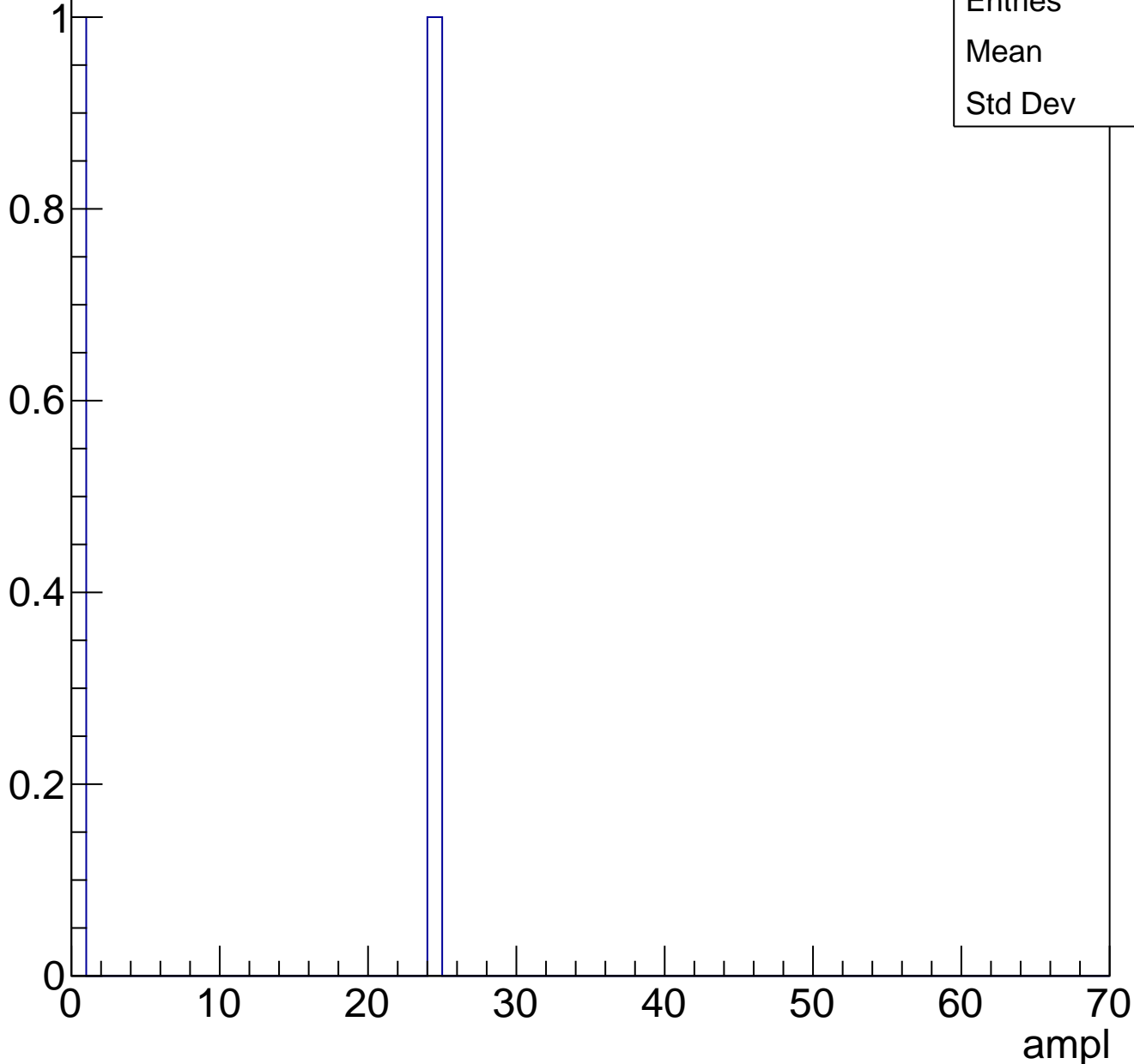
Entry



B1L101S, U23-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch33, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	31.58
Std Dev	5.111

Gaus mean : 32.8359

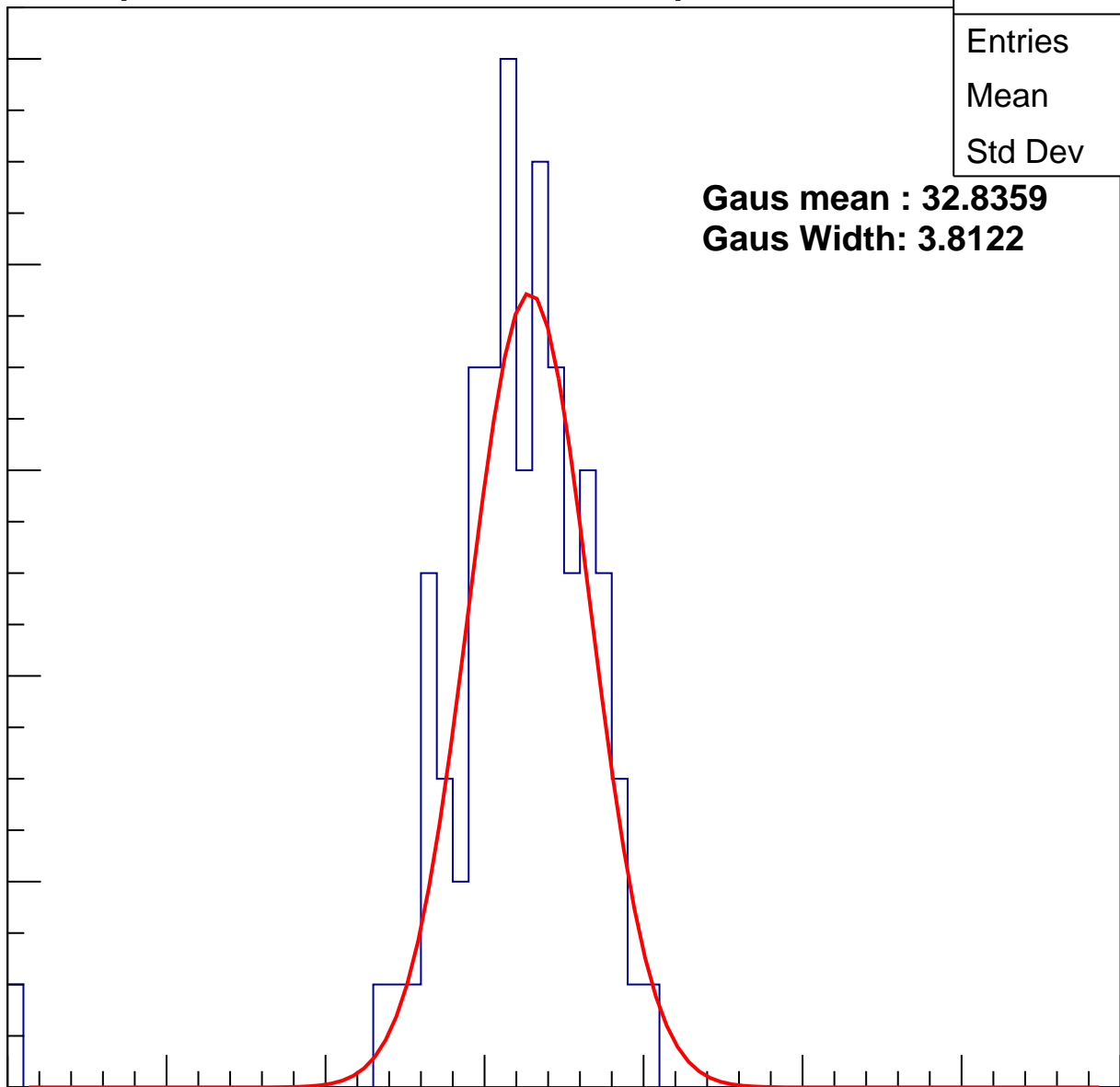
Gaus Width: 3.8122

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



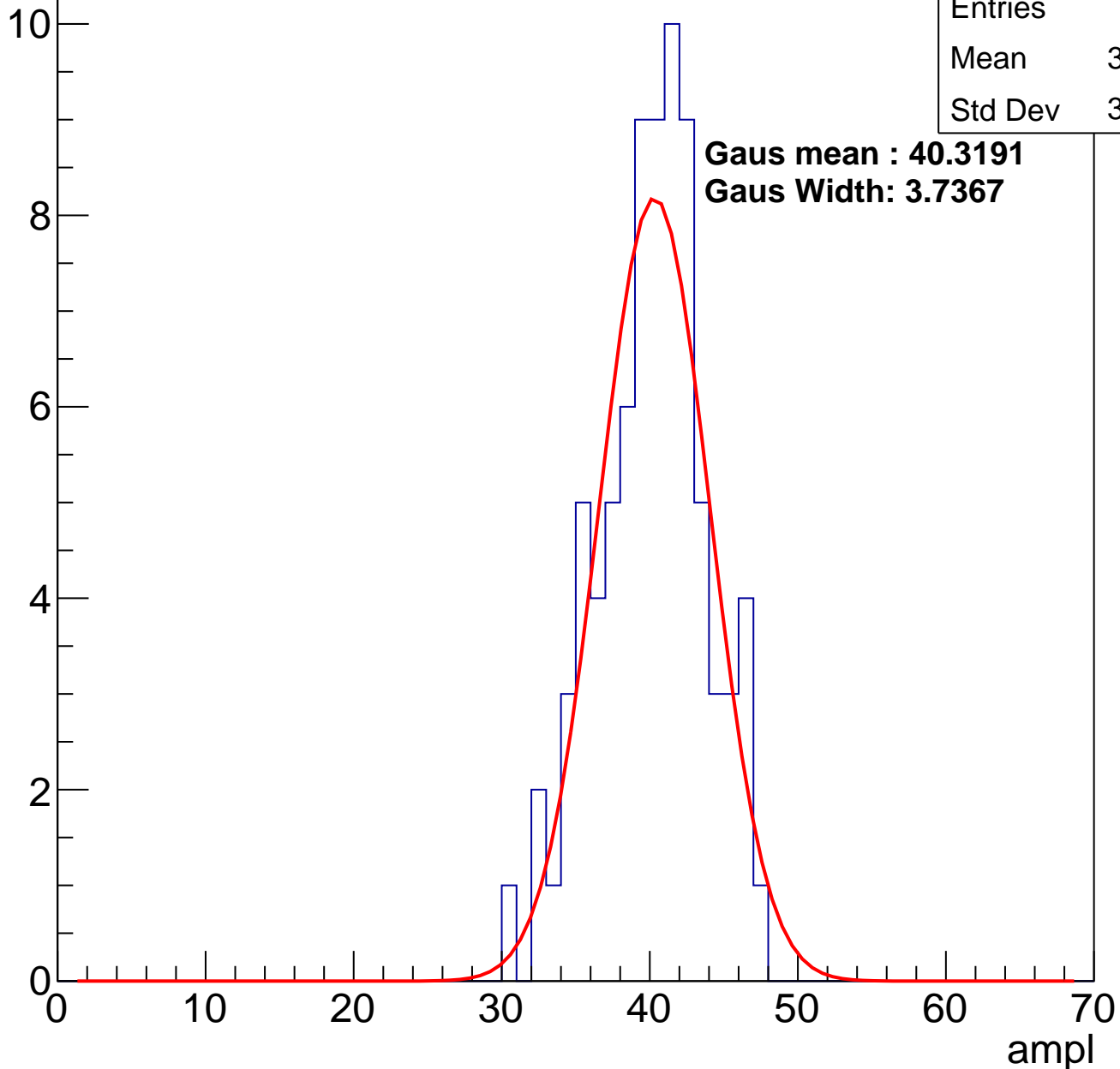
B1L101S, U23-ch33, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	39.66
Std Dev	3.623

Gaus mean : 40.3191
Gaus Width: 3.7367

Entry



B1L101S, U23-ch33, adc2

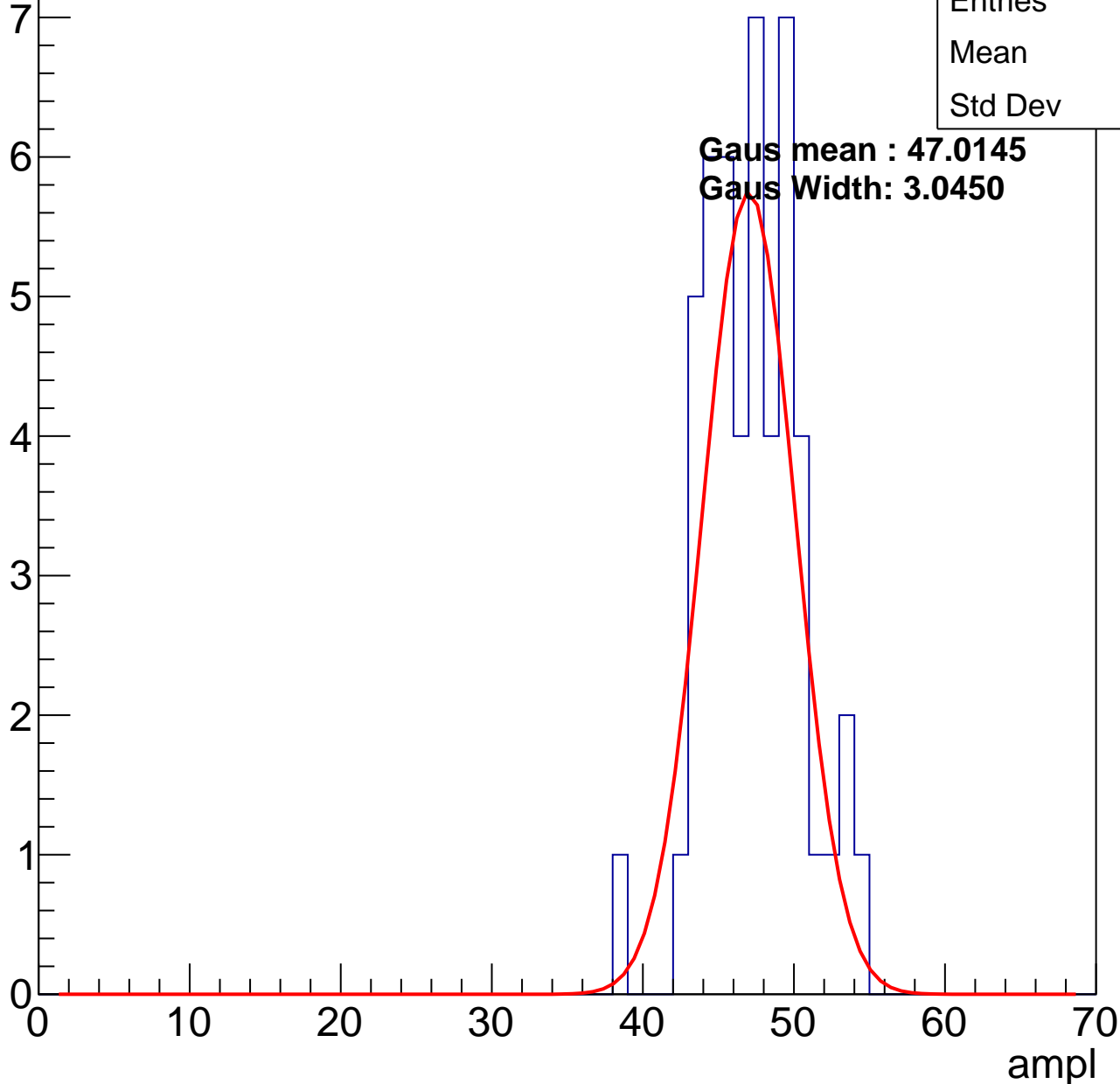
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	46.8
Std Dev	3.15

Gaus mean : 47.0145

Gaus Width: 3.0450

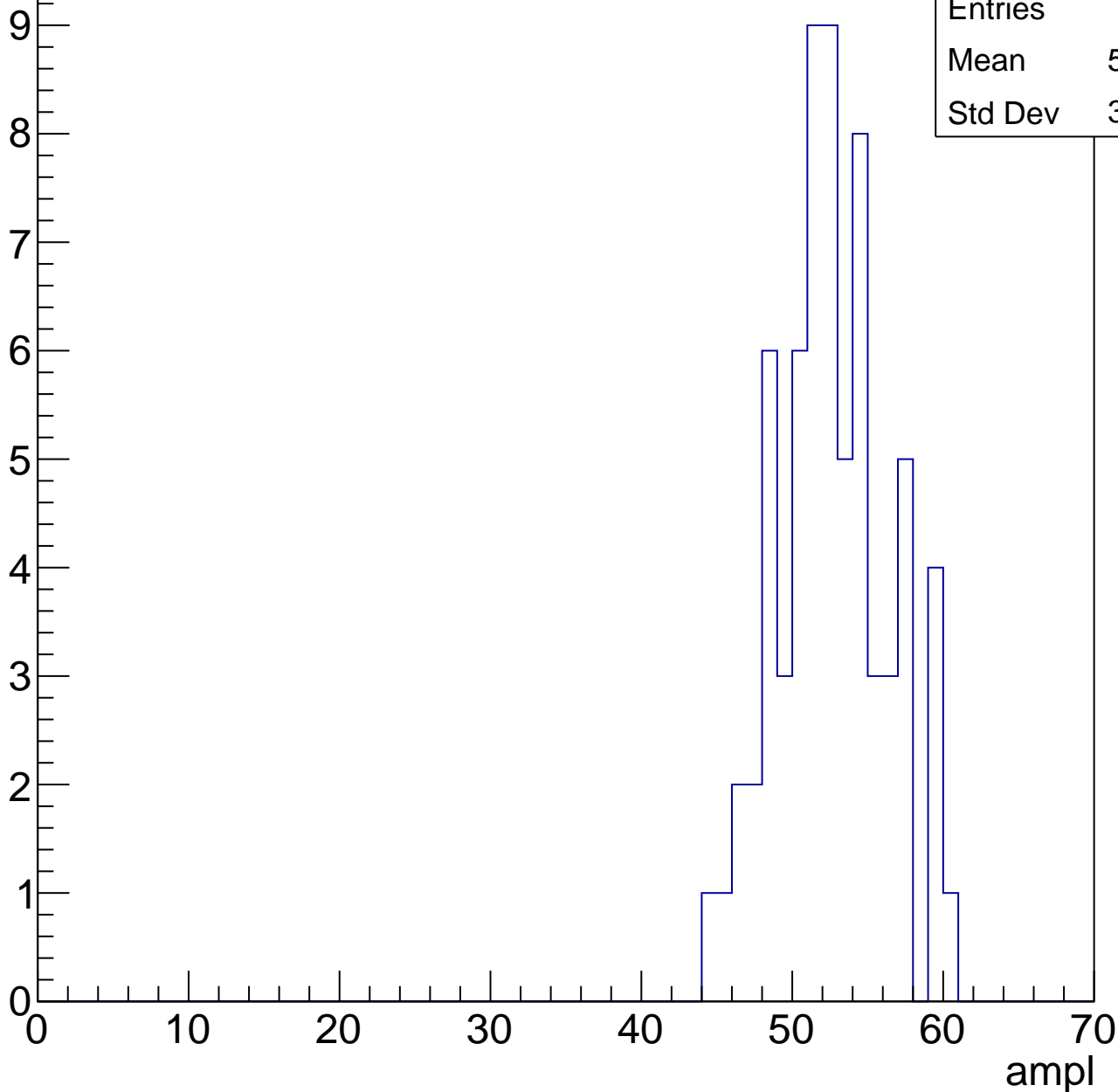


B1L101S, U23-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

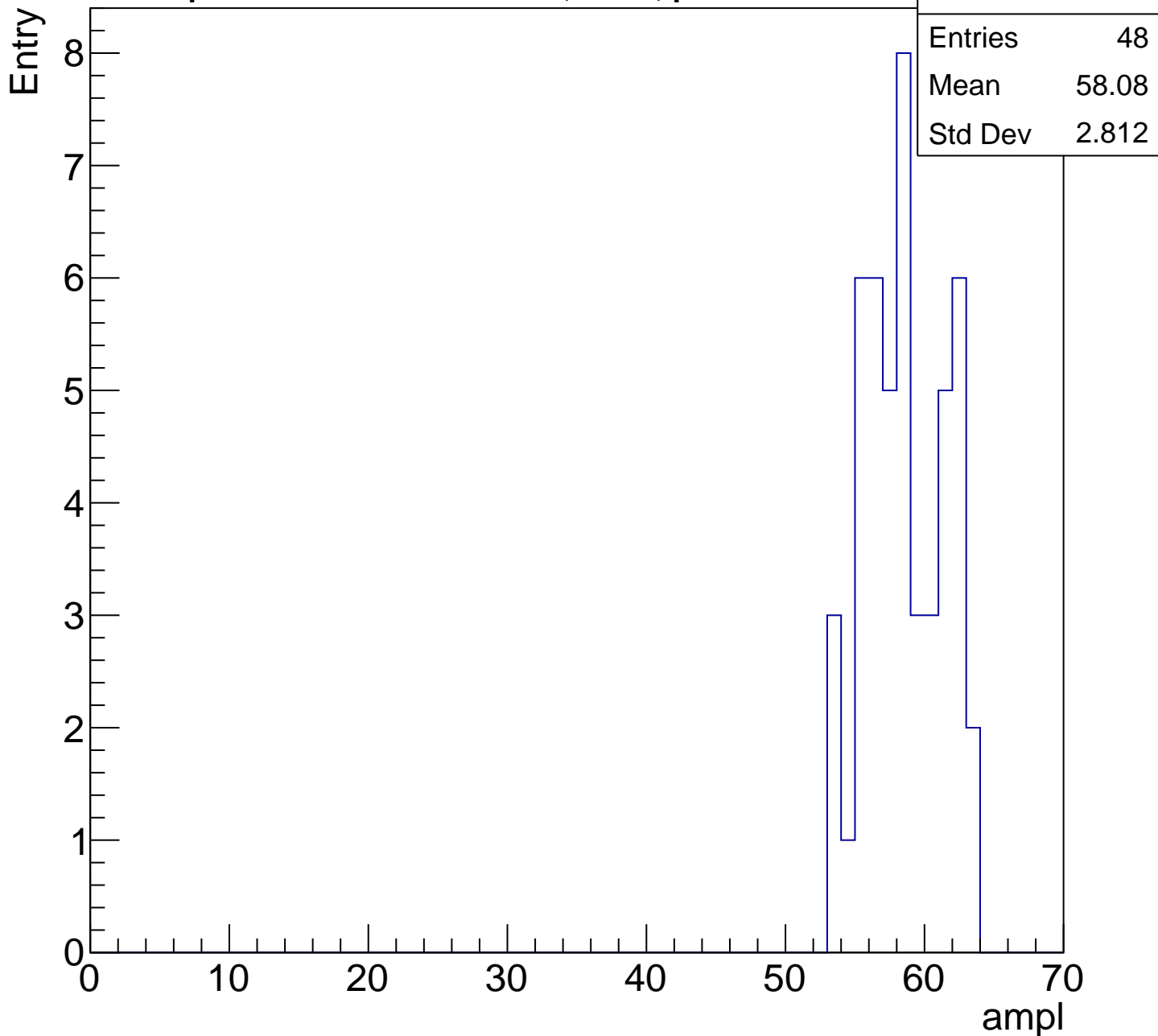
Entry

Entries	68
Mean	52.18
Std Dev	3.609



B1L101S, U23-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

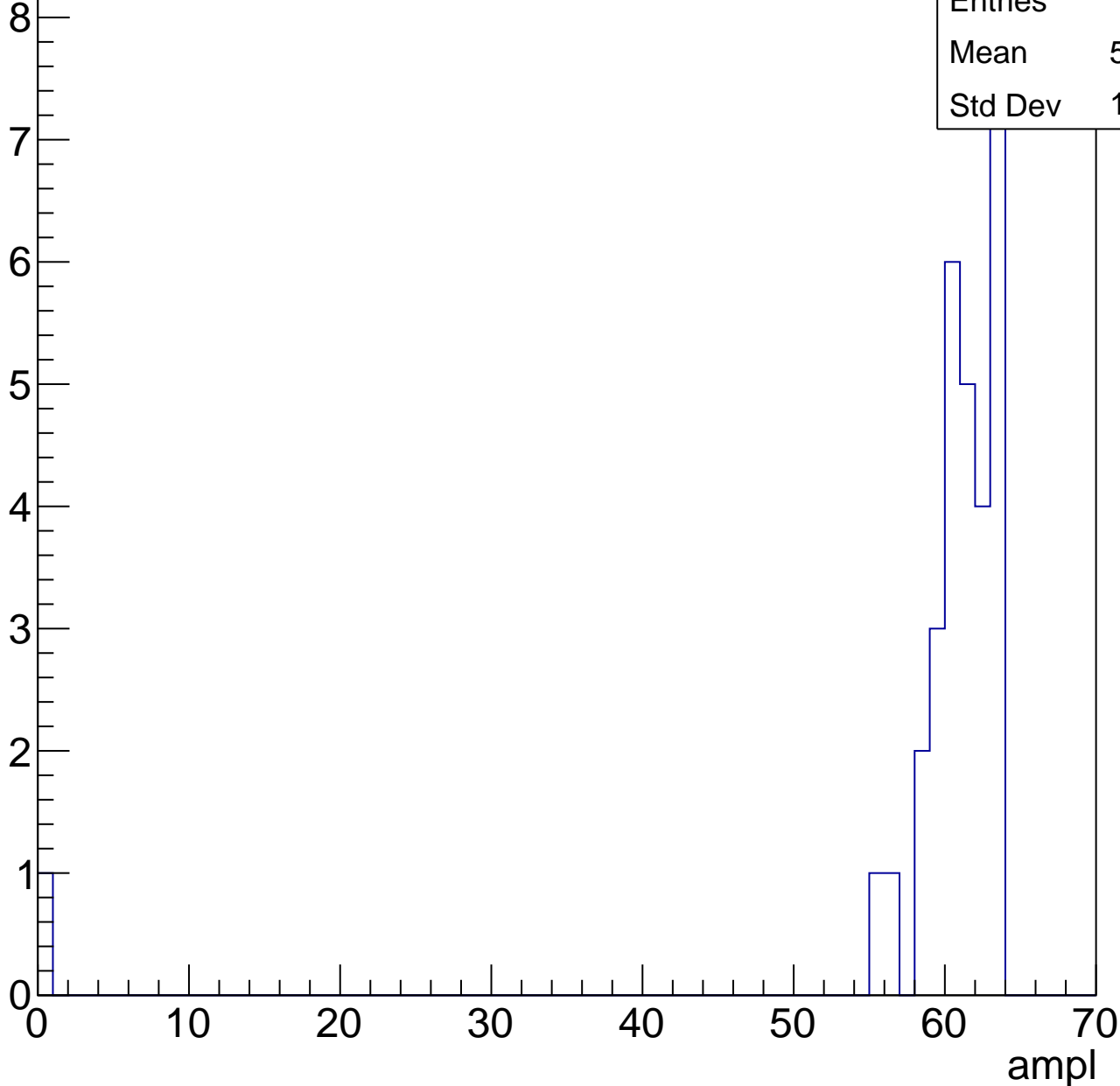


B1L101S, U23-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.74
Std Dev	10.92



B1L101S, U23-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U23-ch34, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	29.27
Std Dev	3.754

Gaus mean : 29.9295

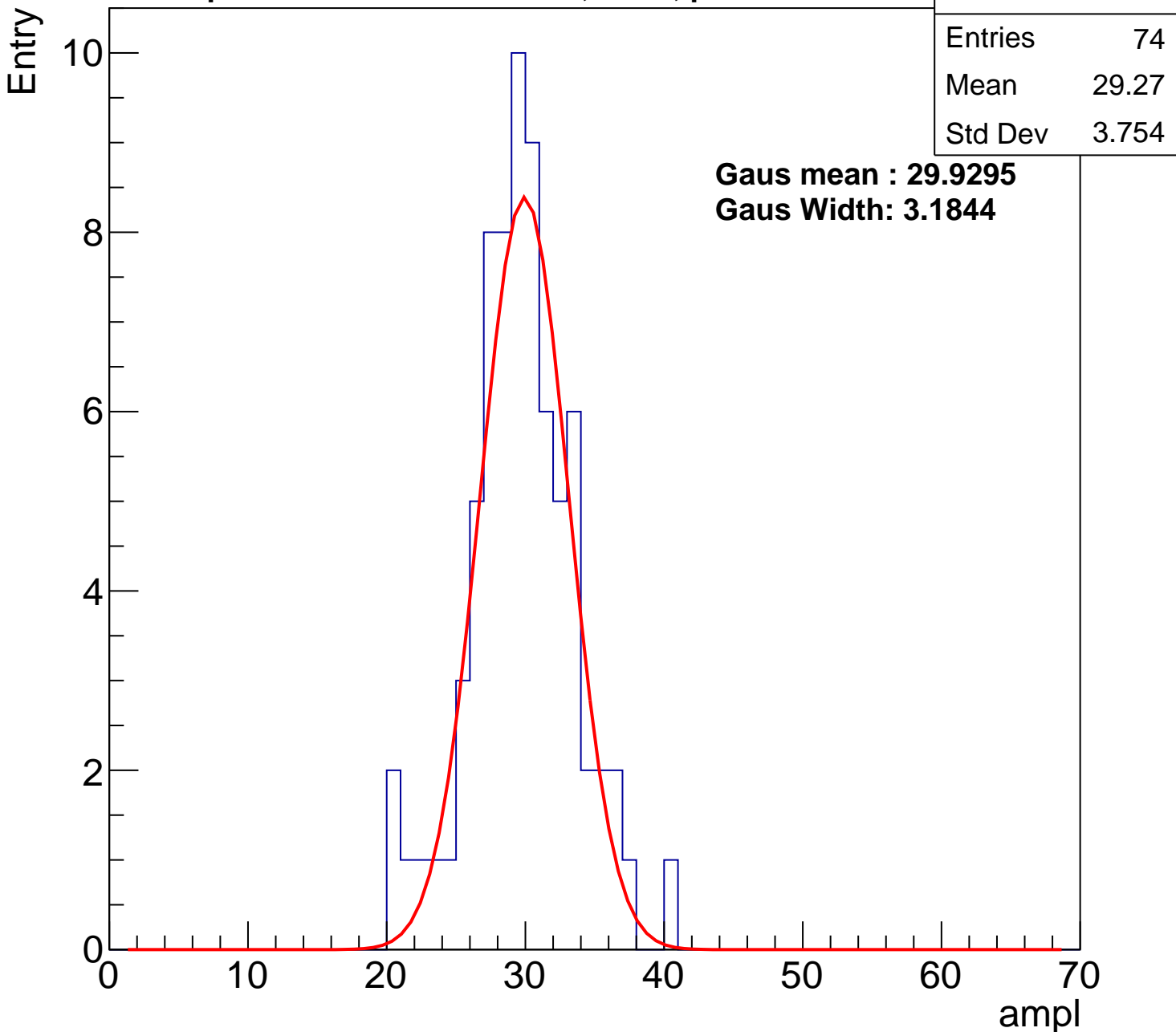
Gaus Width: 3.1844

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch34, adc1

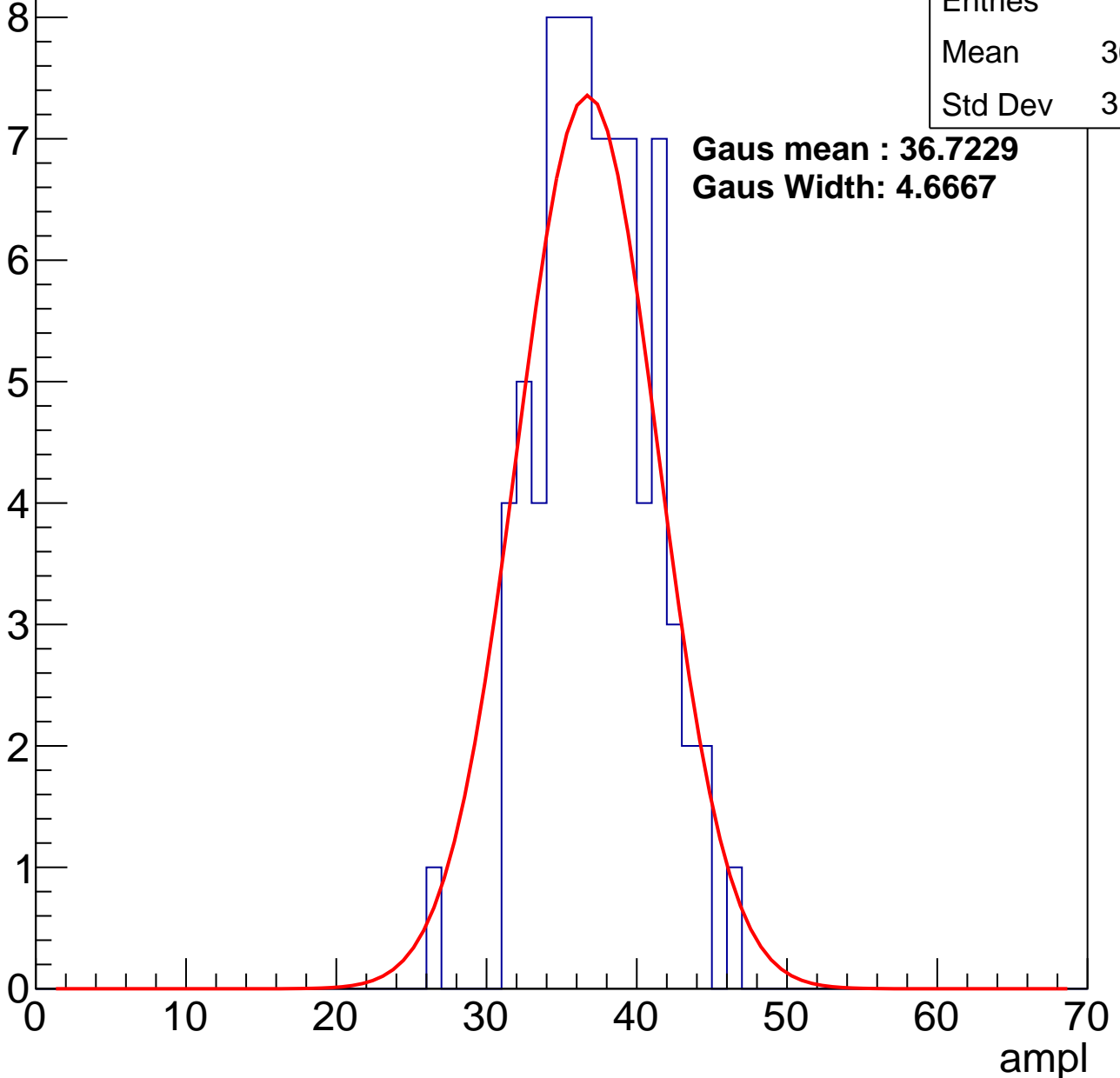
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.83
Std Dev	3.709

Gaus mean : 36.7229

Gaus Width: 4.6667



B1L101S, U23-ch34, adc2

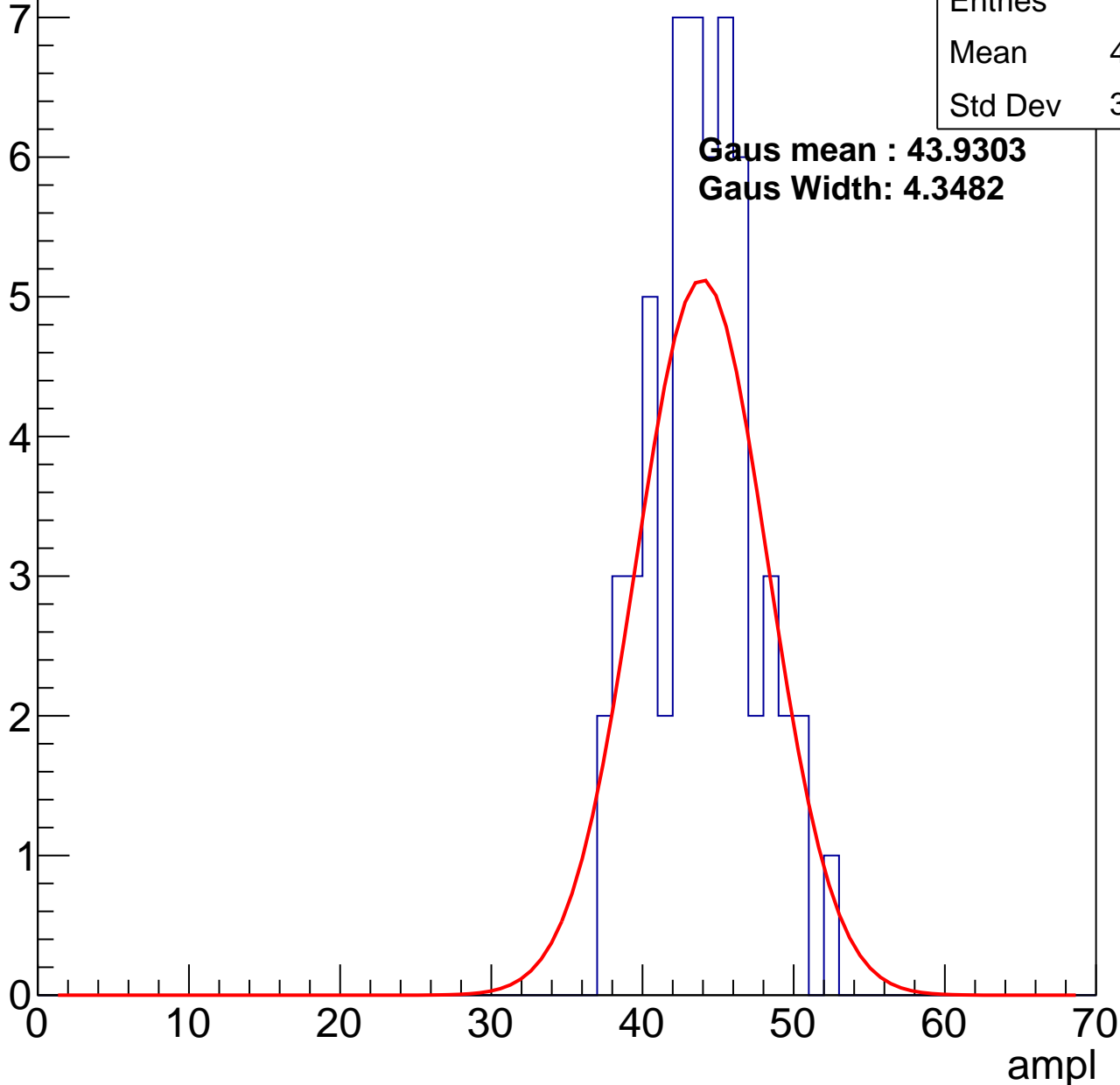
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.53
Std Dev	3.435

Gaus mean : 43.9303

Gaus Width: 4.3482

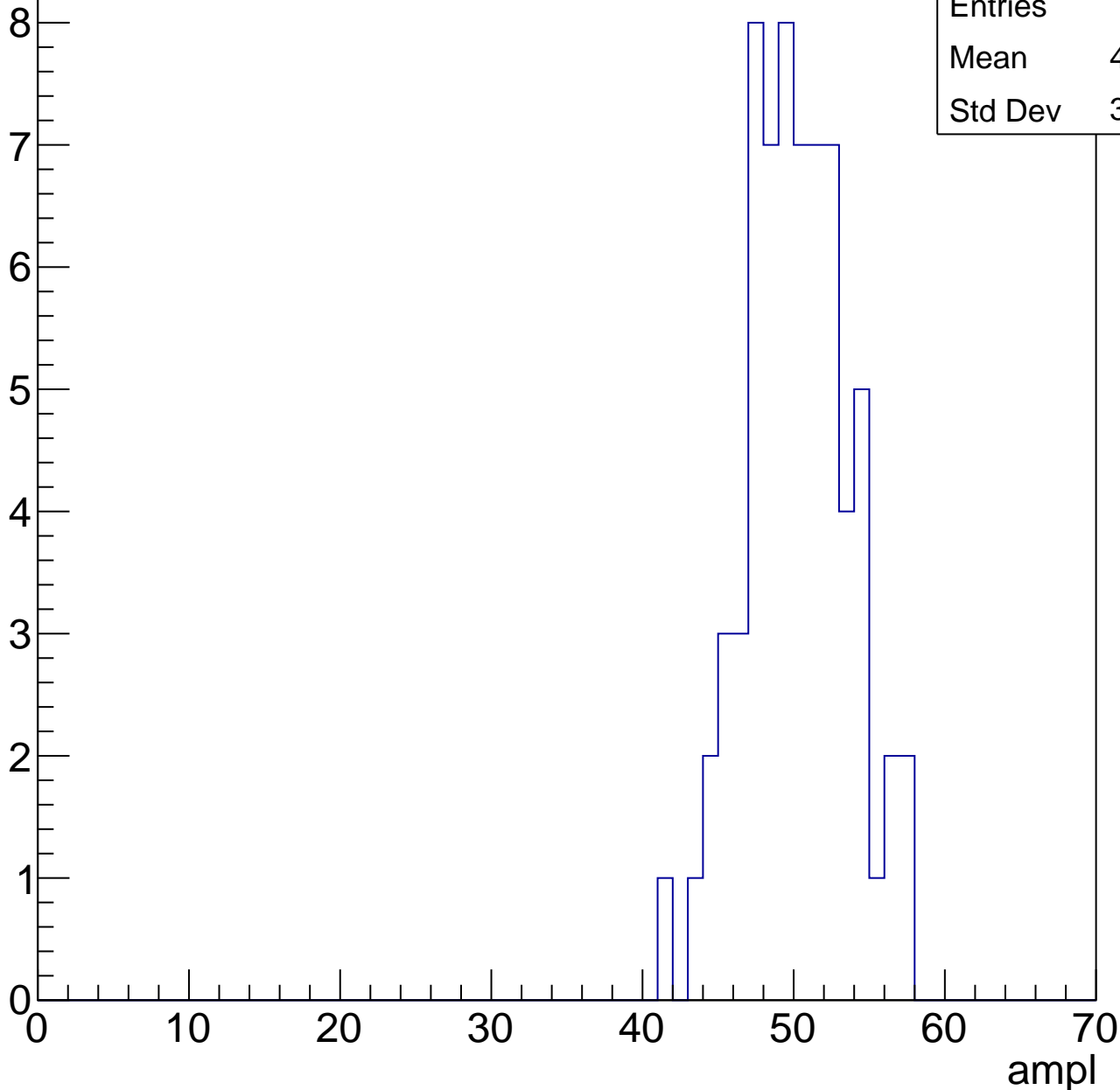


B1L101S, U23-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

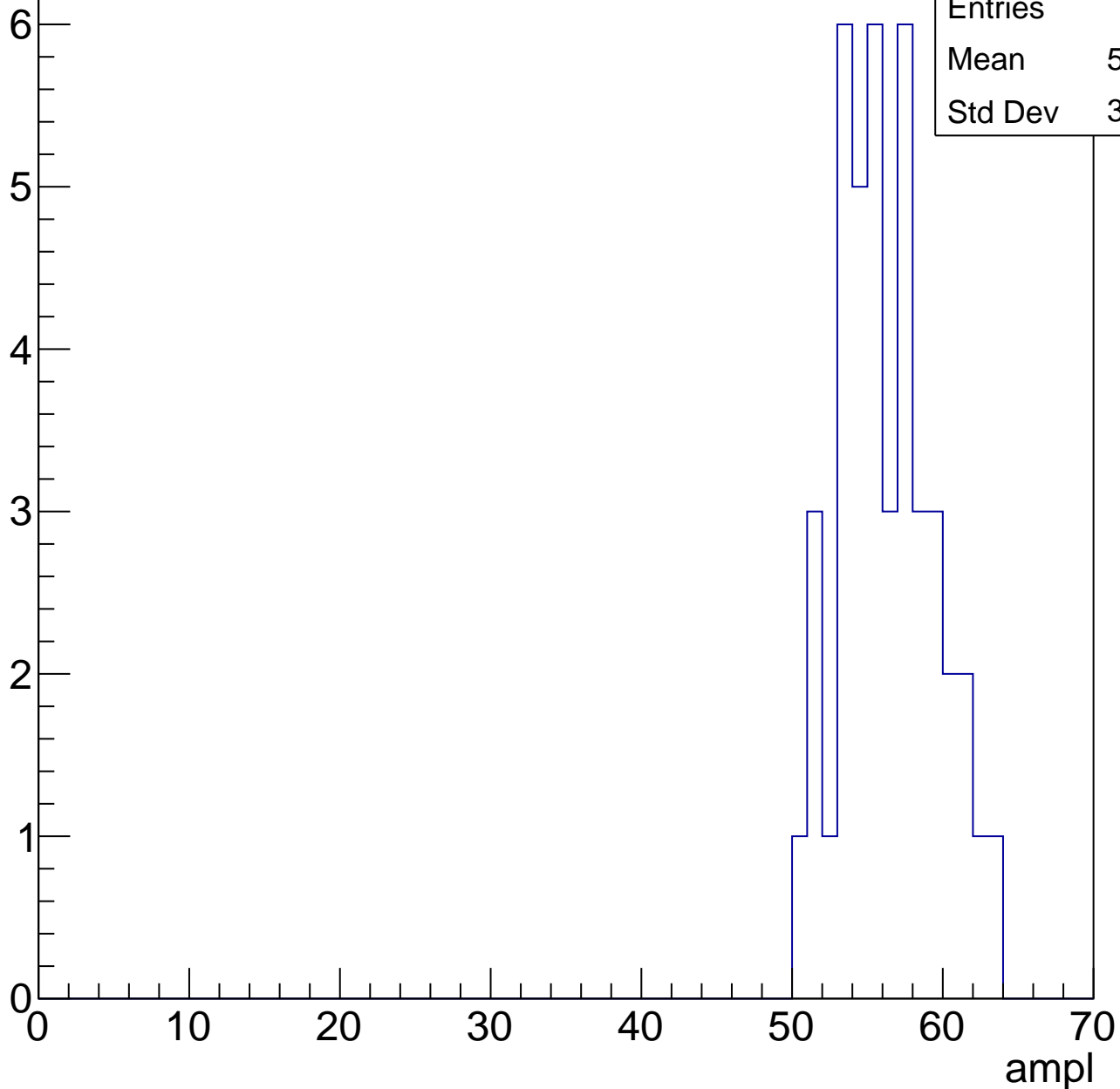
Entries	68
Mean	49.75
Std Dev	3.375



B1L101S, U23-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



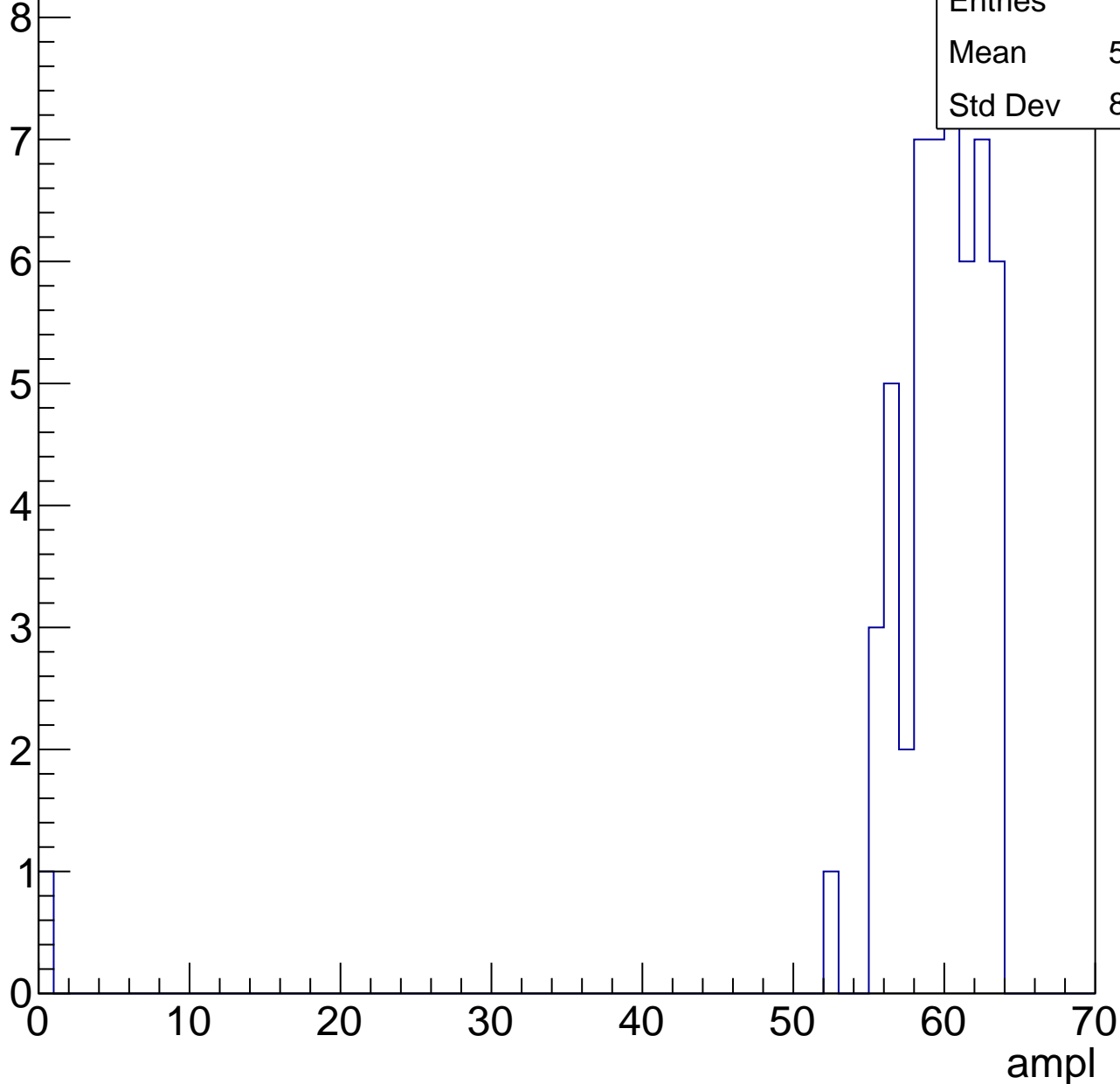
Entries	43
Mean	55.84
Std Dev	3.117

B1L101S, U23-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	58.26
Std Dev	8.468



B1L101S, U23-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch35, adc0

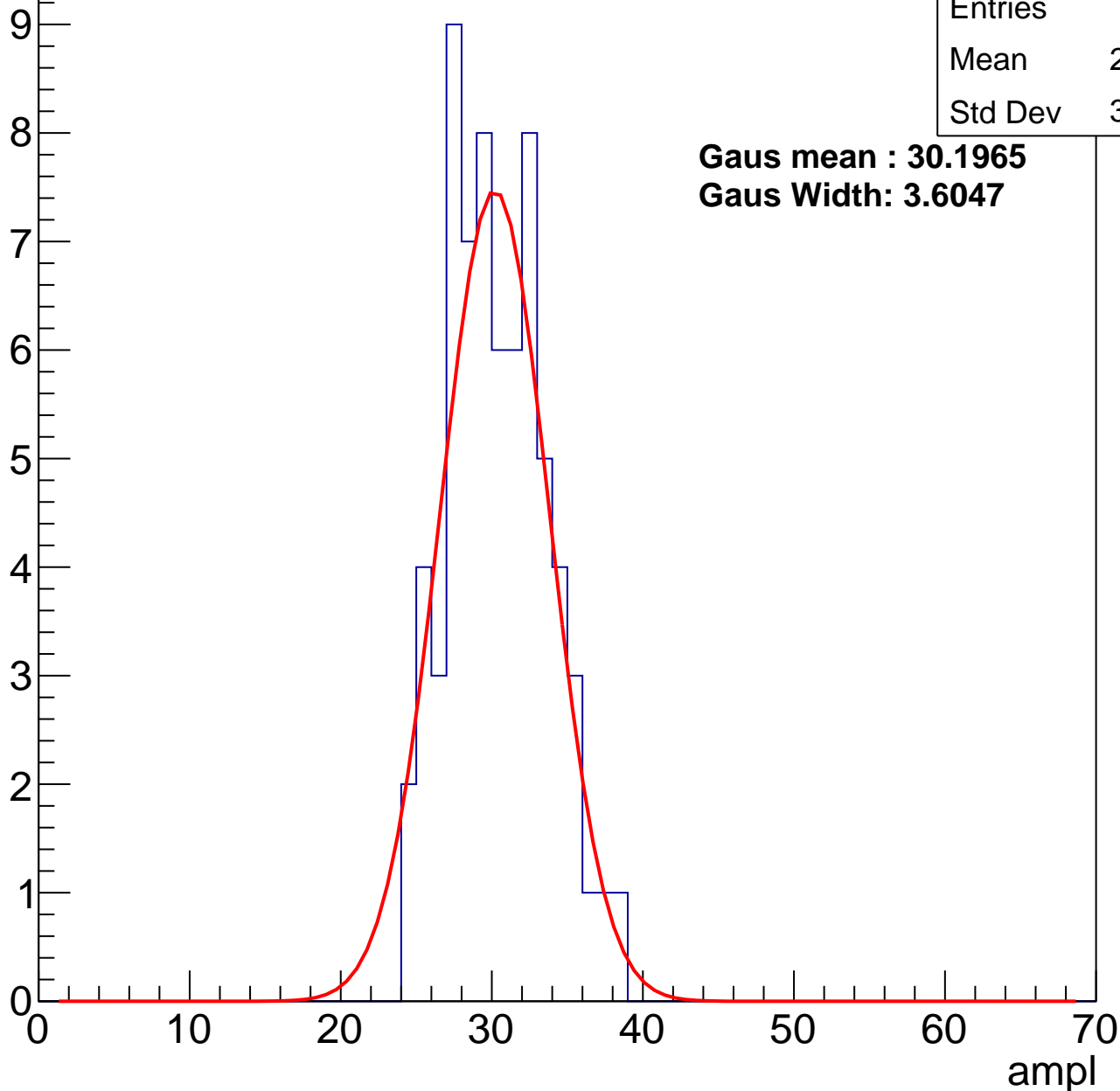
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.94
Std Dev	3.226

Gaus mean : 30.1665

Gaus Width: 3.6047



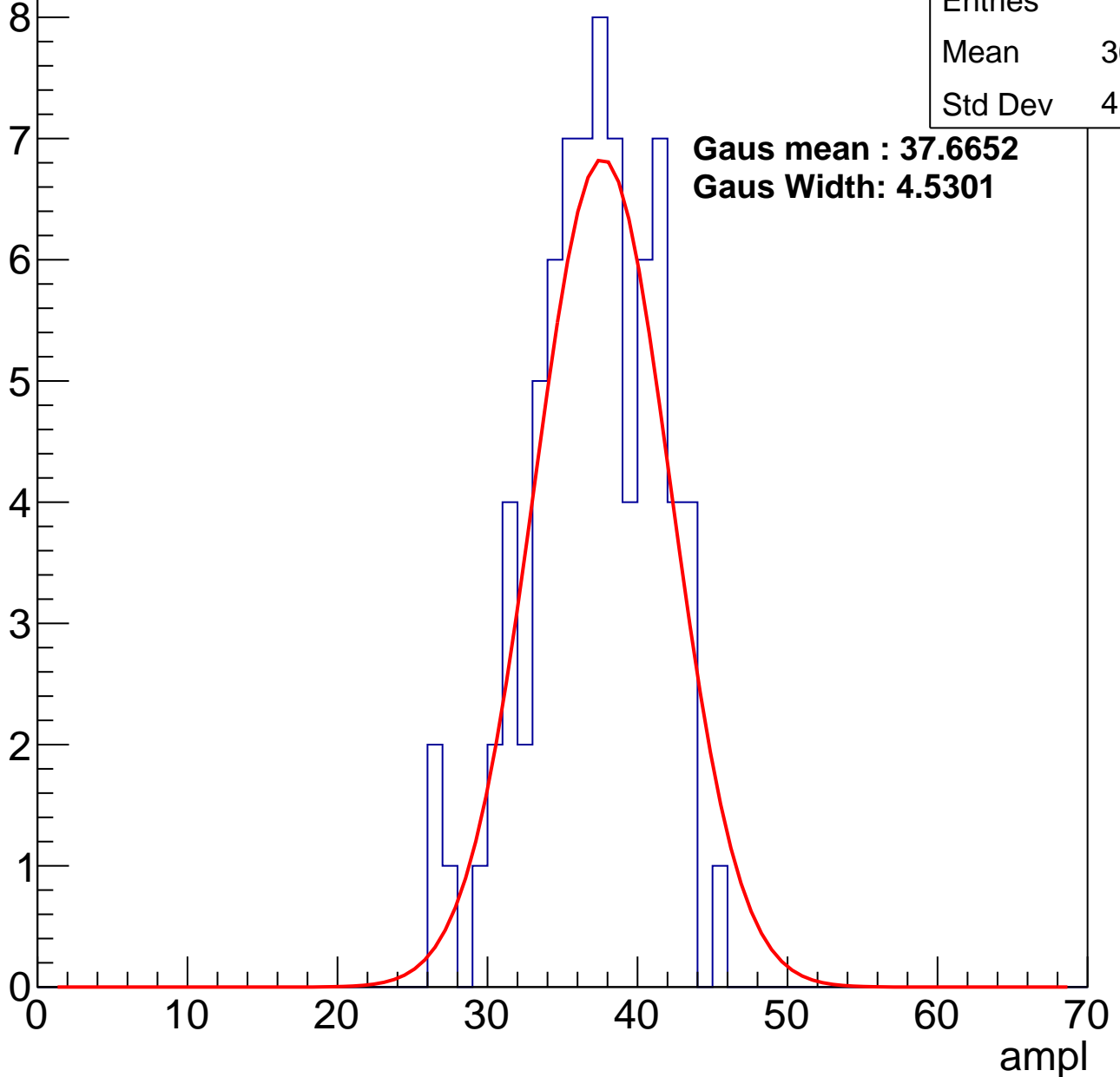
B1L101S, U23-ch35, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.56
Std Dev	4.159

Gaus mean : 37.6652
Gaus Width: 4.5301



B1L101S, U23-ch35, adc2

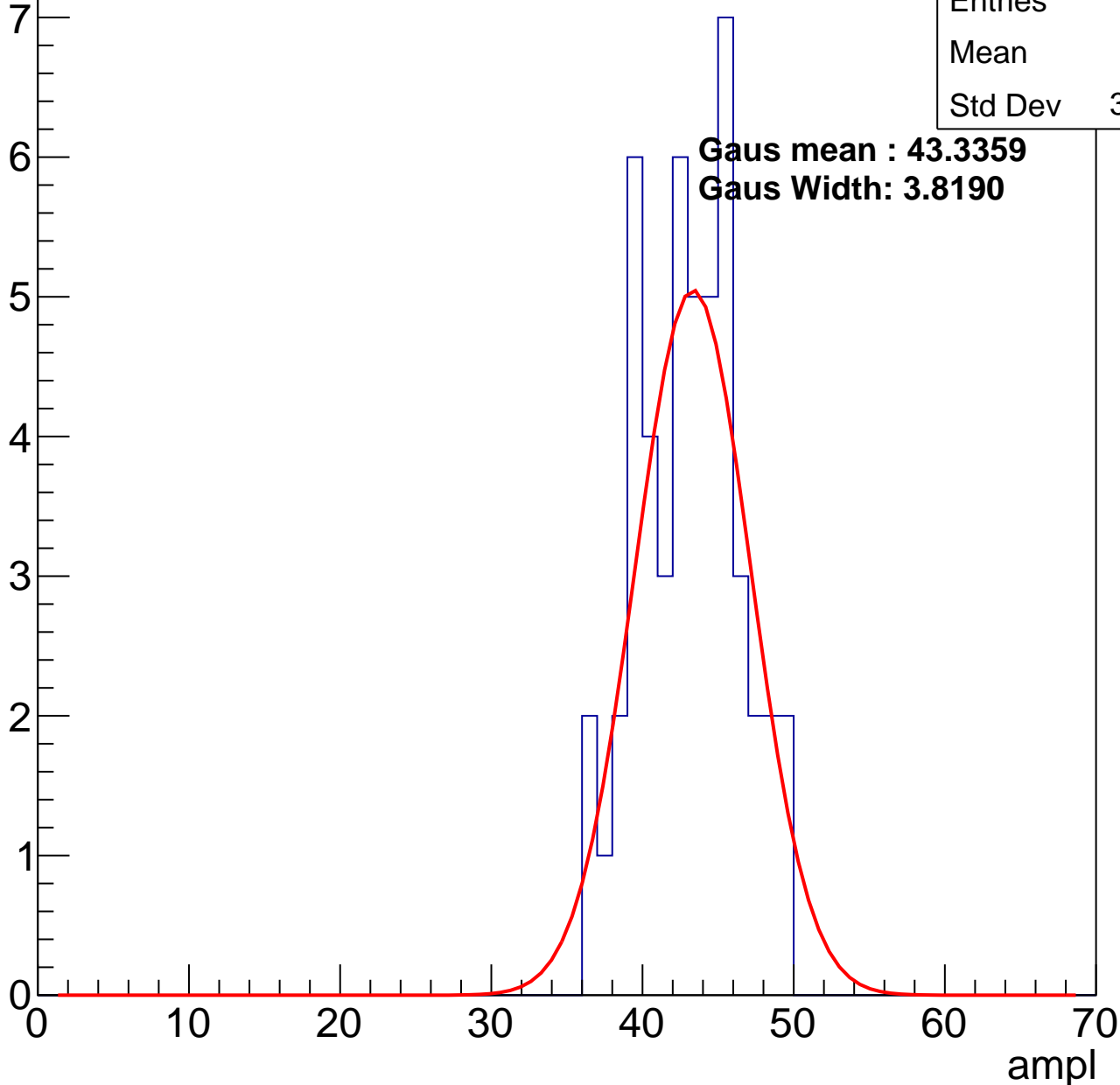
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.6
Std Dev	3.286

Gaus mean : 43.3359

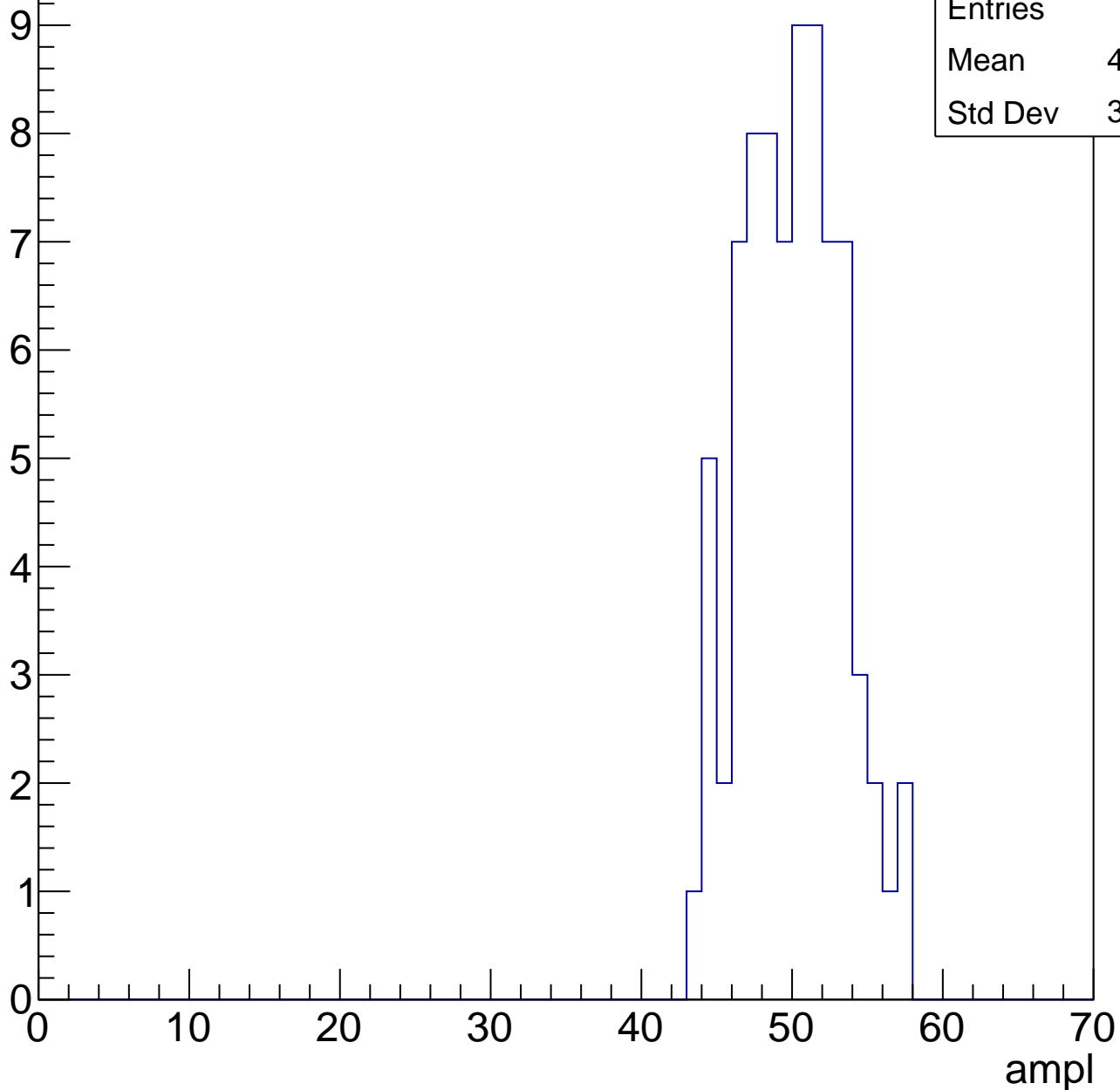
Gaus Width: 3.8190



B1L101S, U23-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

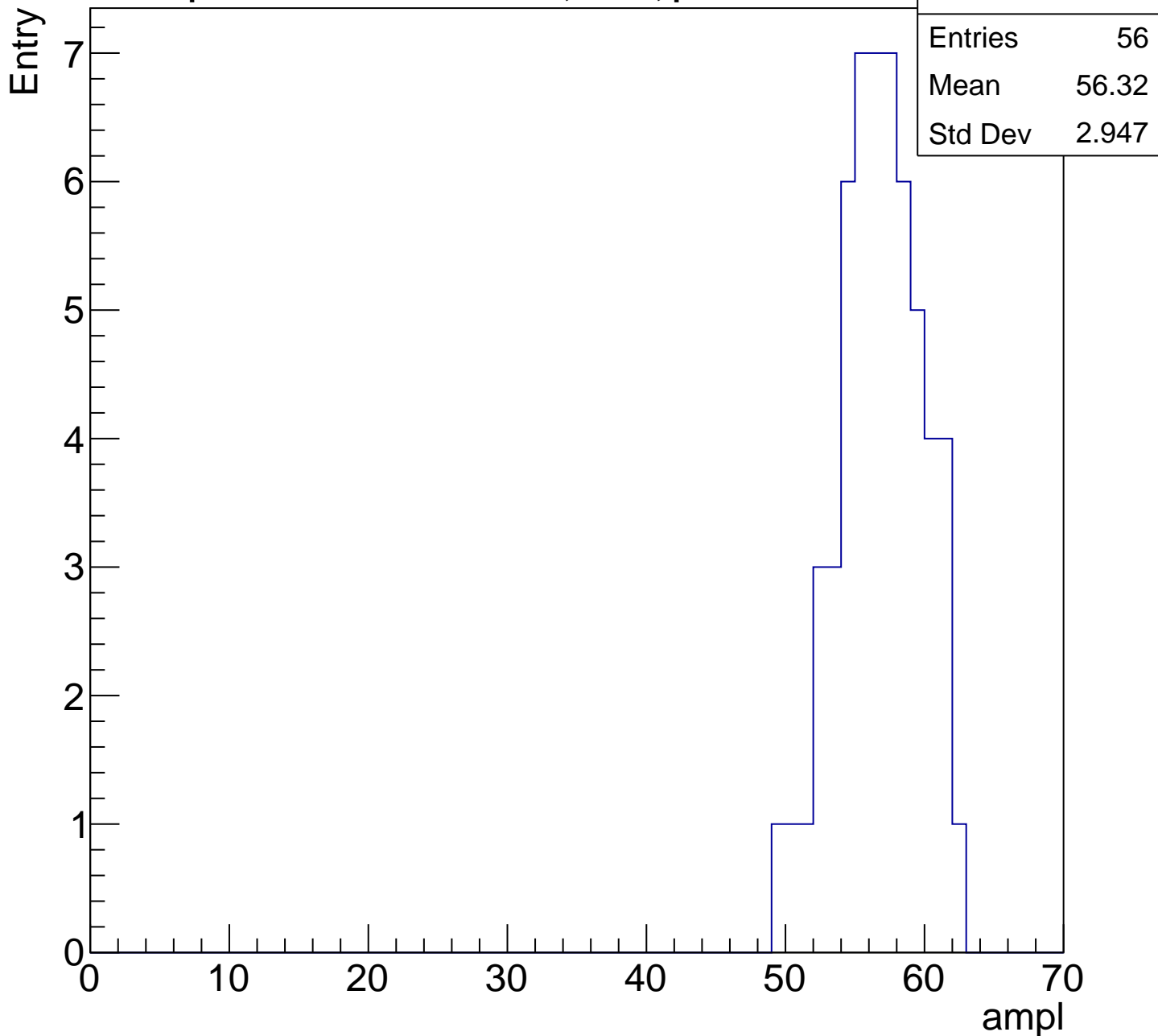
Entry



Entries	78
Mean	49.54
Std Dev	3.237

B1L101S, U23-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

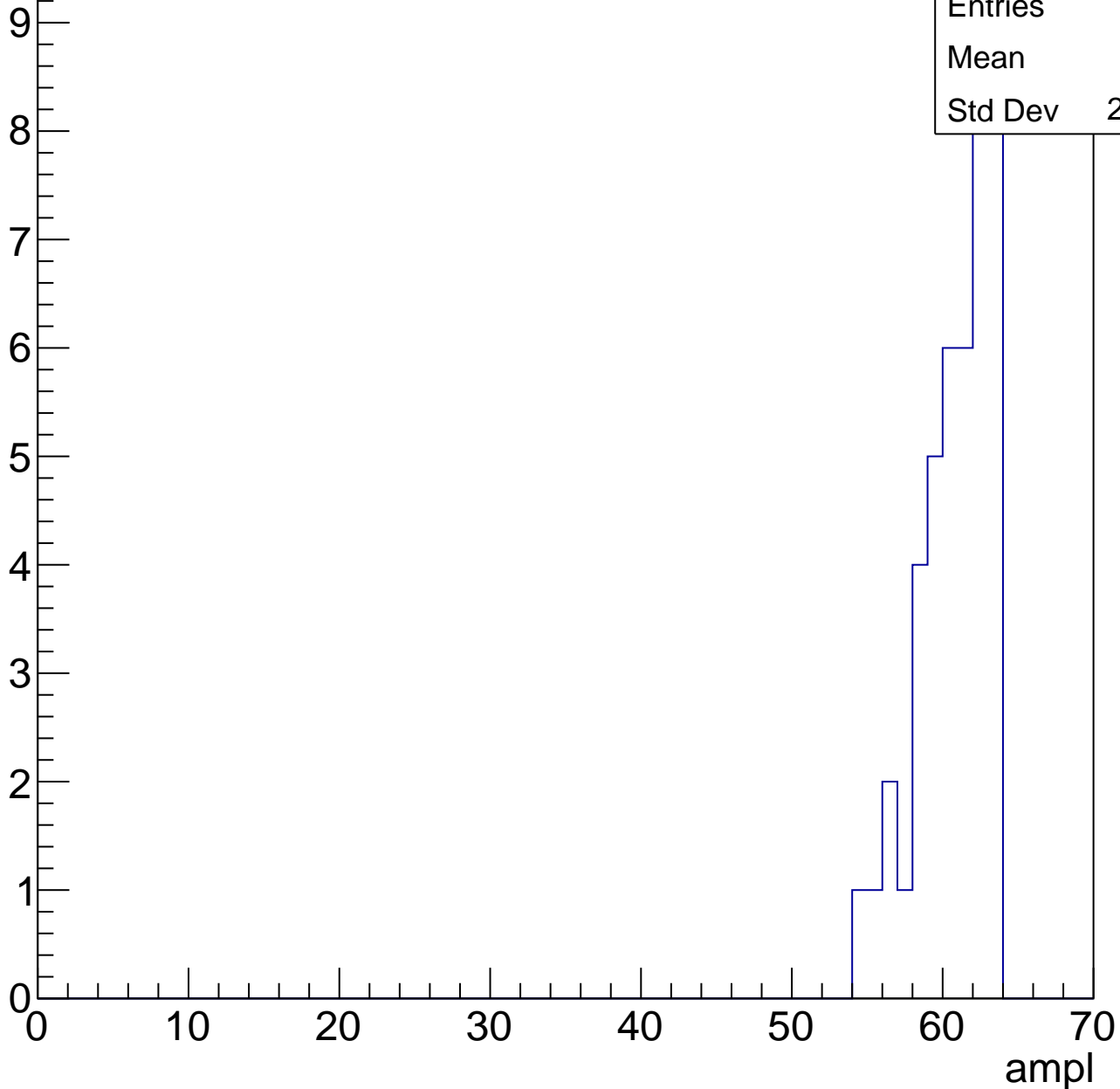


B1L101S, U23-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	60.3
Std Dev	2.328



B1L101S, U23-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch36, adc0

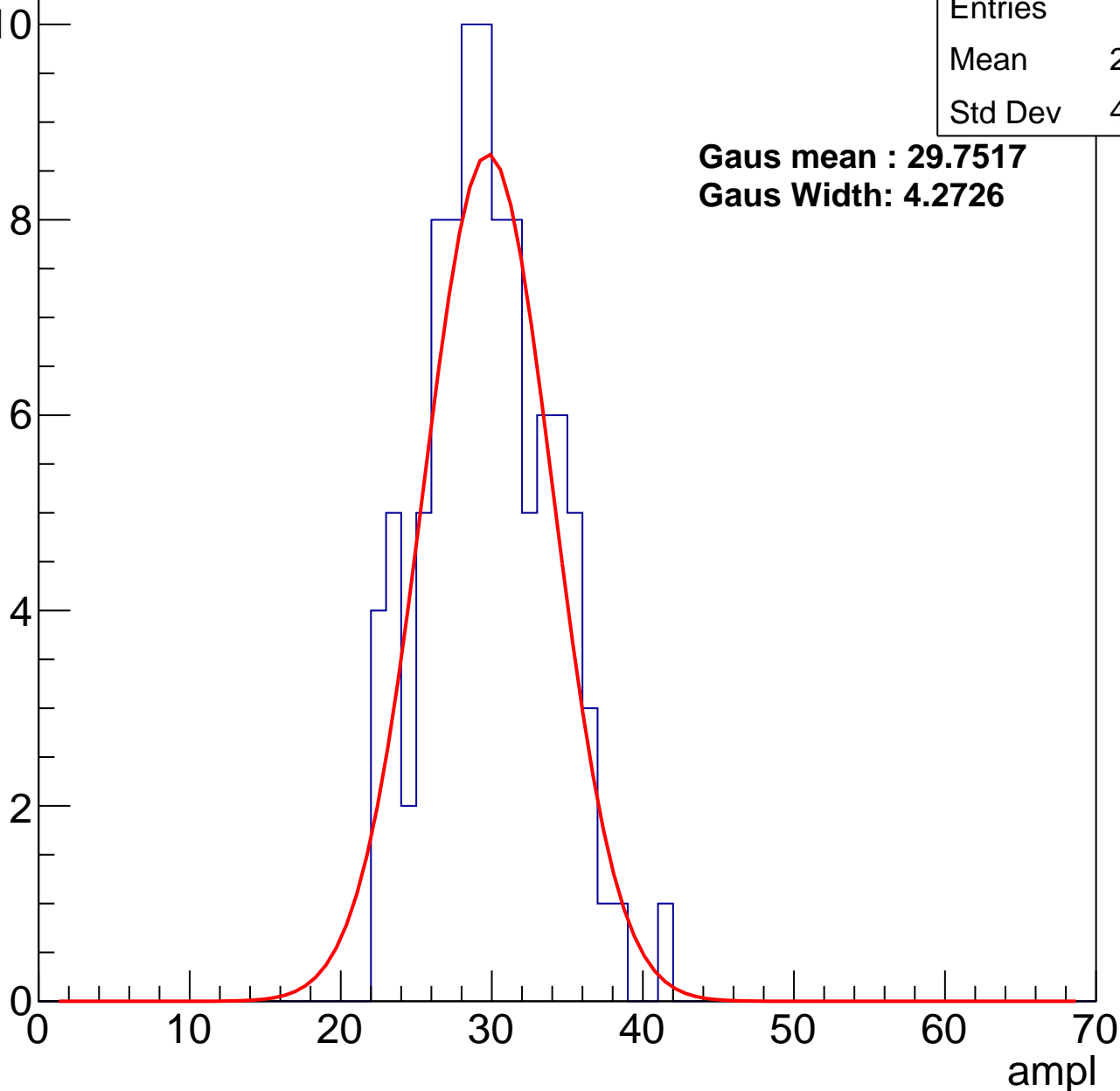
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	96
Mean	29.36
Std Dev	4.024

Gaus mean : 29.7517

Gaus Width: 4.2726



B1L101S, U23-ch36, adc1

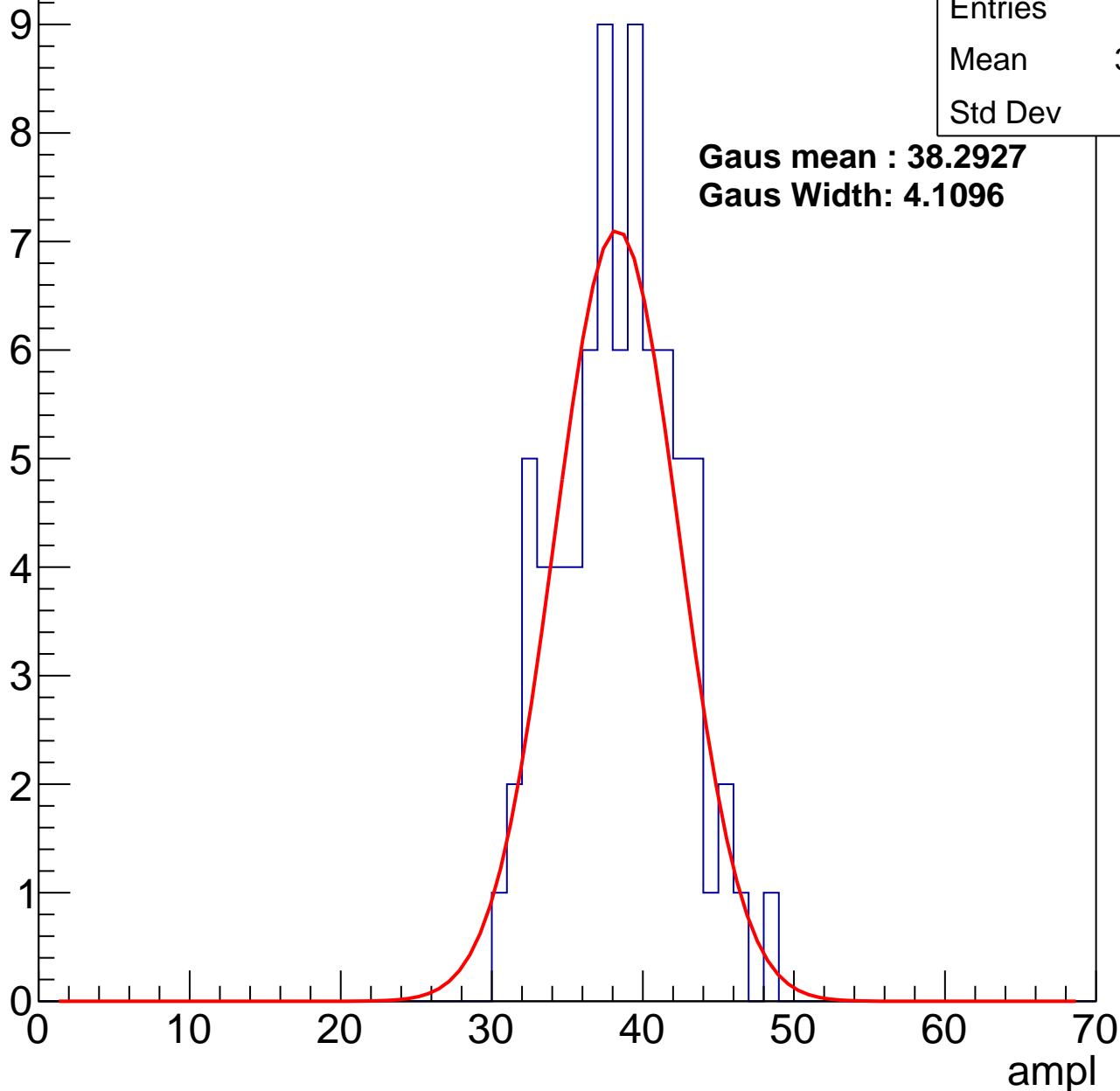
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	38.01
Std Dev	3.89

Gaus mean : 38.2927

Gaus Width: 4.1096



B1L101S, U23-ch36, adc2

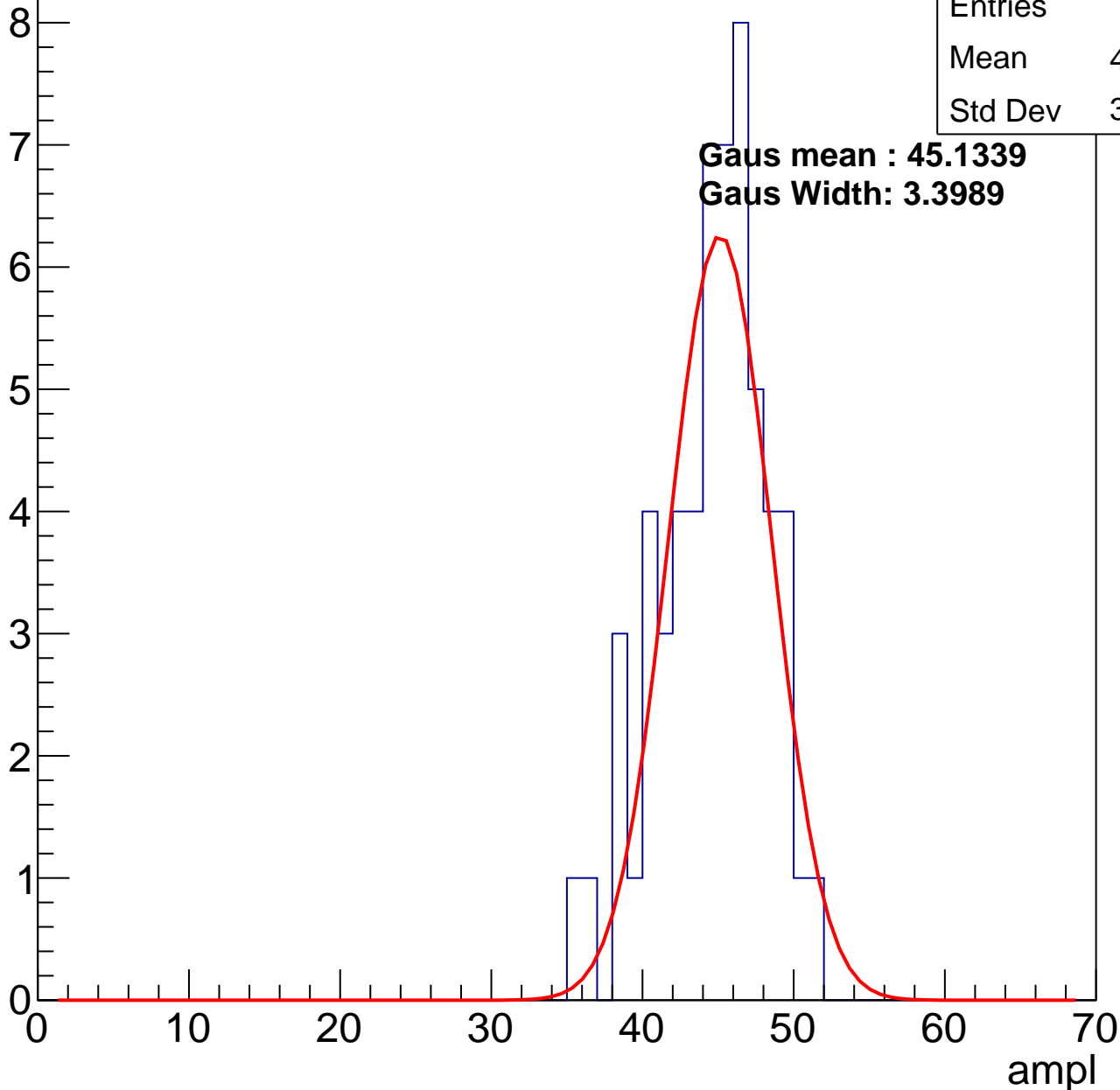
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.17
Std Dev	3.524

Gaus mean : 45.1339

Gaus Width: 3.3989

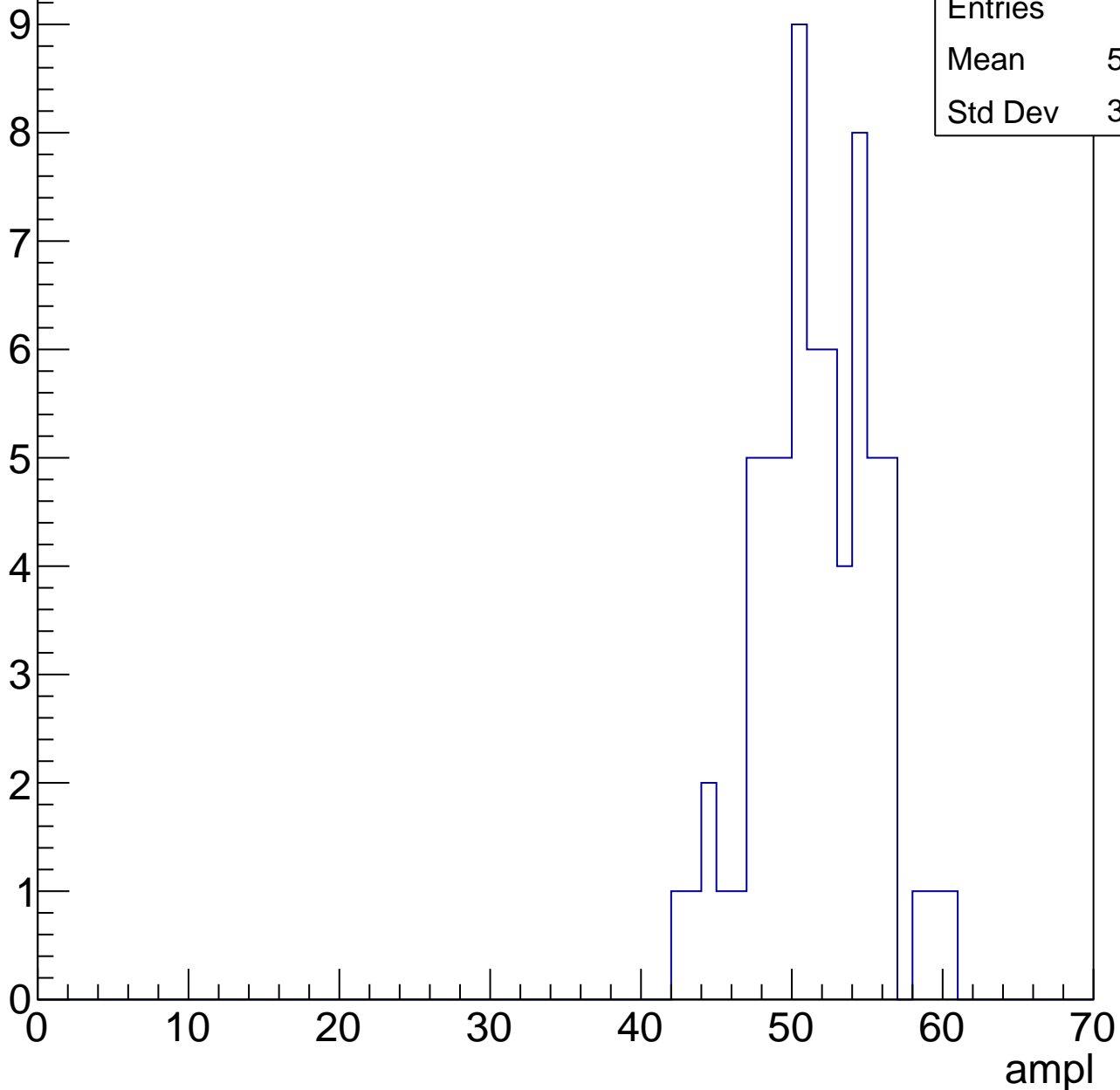


B1L101S, U23-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.16
Std Dev	3.764



B1L101S, U23-ch36, adc4

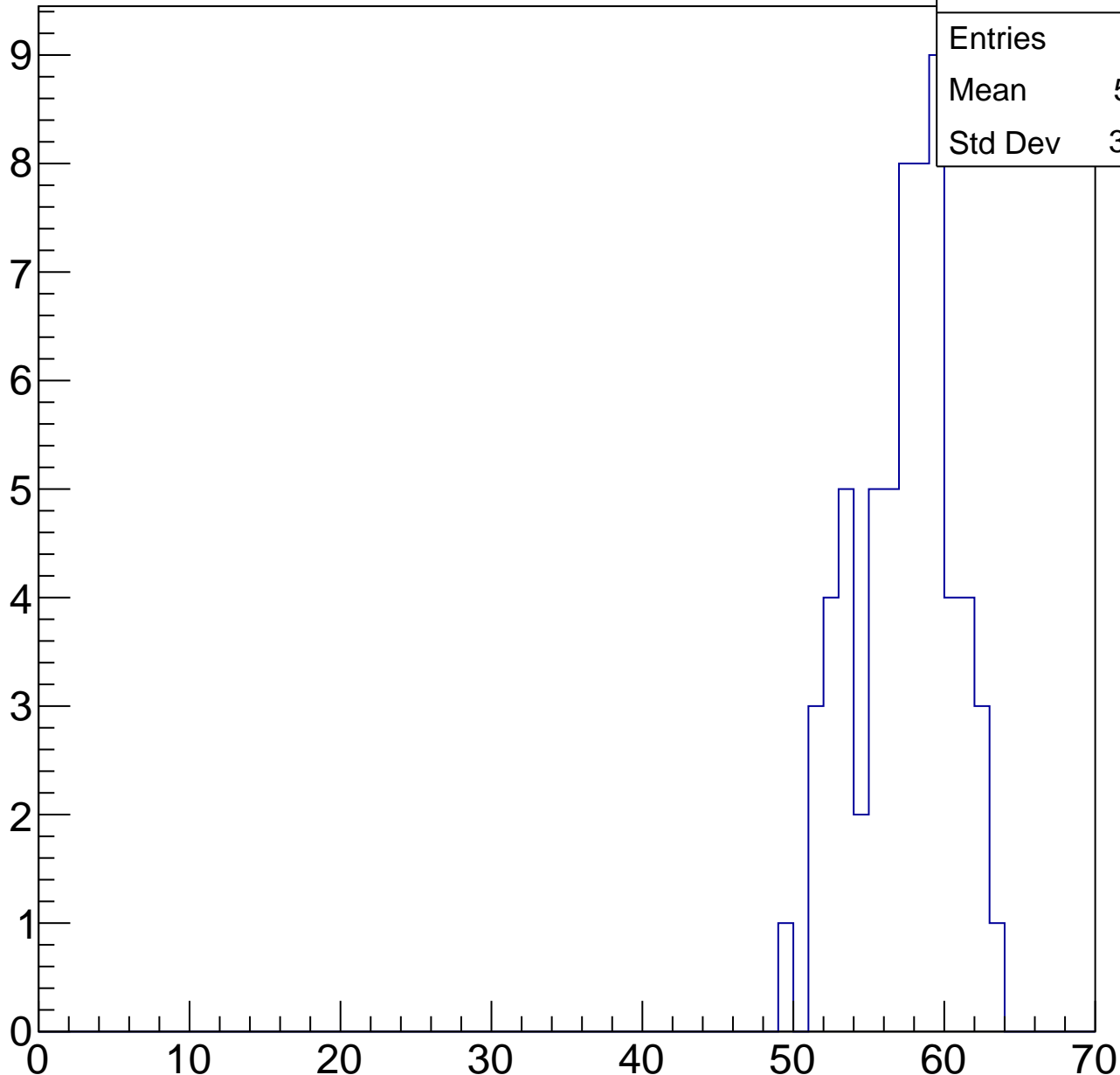
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	56.81
Std Dev	3.232

ampl

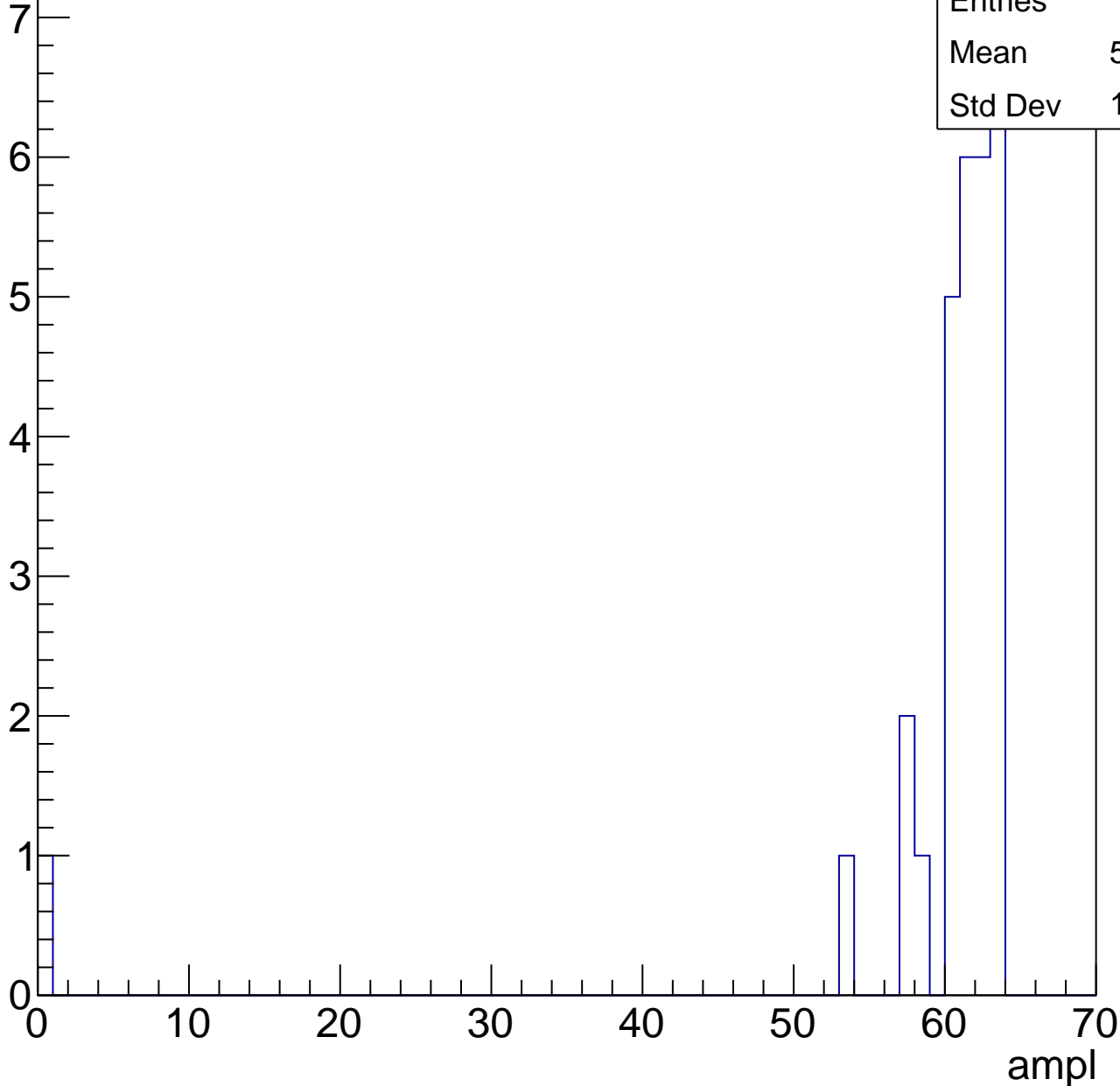


B1L101S, U23-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.76
Std Dev	11.32



B1L101S, U23-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U23-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch37, adc0

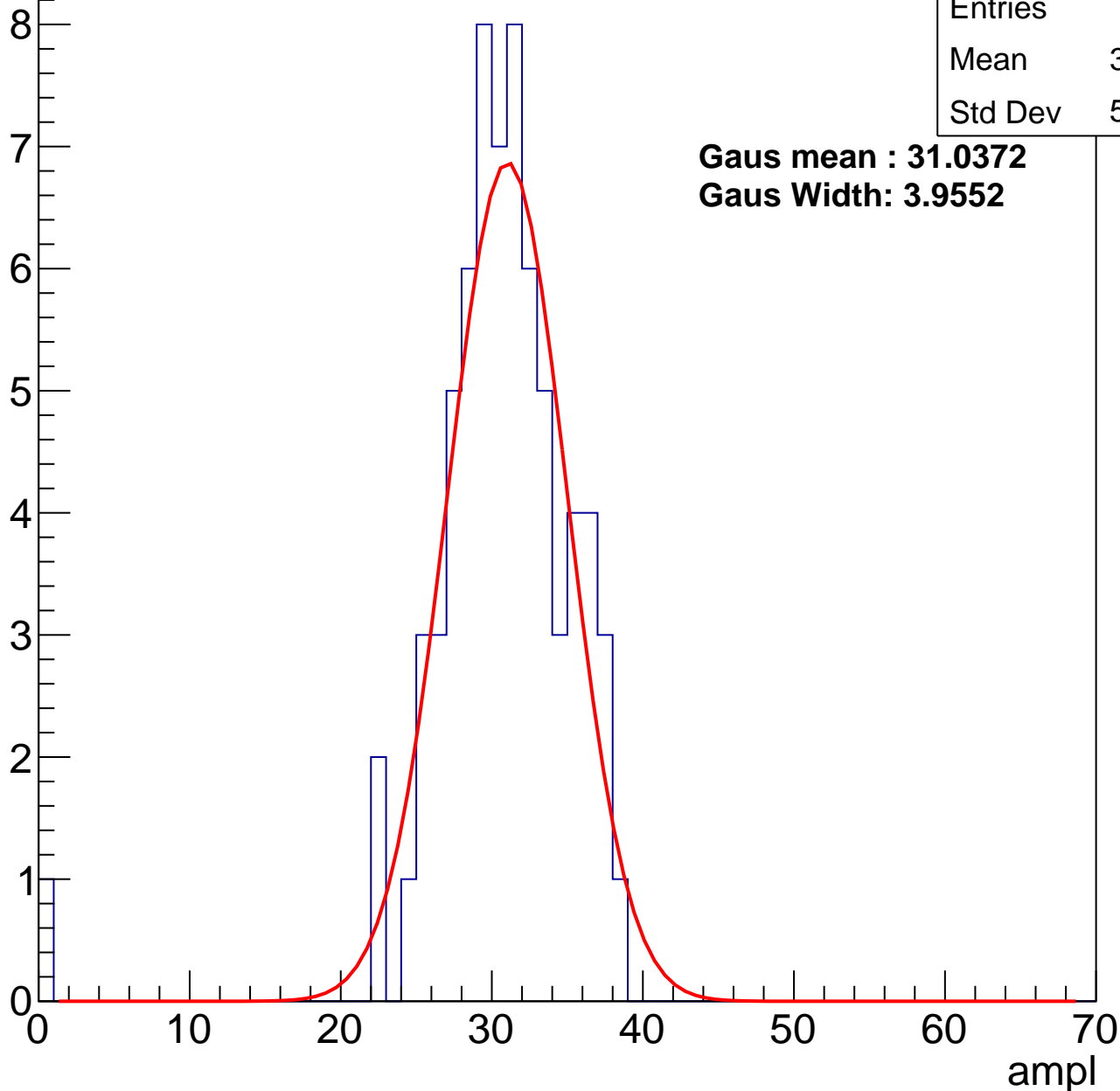
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.09
Std Dev	5.132

Gaus mean : 31.0372

Gaus Width: 3.9552



B1L101S, U23-ch37, adc1

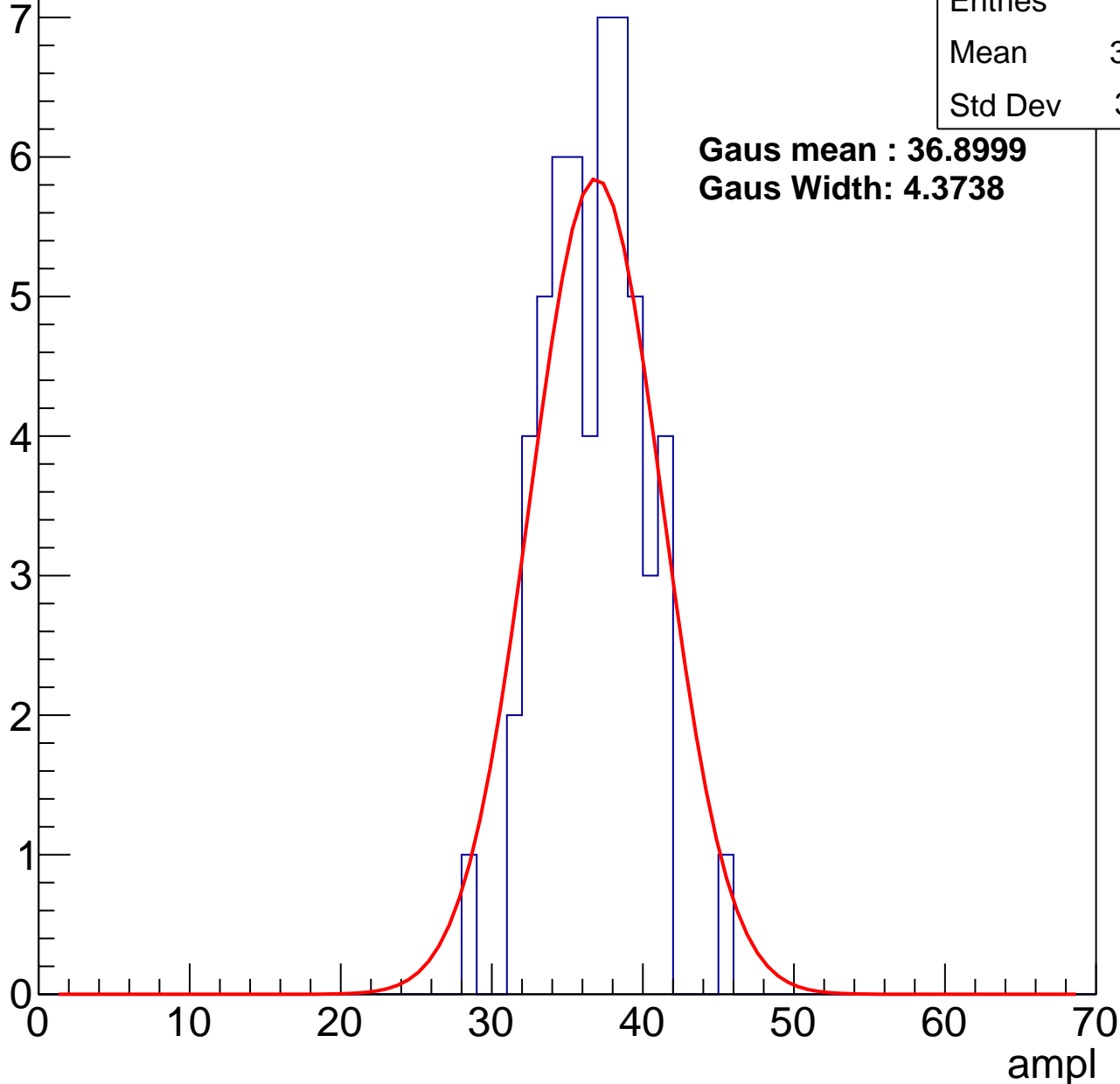
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.18
Std Dev	3.191

Gaus mean : 36.8999

Gaus Width: 4.3738



B1L101S, U23-ch37, adc2

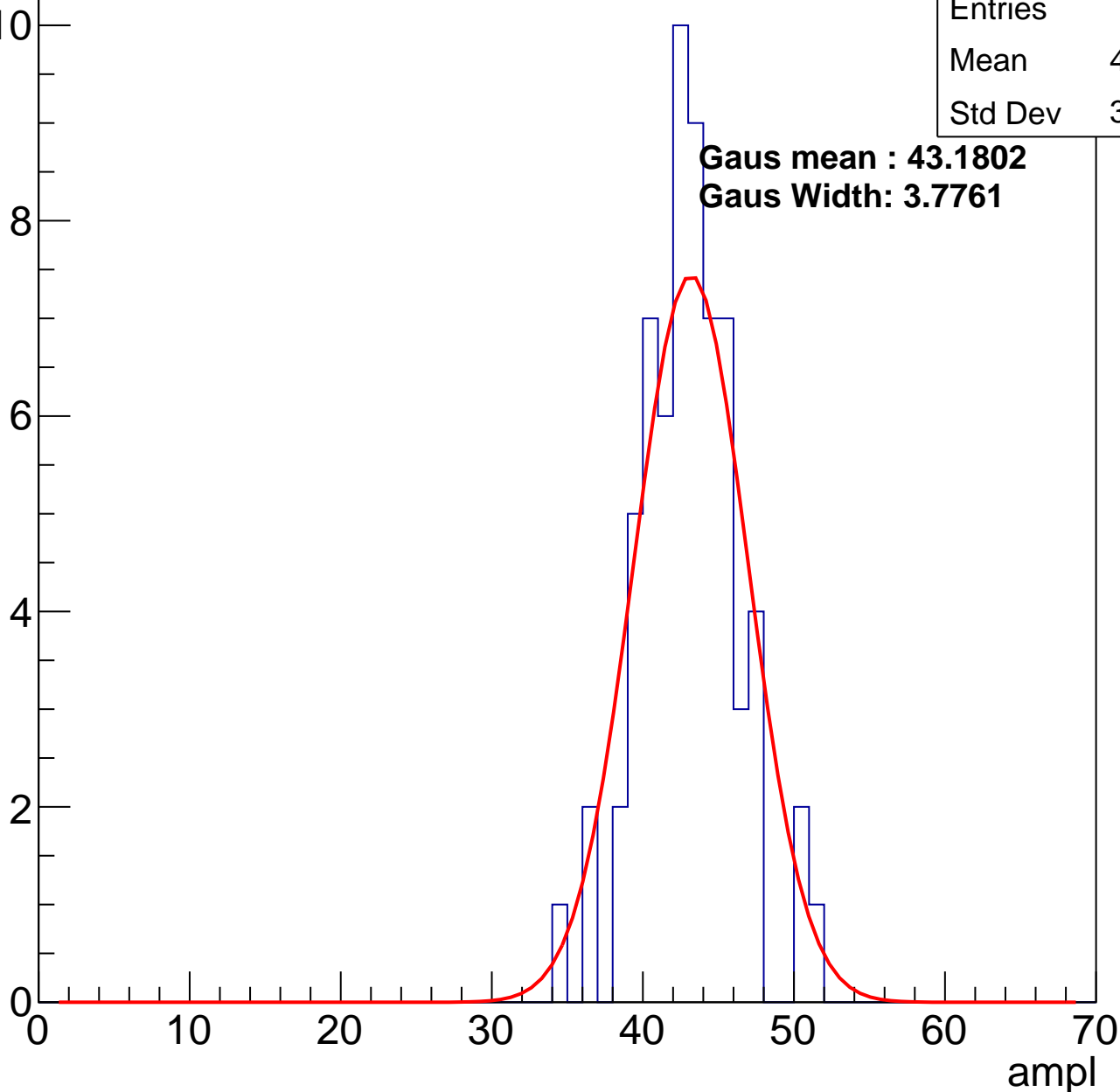
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.58
Std Dev	3.215

Gaus mean : 43.1802

Gaus Width: 3.7761

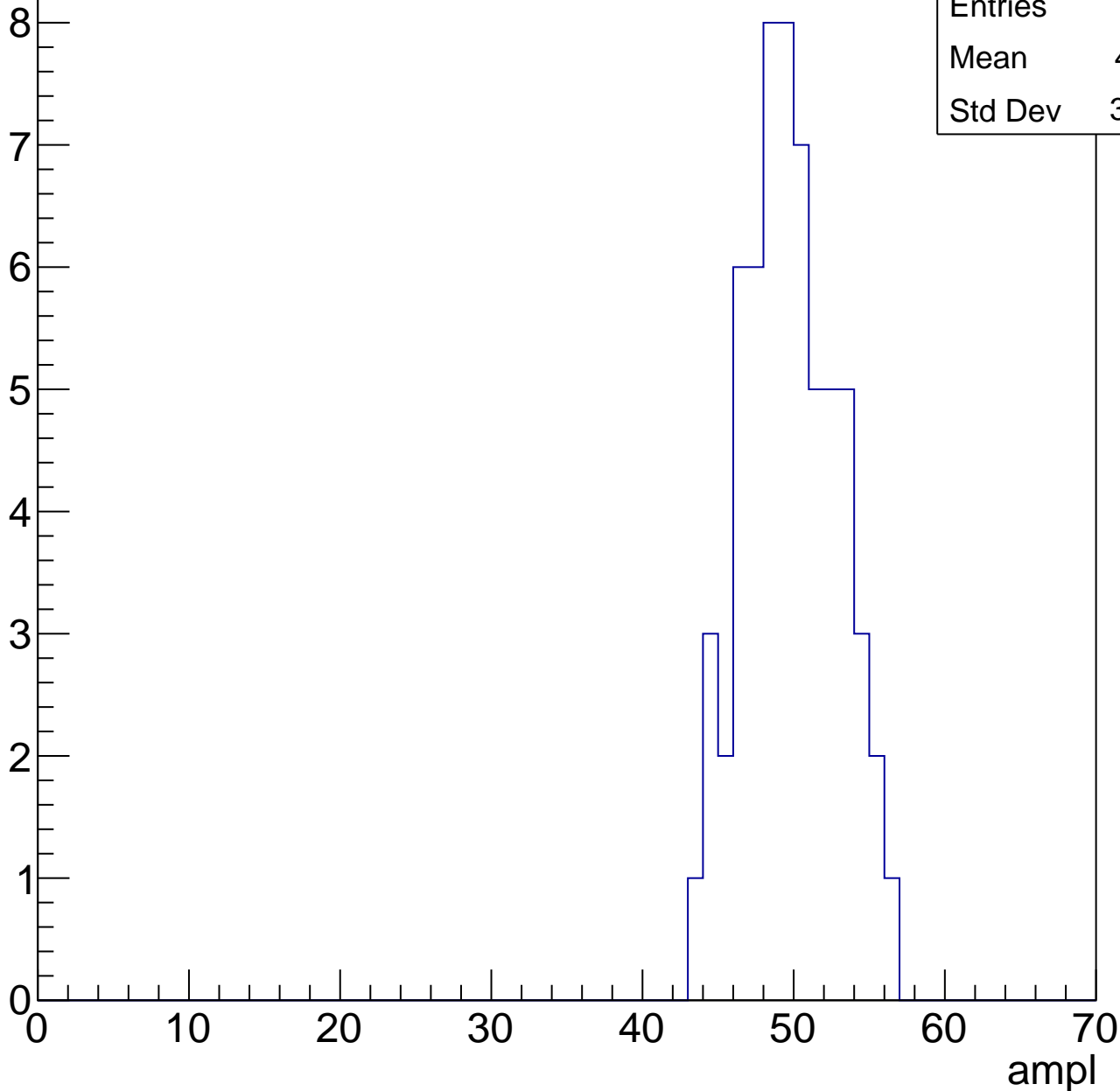


B1L101S, U23-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.31
Std Dev	3.035

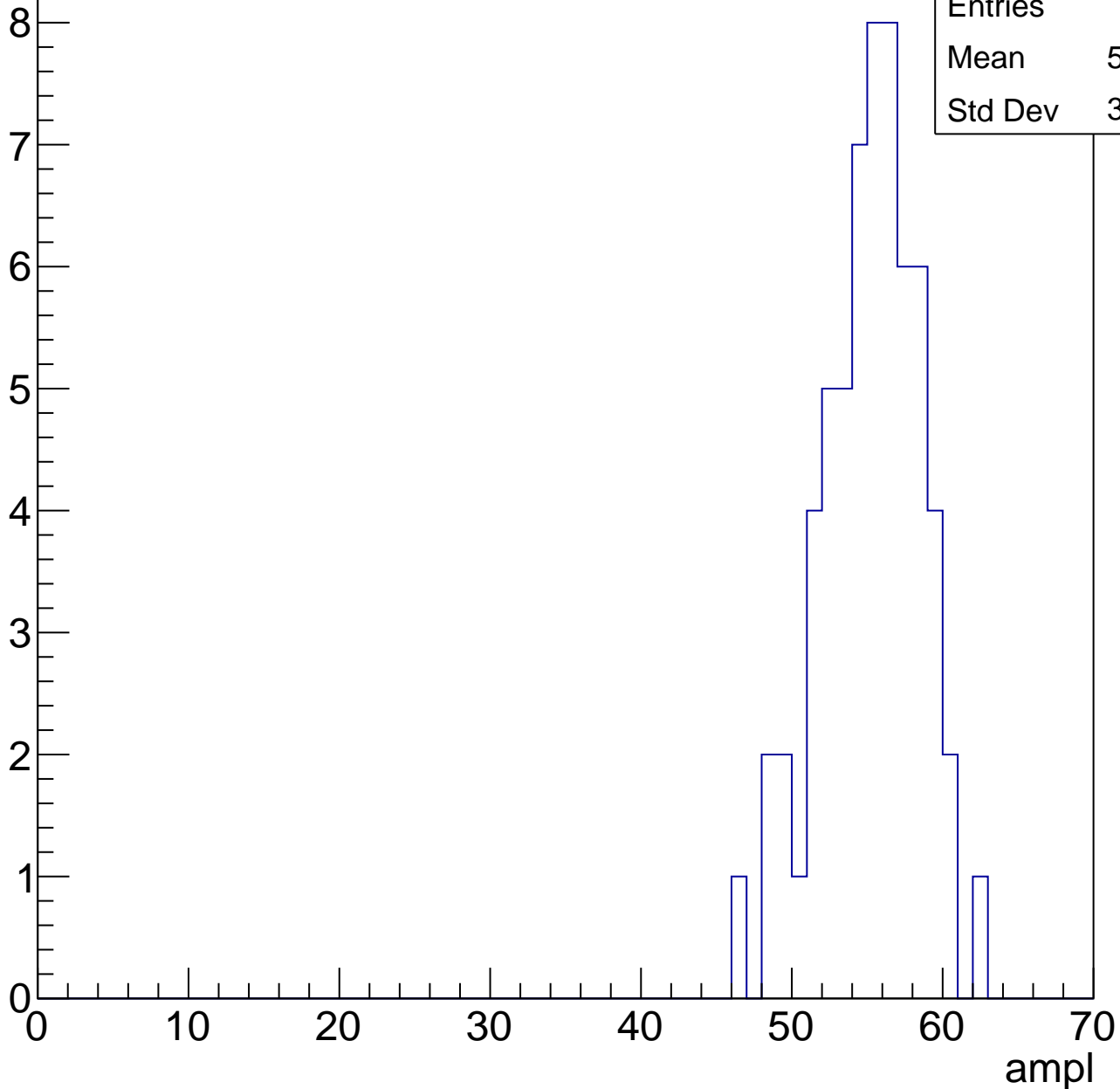


B1L101S, U23-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	54.73
Std Dev	3.254

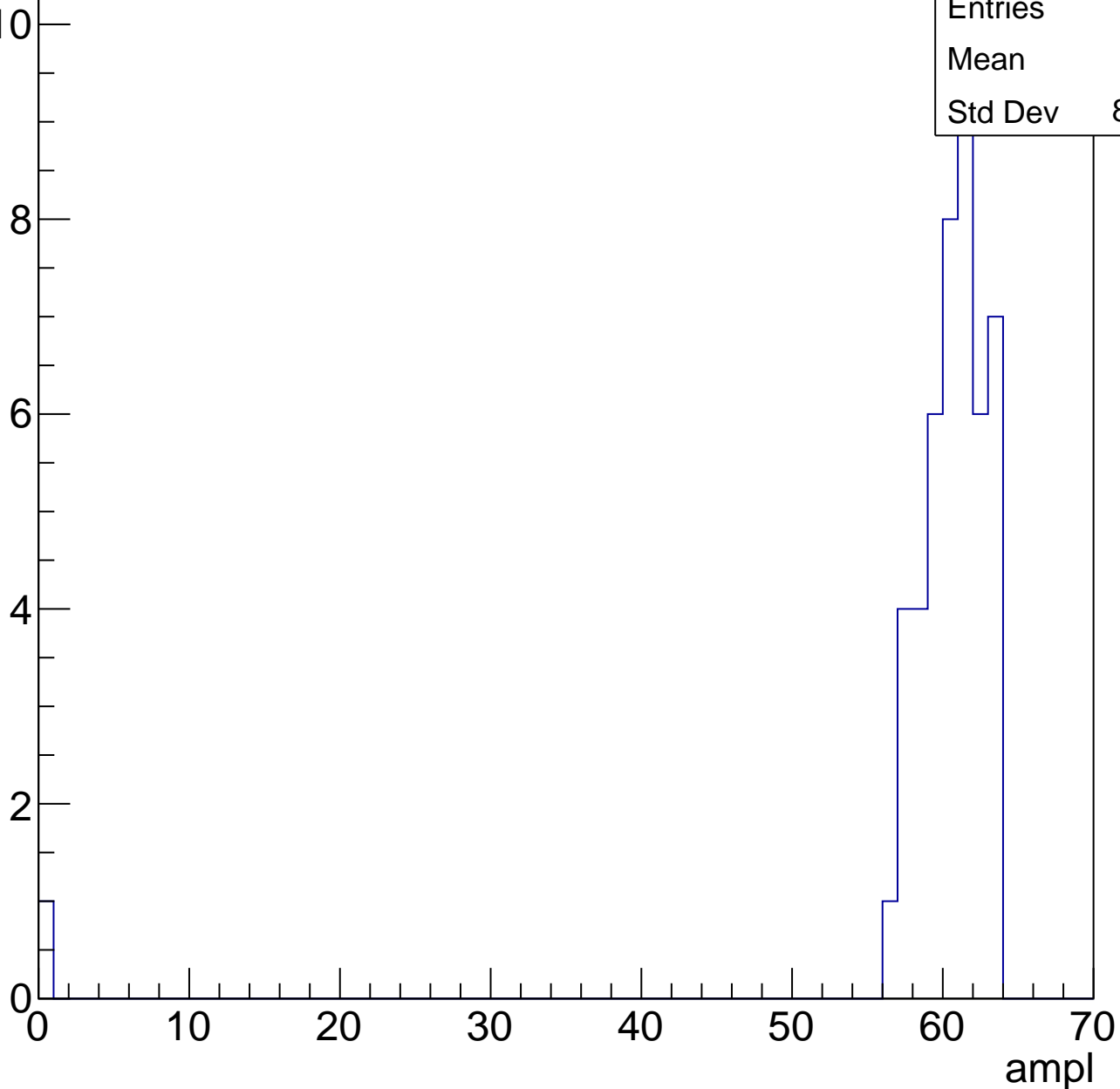


B1L101S, U23-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59
Std Dev	8.901



B1L101S, U23-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L101S, U23-ch38, adc0

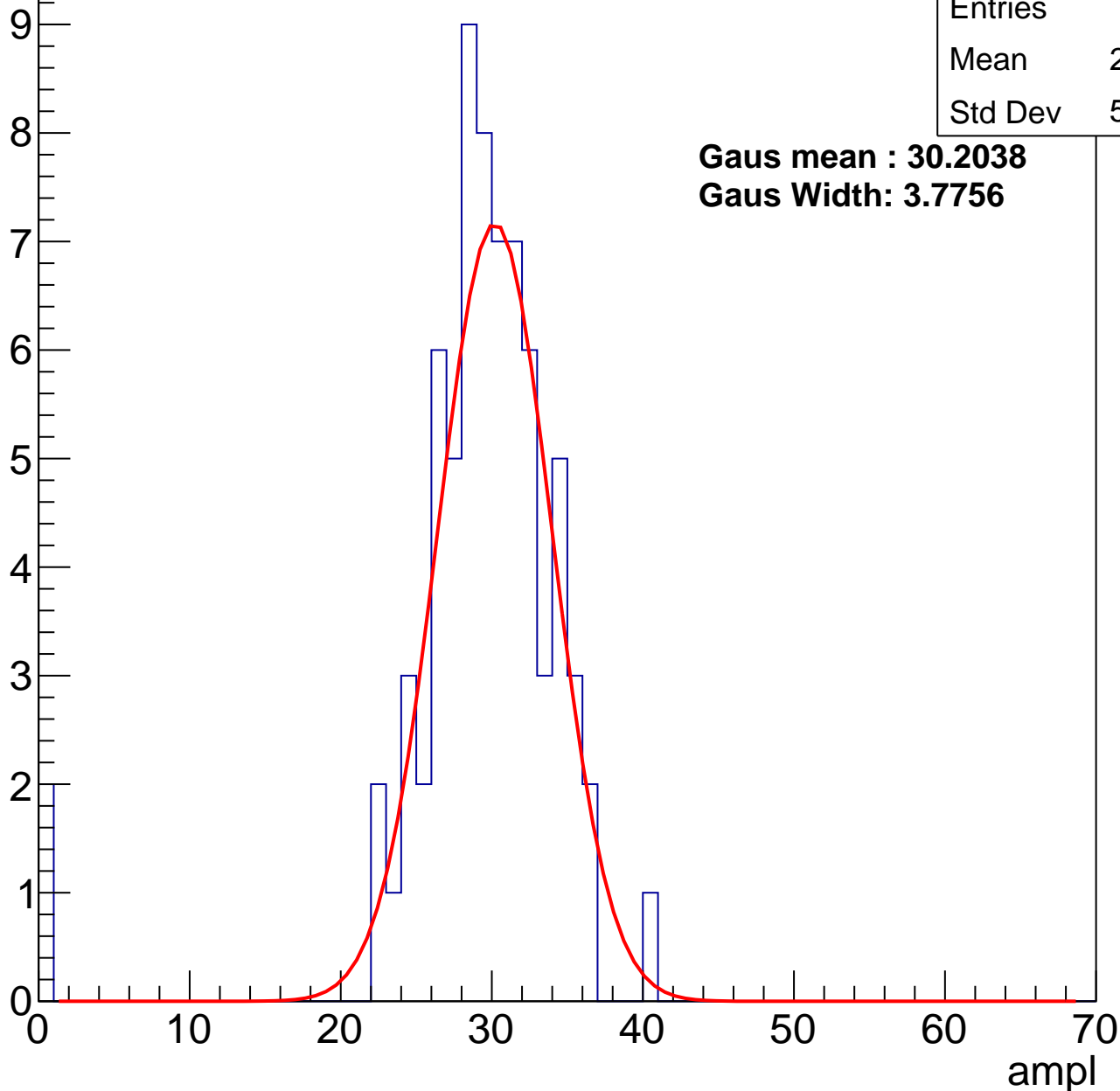
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.74
Std Dev	5.998

Gaus mean : 30.2038

Gaus Width: 3.7756



B1L101S, U23-ch38, adc1

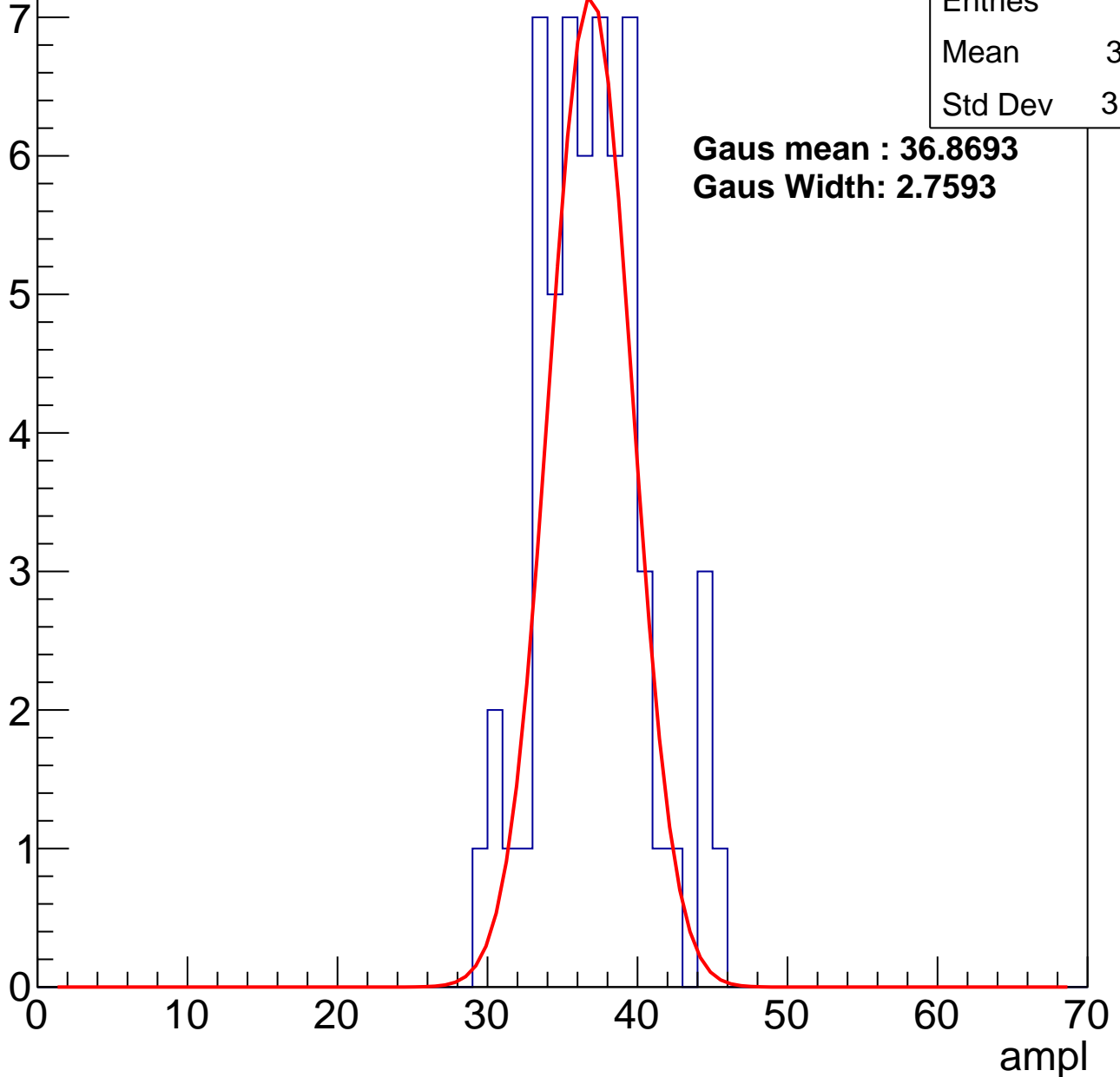
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.51
Std Dev	3.476

Gaus mean : 36.8693

Gaus Width: 2.7593



B1L101S, U23-ch38, adc2

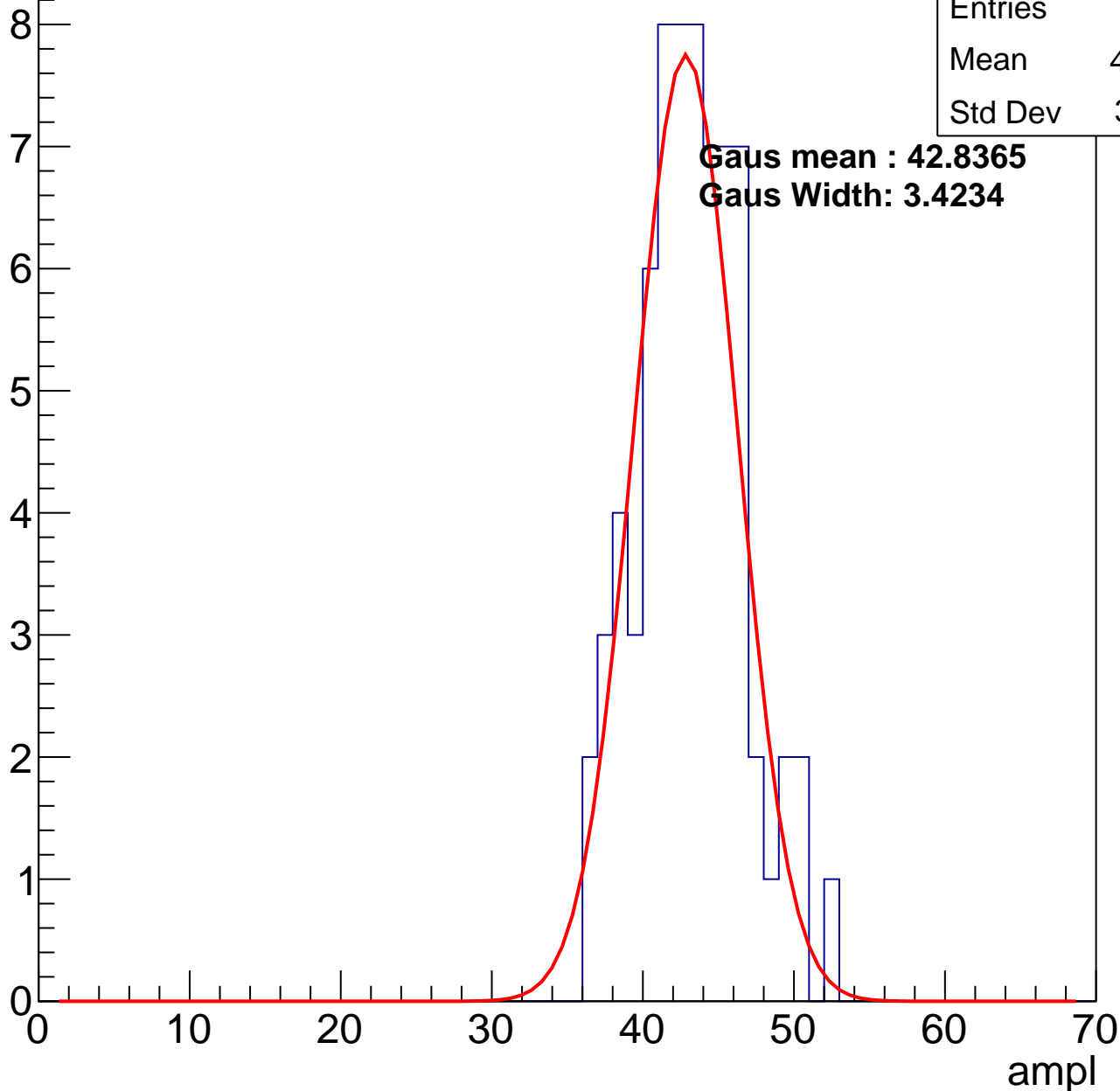
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.77
Std Dev	3.461

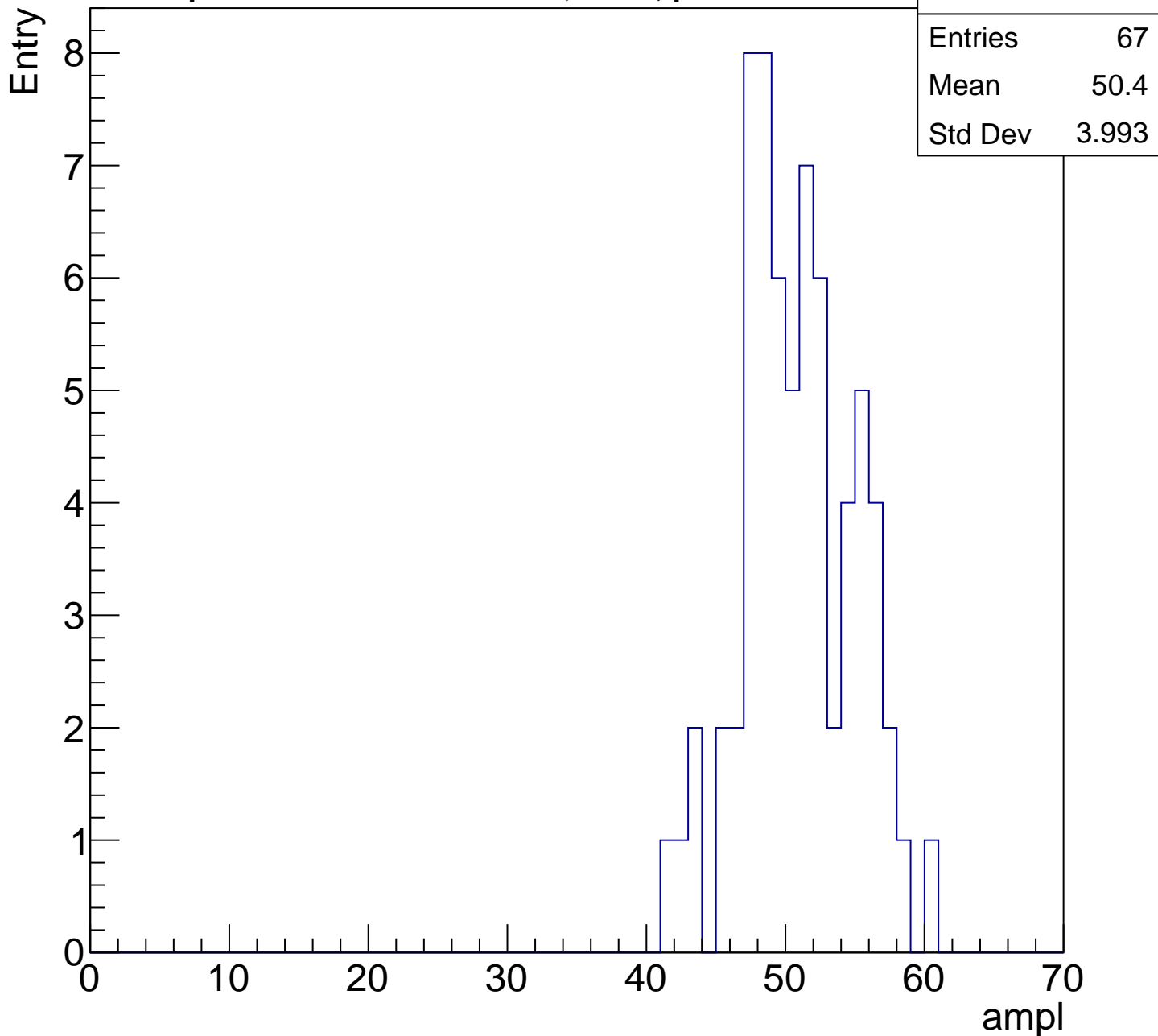
Gaus mean : 42.8365

Gaus Width: 3.4234



B1L101S, U23-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

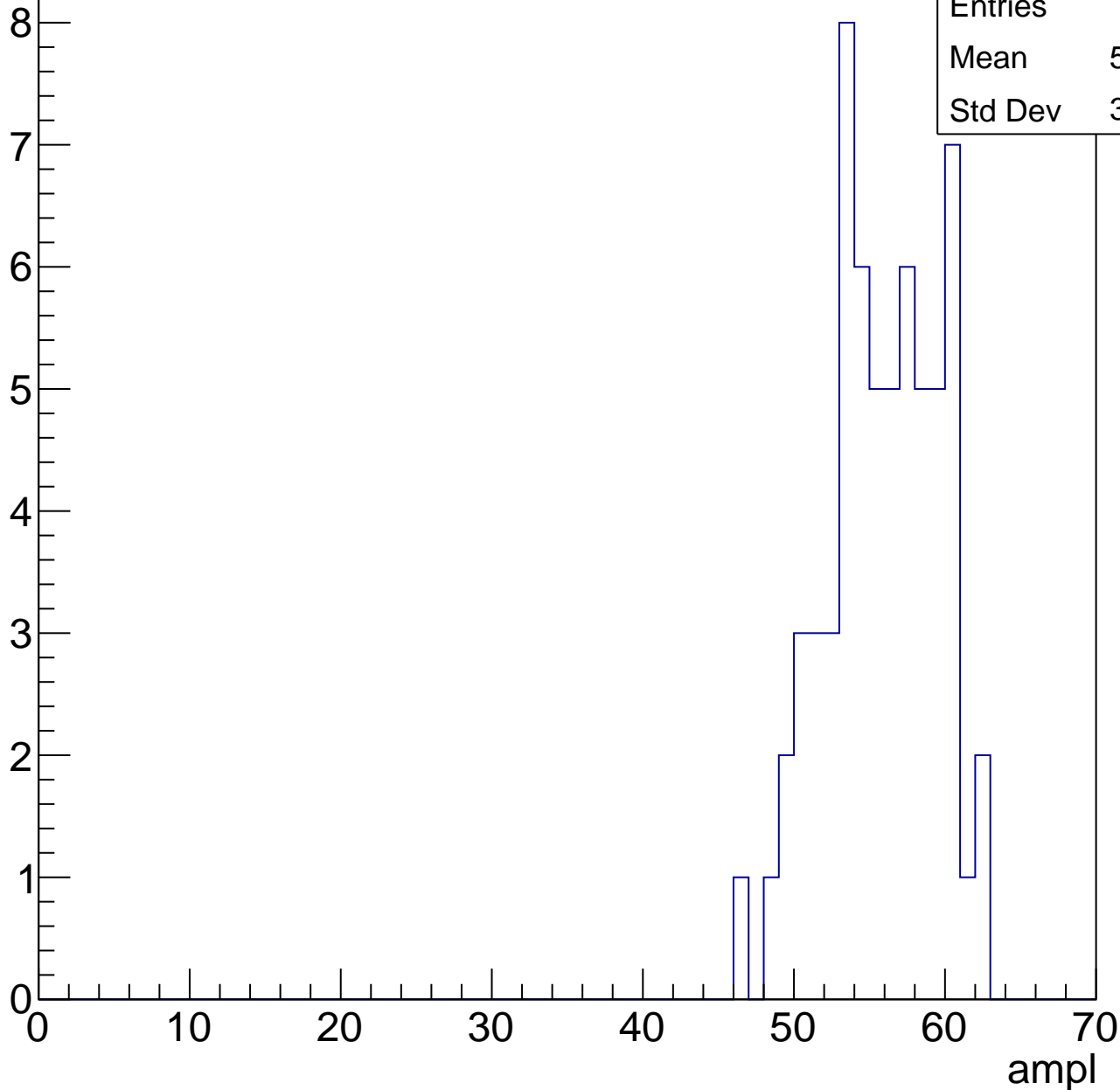


B1L101S, U23-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.33
Std Dev	3.665

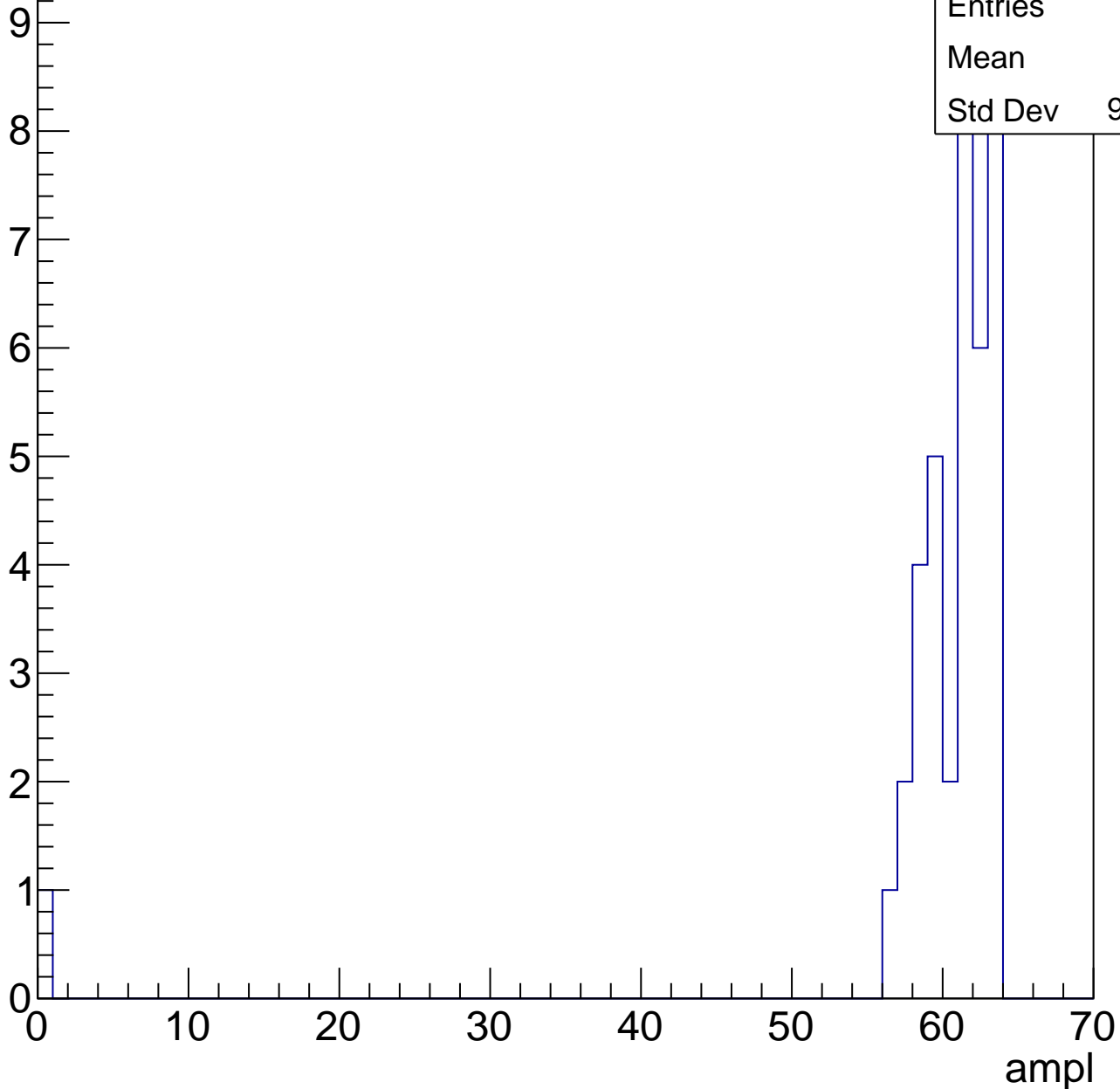


B1L101S, U23-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	59
Std Dev	9.897



B1L101S, U23-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U23-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

B1L101S, U23-ch39, adc0

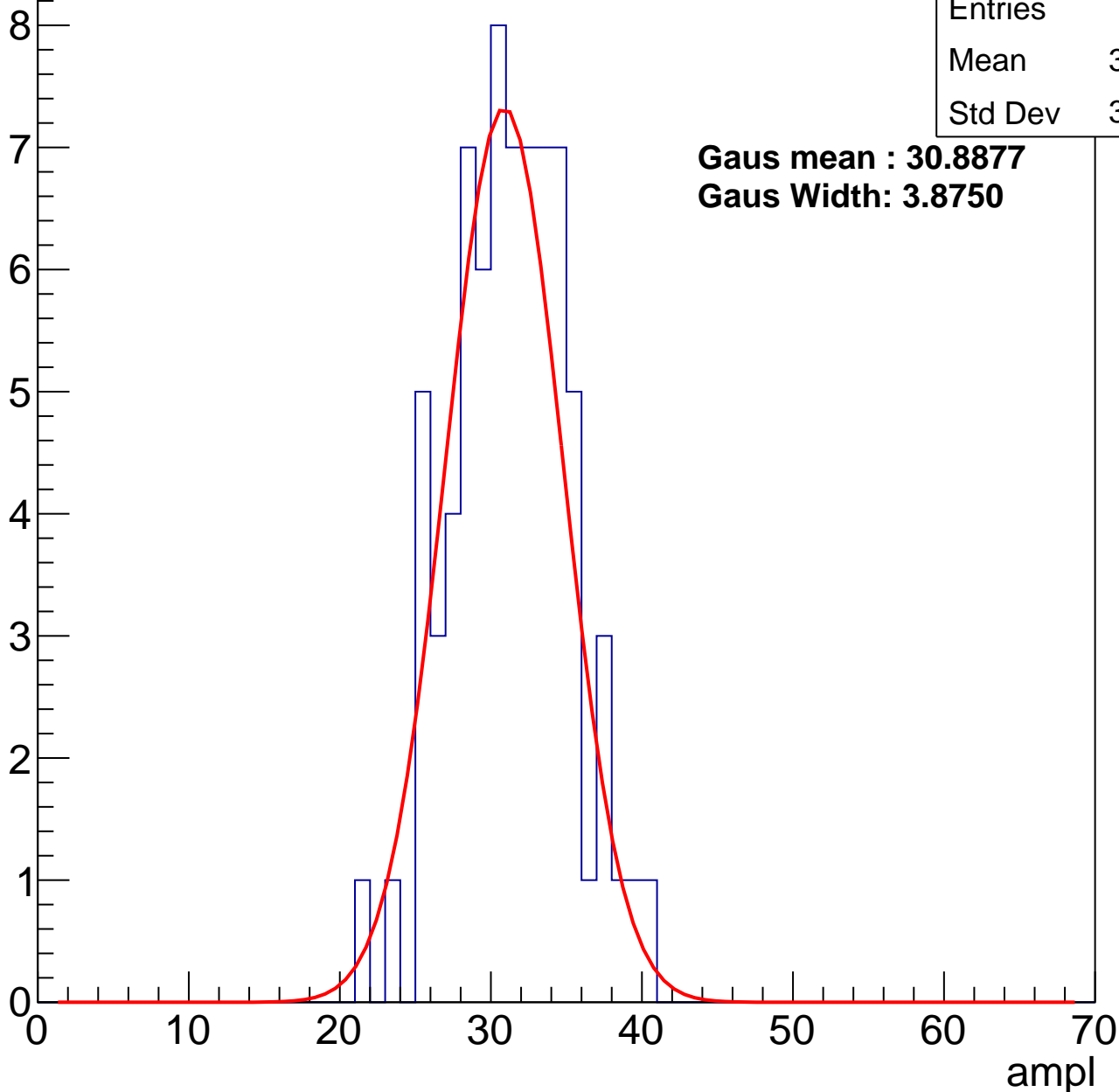
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.85
Std Dev	3.797

Gaus mean : 30.8877

Gaus Width: 3.8750



B1L101S, U23-ch39, adc1

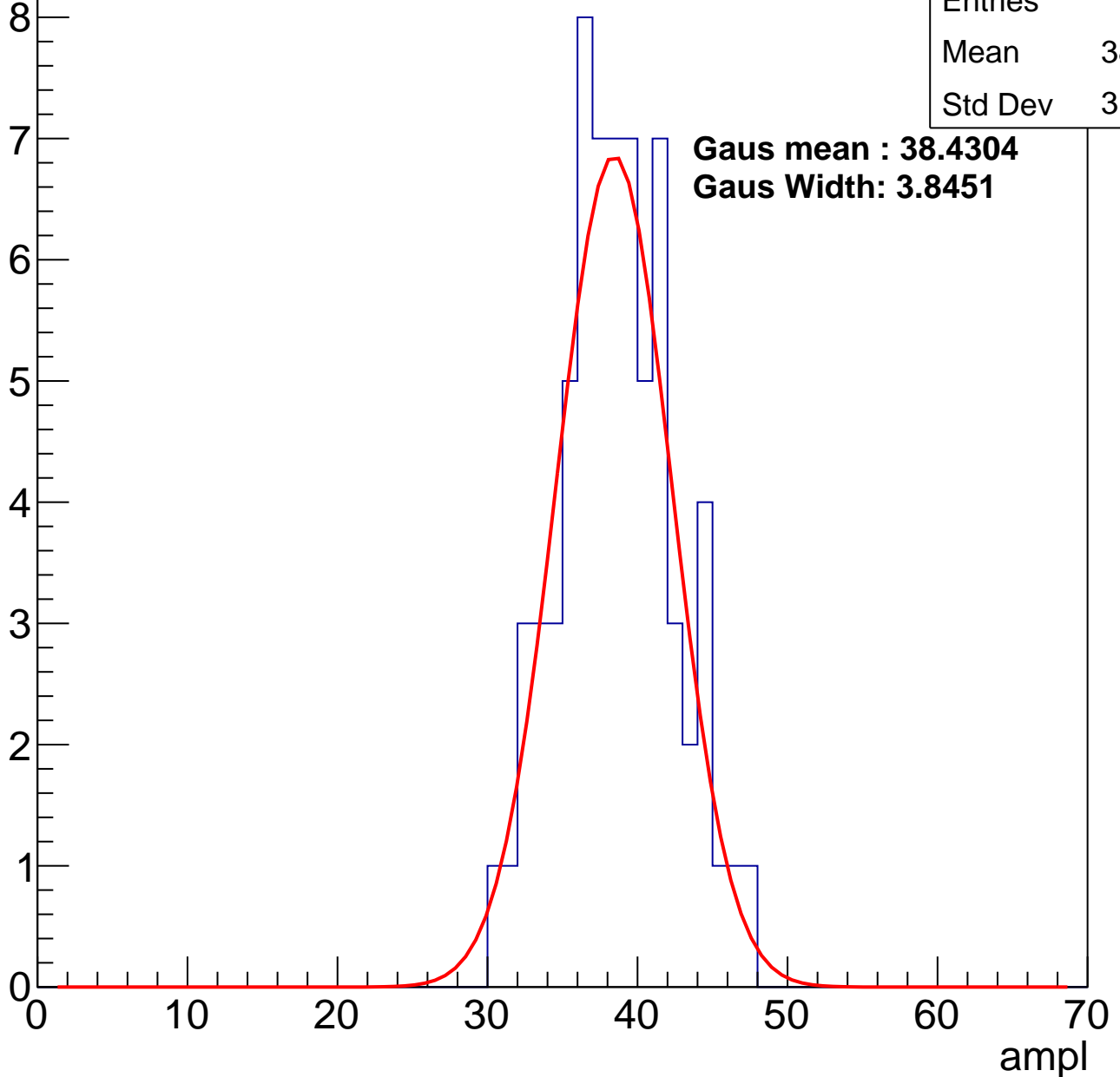
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.14
Std Dev	3.716

Gaus mean : 38.4304

Gaus Width: 3.8451



B1L101S, U23-ch39, adc2

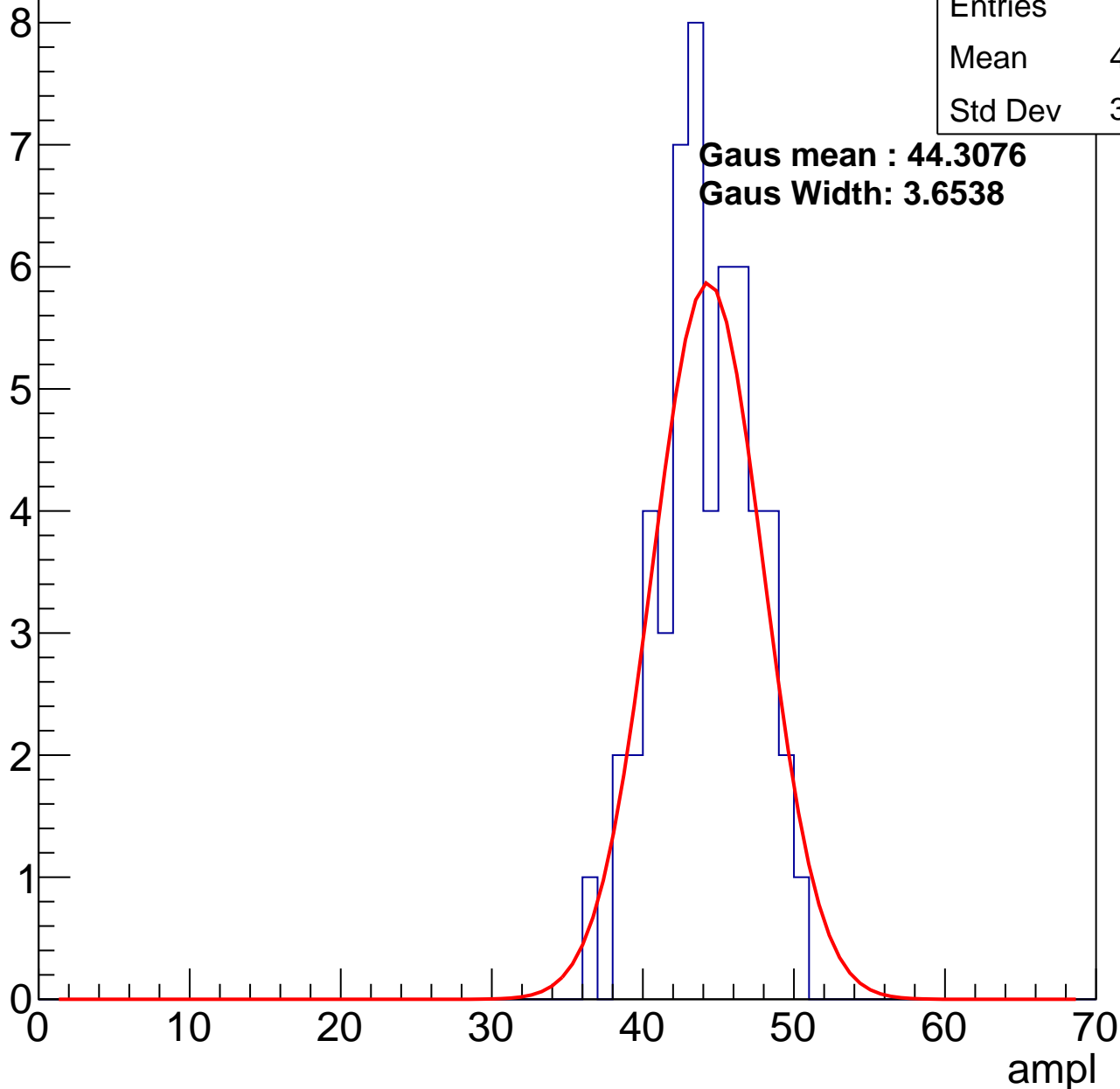
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.72
Std Dev	3.118

Gaus mean : 44.3076

Gaus Width: 3.6538

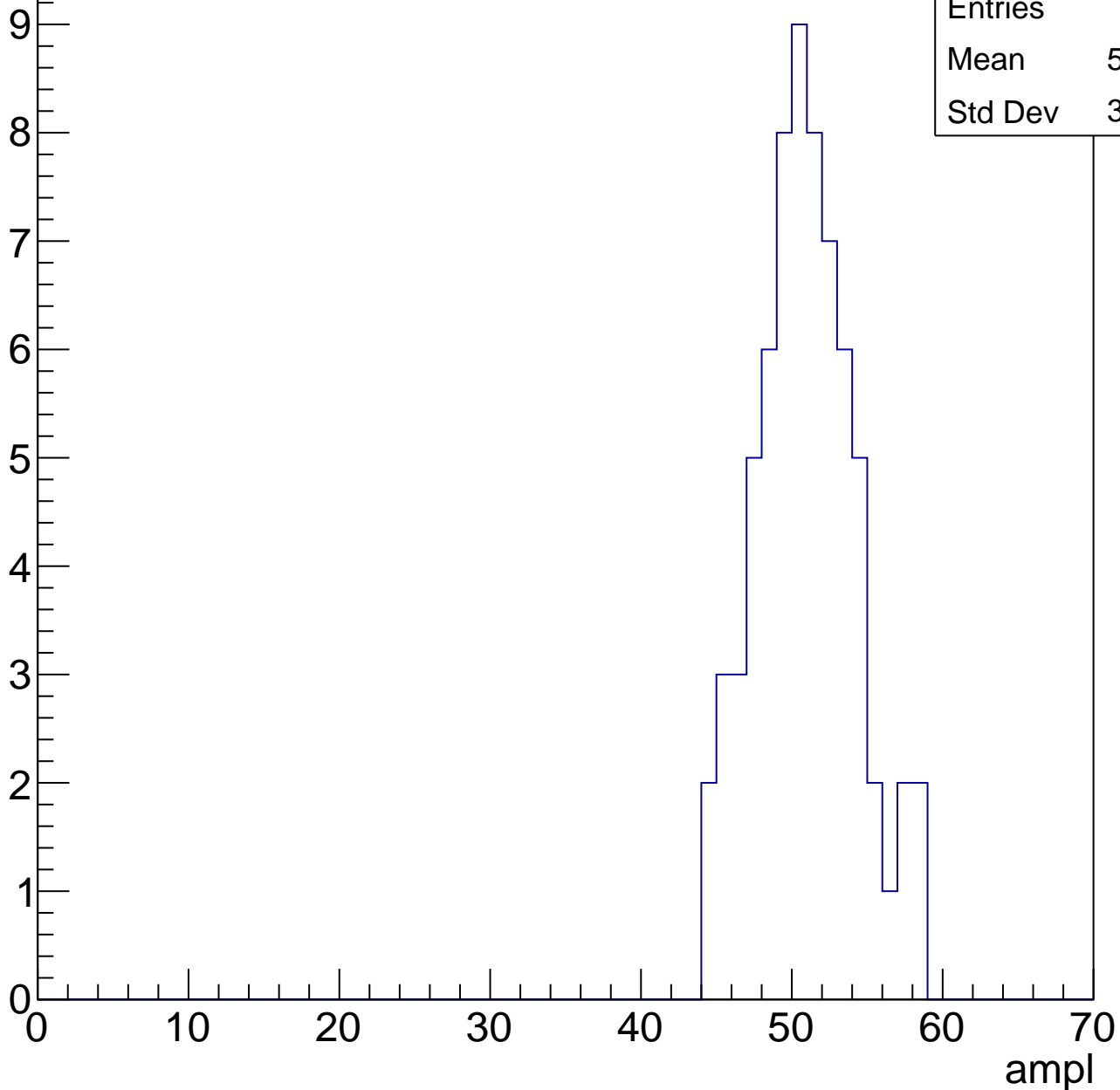


B1L101S, U23-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.46
Std Dev	3.273



B1L101S, U23-ch39, adc4

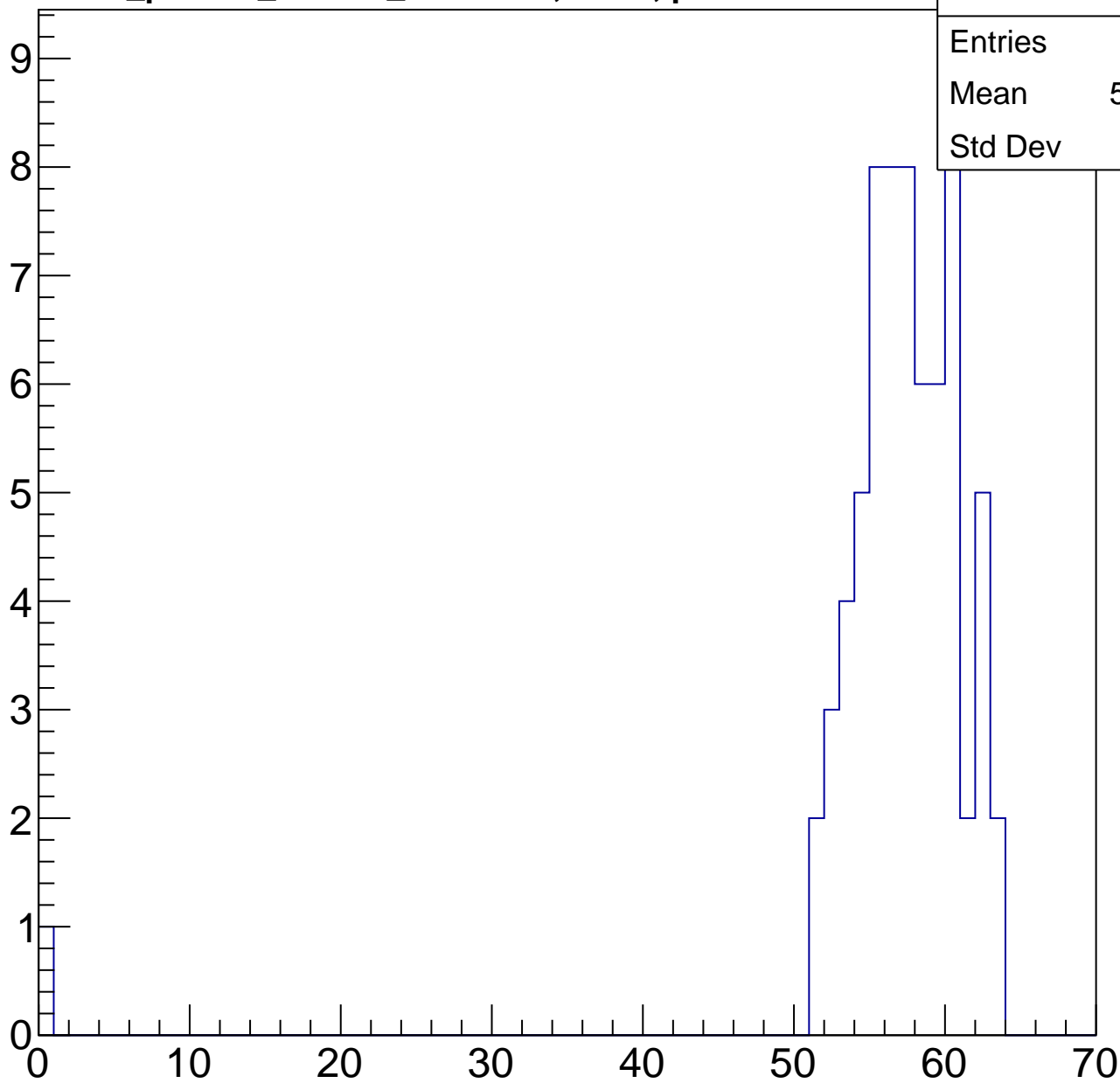
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	69
Mean	56.29
Std Dev	7.47

ampl

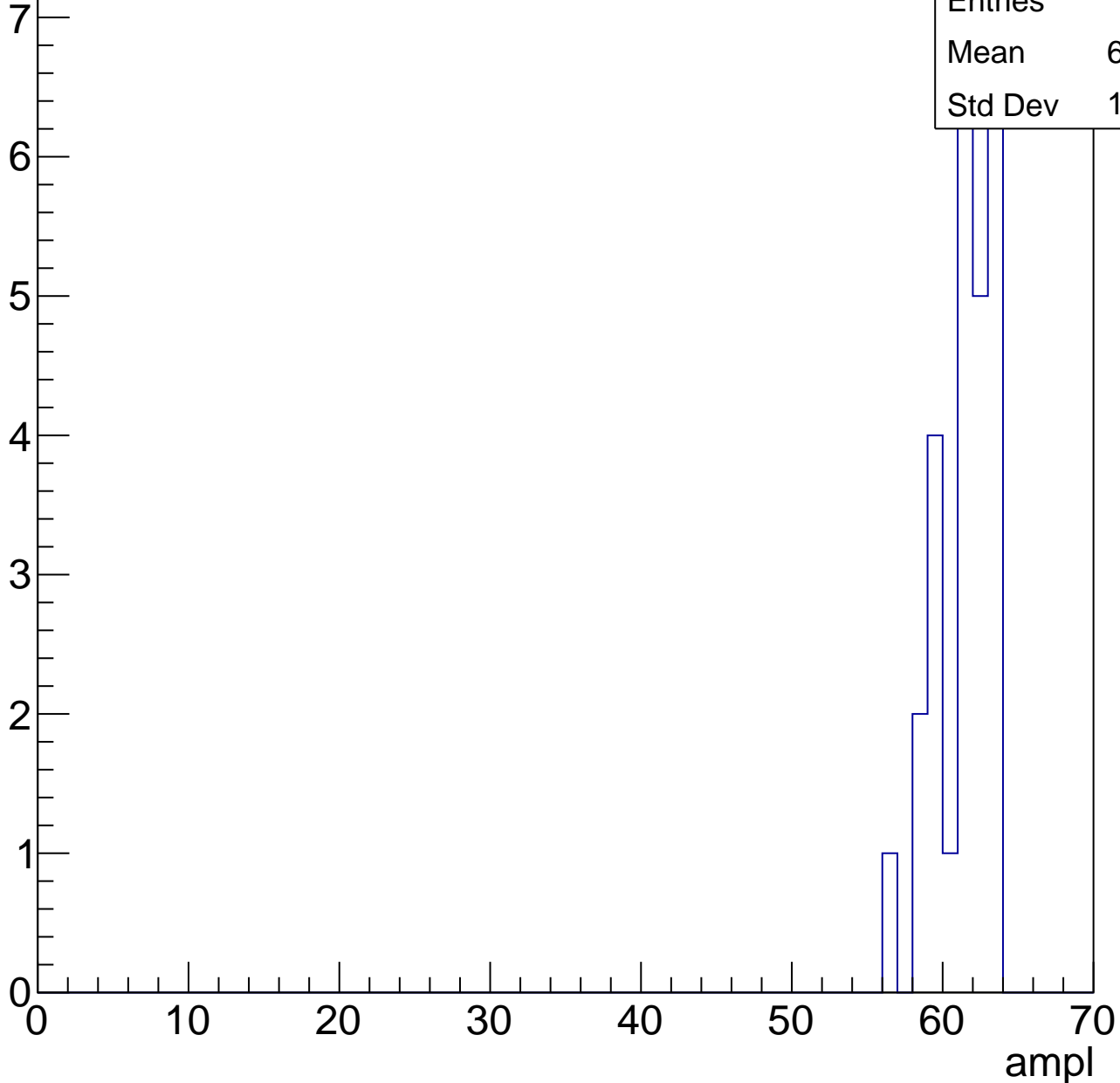


B1L101S, U23-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	60.96
Std Dev	1.856



B1L101S, U23-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch40, adc0

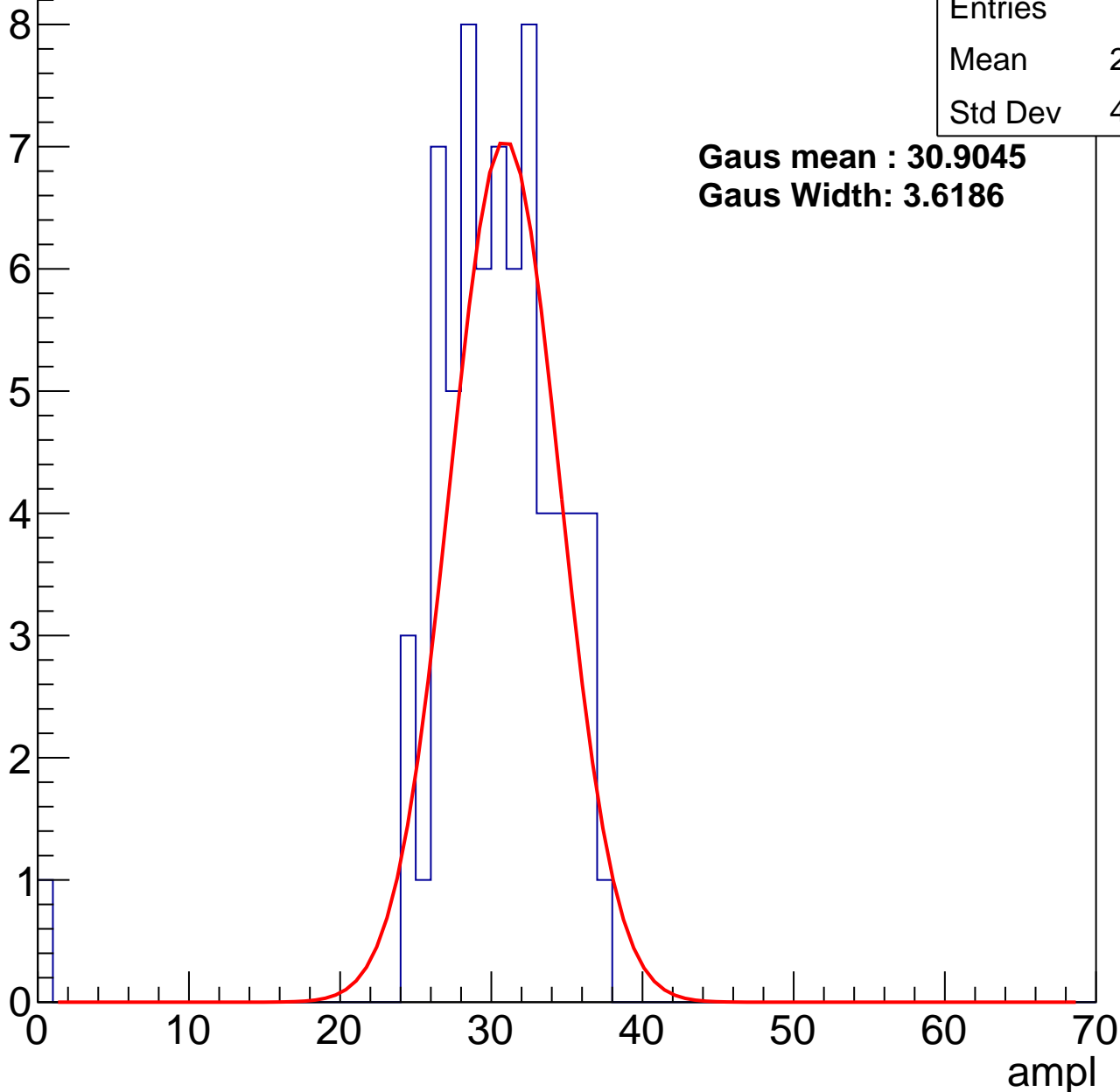
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.75
Std Dev	4.897

Gaus mean : 30.9045

Gaus Width: 3.6186



B1L101S, U23-ch40, adc1

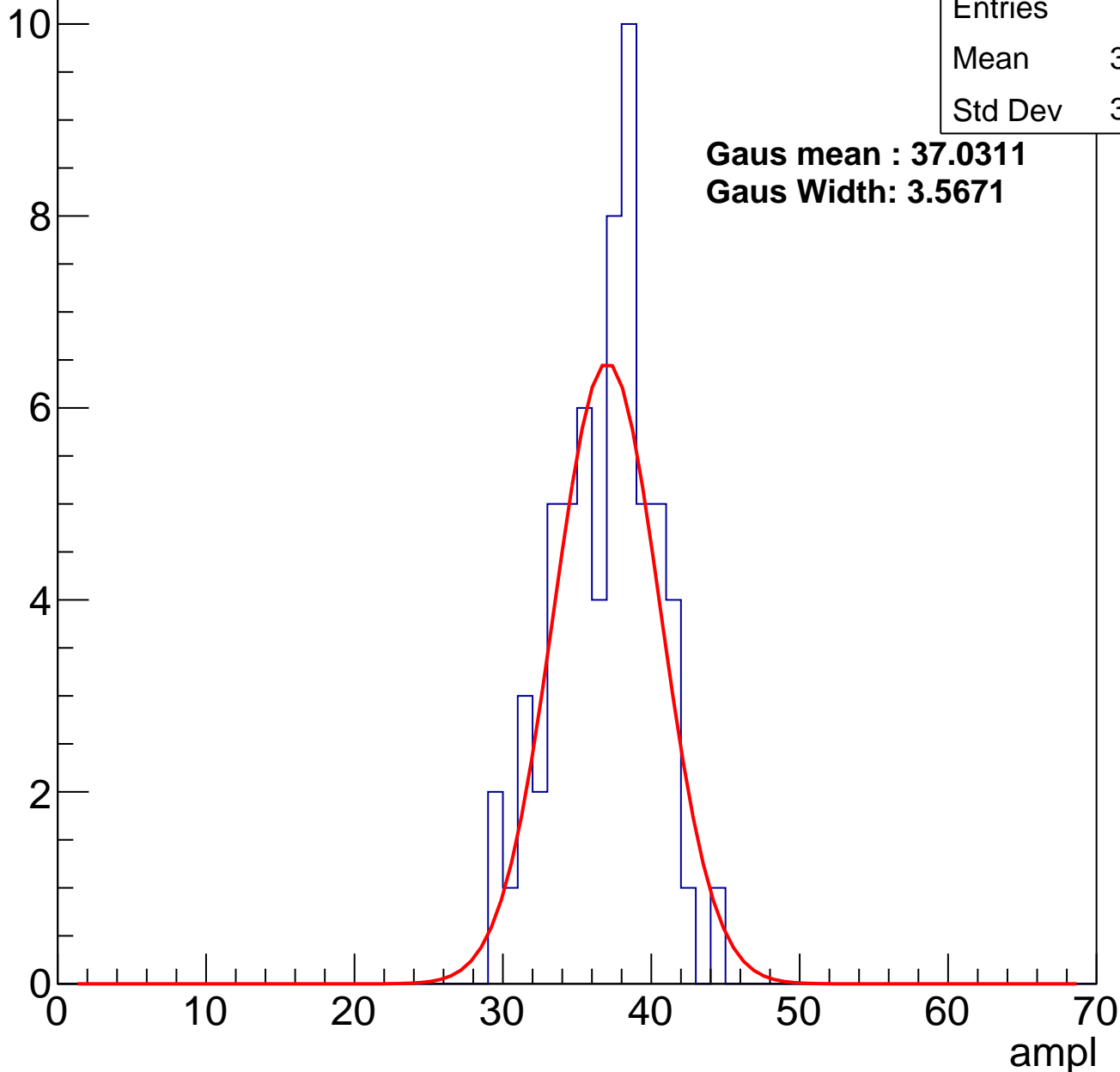
calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	36.37
Std Dev	3.303

Gaus mean : 37.0311

Gaus Width: 3.5671

Entry



B1L101S, U23-ch40, adc2

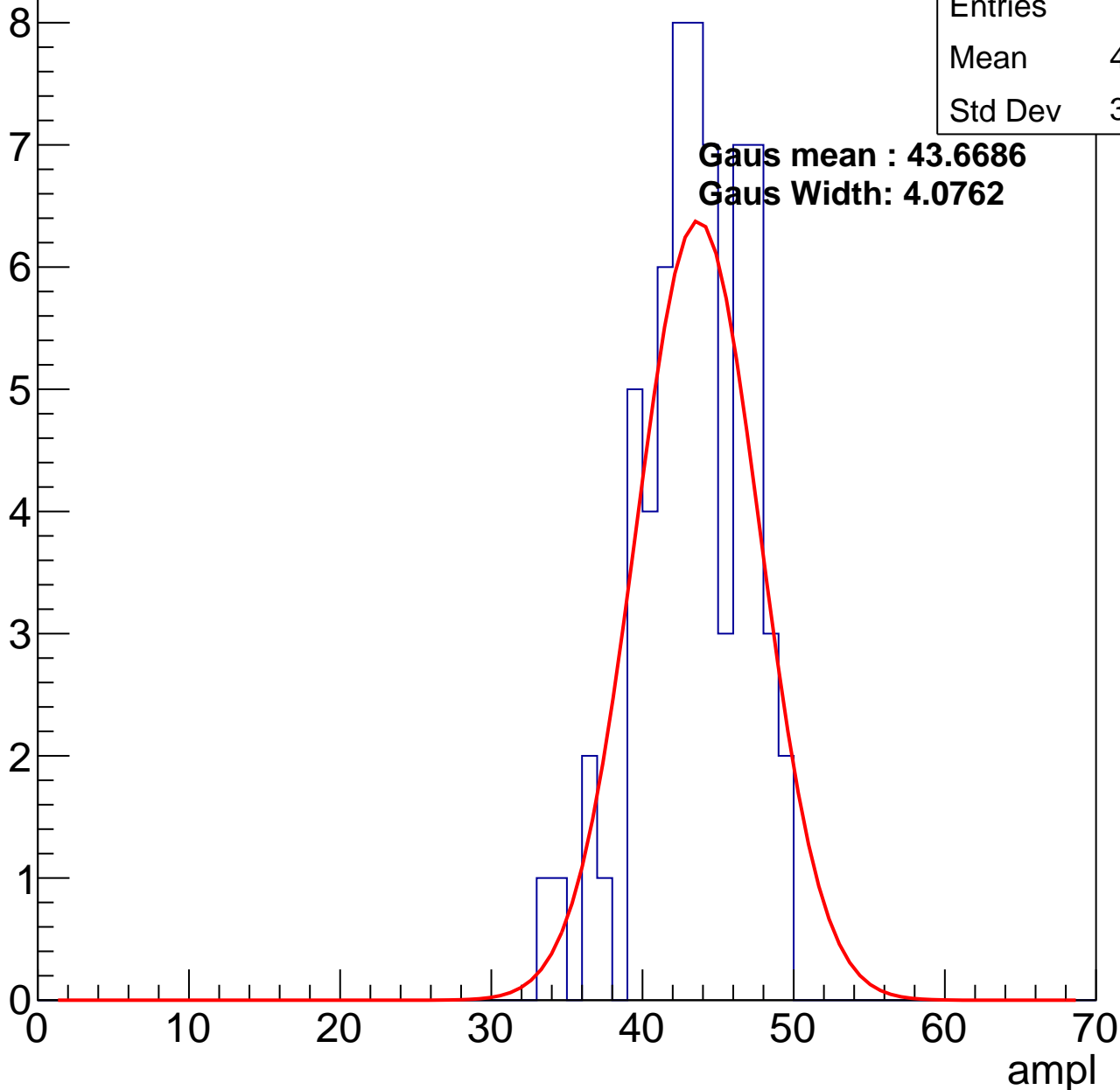
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.97
Std Dev	3.517

Gaus mean : 43.6686

Gaus Width: 4.0762

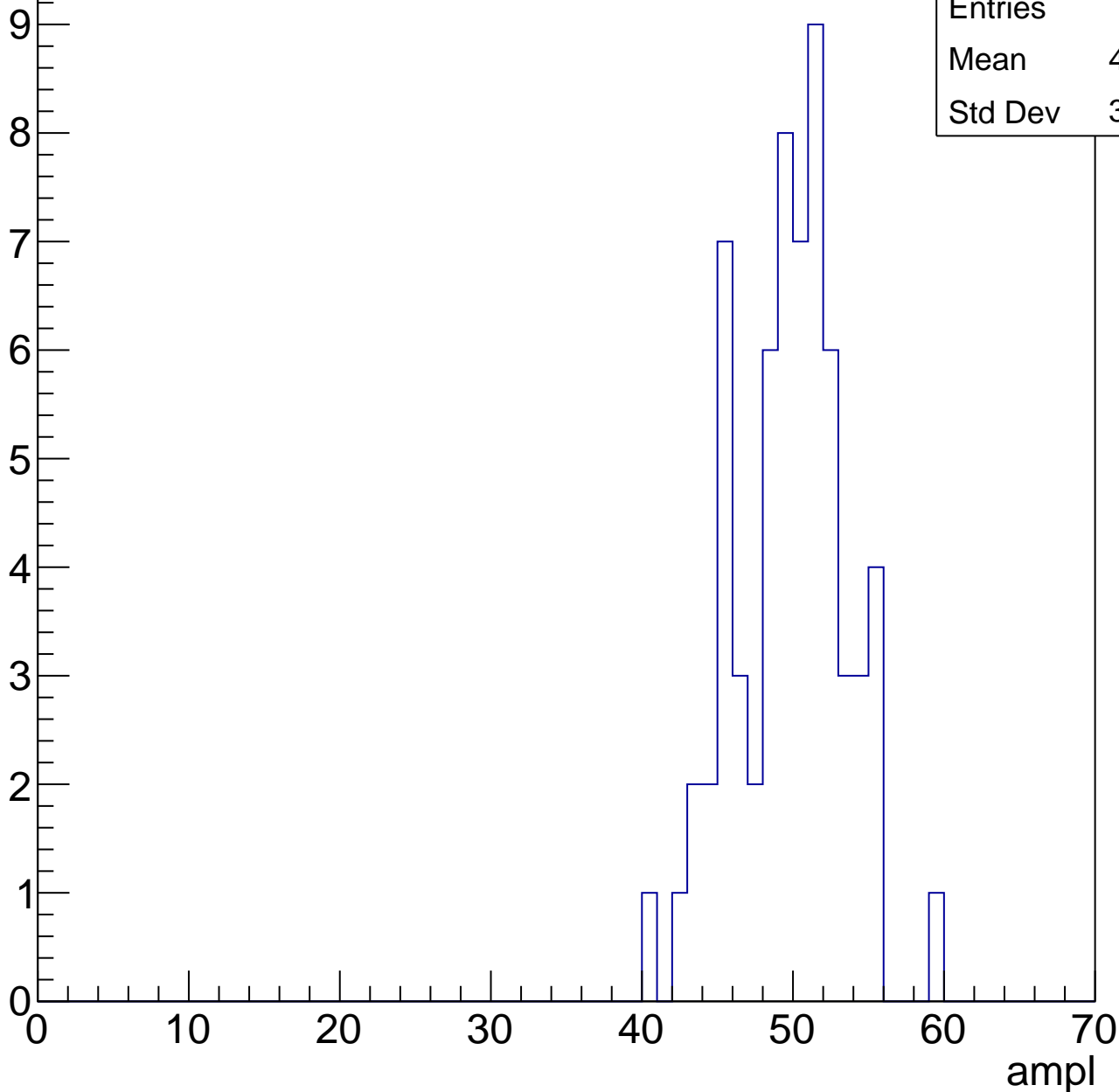


B1L101S, U23-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.29
Std Dev	3.645

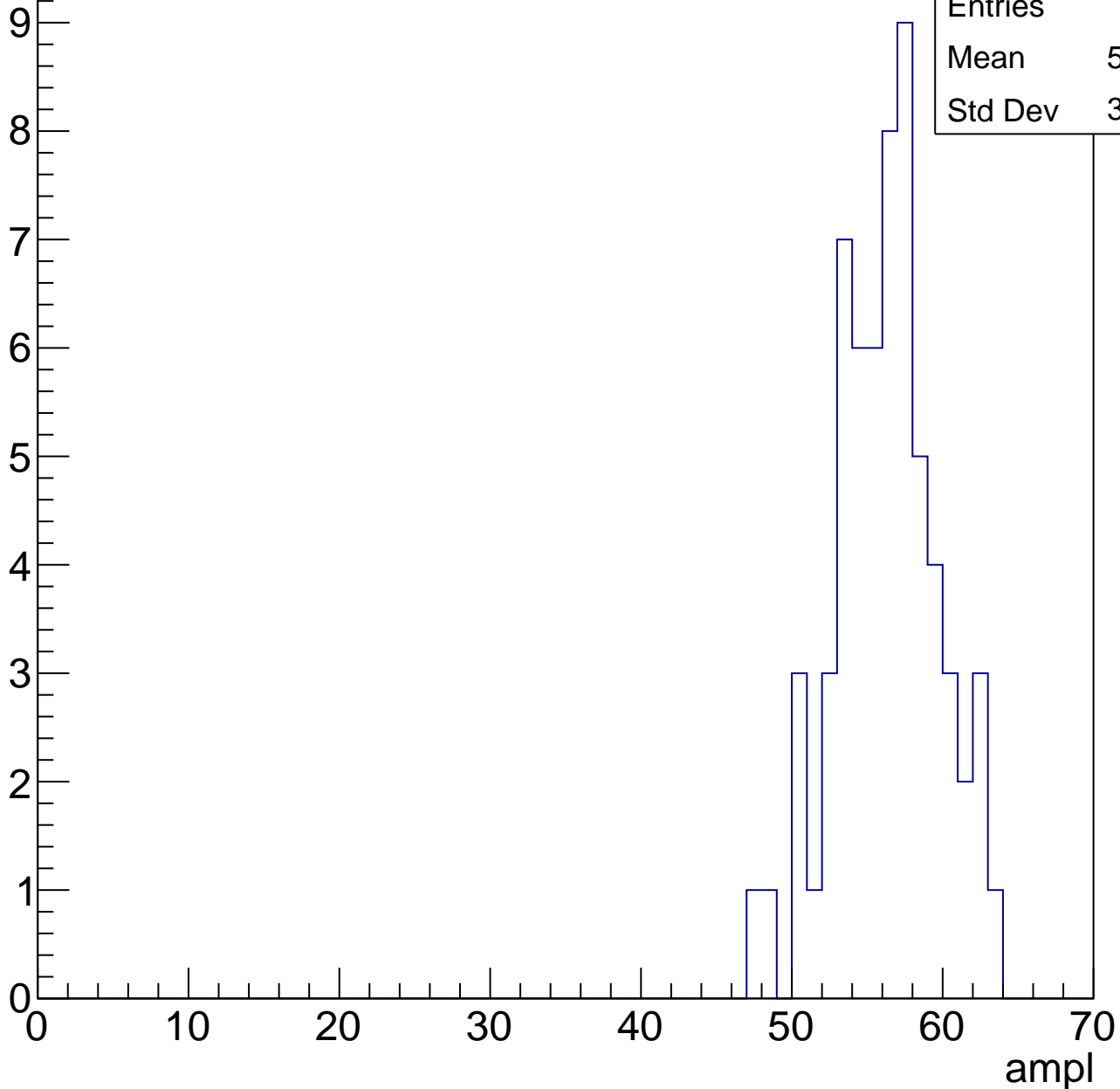


B1L101S, U23-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

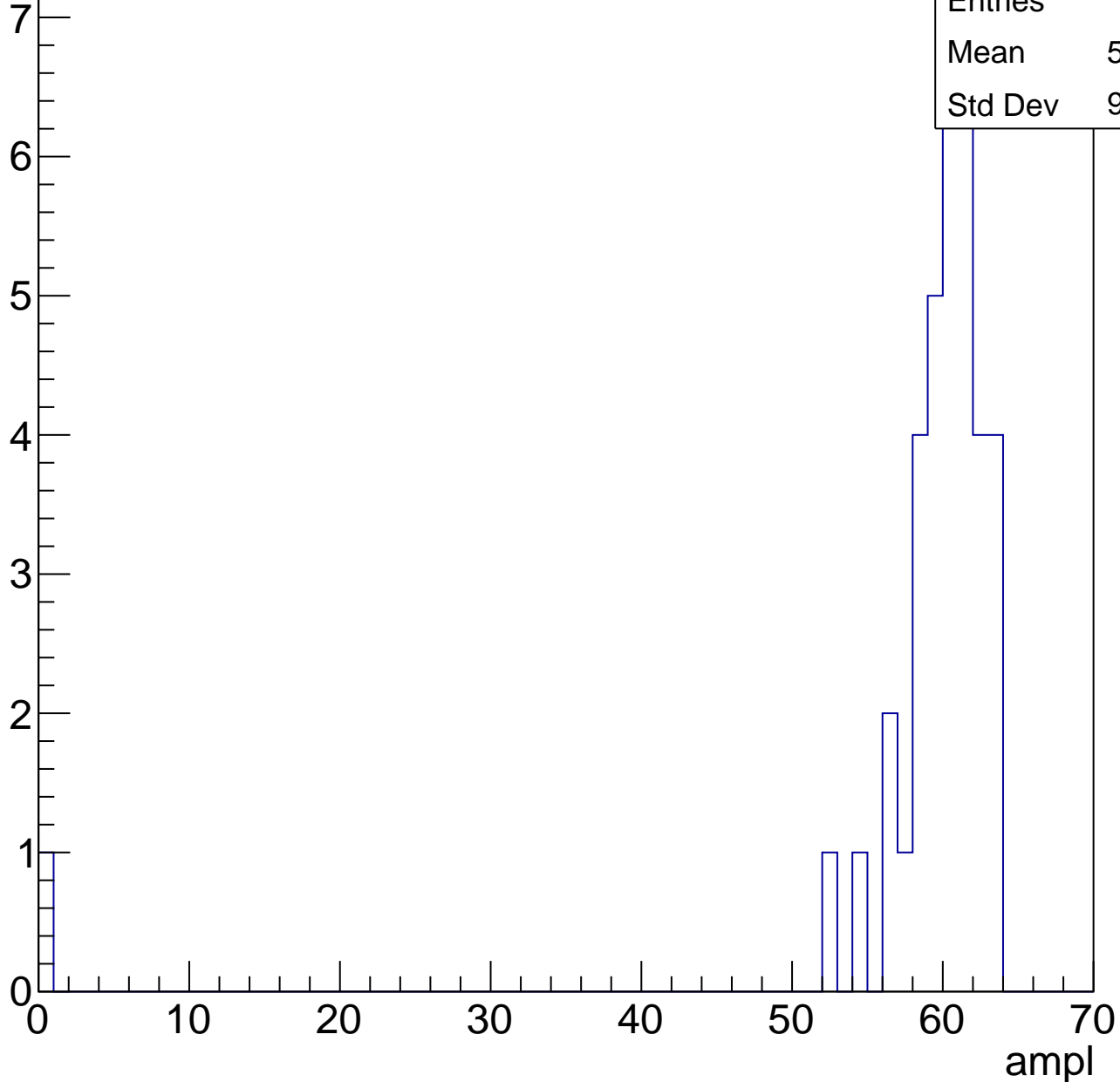
Entries	63
Mean	55.79
Std Dev	3.419



B1L101S, U23-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

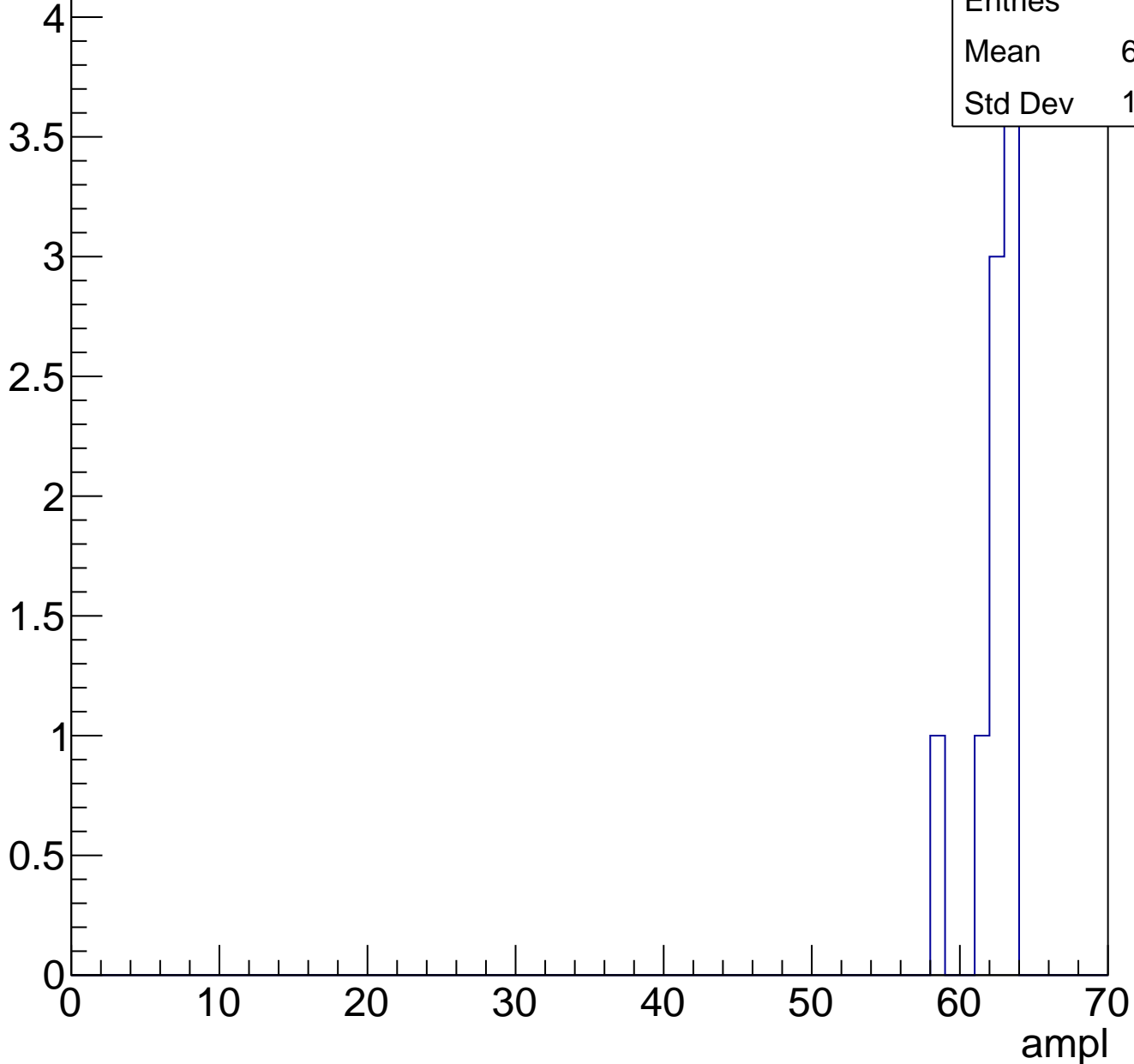
Entry



B1L101S, U23-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

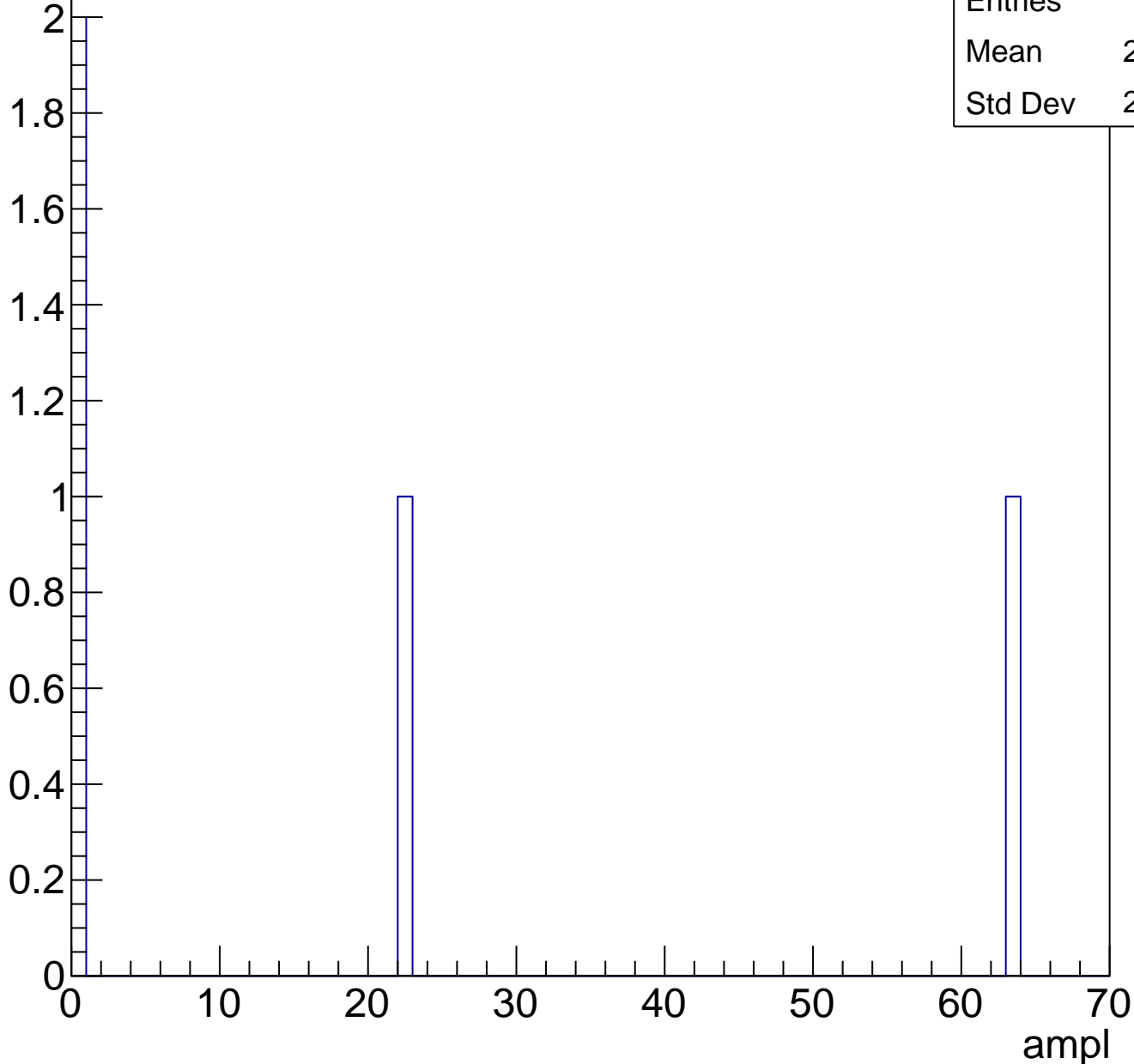
Entry



B1L101S, U23-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B1L101S, U23-ch41, adc0

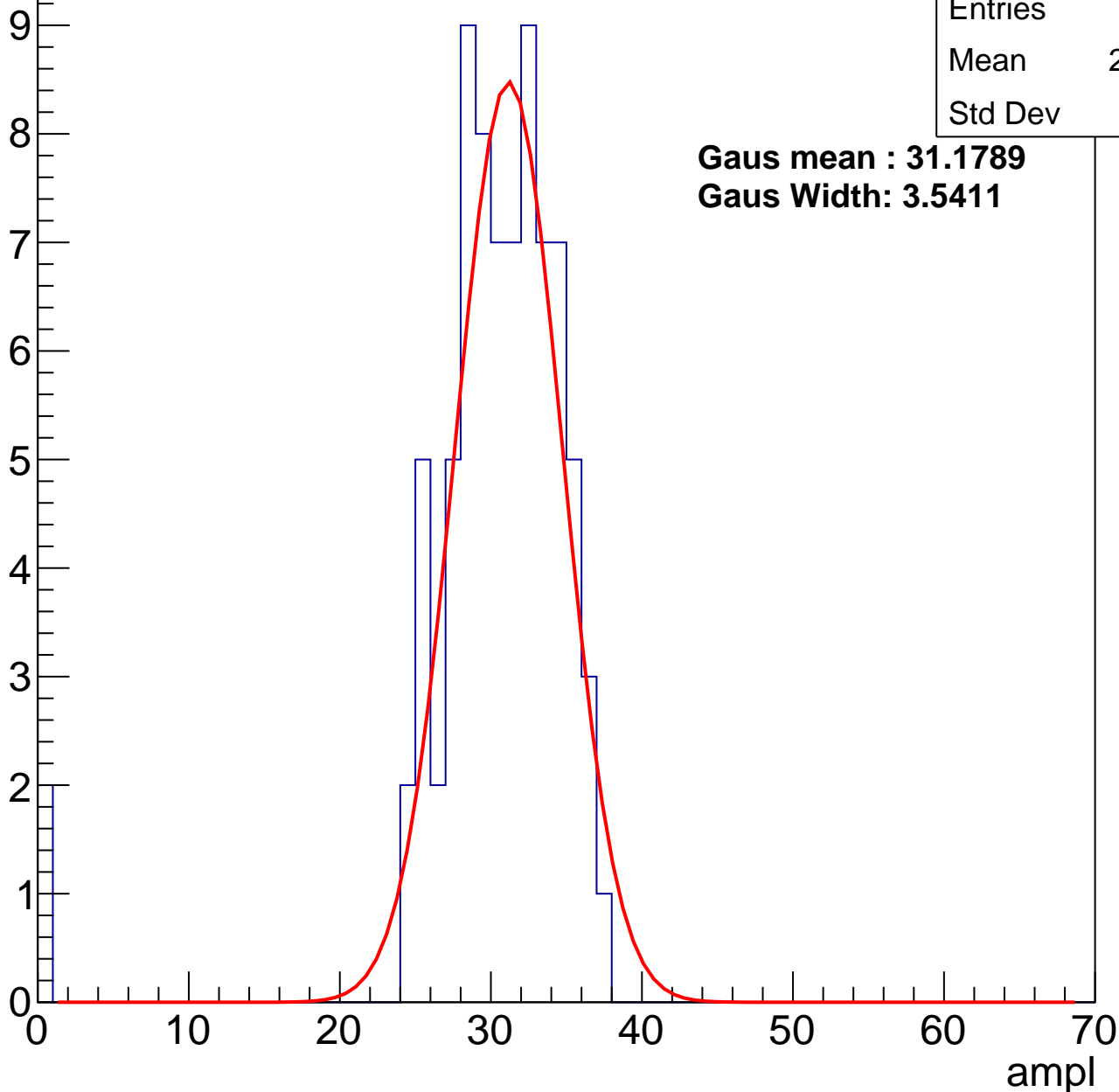
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.72
Std Dev	5.75

Gaus mean : 31.1789

Gaus Width: 3.5411



B1L101S, U23-ch41, adc1

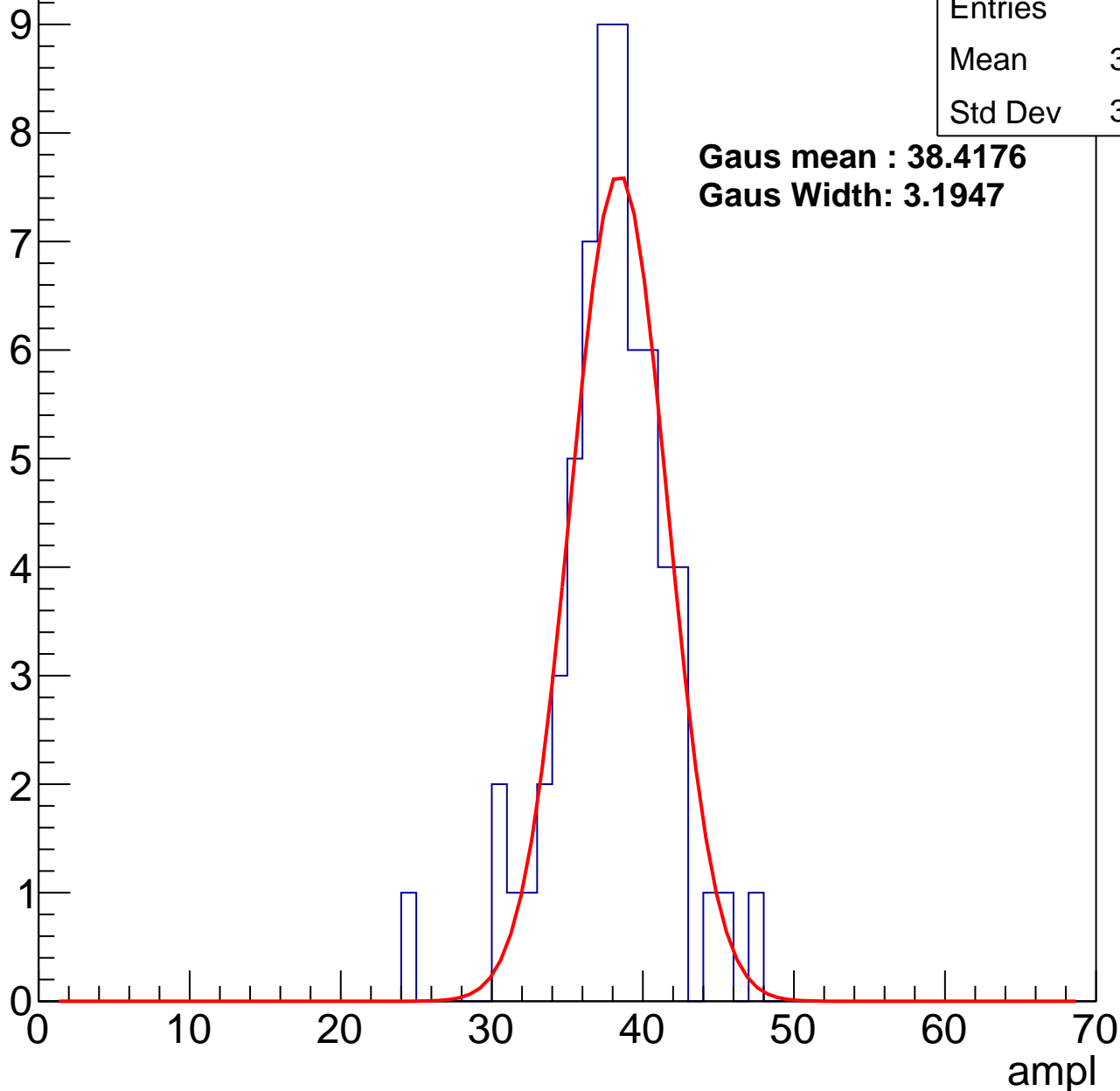
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.44
Std Dev	3.707

Gaus mean : 38.4176

Gaus Width: 3.1947



B1L101S, U23-ch41, adc2

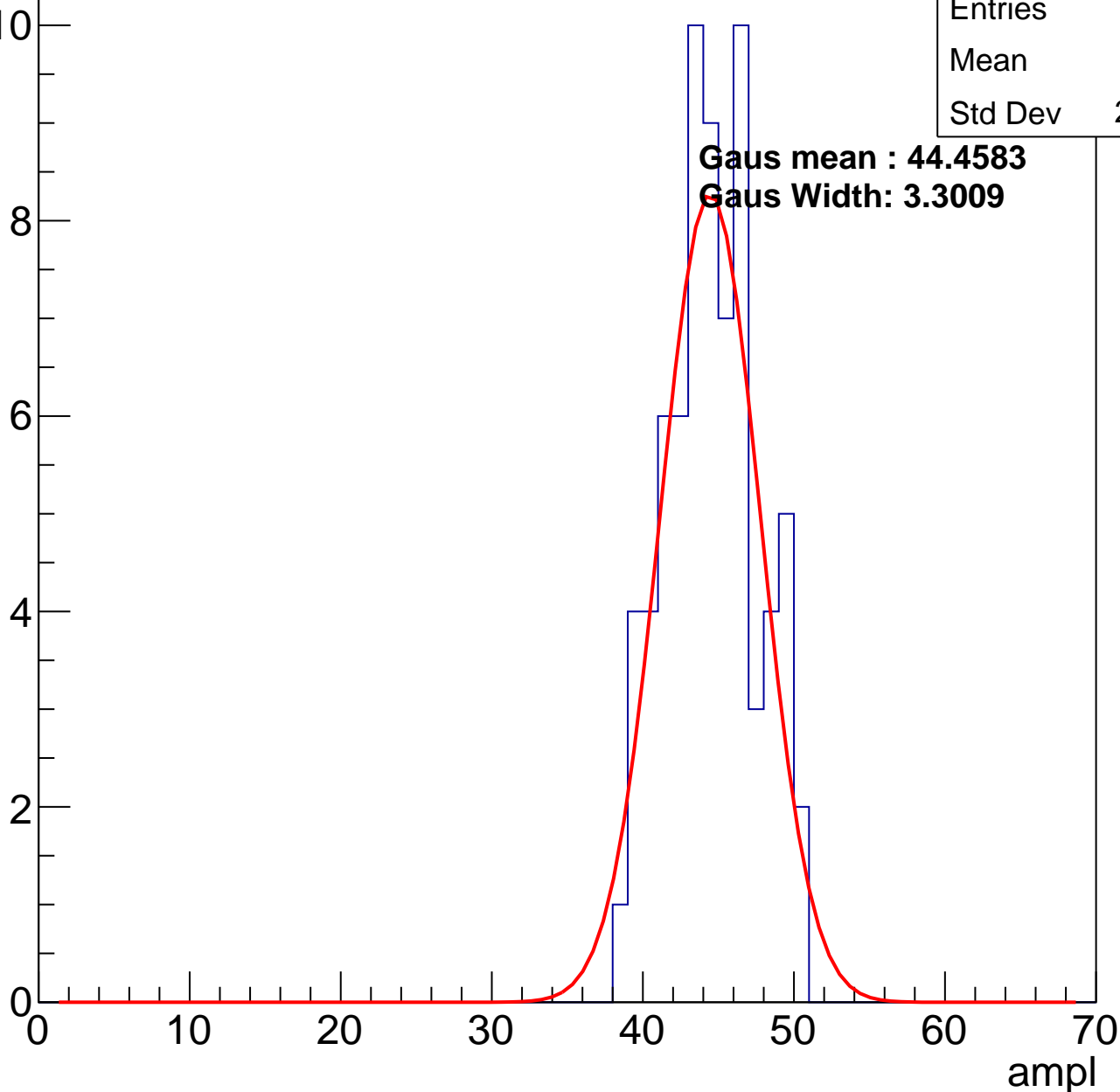
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.1
Std Dev	2.961

Gaus mean : 44.4583

Gaus Width: 3.3009

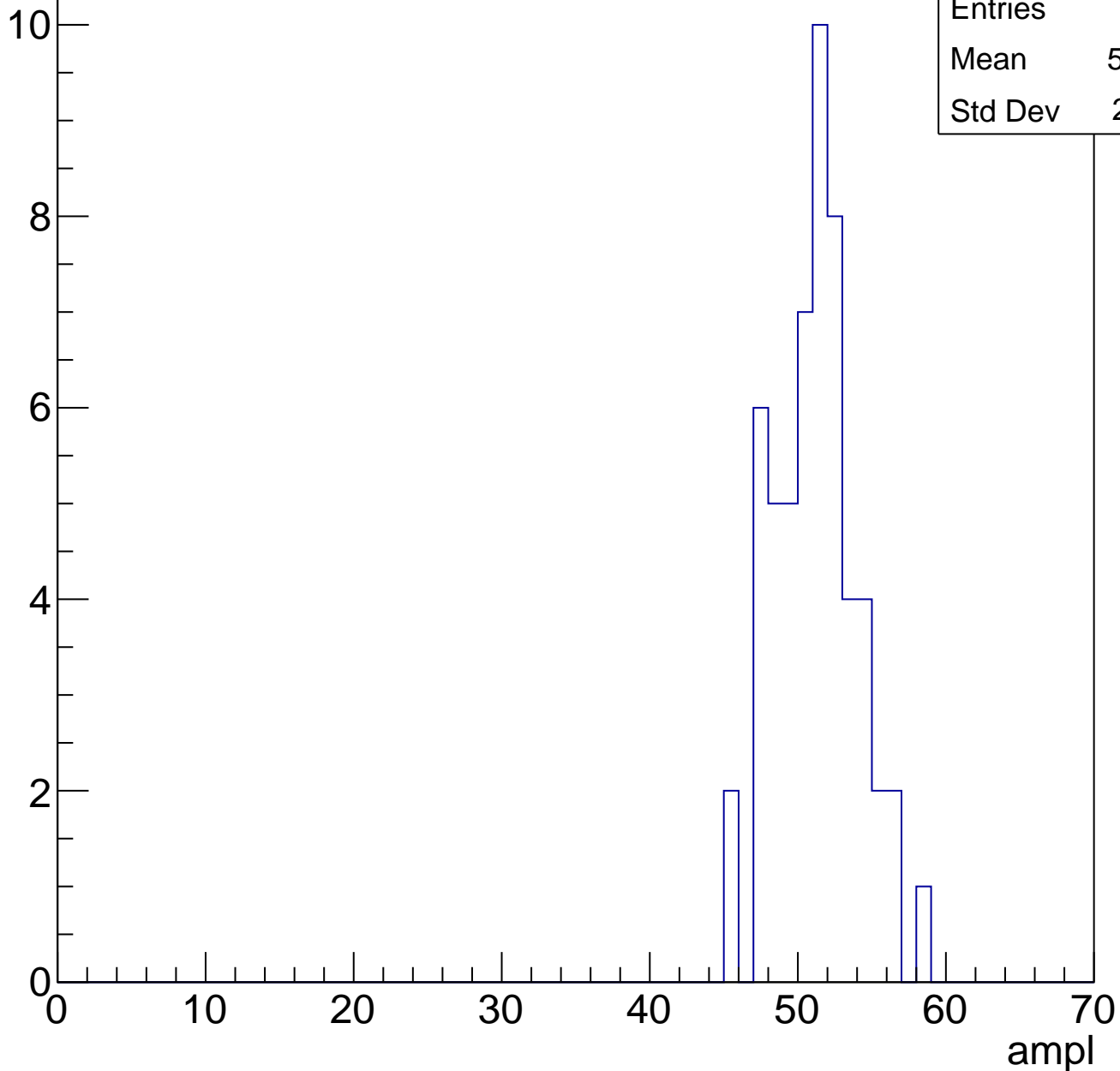


B1L101S, U23-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	56
Mean	50.73
Std Dev	2.761

Entry

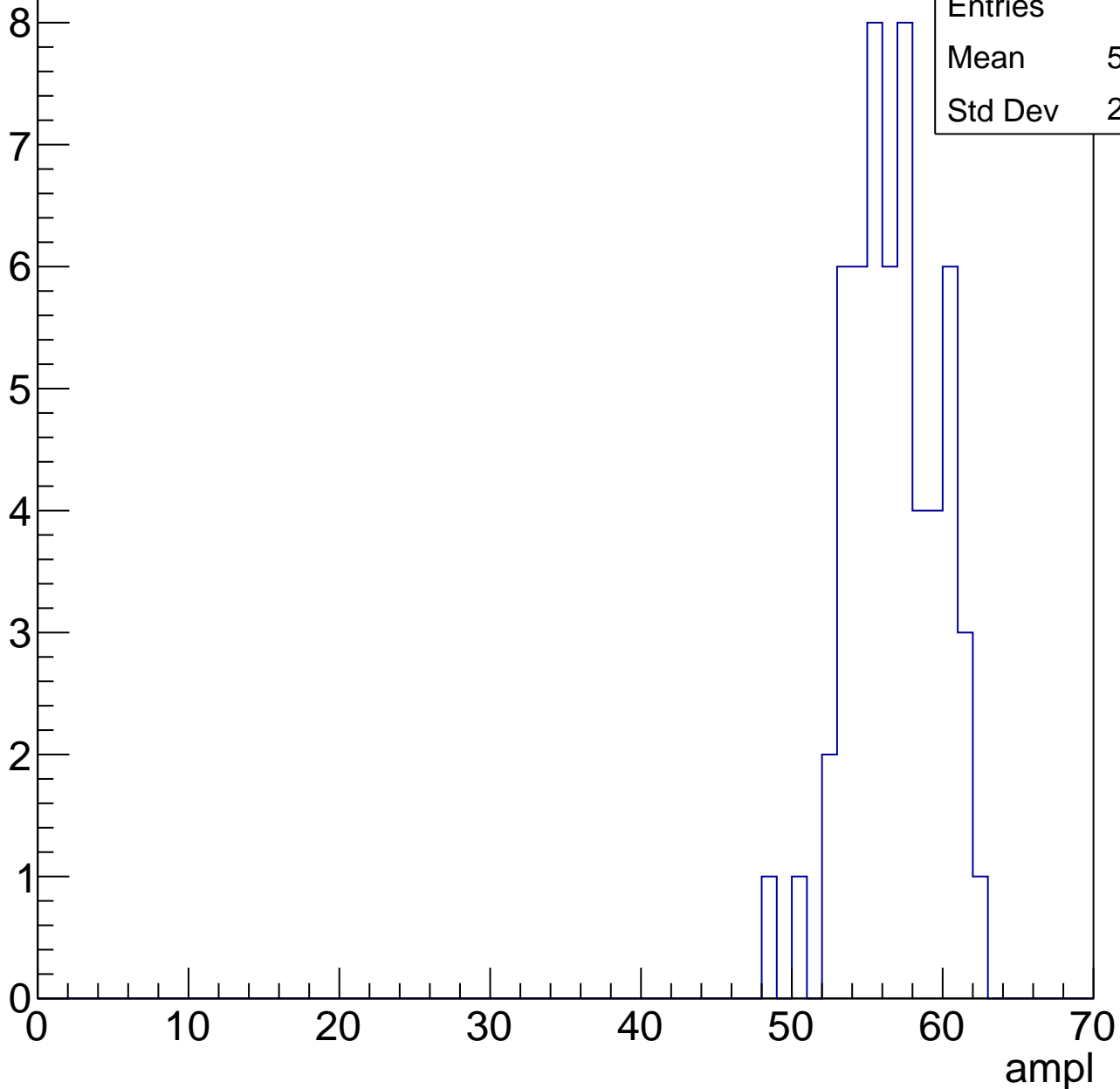


B1L101S, U23-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

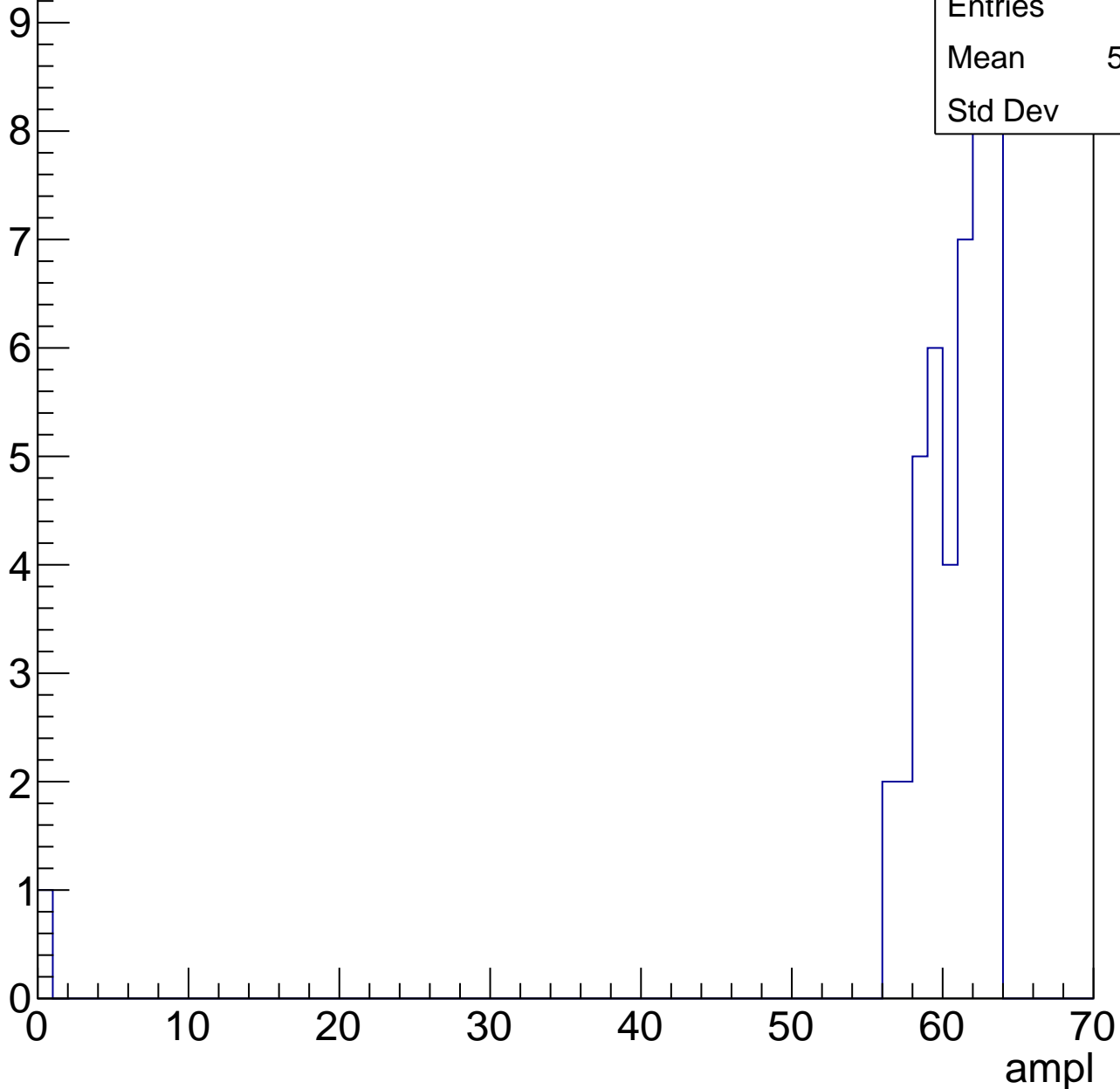
Entries	56
Mean	56.23
Std Dev	2.934



B1L101S, U23-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch42, adc0

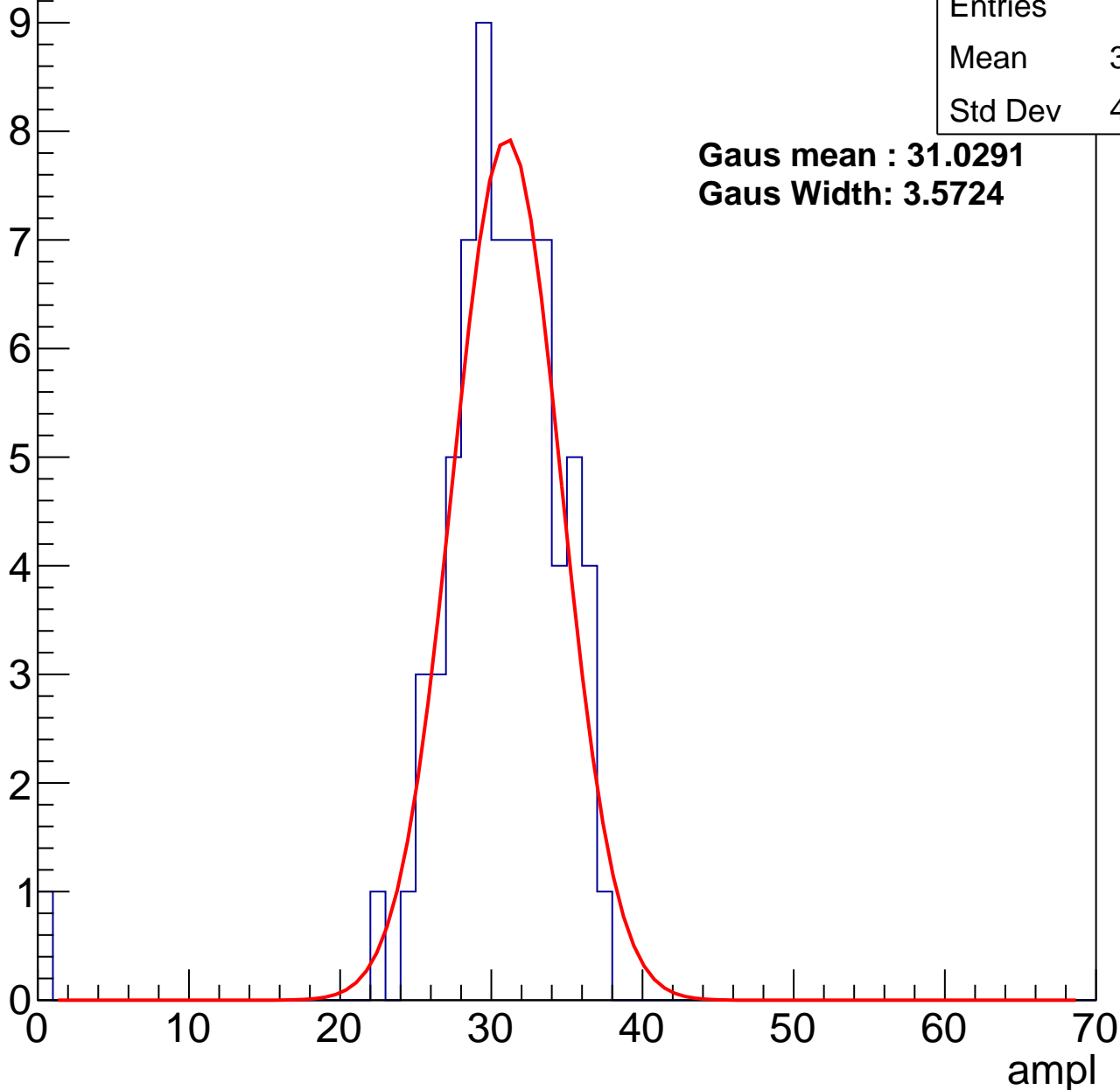
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.07
Std Dev	4.837

Gaus mean : 31.0291

Gaus Width: 3.5724



B1L101S, U23-ch42, adc1

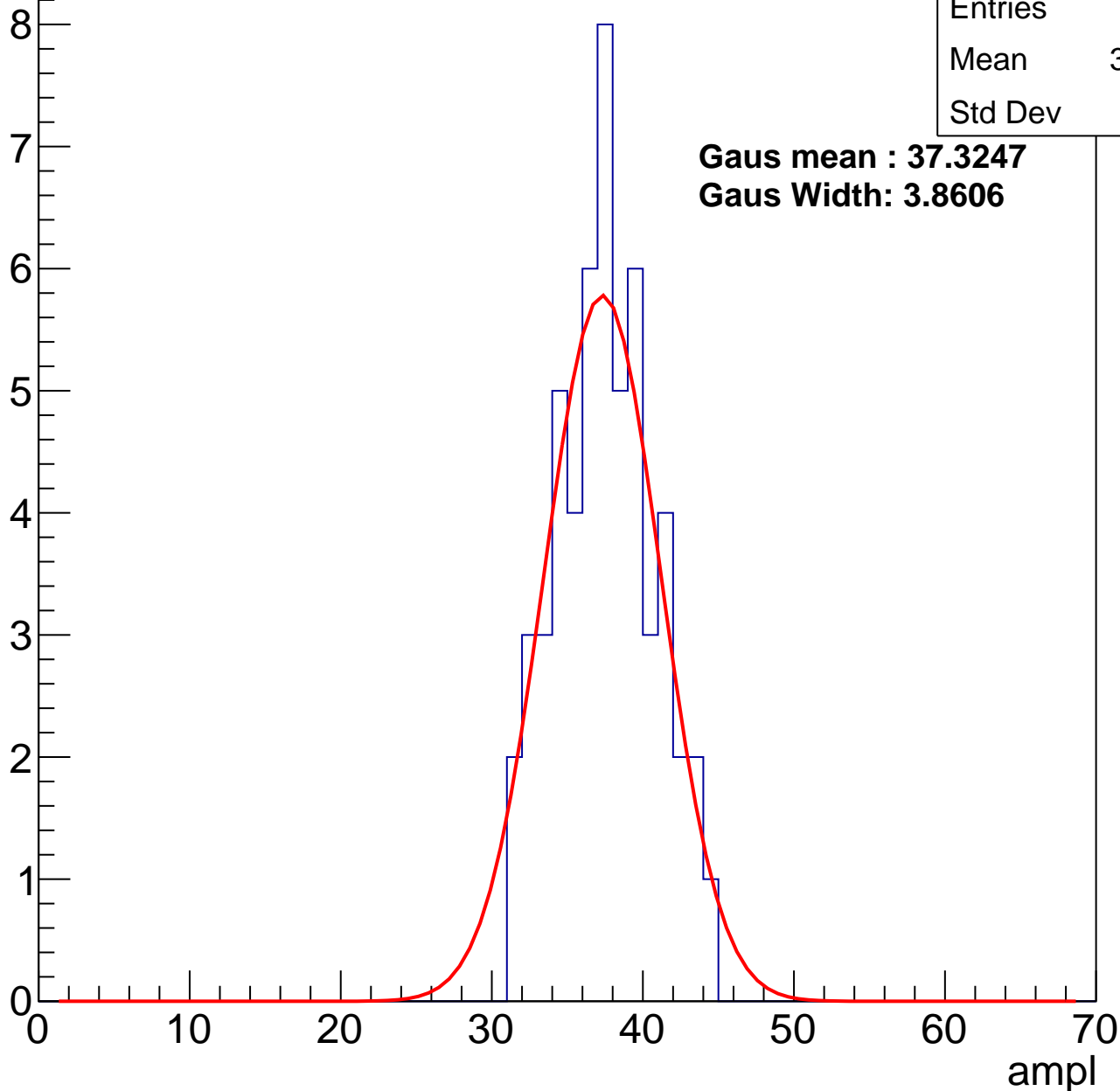
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	37.06
Std Dev	3.2

Gaus mean : 37.3247

Gaus Width: 3.8606



B1L101S, U23-ch42, adc2

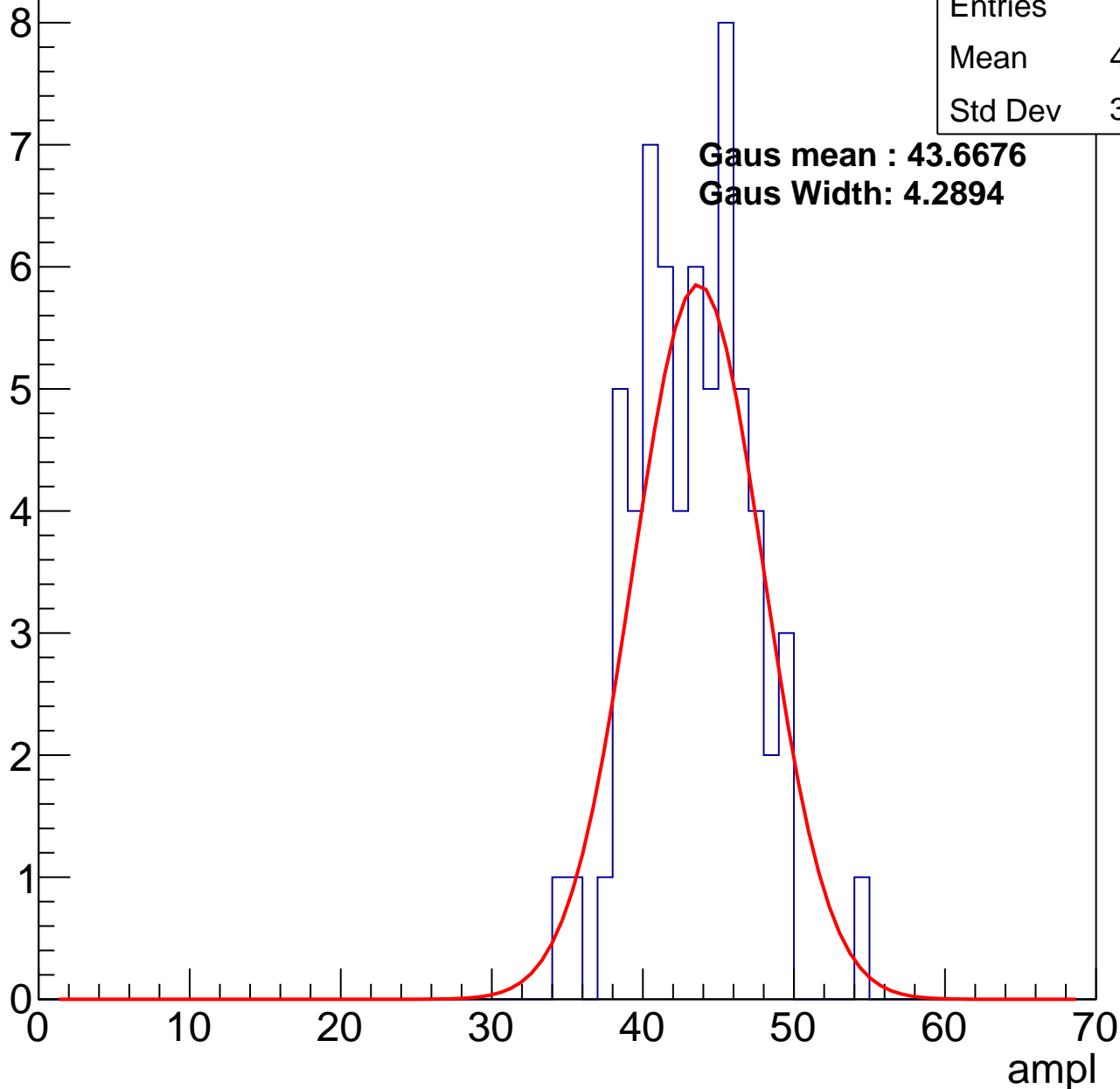
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	42.84
Std Dev	3.747

Gaus mean : 43.6676

Gaus Width: 4.2894

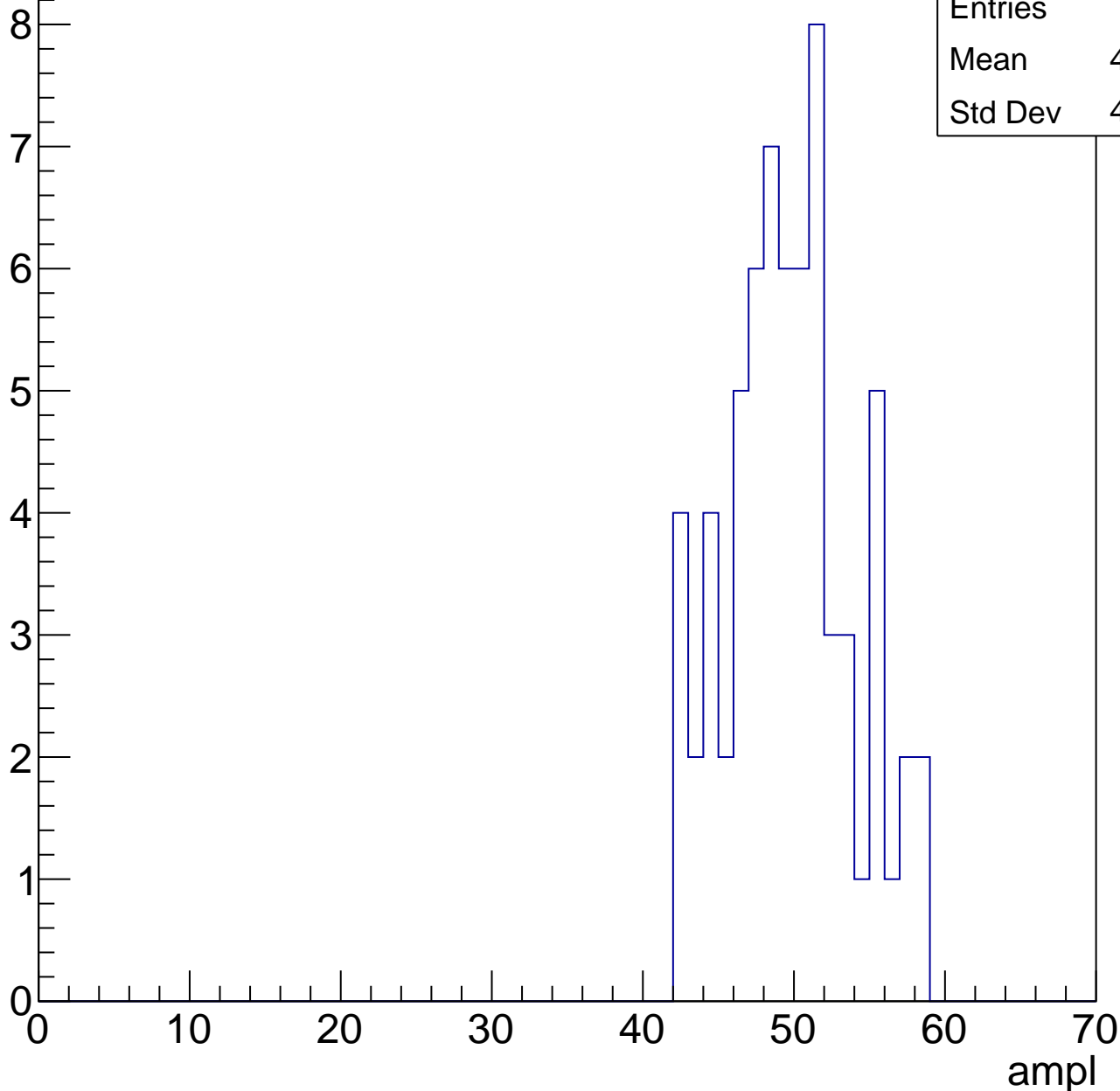


B1L101S, U23-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

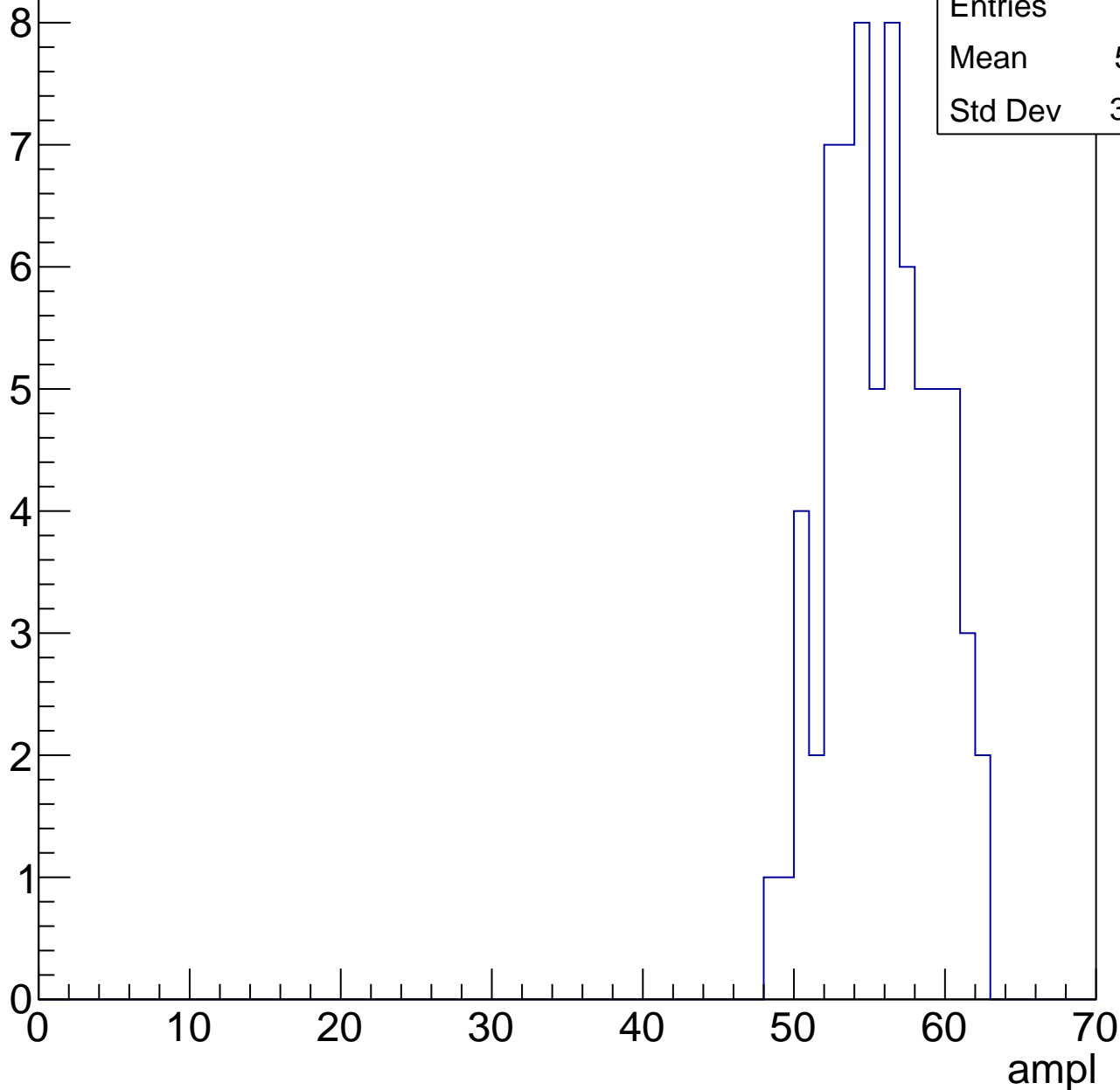
Entries	67
Mean	49.25
Std Dev	4.104



B1L101S, U23-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



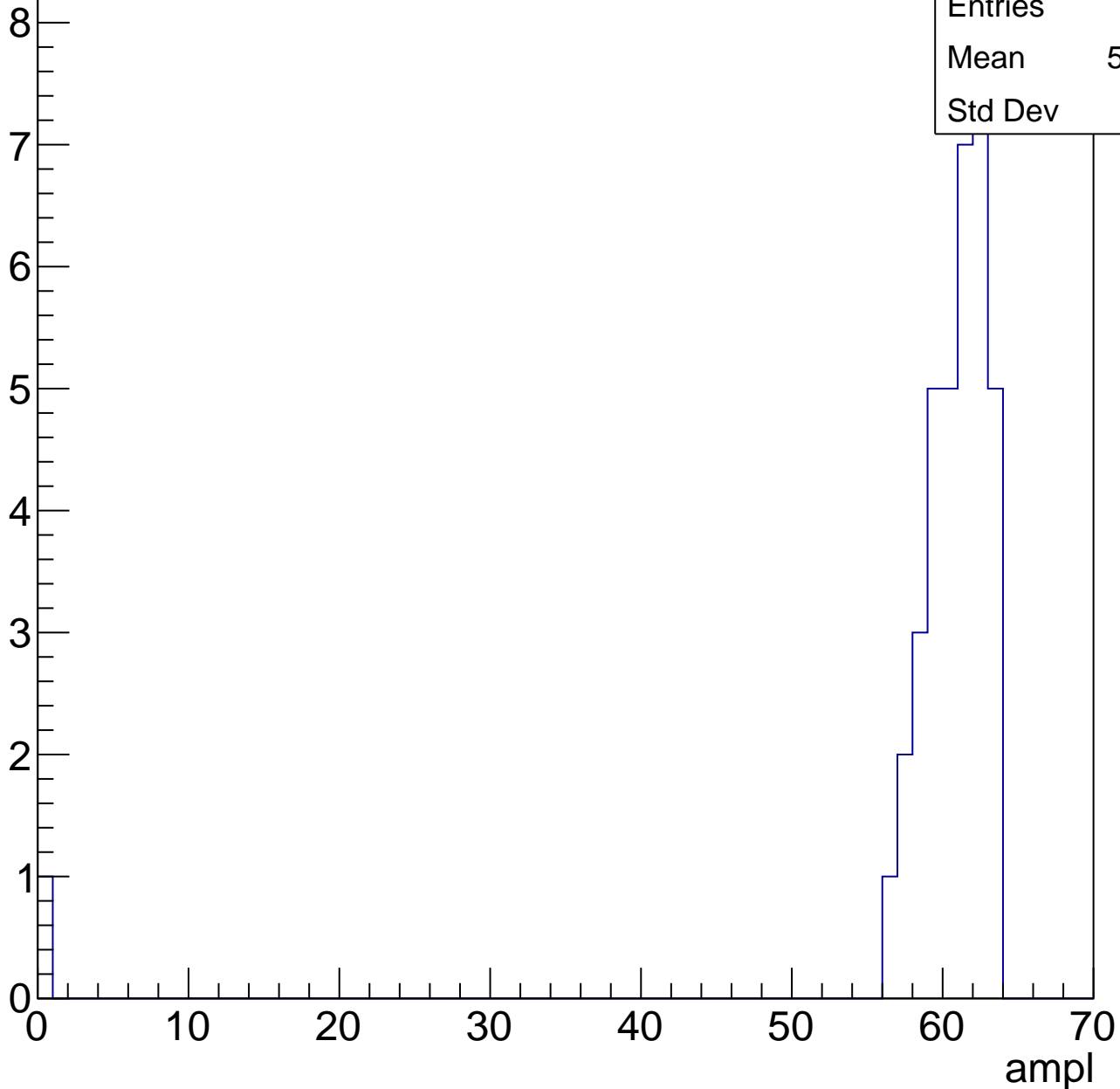
Entries	69
Mean	55.41
Std Dev	3.389

B1L101S, U23-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

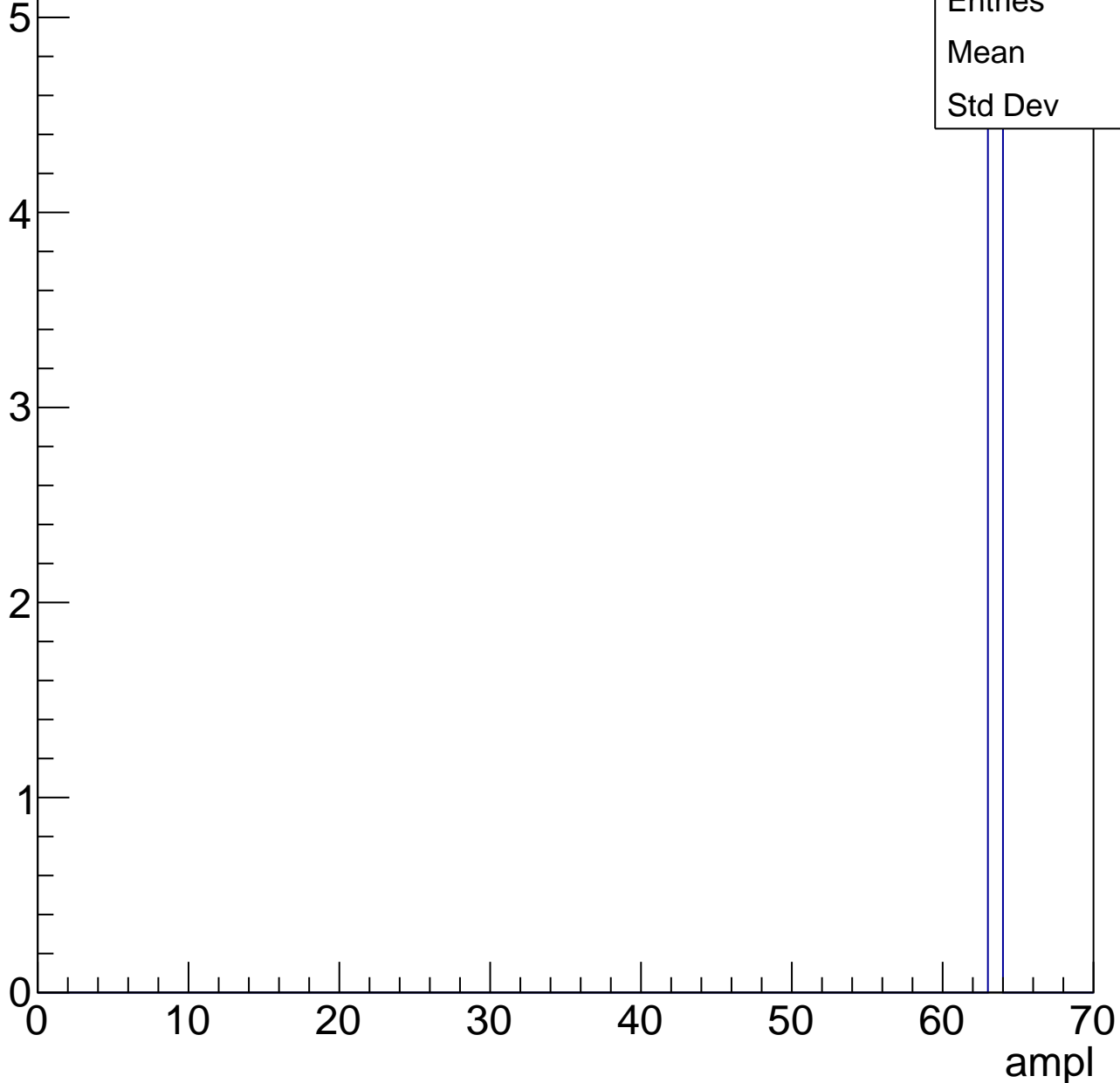
Entries	37
Mean	58.84
Std Dev	9.98



B1L101S, U23-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch43, adc0

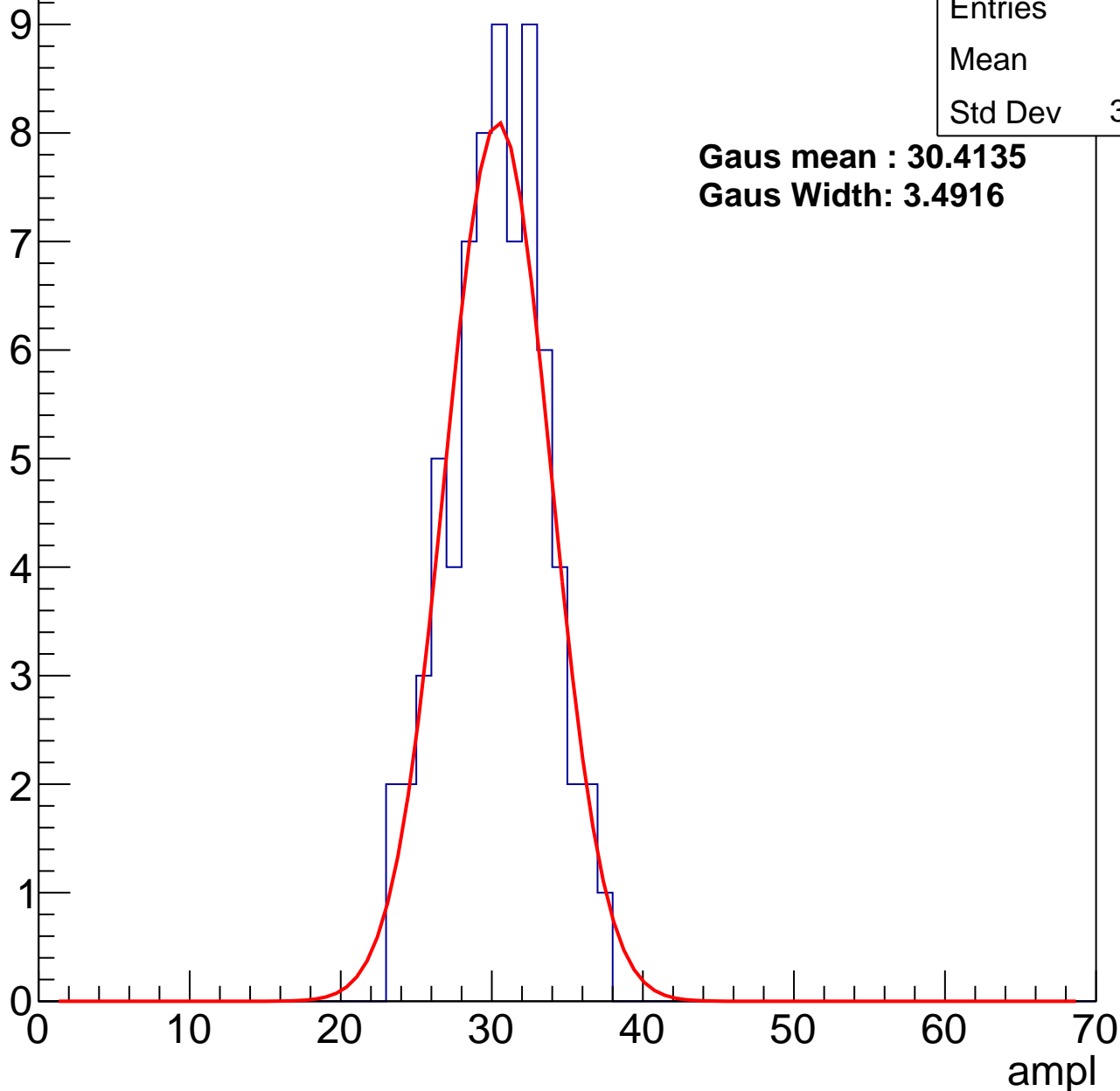
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.9
Std Dev	3.203

Gaus mean : 30.4135

Gaus Width: 3.4916



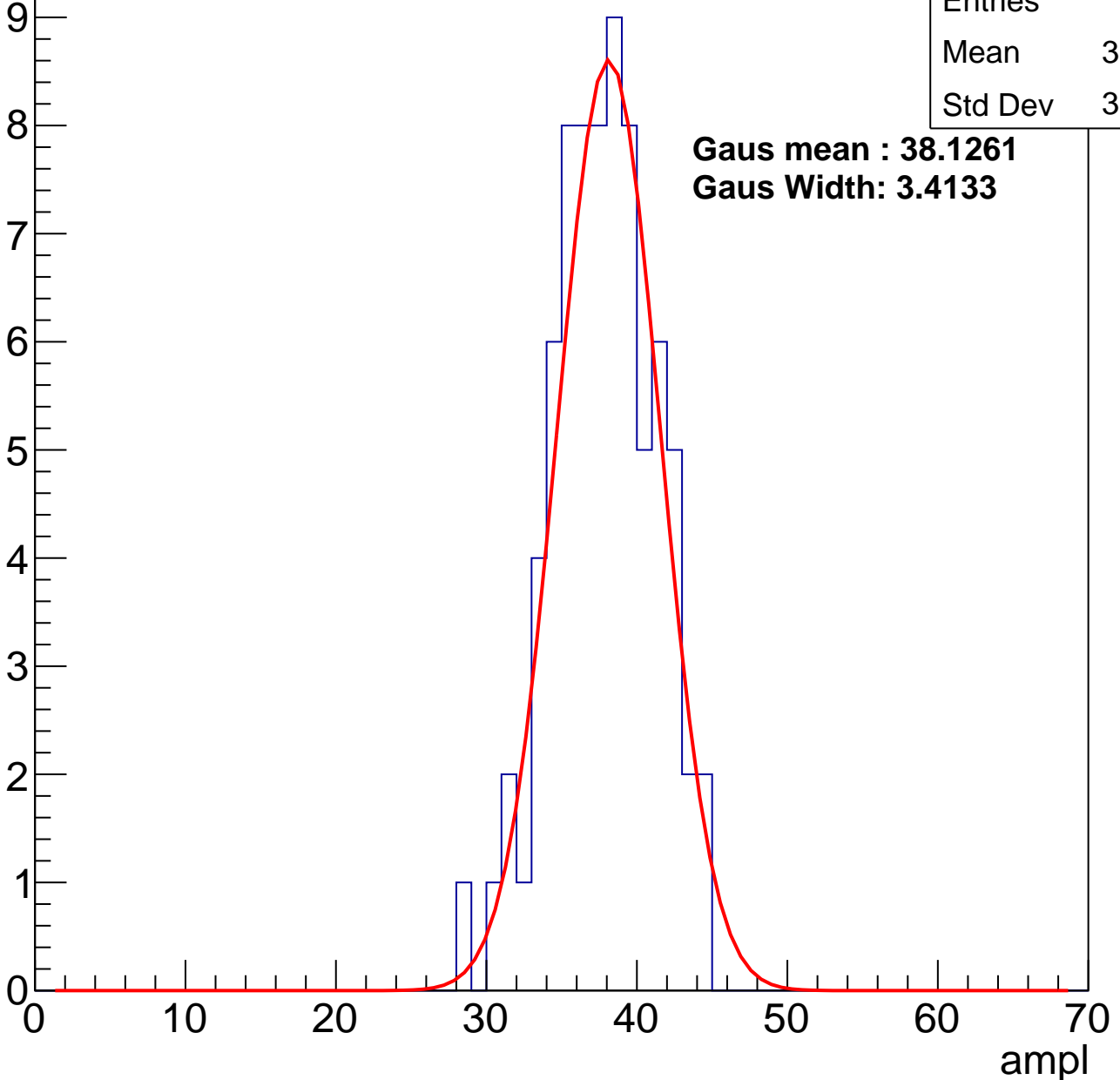
B1L101S, U23-ch43, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.32
Std Dev	3.353

Gaus mean : 38.1261
Gaus Width: 3.4133



B1L101S, U23-ch43, adc2

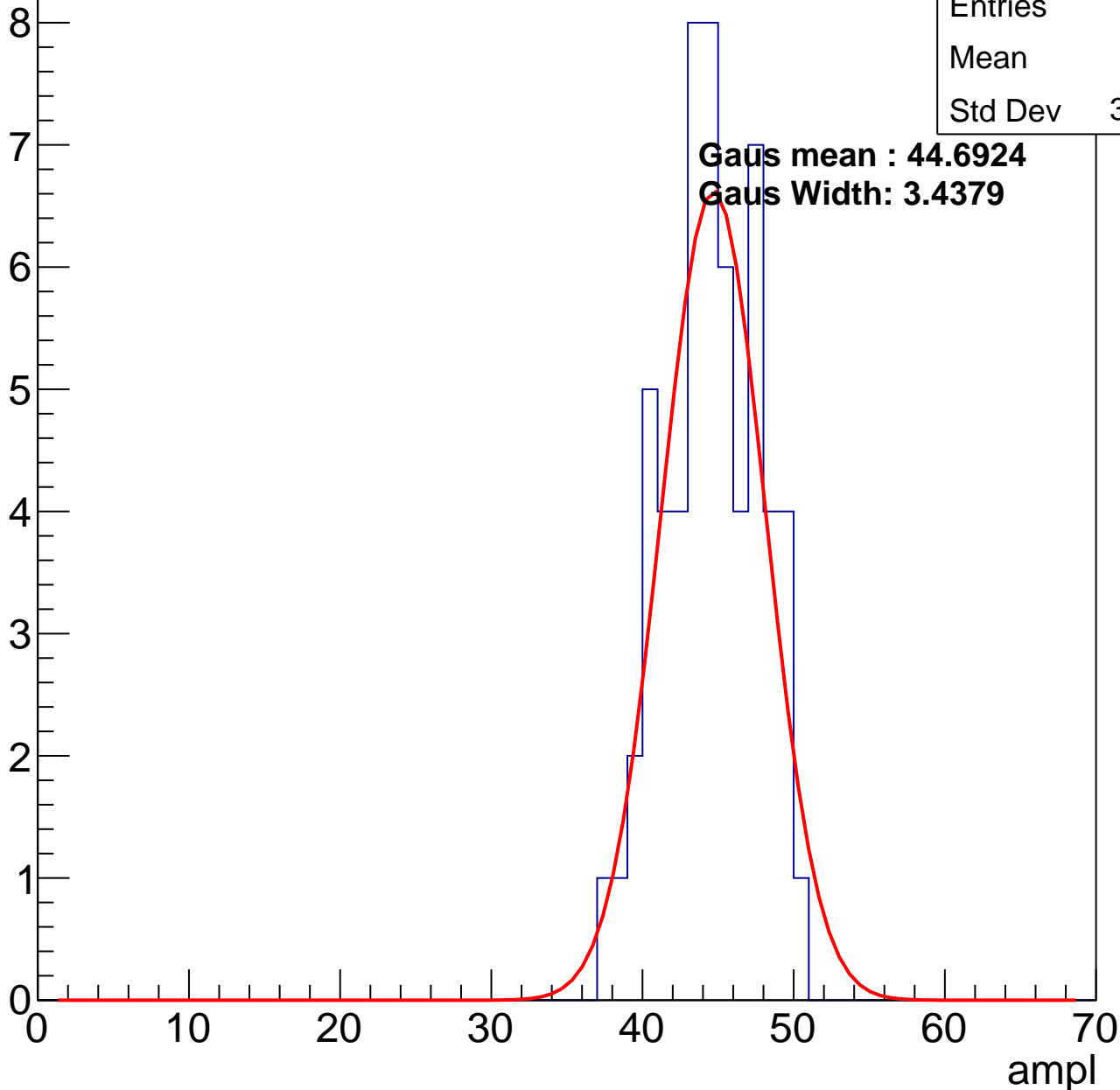
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.1
Std Dev	3.079

Gaus mean : 44.6924

Gaus Width: 3.4379

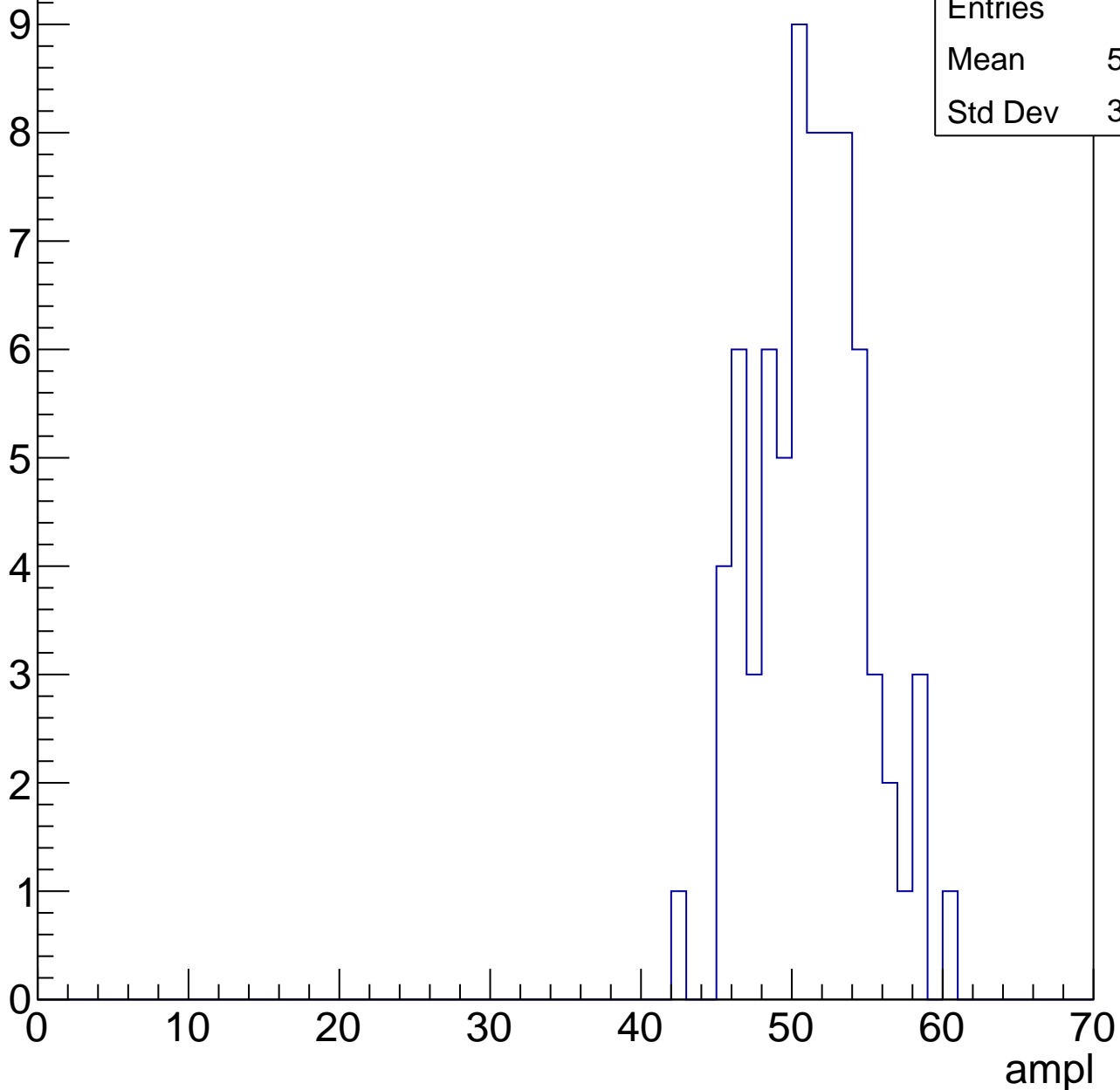


B1L101S, U23-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	50.84
Std Dev	3.602

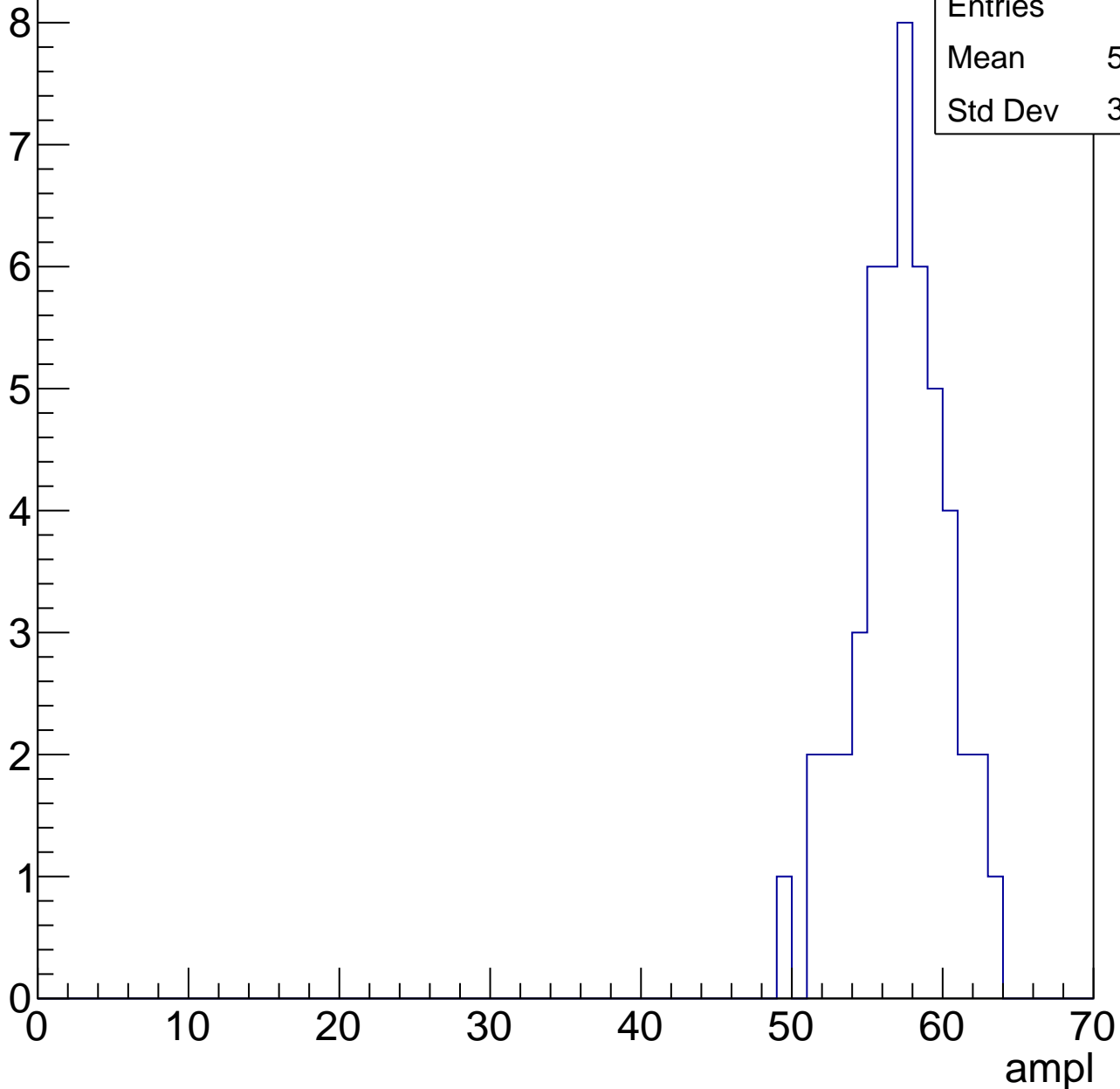


B1L101S, U23-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

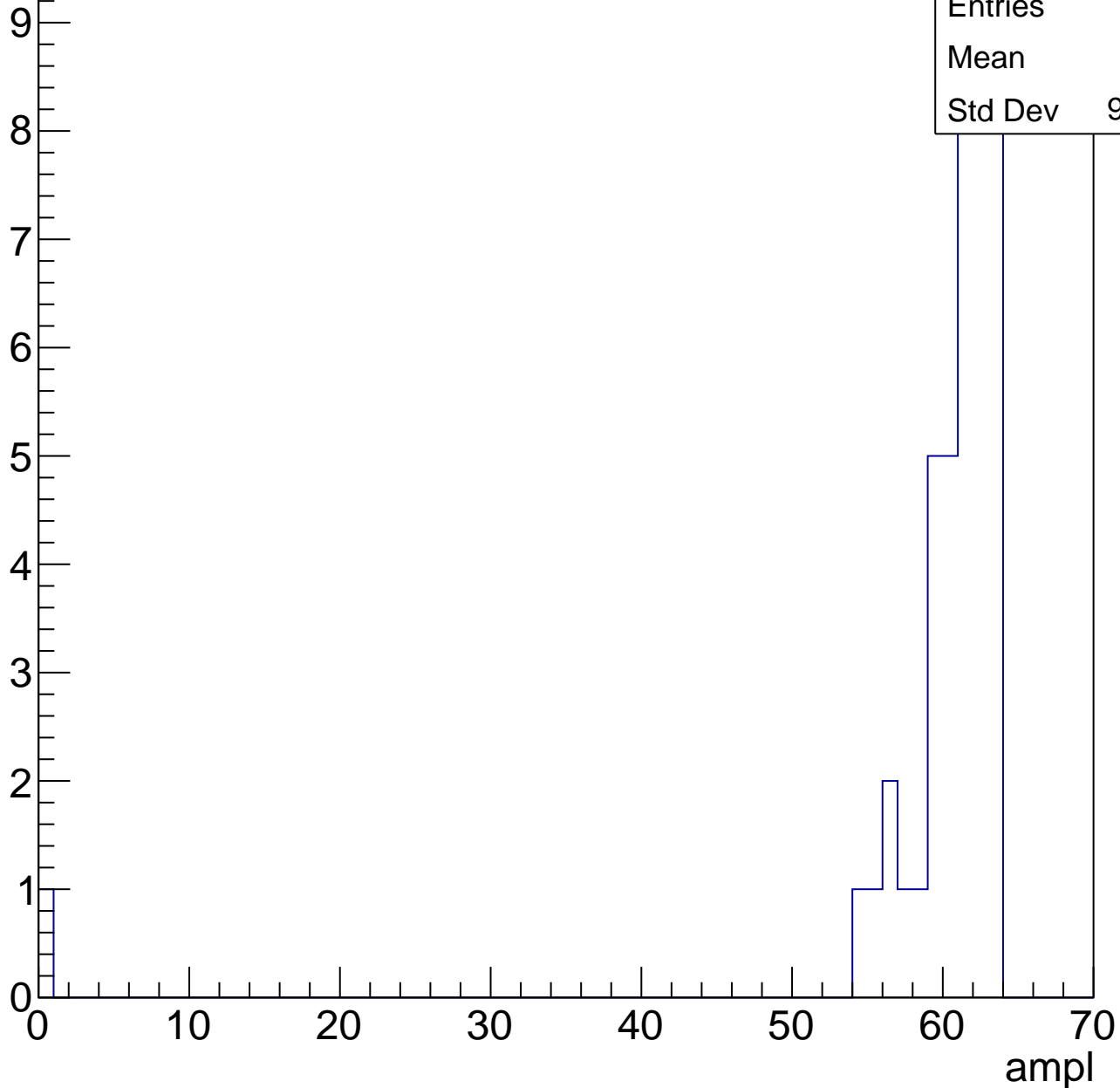
Entries	50
Mean	56.74
Std Dev	3.005



B1L101S, U23-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch44, adc0

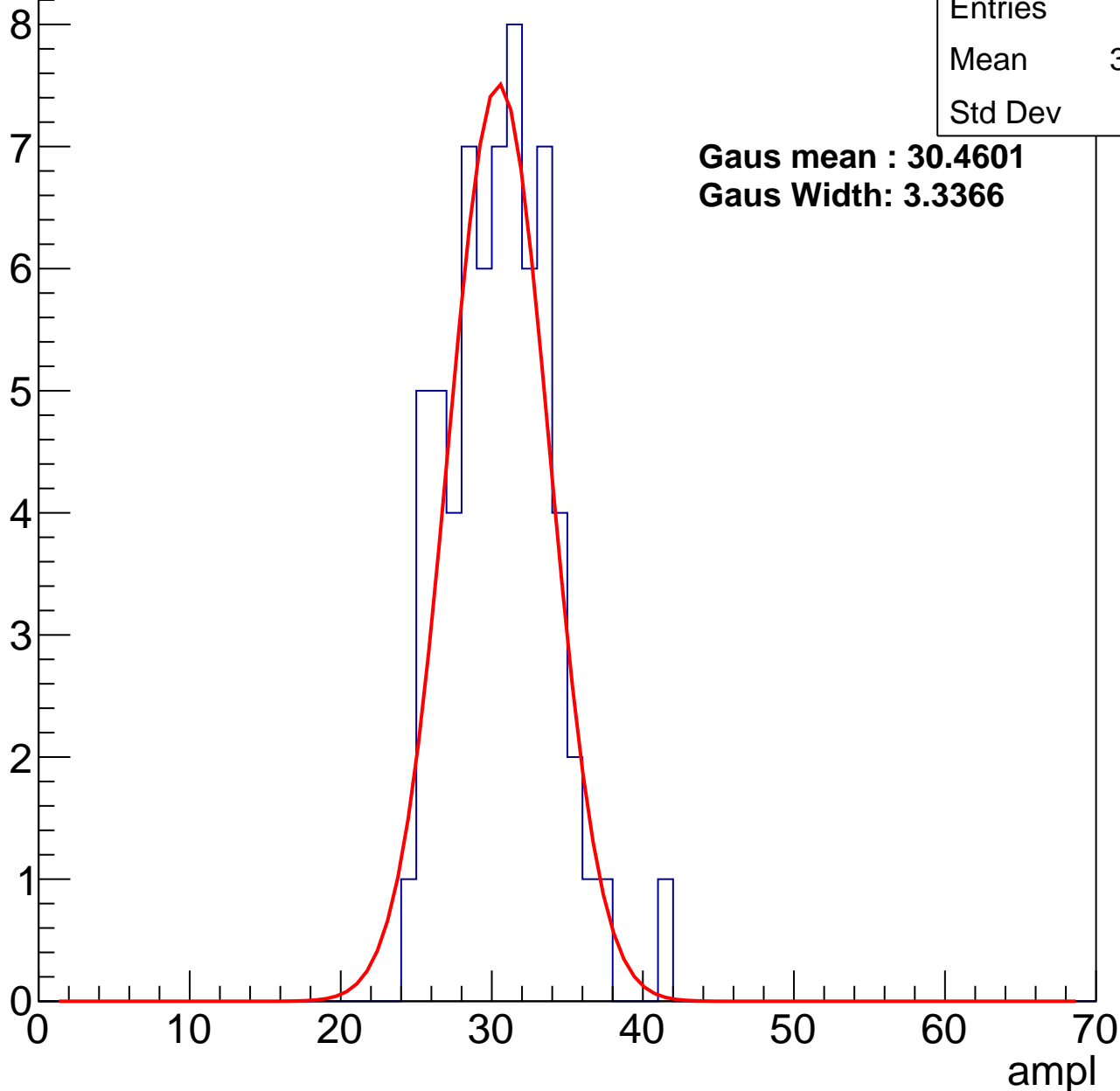
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.12
Std Dev	3.34

Gaus mean : 30.4601

Gaus Width: 3.3366



B1L101S, U23-ch44, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	38.11
Std Dev	3.95

Gaus mean : 38.5679

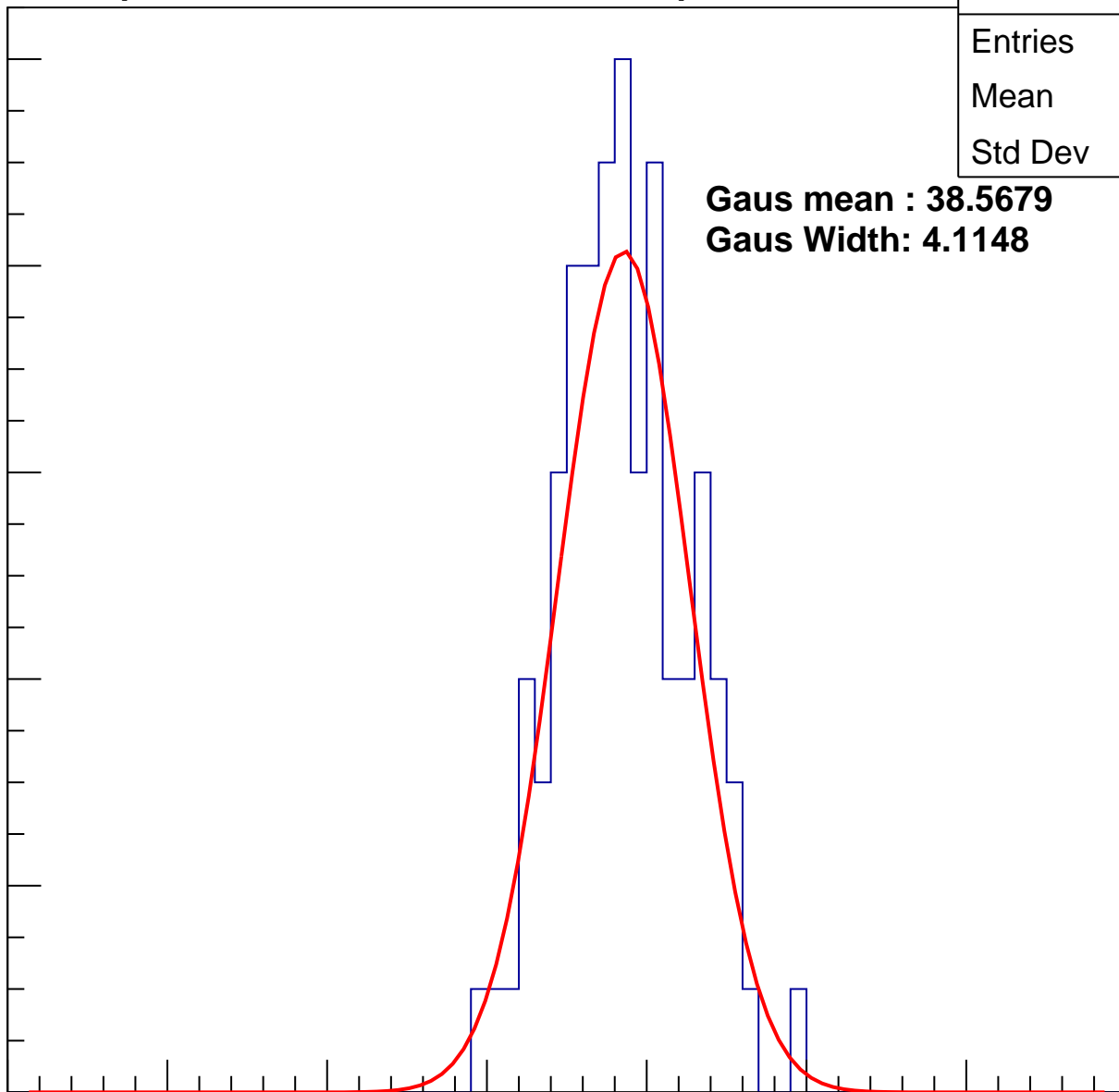
Gaus Width: 4.1148

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch44, adc2

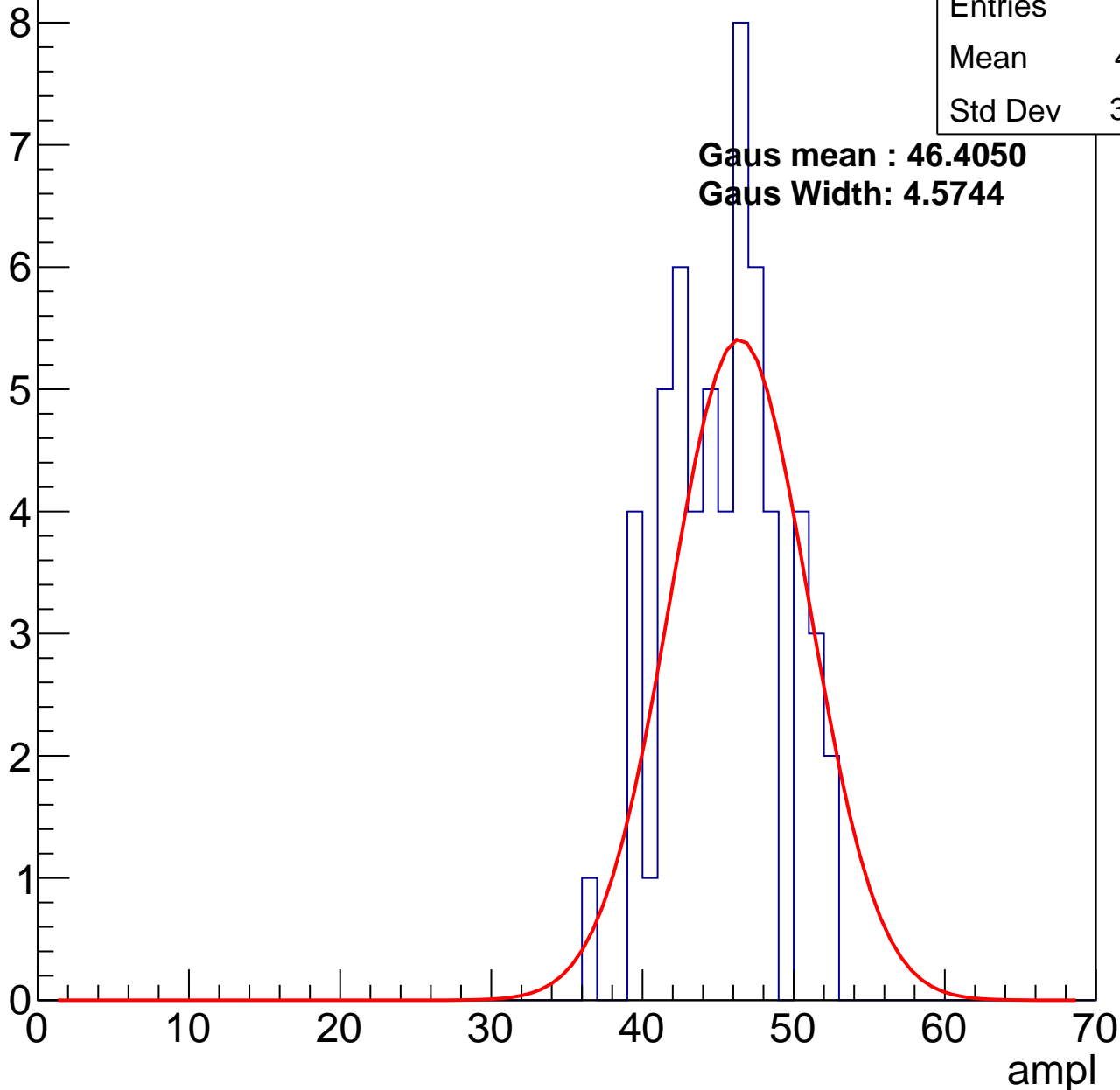
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	44.91
Std Dev	3.696

Gaus mean : 46.4050

Gaus Width: 4.5744

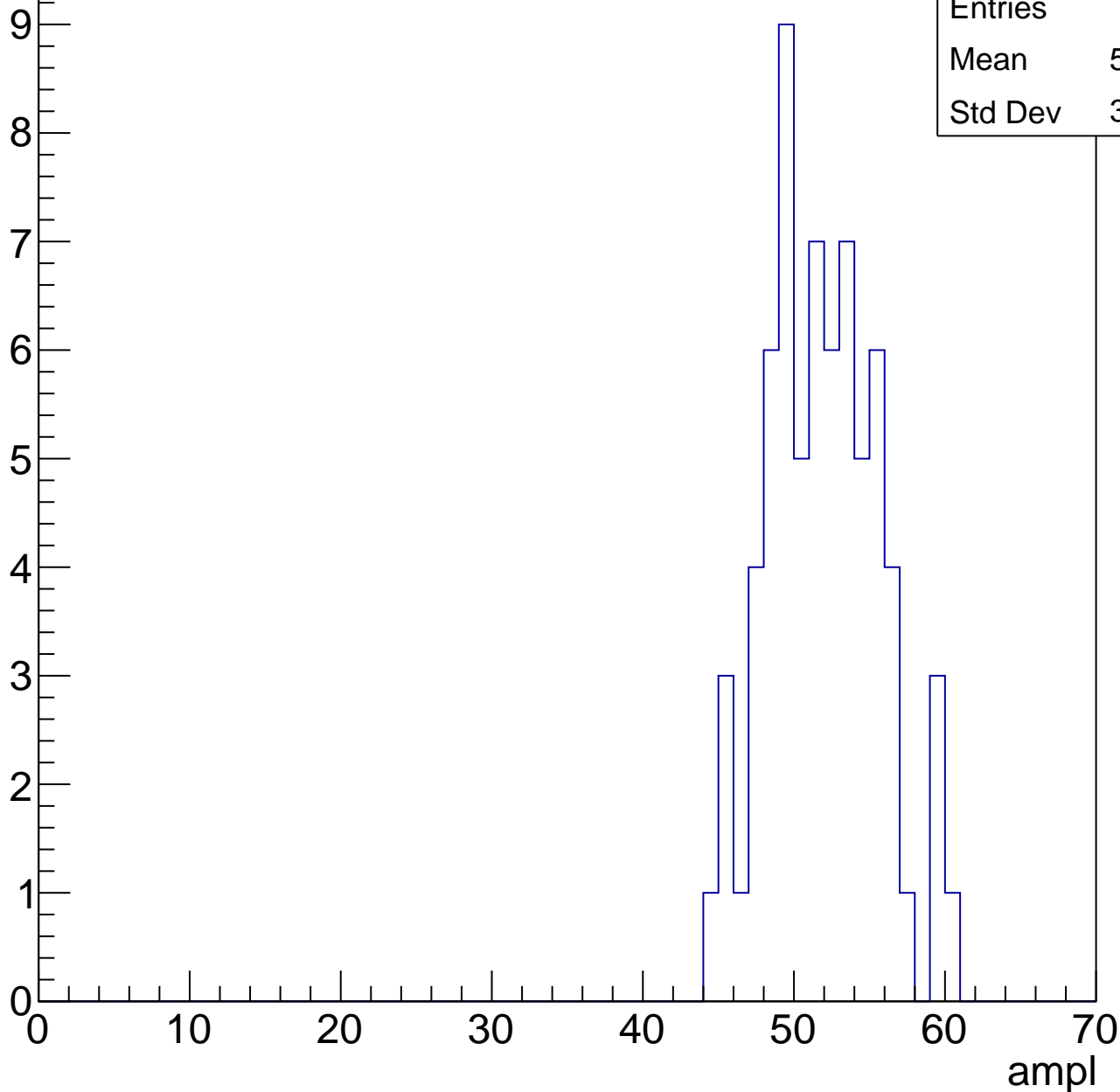


B1L101S, U23-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

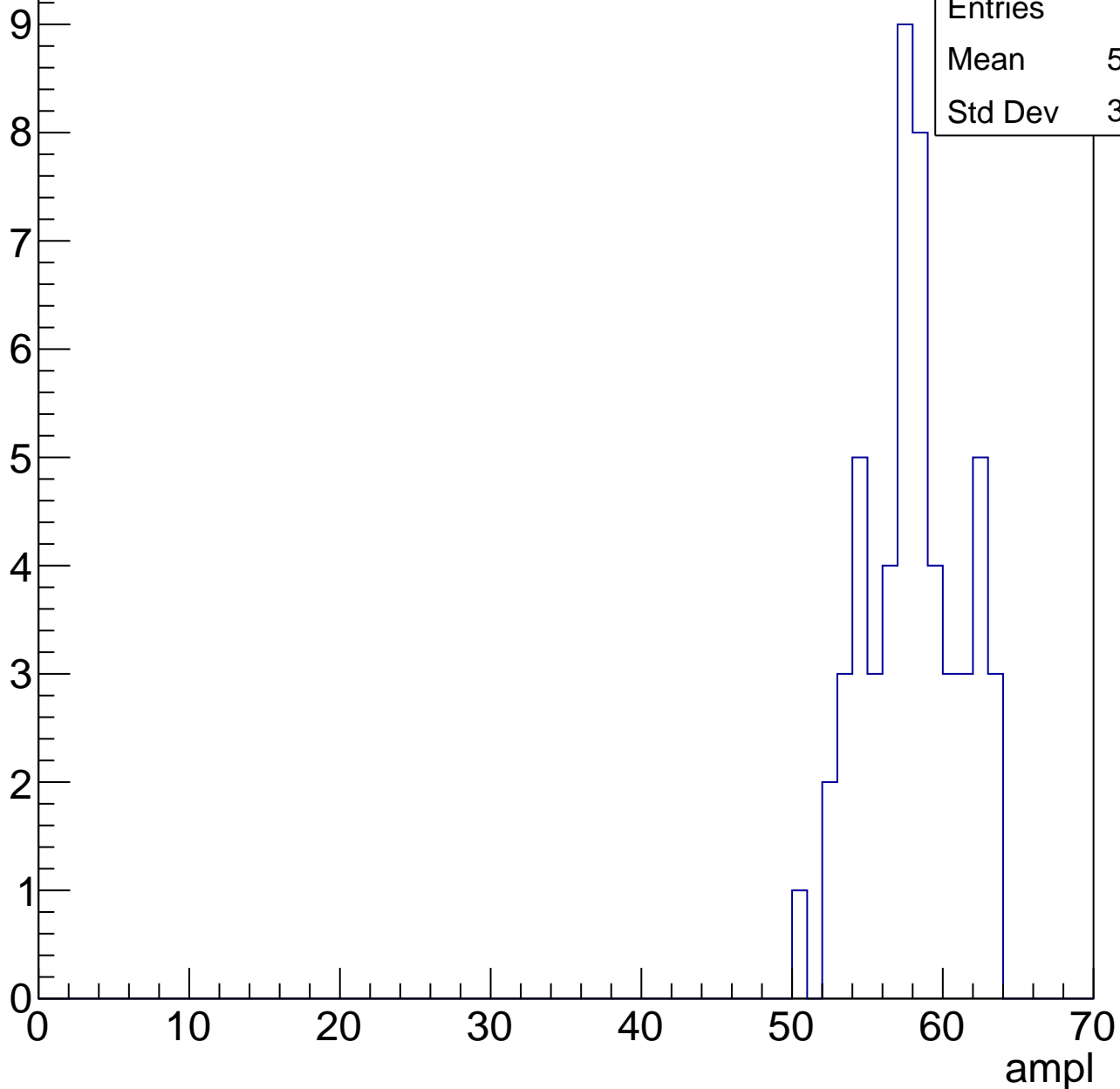
Entries	69
Mean	51.45
Std Dev	3.634



B1L101S, U23-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

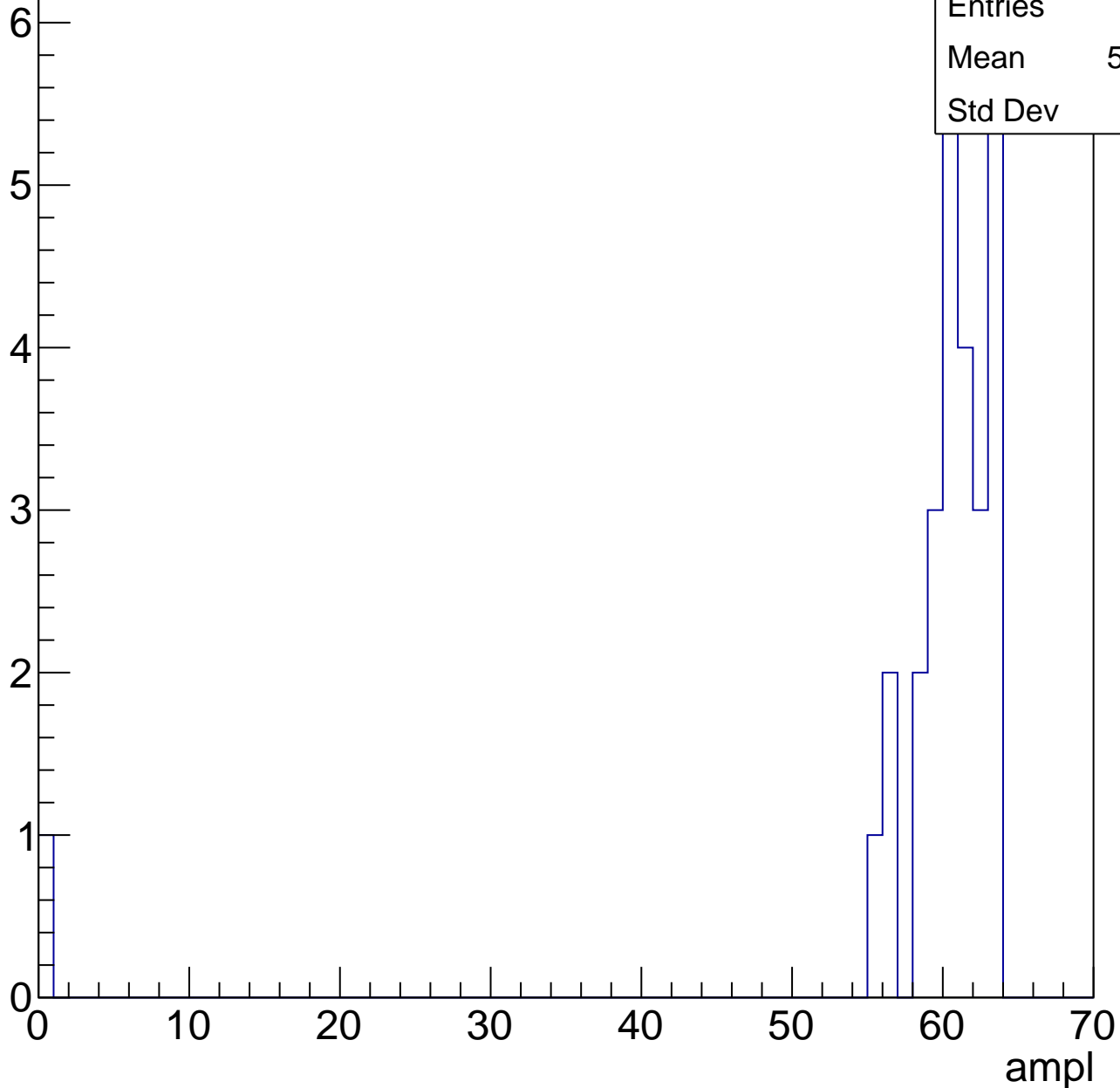
Entry



B1L101S, U23-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

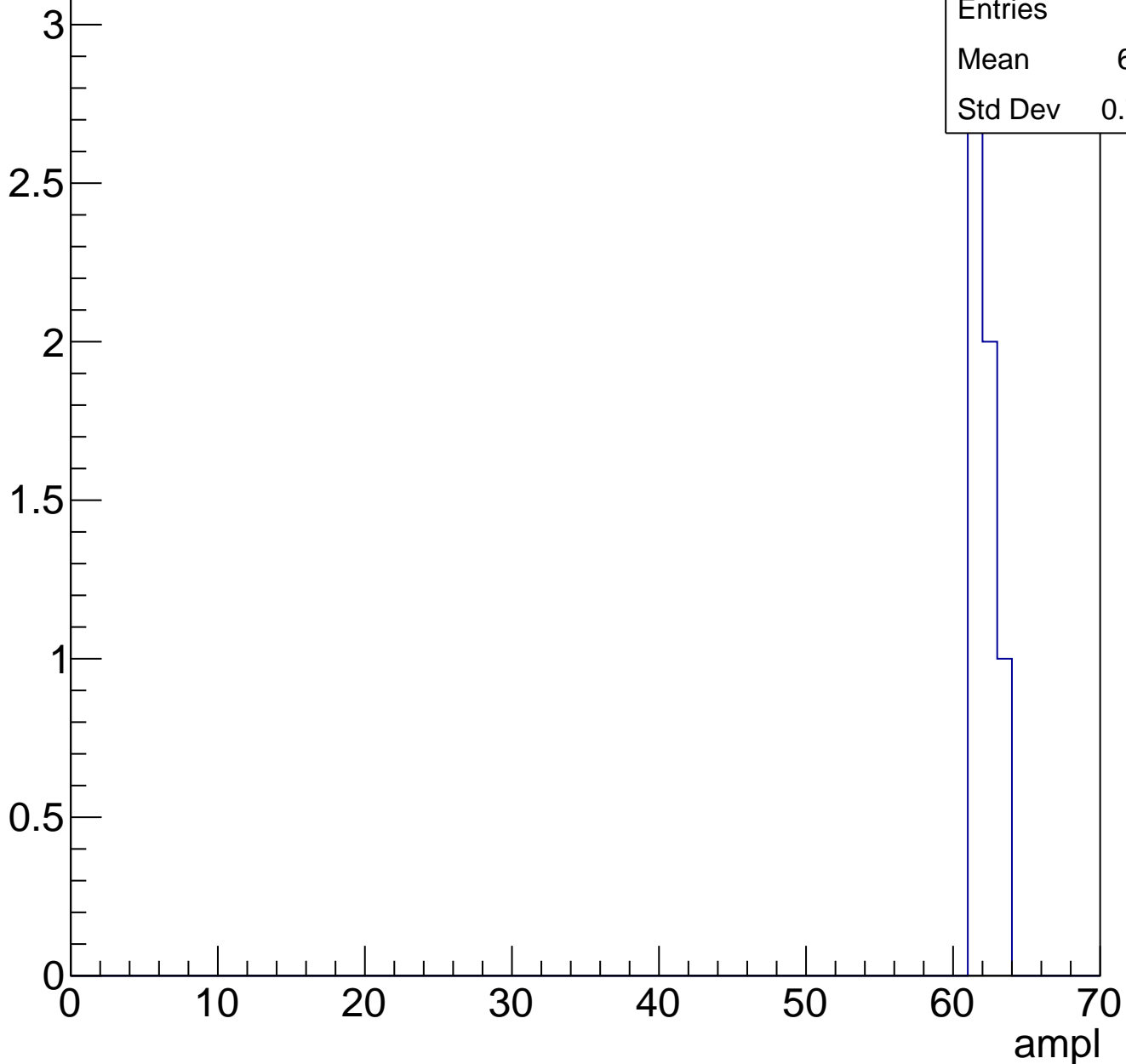
Entry



B1L101S, U23-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch45, adc0

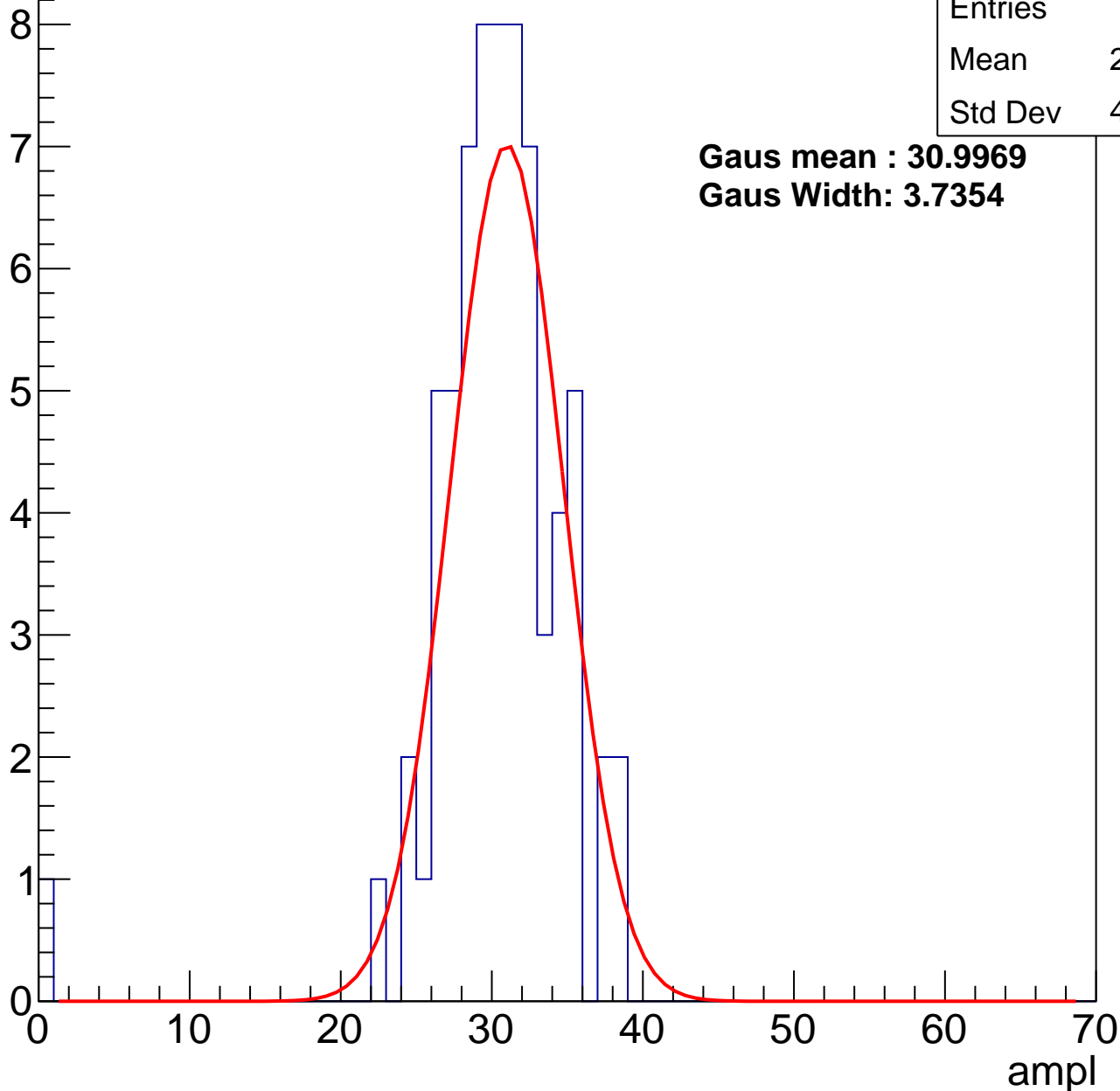
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.86
Std Dev	4.962

Gaus mean : 30.9969

Gaus Width: 3.7354



B1L101S, U23-ch45, adc1

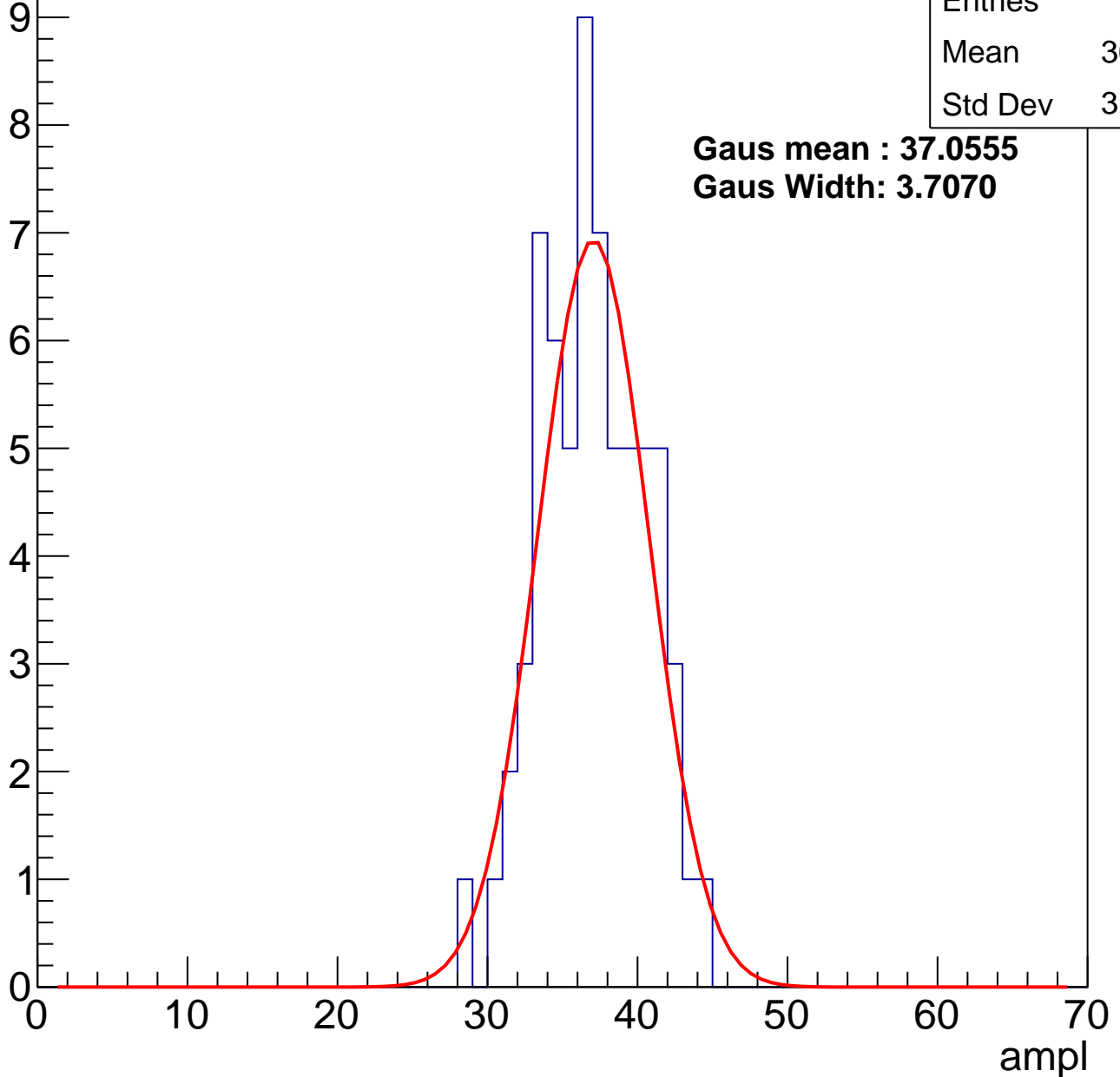
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.55
Std Dev	3.412

Gaus mean : 37.0555

Gaus Width: 3.7070



B1L101S, U23-ch45, adc2

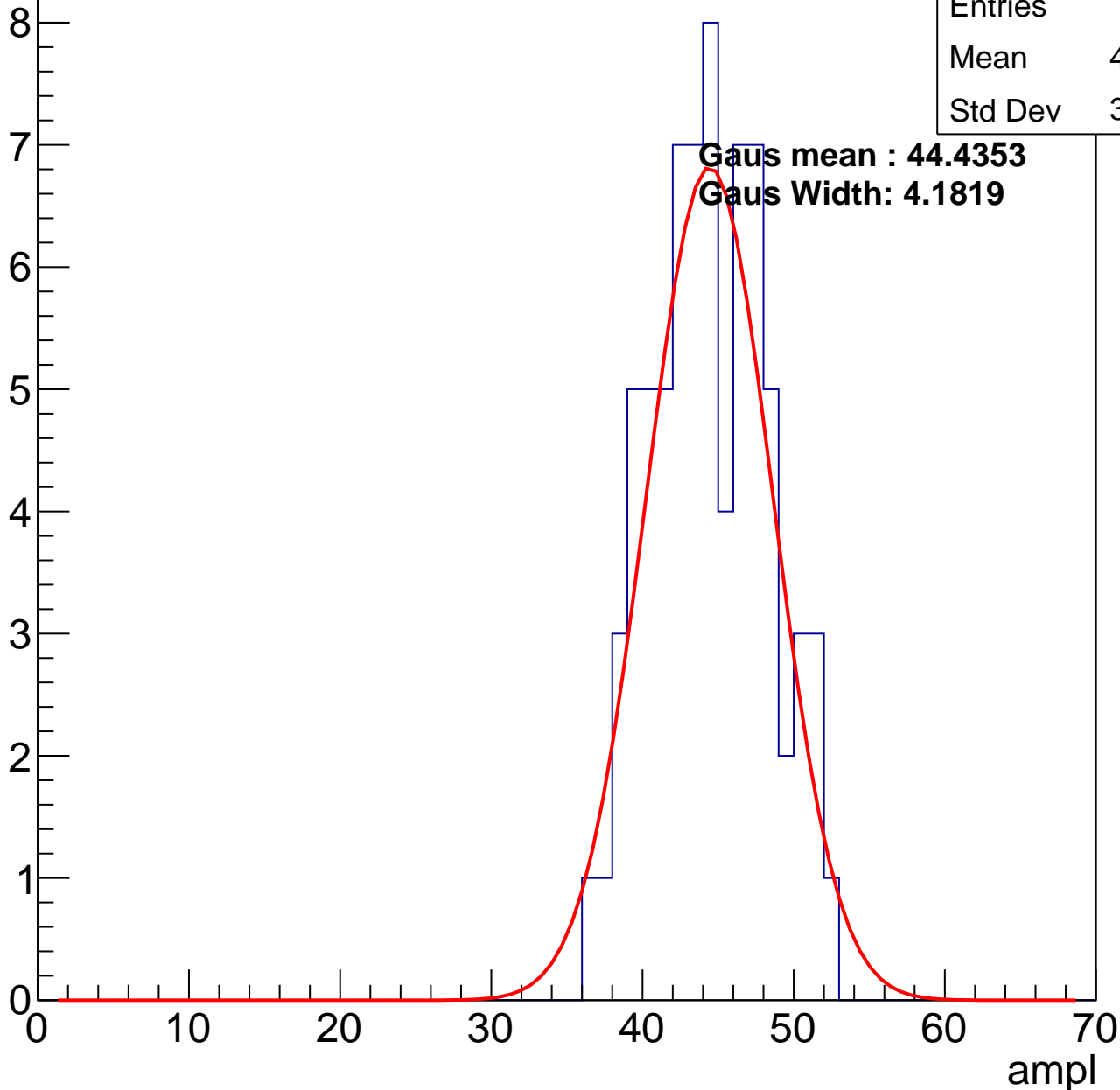
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.03
Std Dev	3.767

Gaus mean : 44.4353

Gaus Width: 4.1819

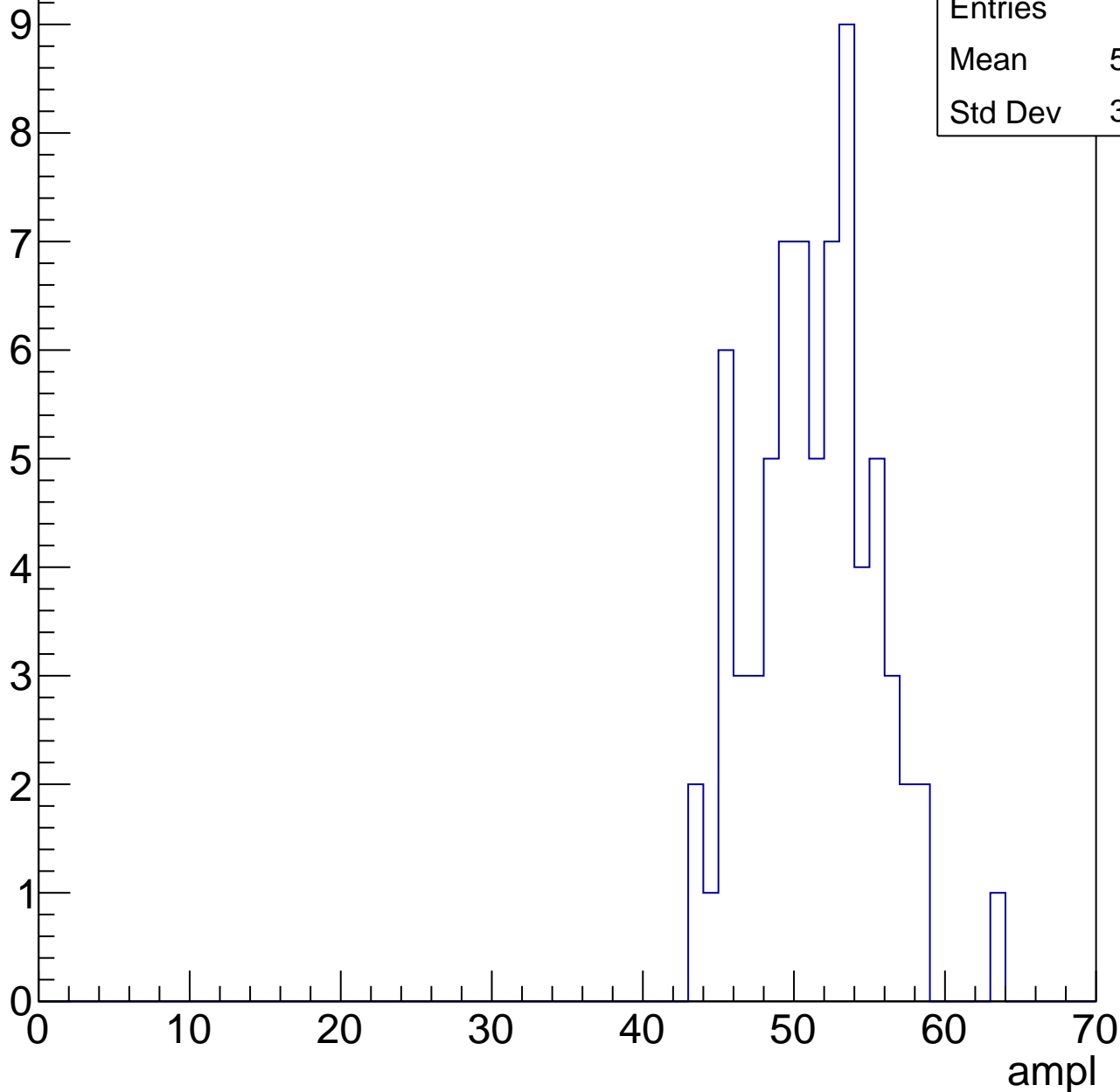


B1L101S, U23-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	50.83
Std Dev	3.983

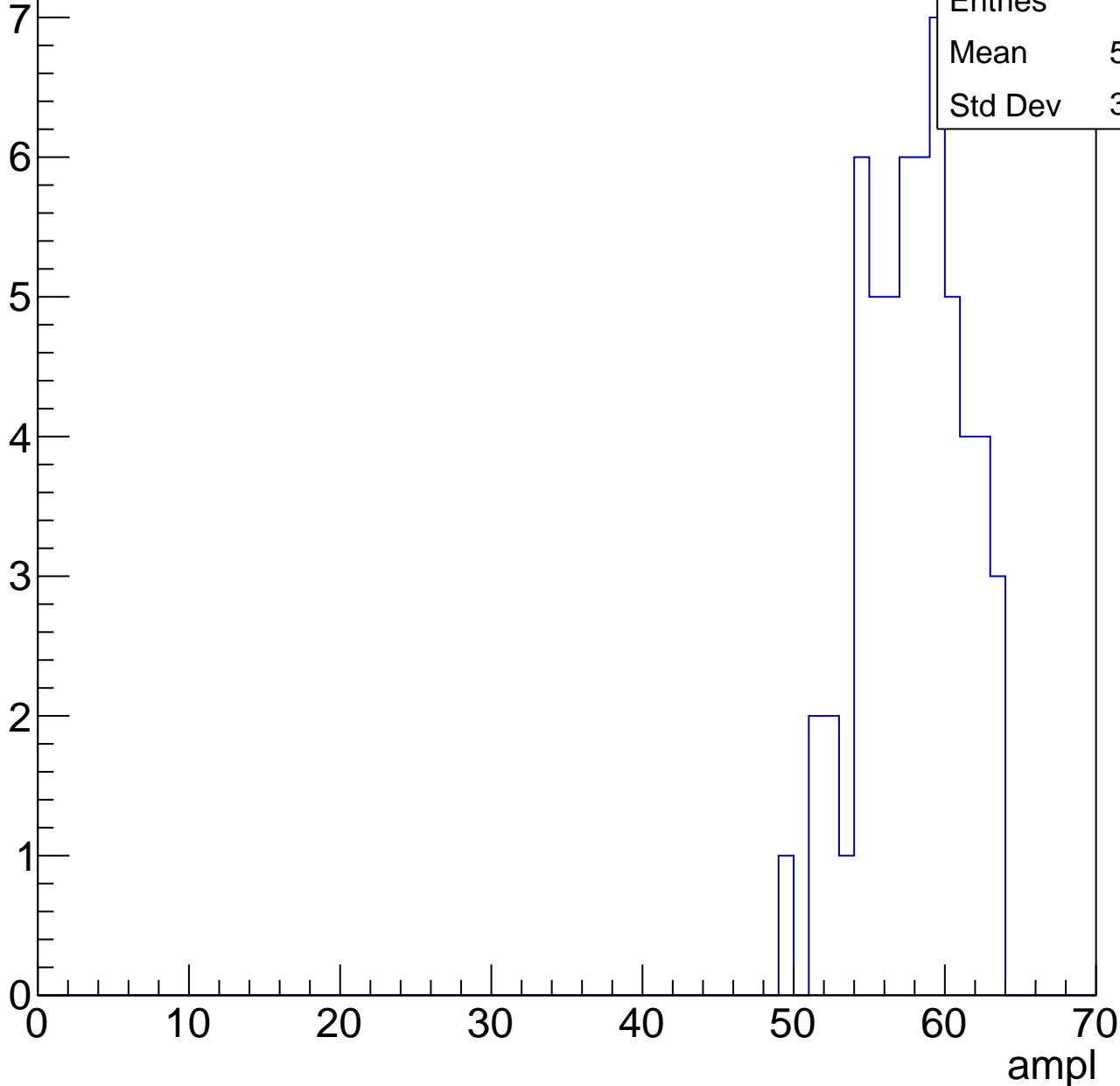


B1L101S, U23-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

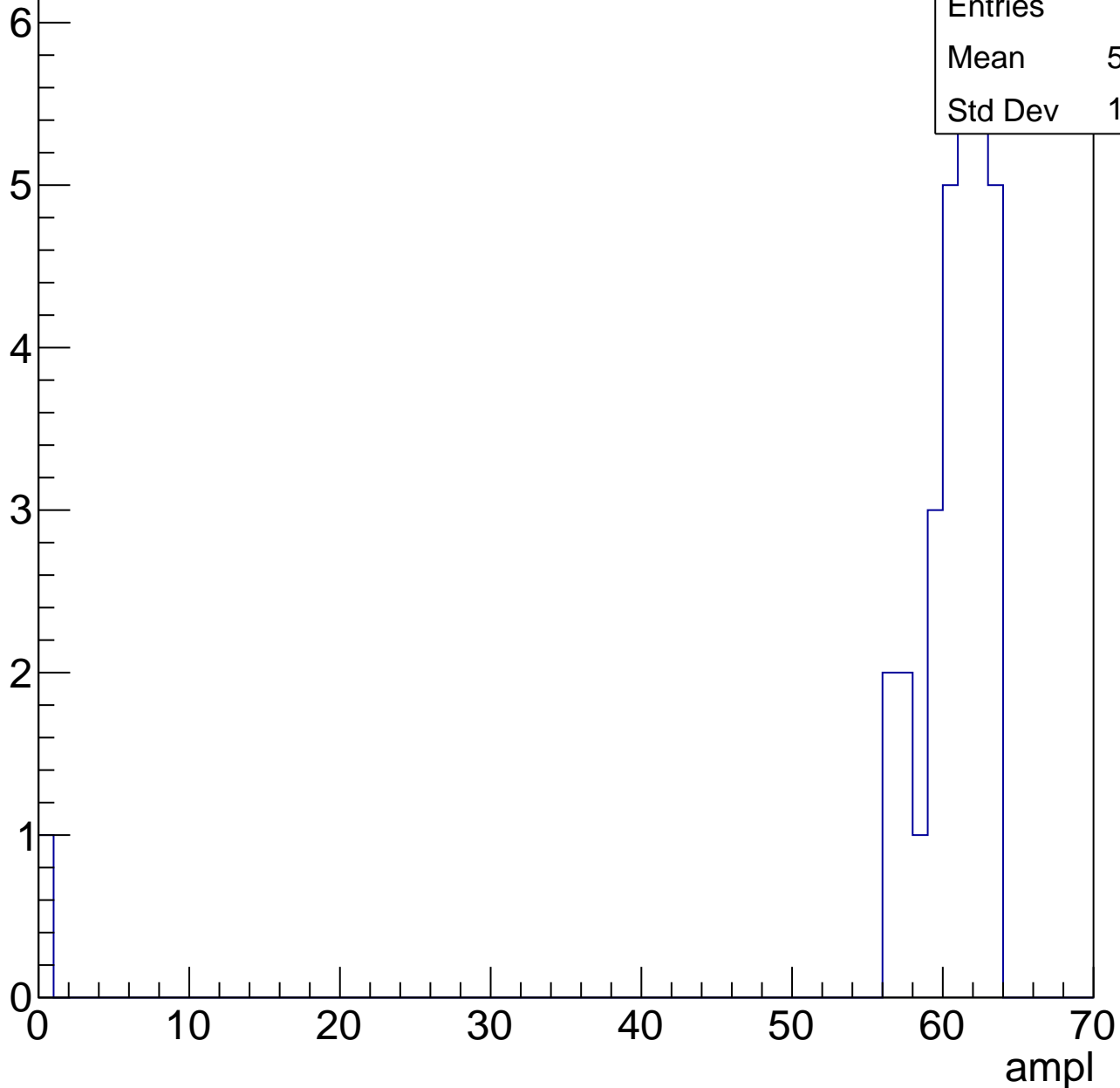
Entries	57
Mean	57.39
Std Dev	3.307



B1L101S, U23-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	60.5
Std Dev	2.5

0 10 20 30 40 50 60 70

ampl

B1L101S, U23-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch46, adc0

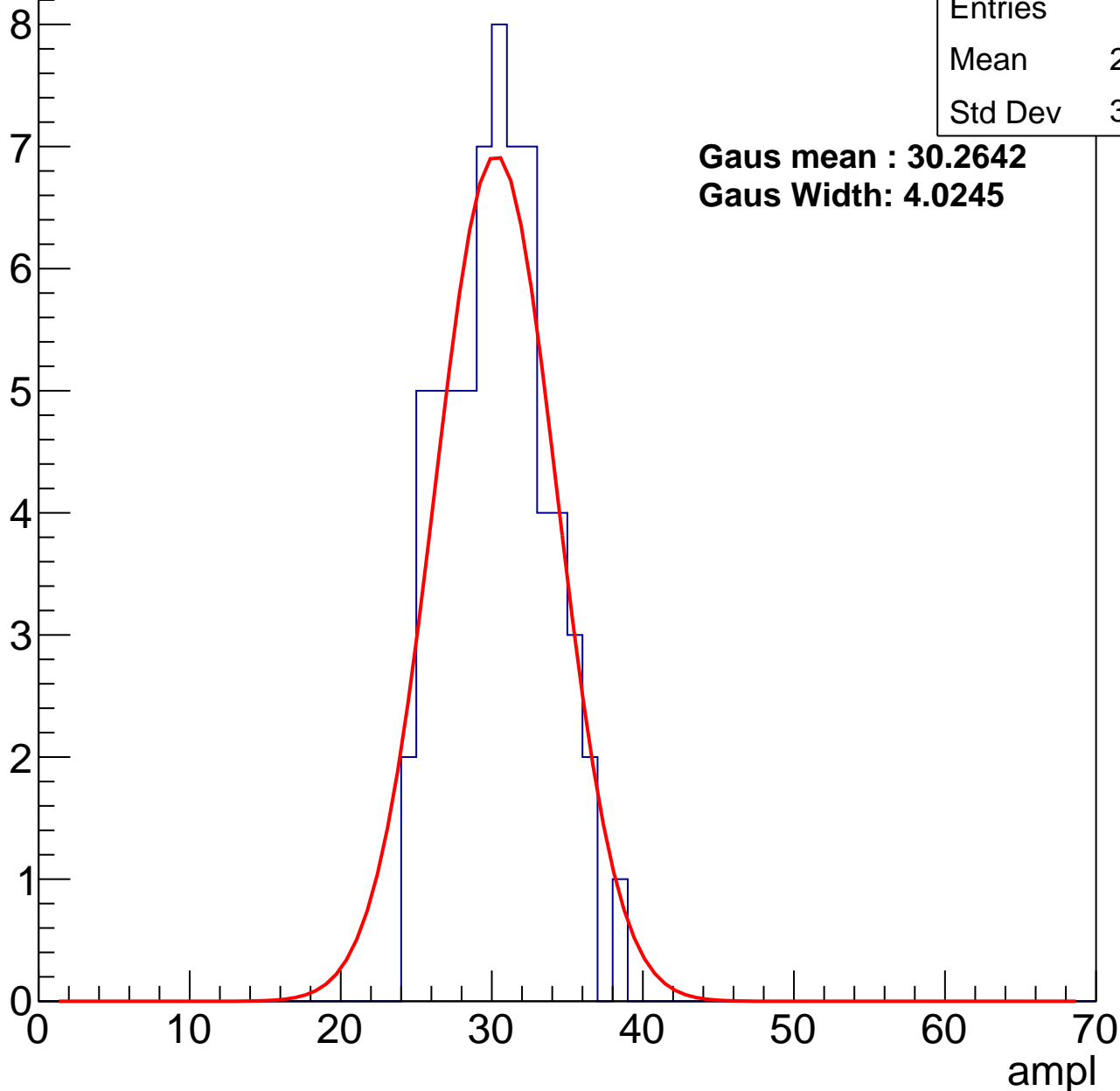
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.92
Std Dev	3.269

Gaus mean : 30.2642

Gaus Width: 4.0245



B1L101S, U23-ch46, adc1

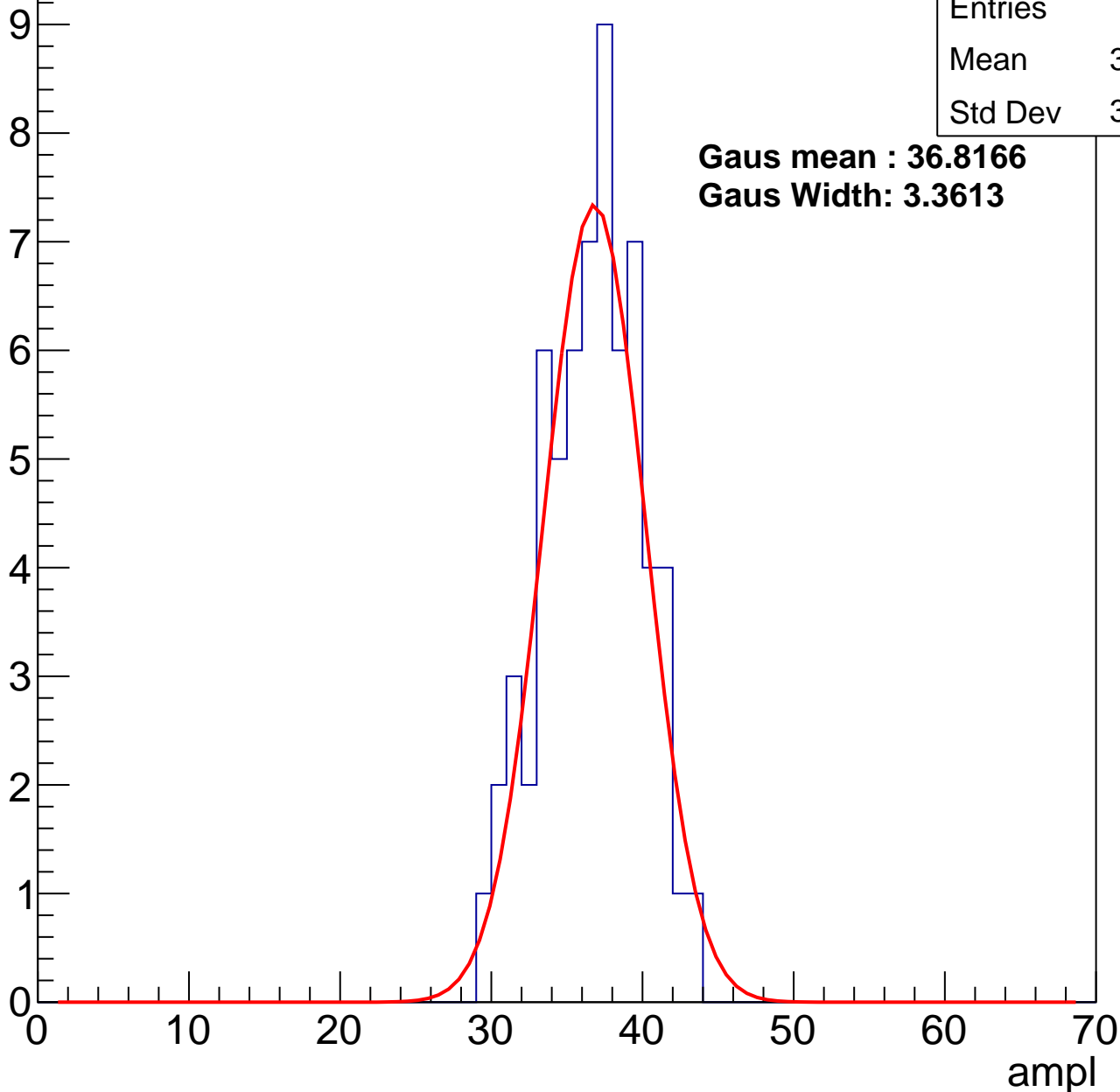
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.23
Std Dev	3.186

Gaus mean : 36.8166

Gaus Width: 3.3613



B1L101S, U23-ch46, adc2

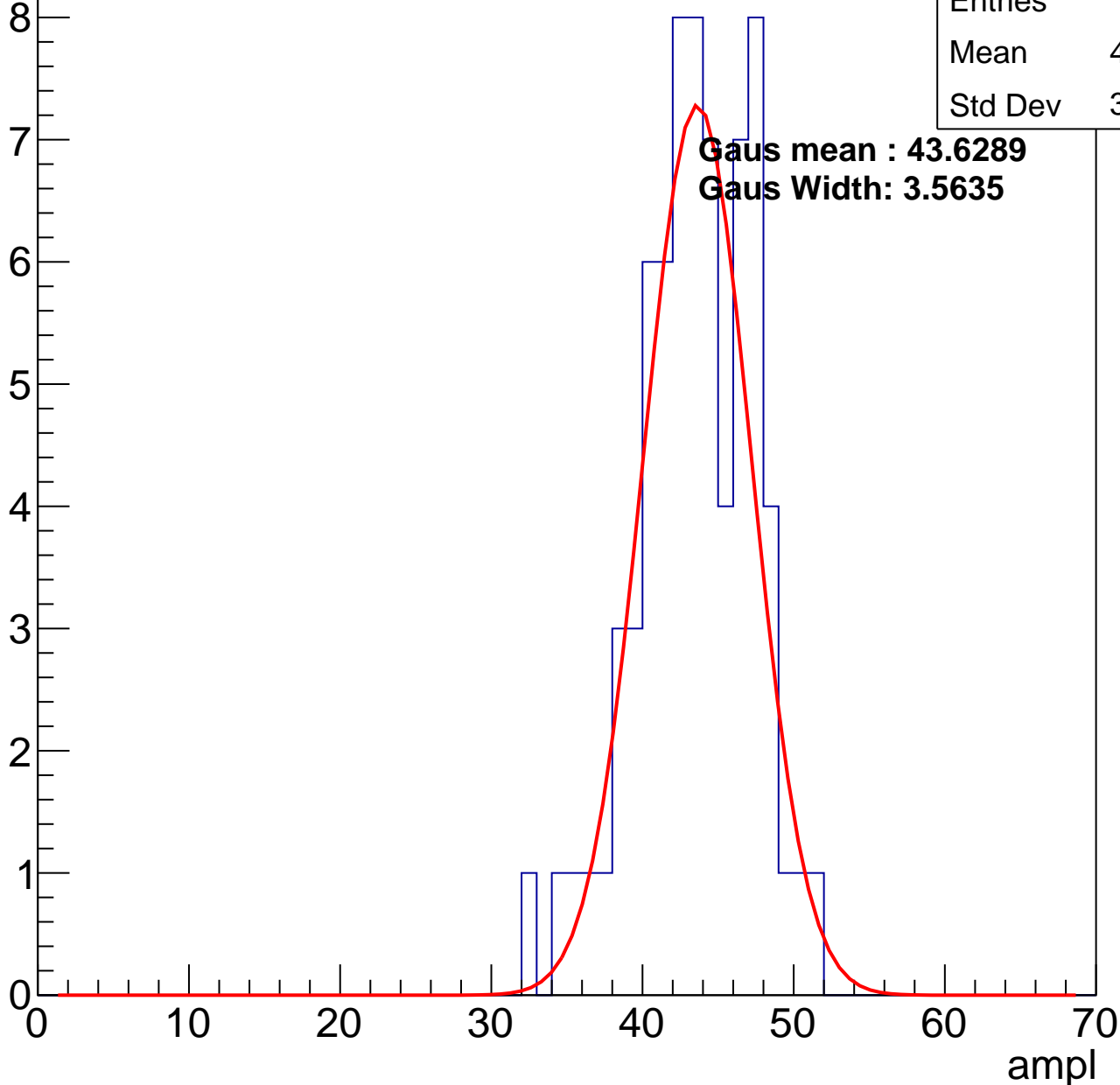
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.04
Std Dev	3.769

Gaus mean : 43.6289

Gaus Width: 3.5635

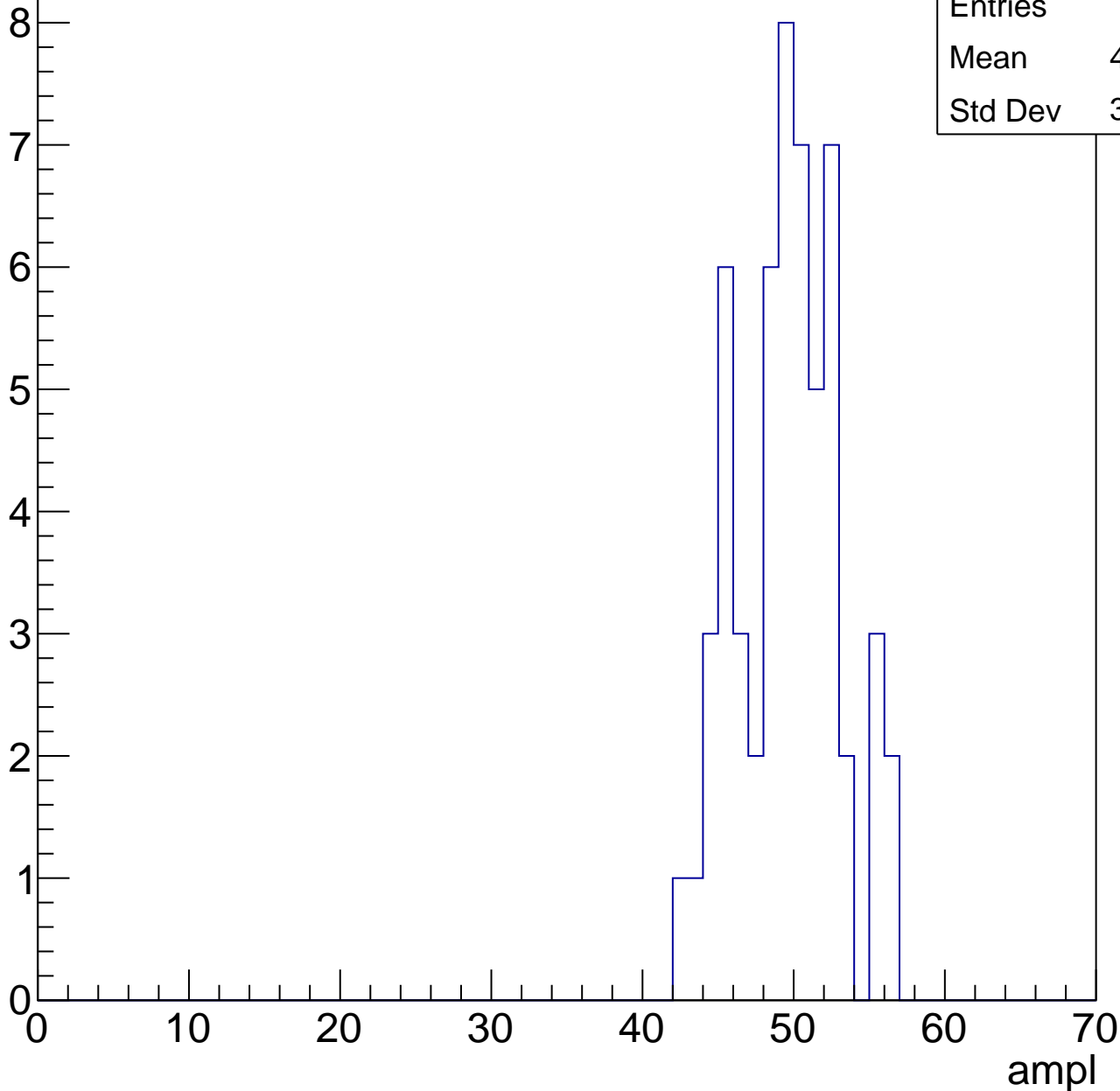


B1L101S, U23-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	49.12
Std Dev	3.338

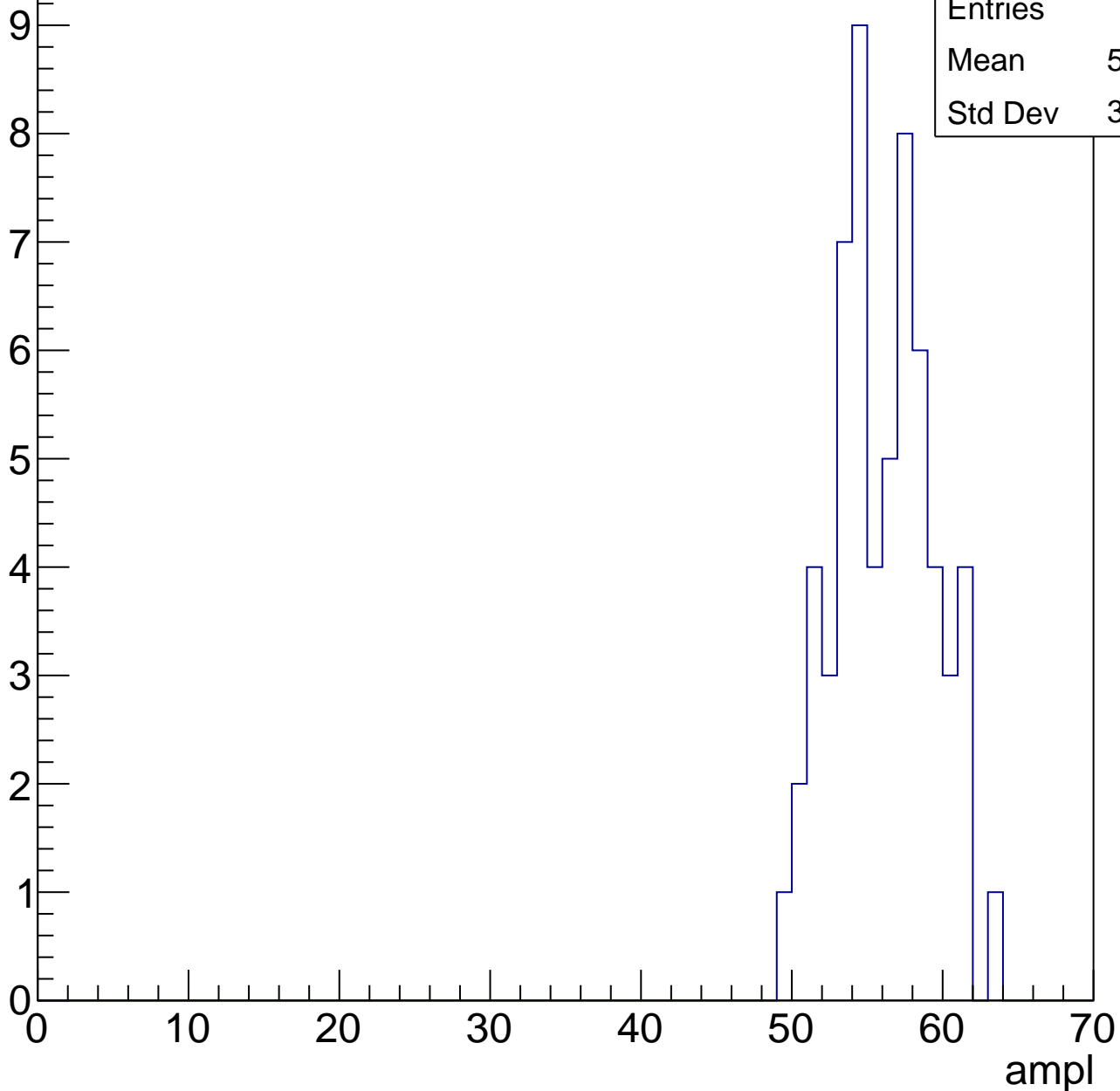


B1L101S, U23-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.62
Std Dev	3.209

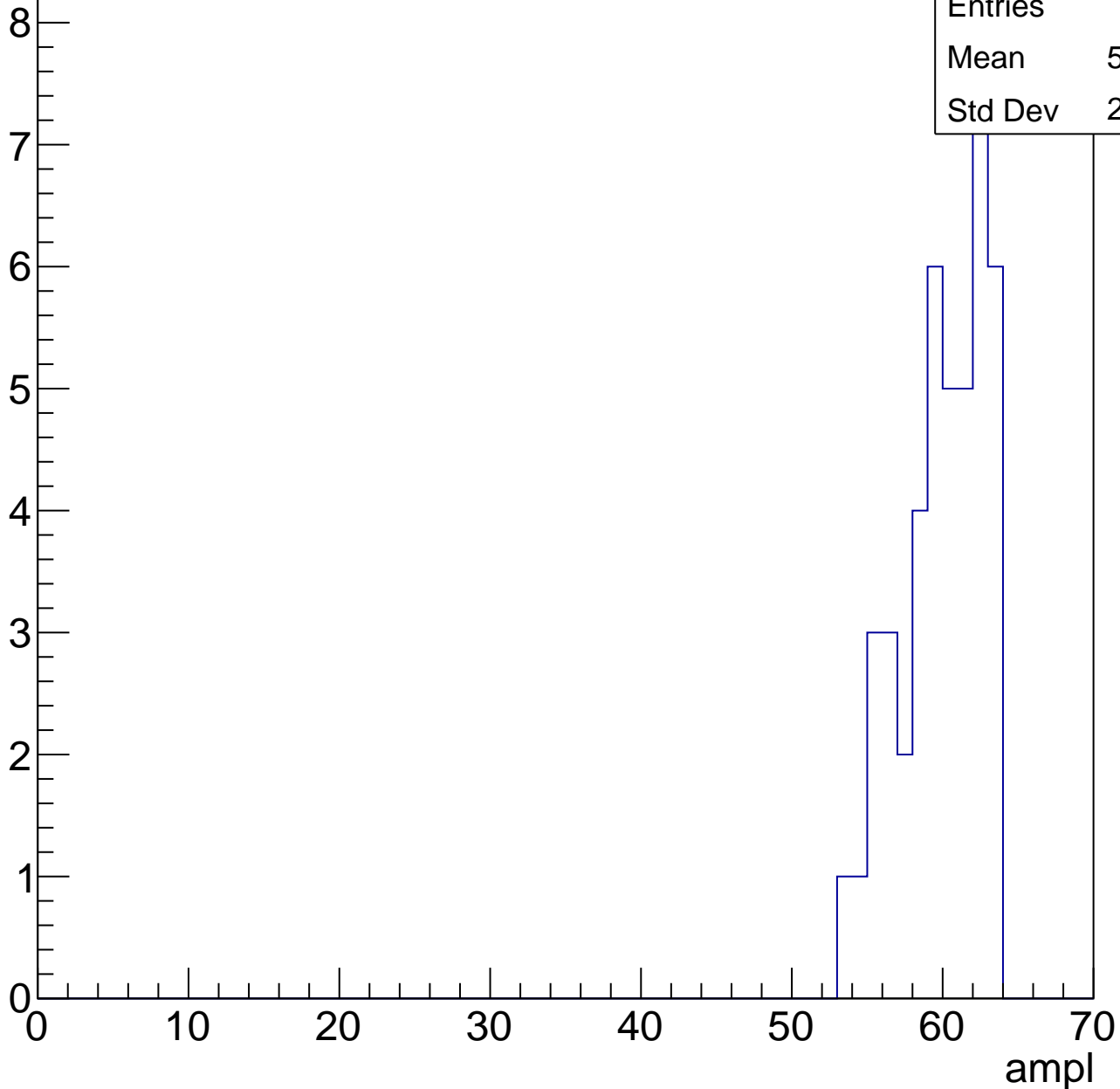


B1L101S, U23-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	59.52
Std Dev	2.734



B1L101S, U23-ch46, adc6

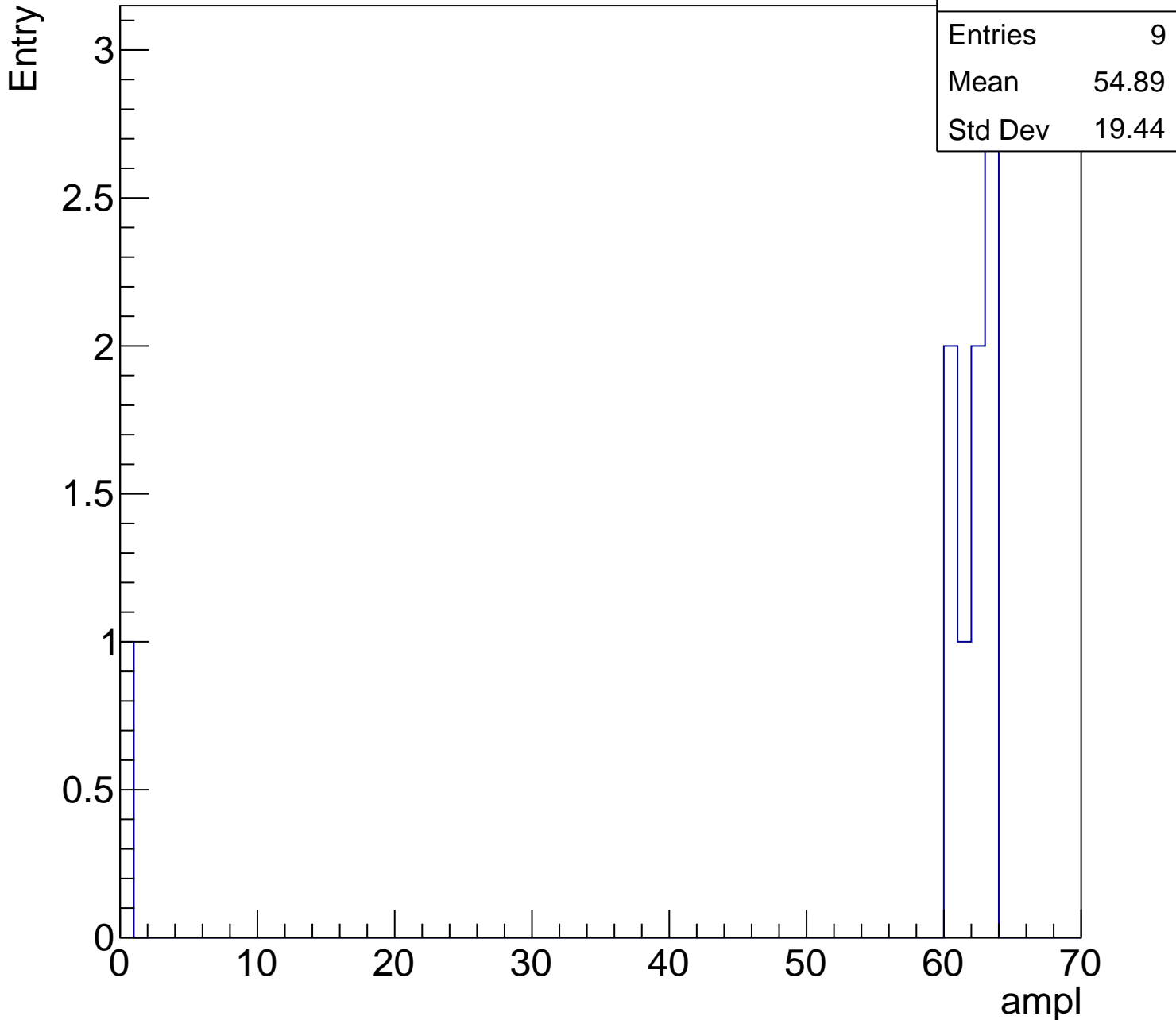
calib_packv5_042523_0143.root, FC#0, port D2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	54.89
Std Dev	19.44

ampl



B1L101S, U23-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L101S, U23-ch47, adc0

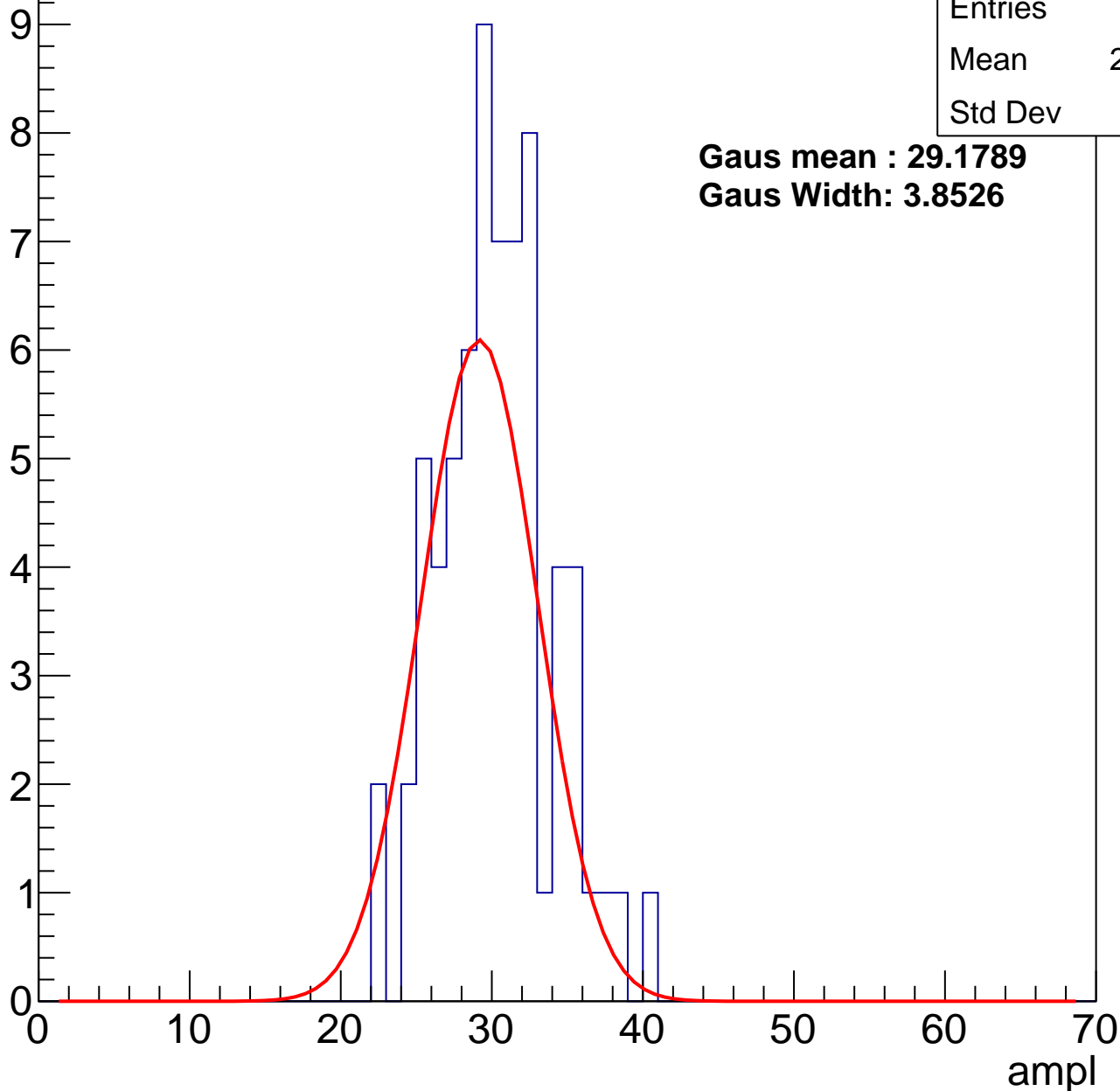
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.82
Std Dev	3.69

Gaus mean : 29.1789

Gaus Width: 3.8526



B1L101S, U23-ch47, adc1

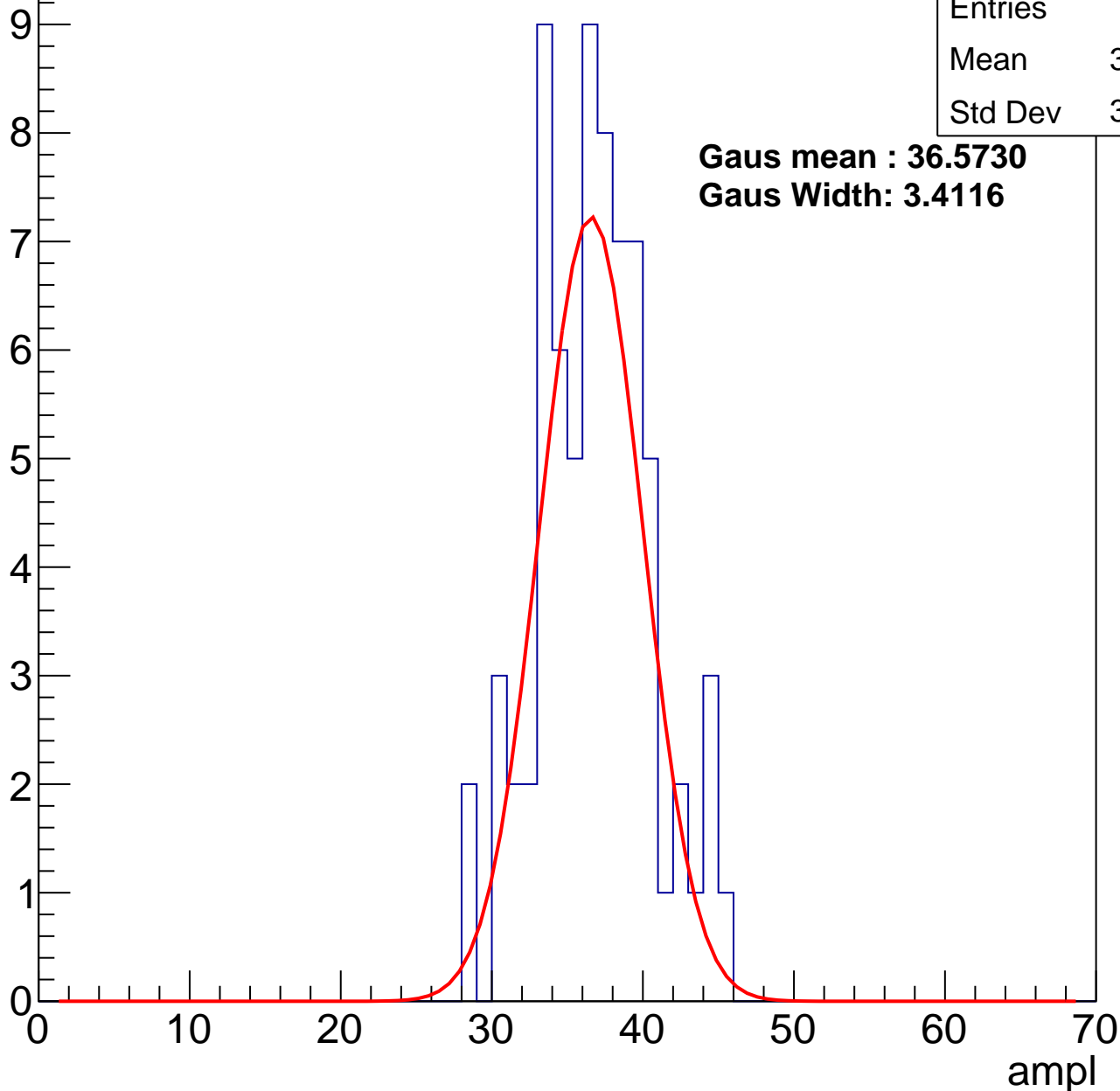
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.33
Std Dev	3.735

Gaus mean : 36.5730

Gaus Width: 3.4116



B1L101S, U23-ch47, adc2

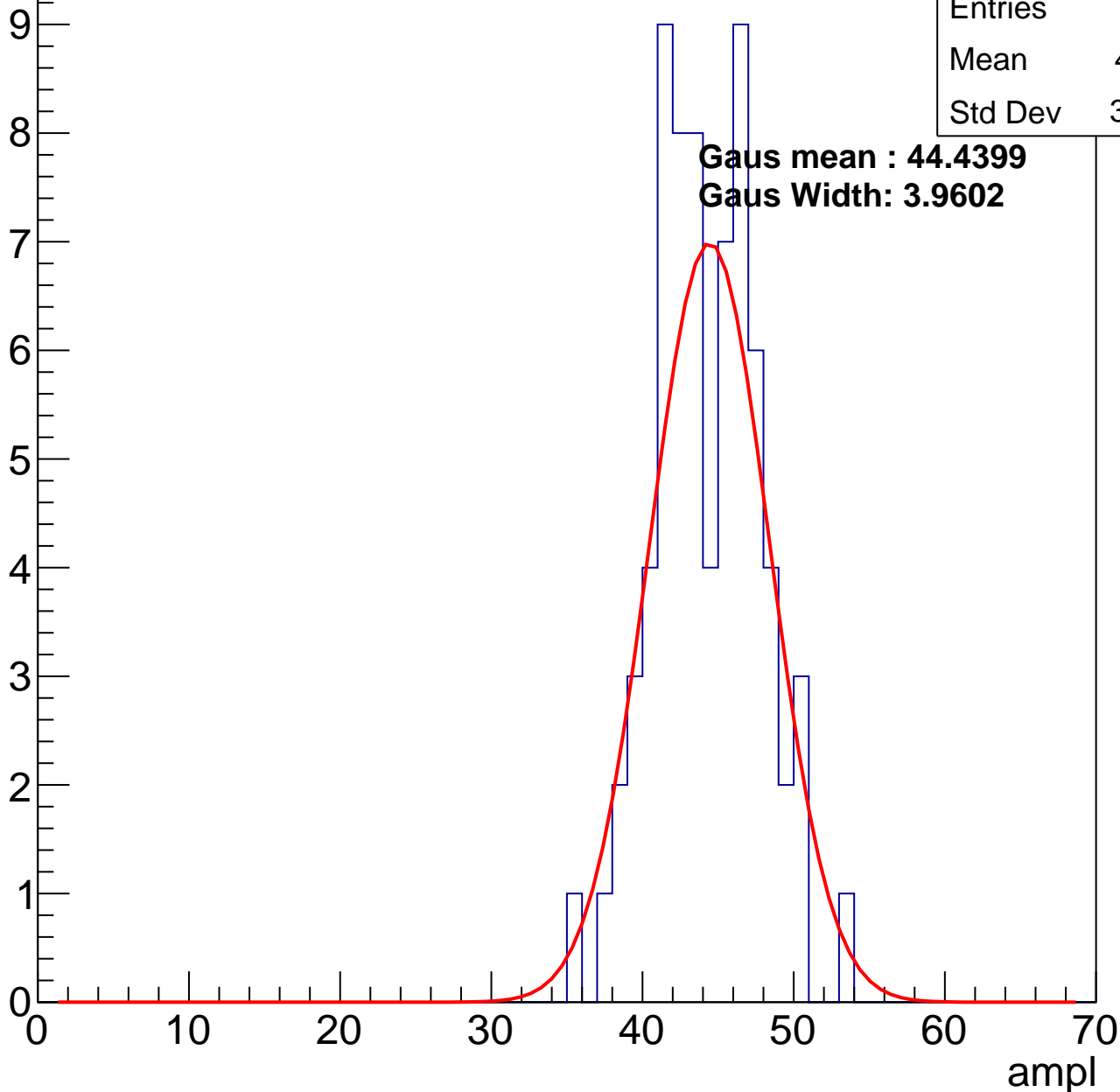
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.81
Std Dev	3.455

Gaus mean : 44.4399

Gaus Width: 3.9602

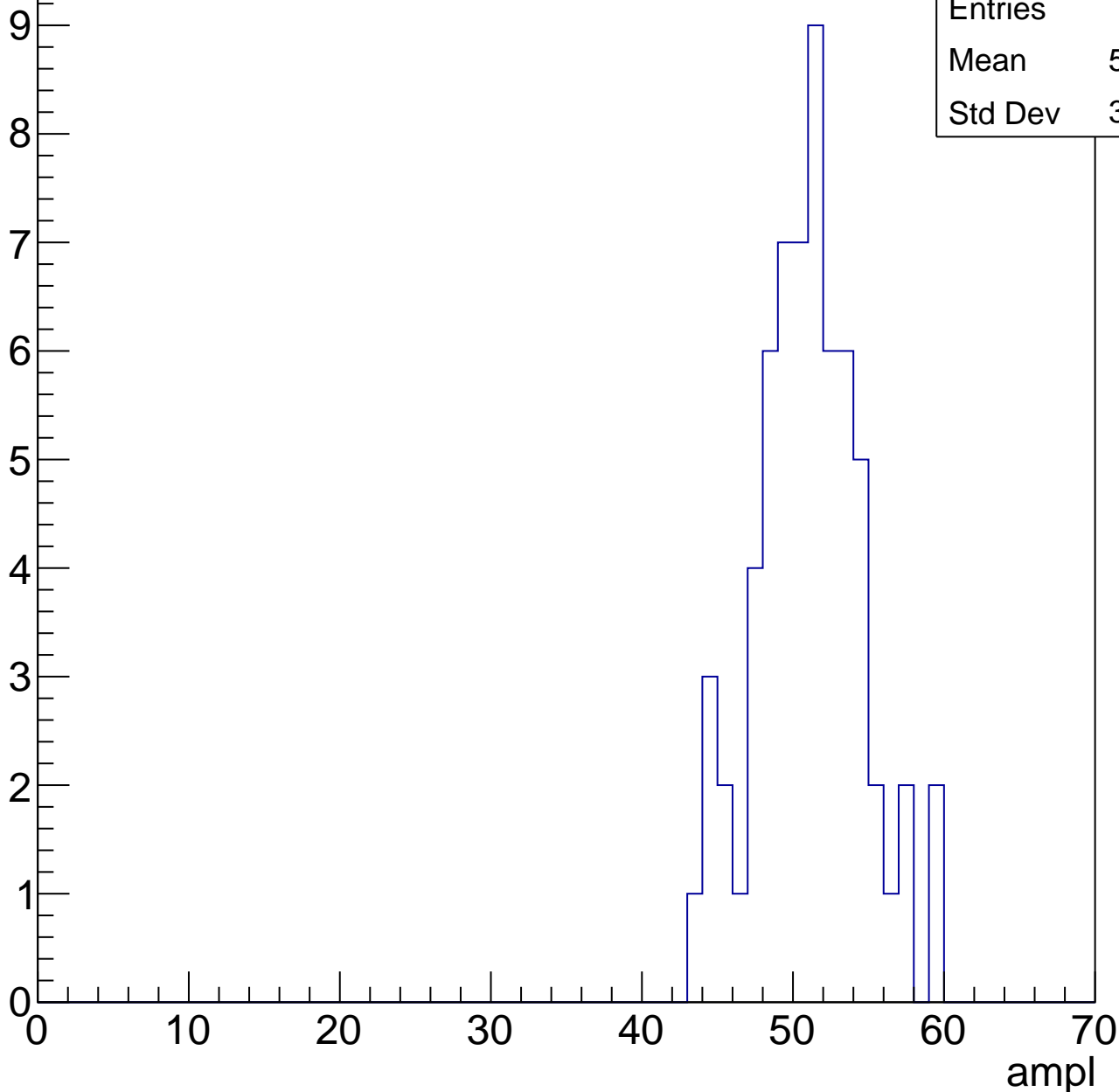


B1L101S, U23-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

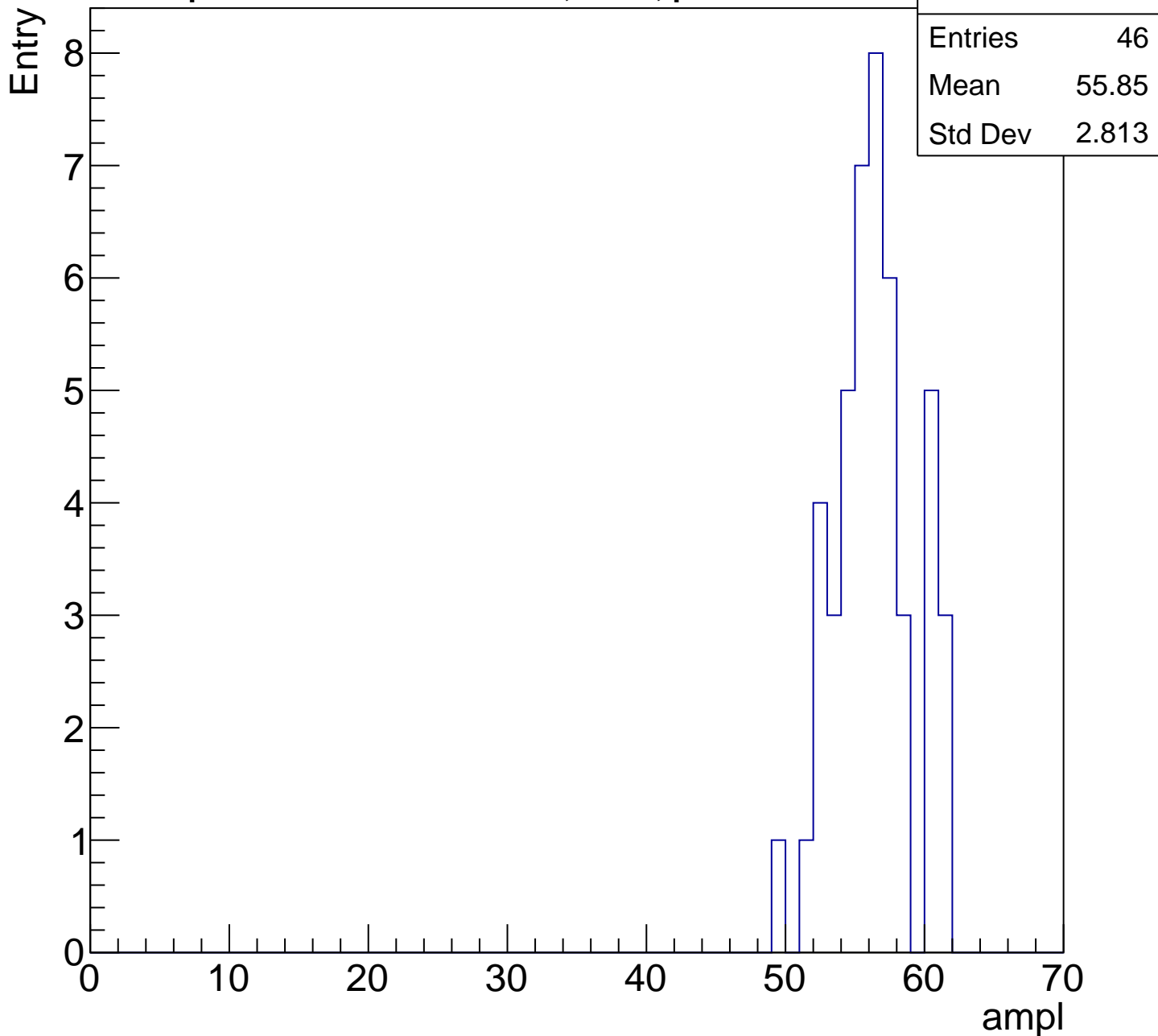
Entry

Entries	64
Mean	50.58
Std Dev	3.495



B1L101S, U23-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

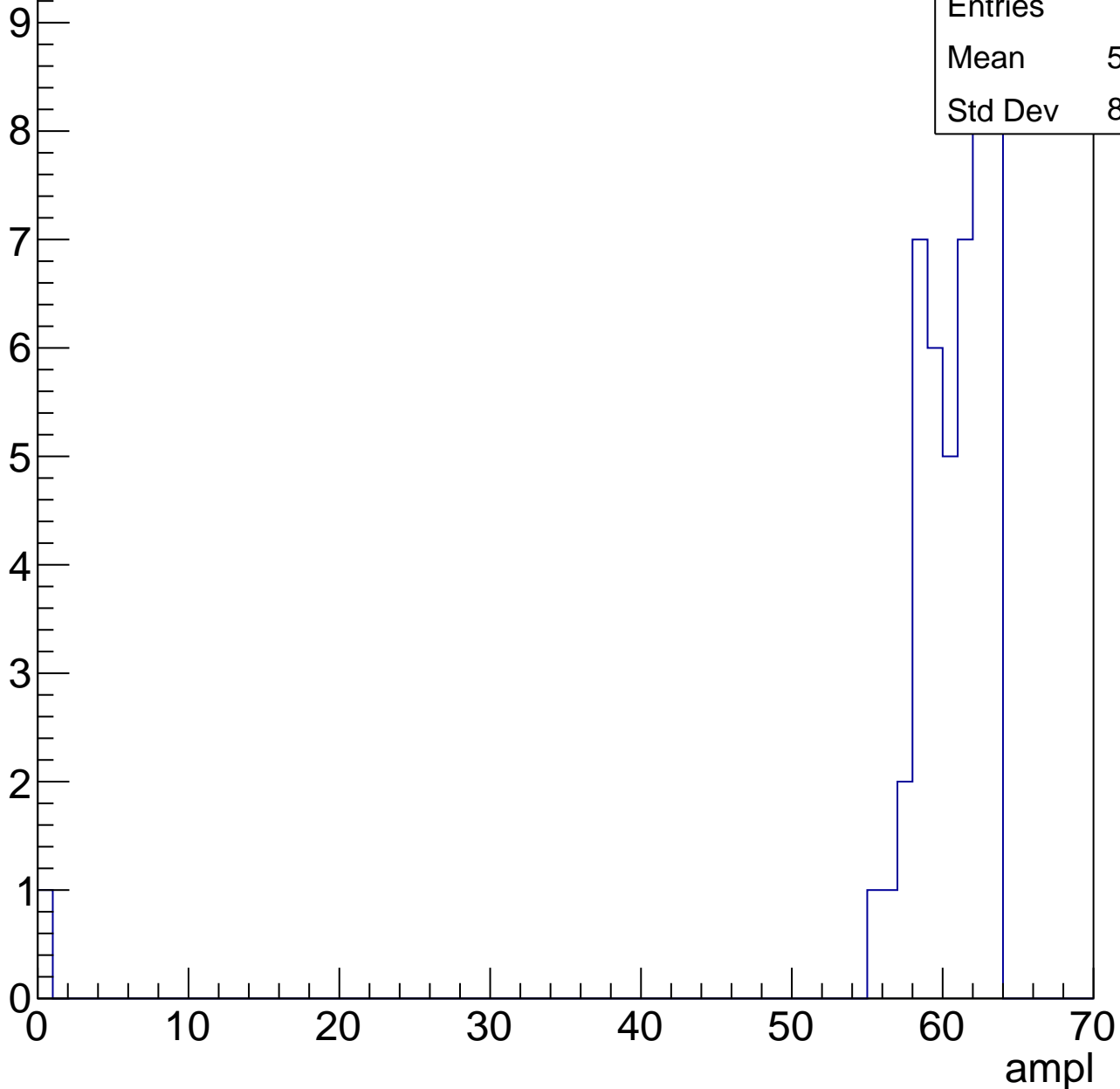


B1L101S, U23-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	59.02
Std Dev	8.948



B1L101S, U23-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	5
Mean	61.2
Std Dev	1.833

B1L101S, U23-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch48, adc0

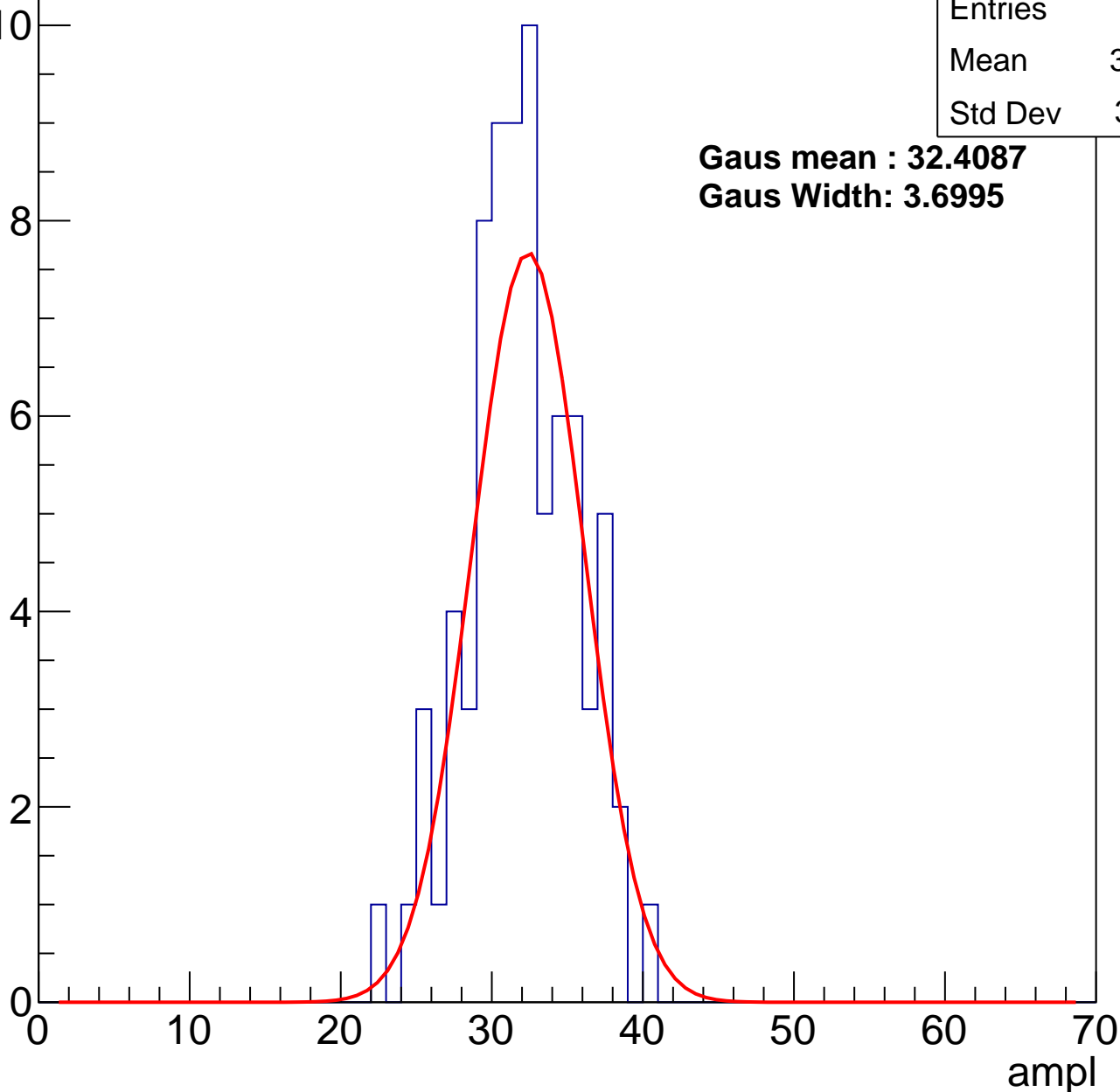
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	31.53
Std Dev	3.581

Gaus mean : 32.4087

Gaus Width: 3.6995

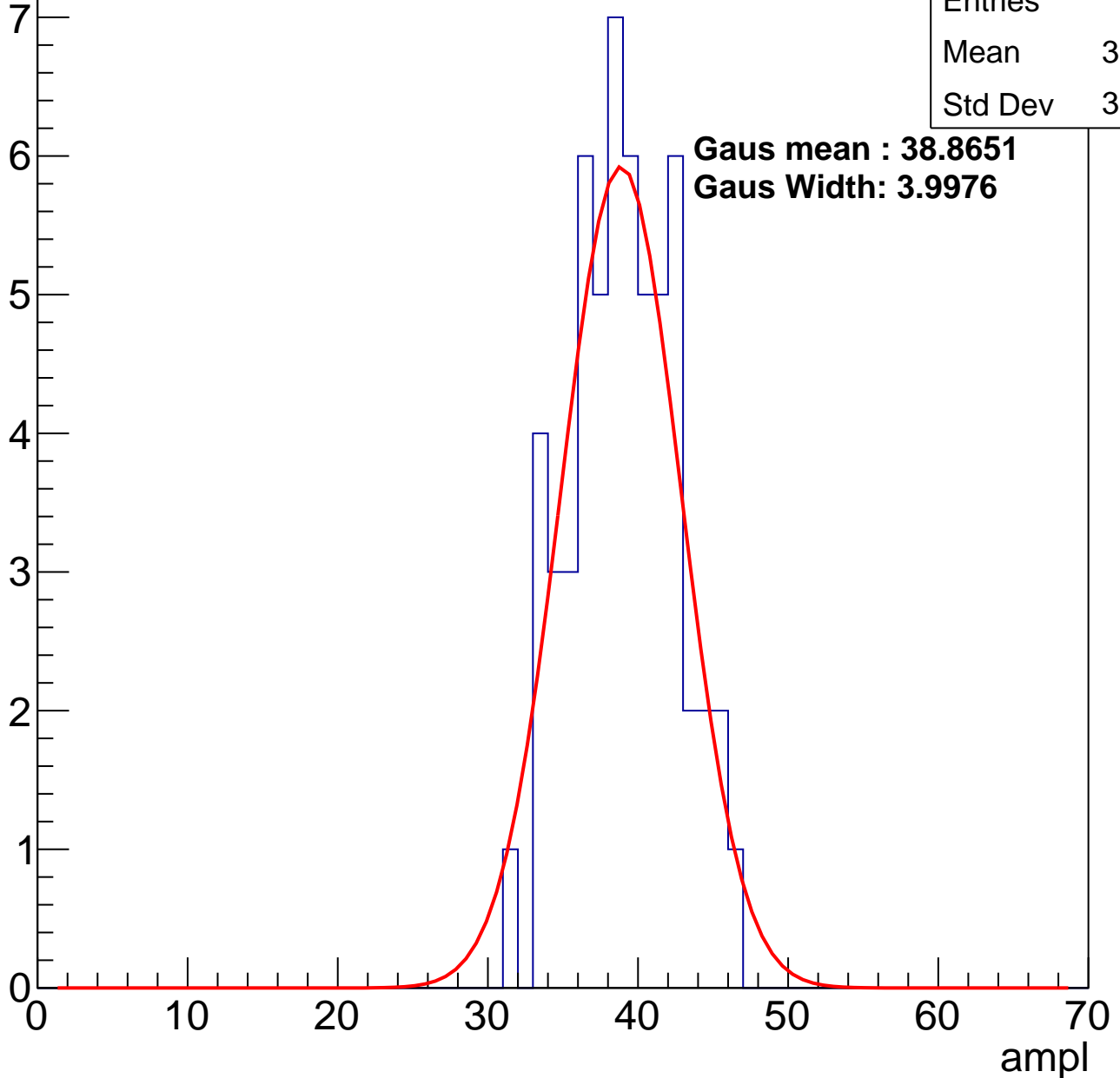


B1L101S, U23-ch48, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	38.59
Std Dev	3.434



B1L101S, U23-ch48, adc2

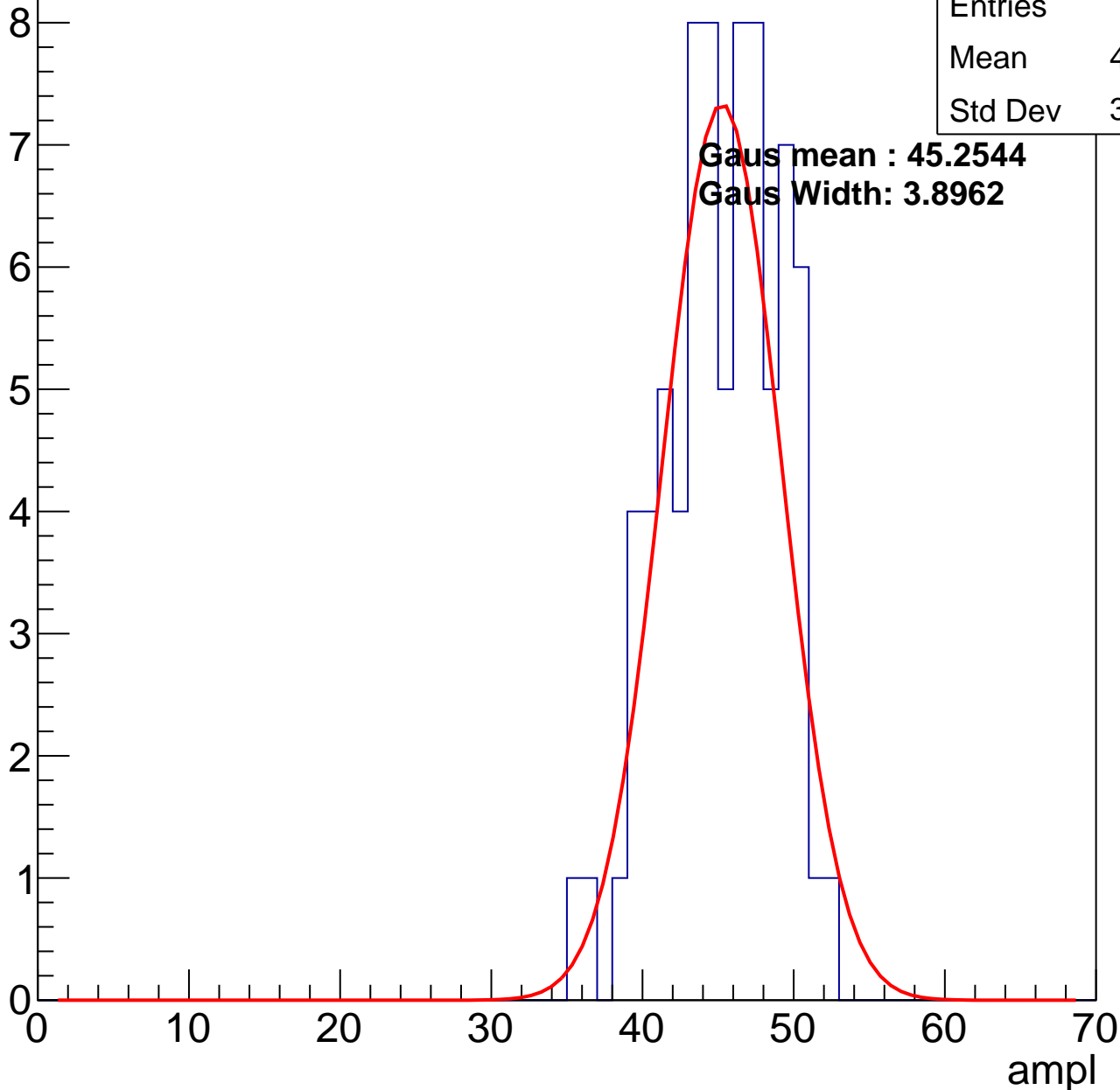
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44.79
Std Dev	3.708

Gaus mean : 45.2544

Gaus Width: 3.8962

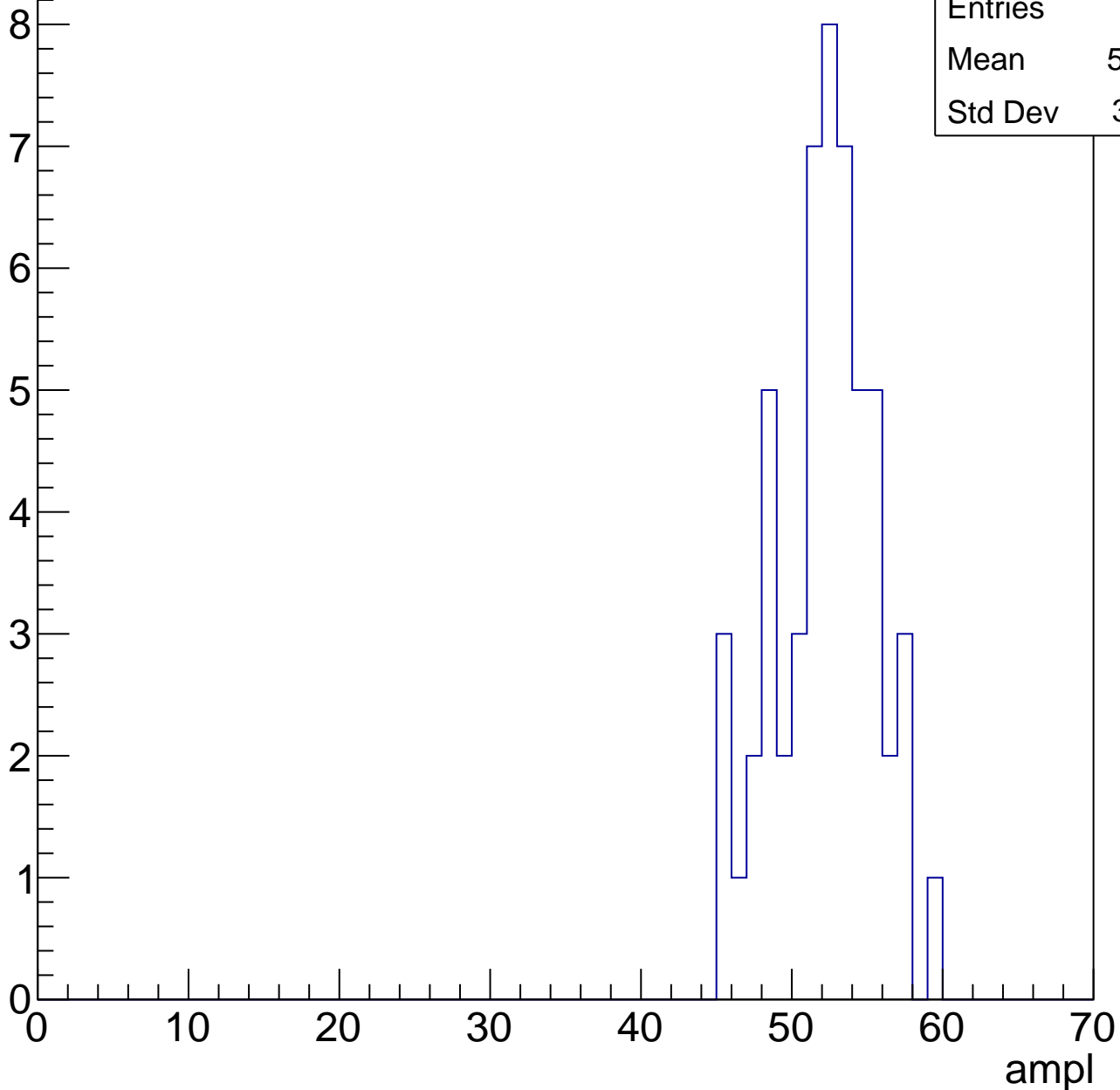


B1L101S, U23-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	51.74
Std Dev	3.261

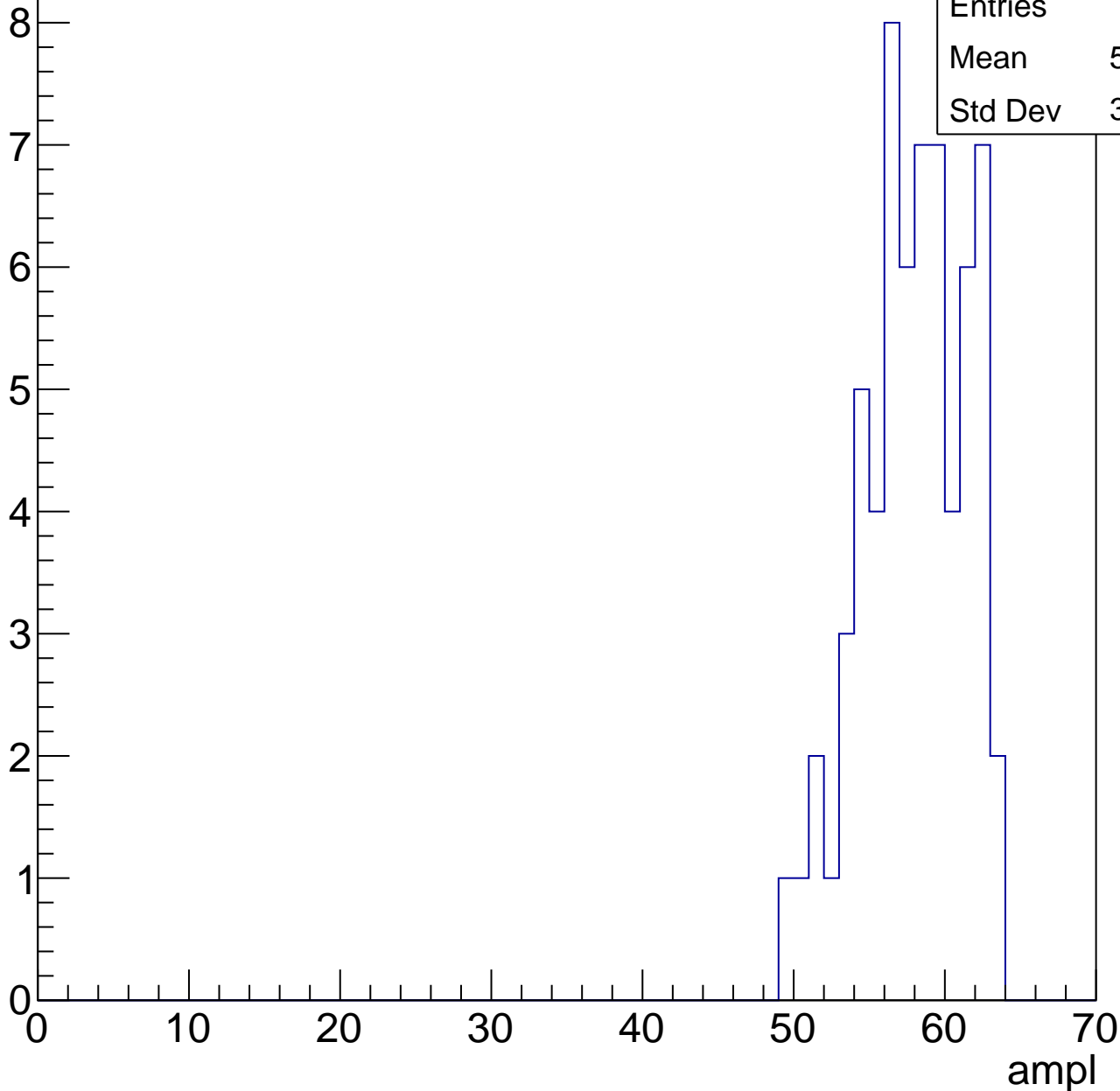


B1L101S, U23-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

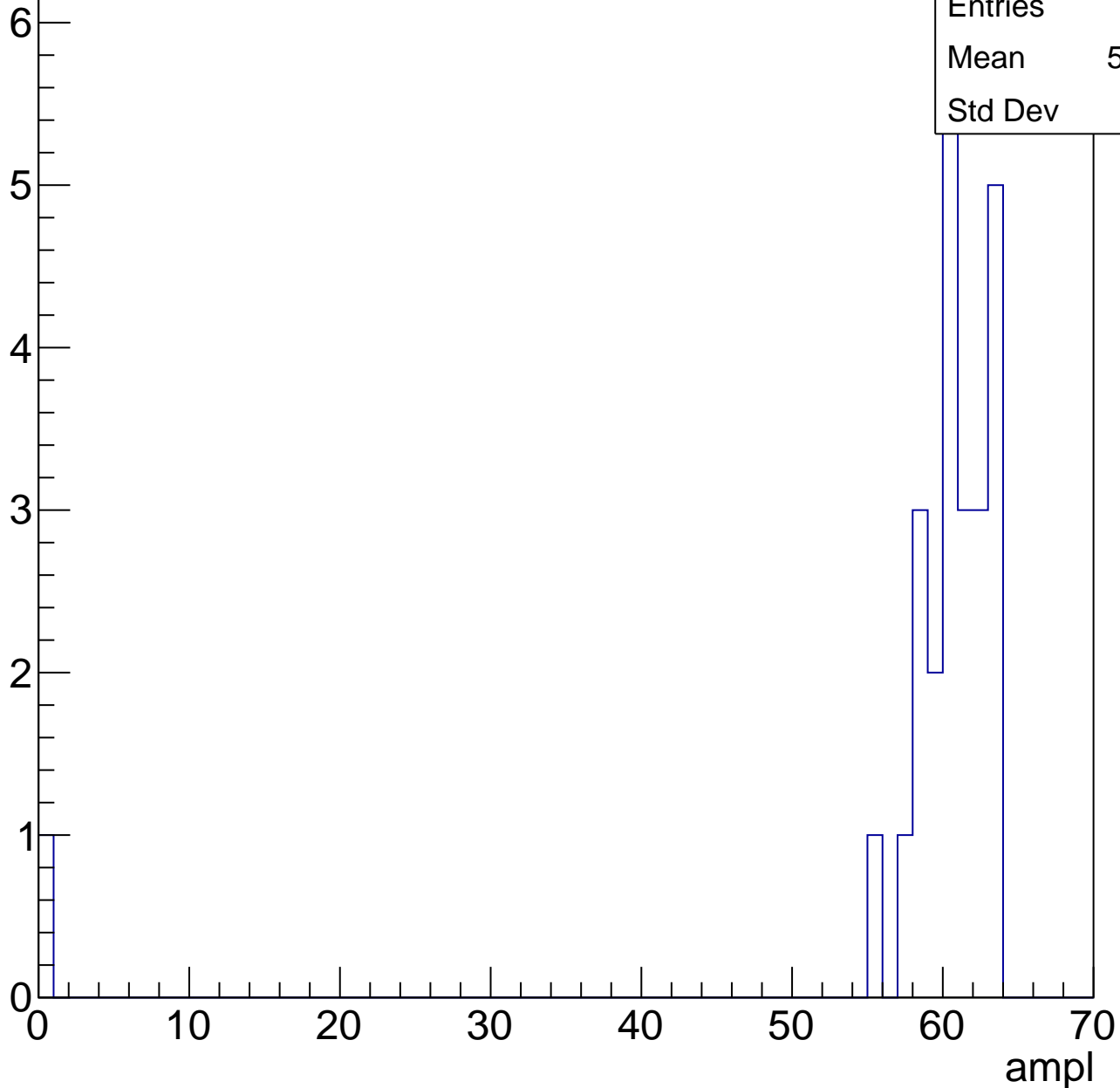
Entries	64
Mean	57.45
Std Dev	3.372



B1L101S, U23-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U23-ch49, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	30.47
Std Dev	3.363

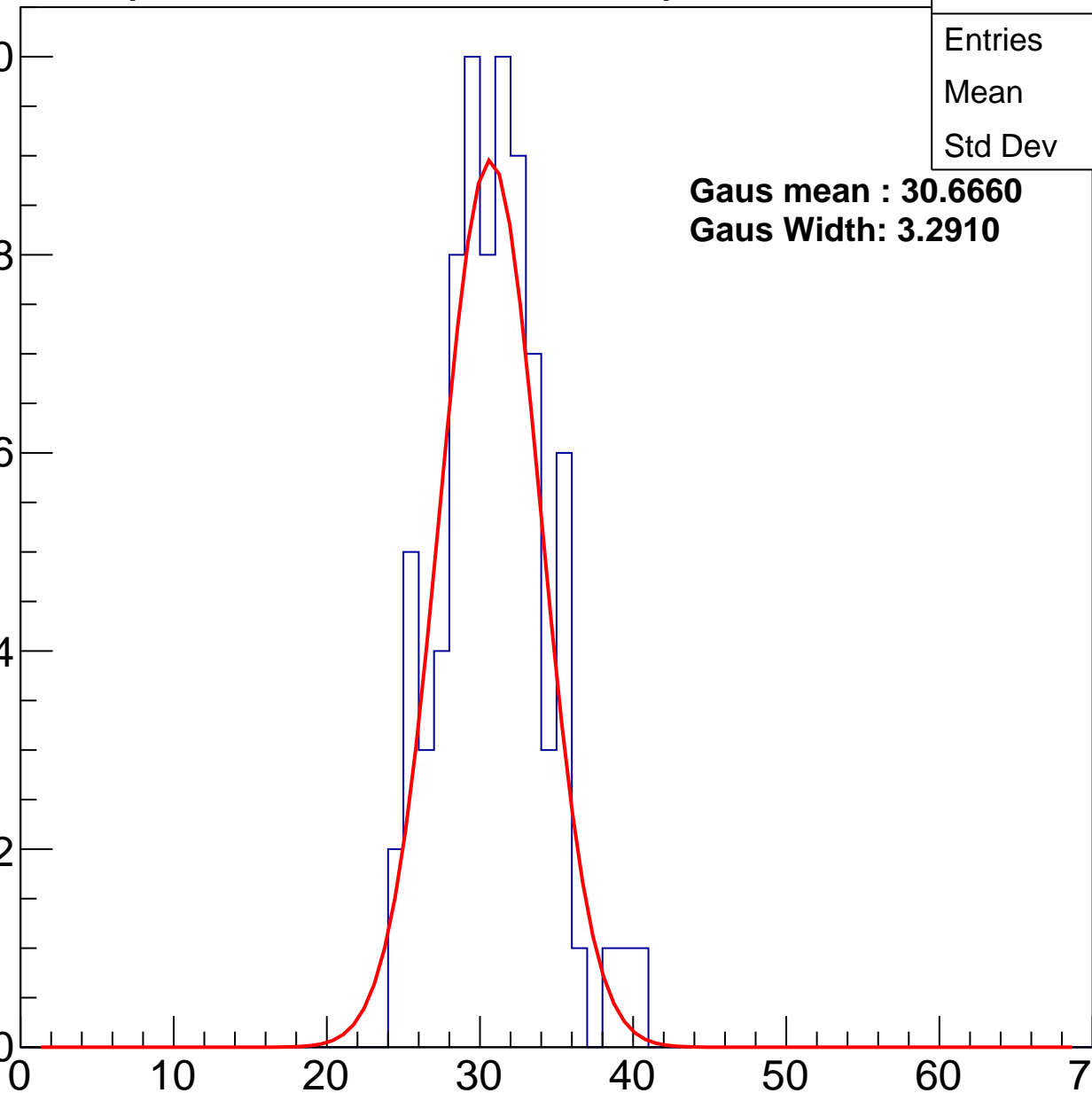
Gaus mean : 30.6660

Gaus Width: 3.2910

Entry

10
8
6
4
2
0

ampl



B1L101S, U23-ch49, adc1

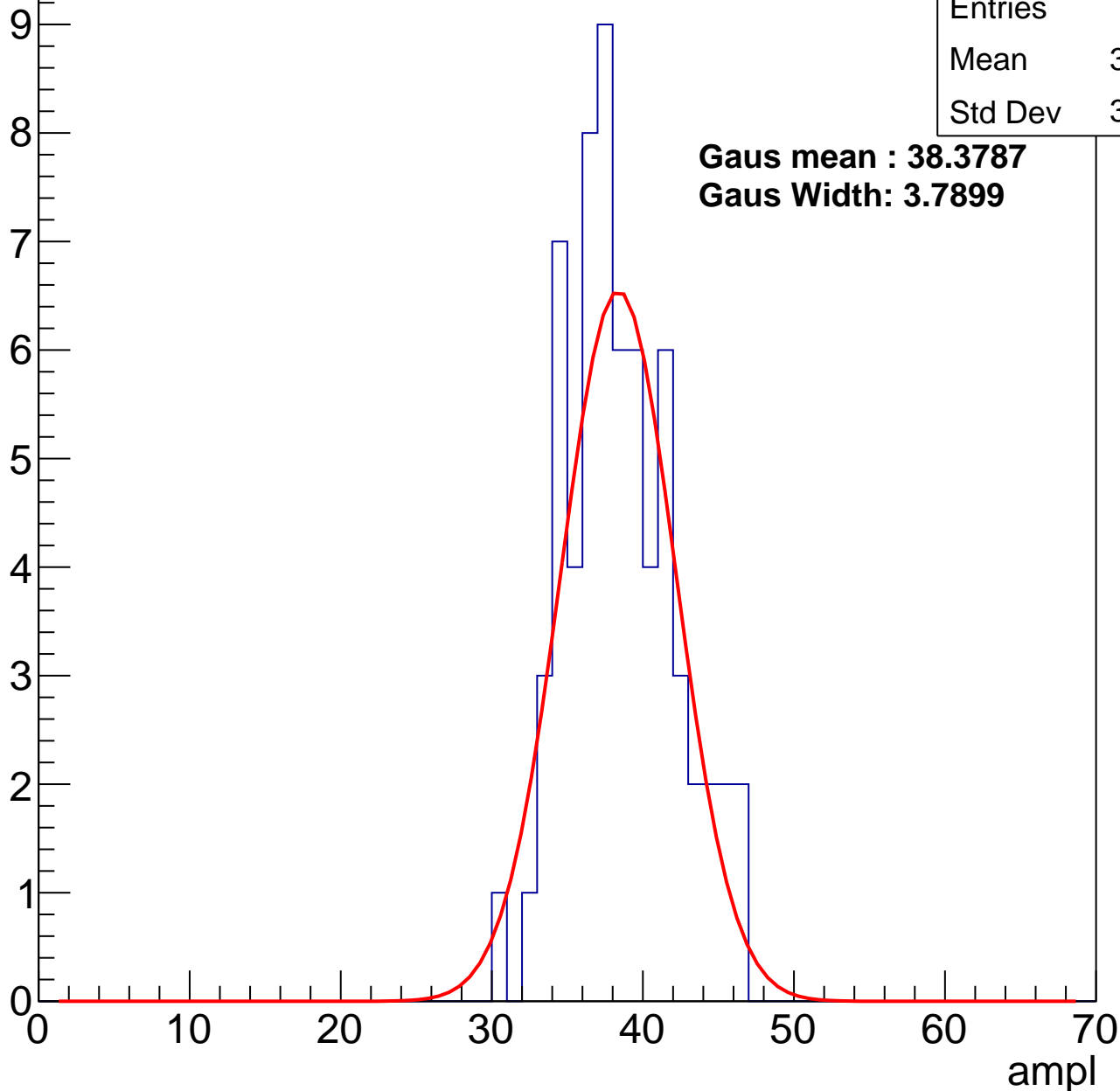
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	38.03
Std Dev	3.559

Gaus mean : 38.3787

Gaus Width: 3.7899



B1L101S, U23-ch49, adc2

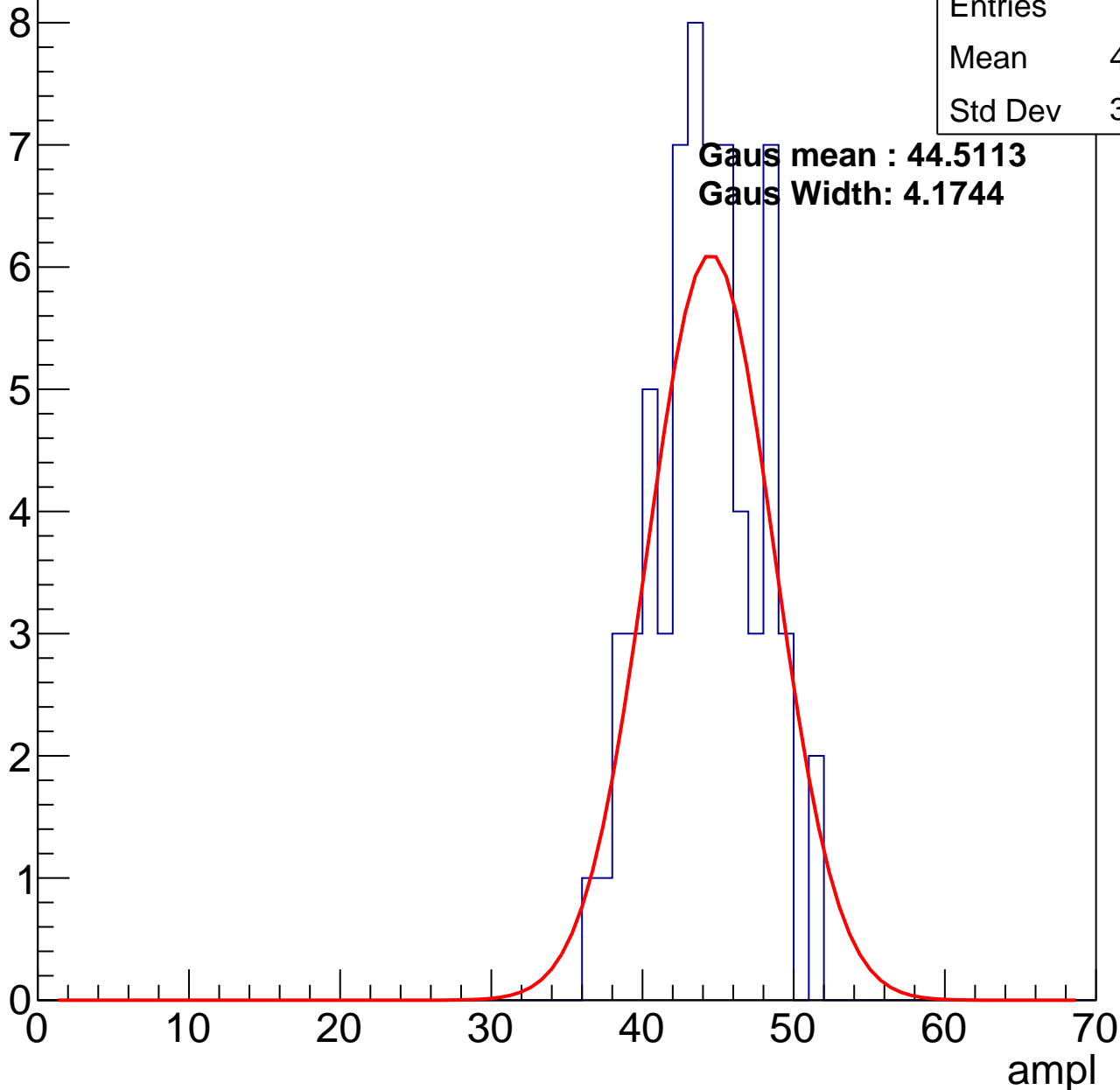
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.72
Std Dev	3.466

Gaus mean : 44.5113

Gaus Width: 4.1744



B1L101S, U23-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	50.91
Std Dev	3.449

Entry

10

8

6

4

2

0

0

10

20

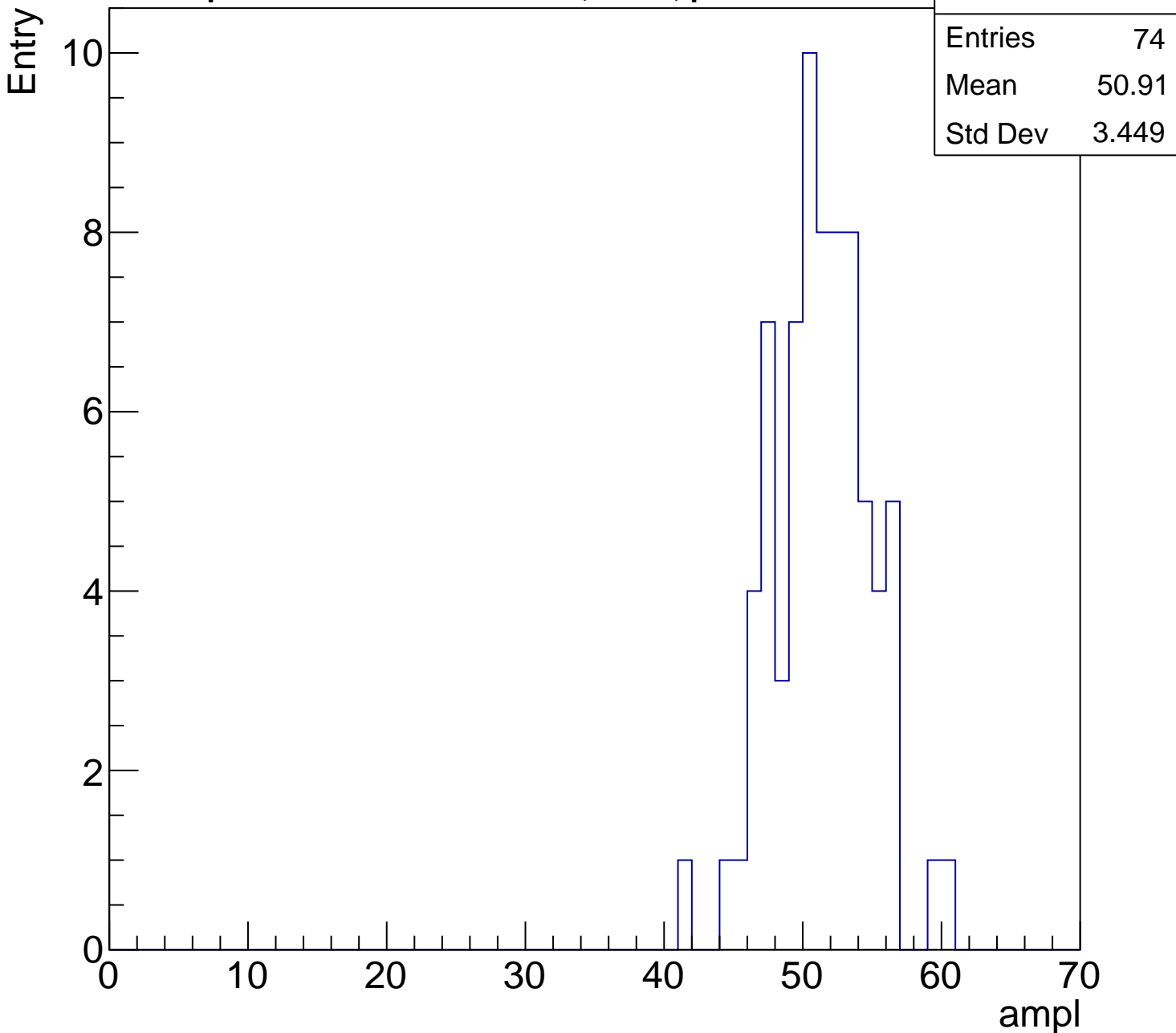
30

40

50

60

ampl

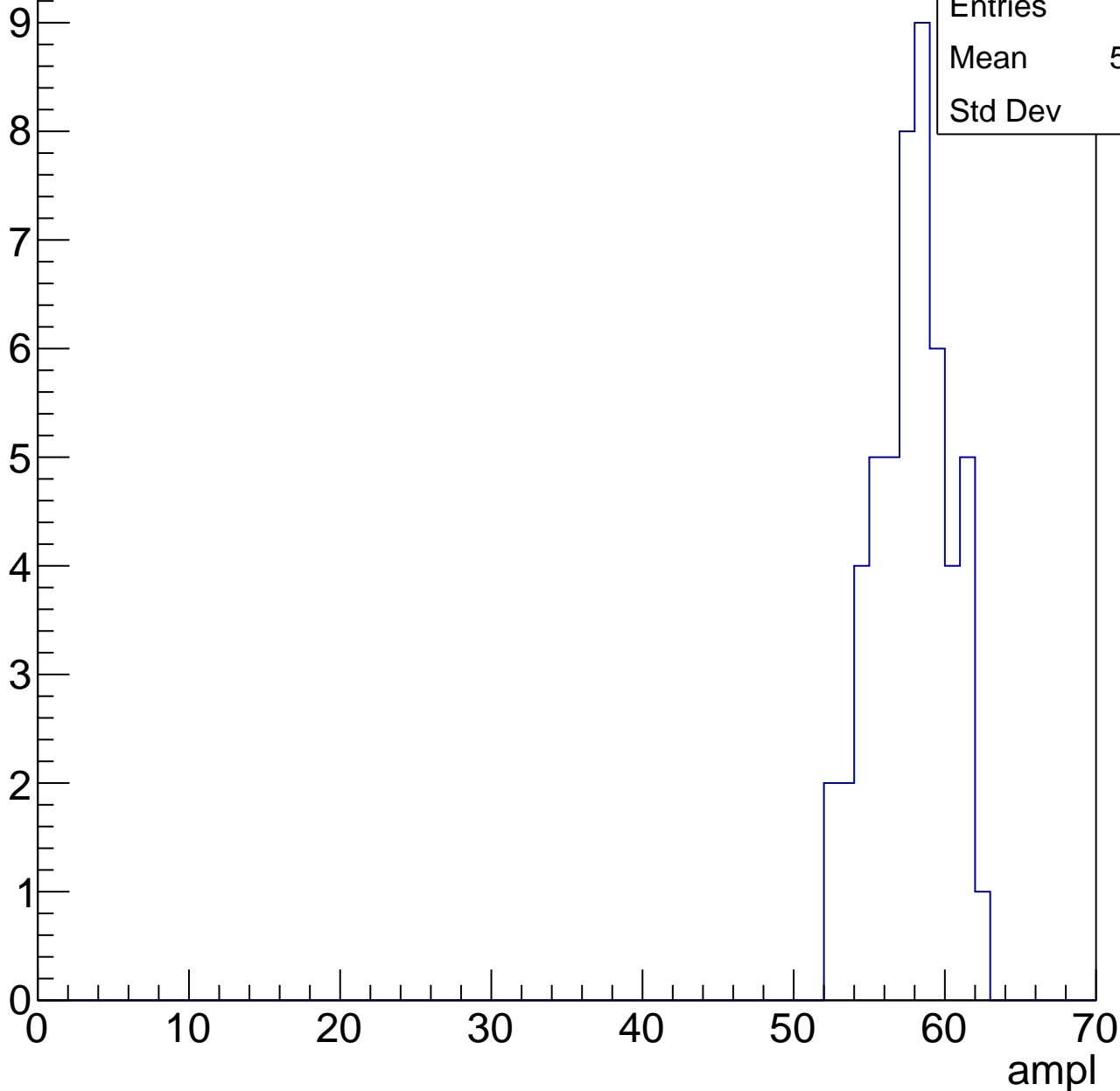


B1L101S, U23-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.25
Std Dev	2.48

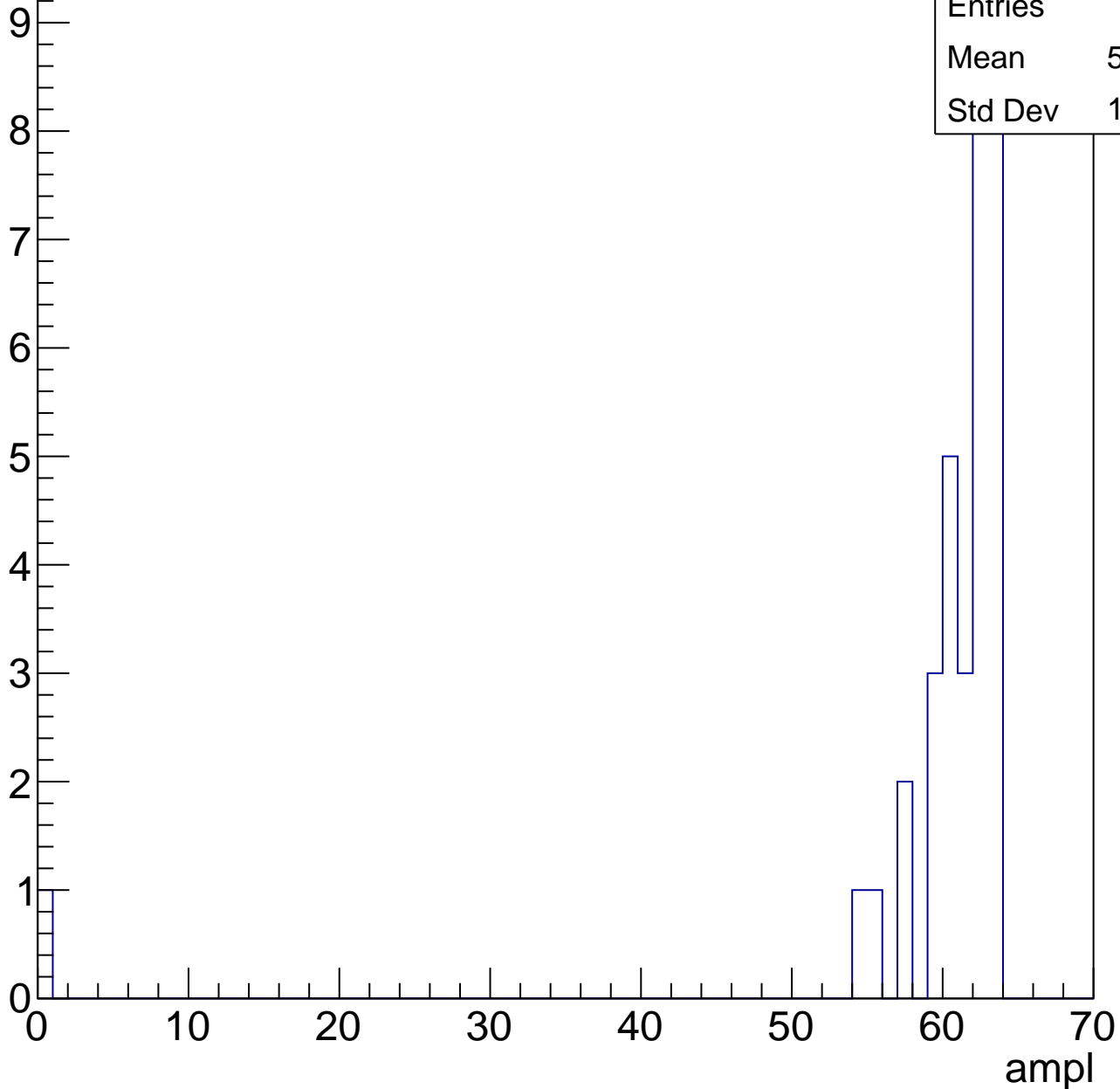


B1L101S, U23-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

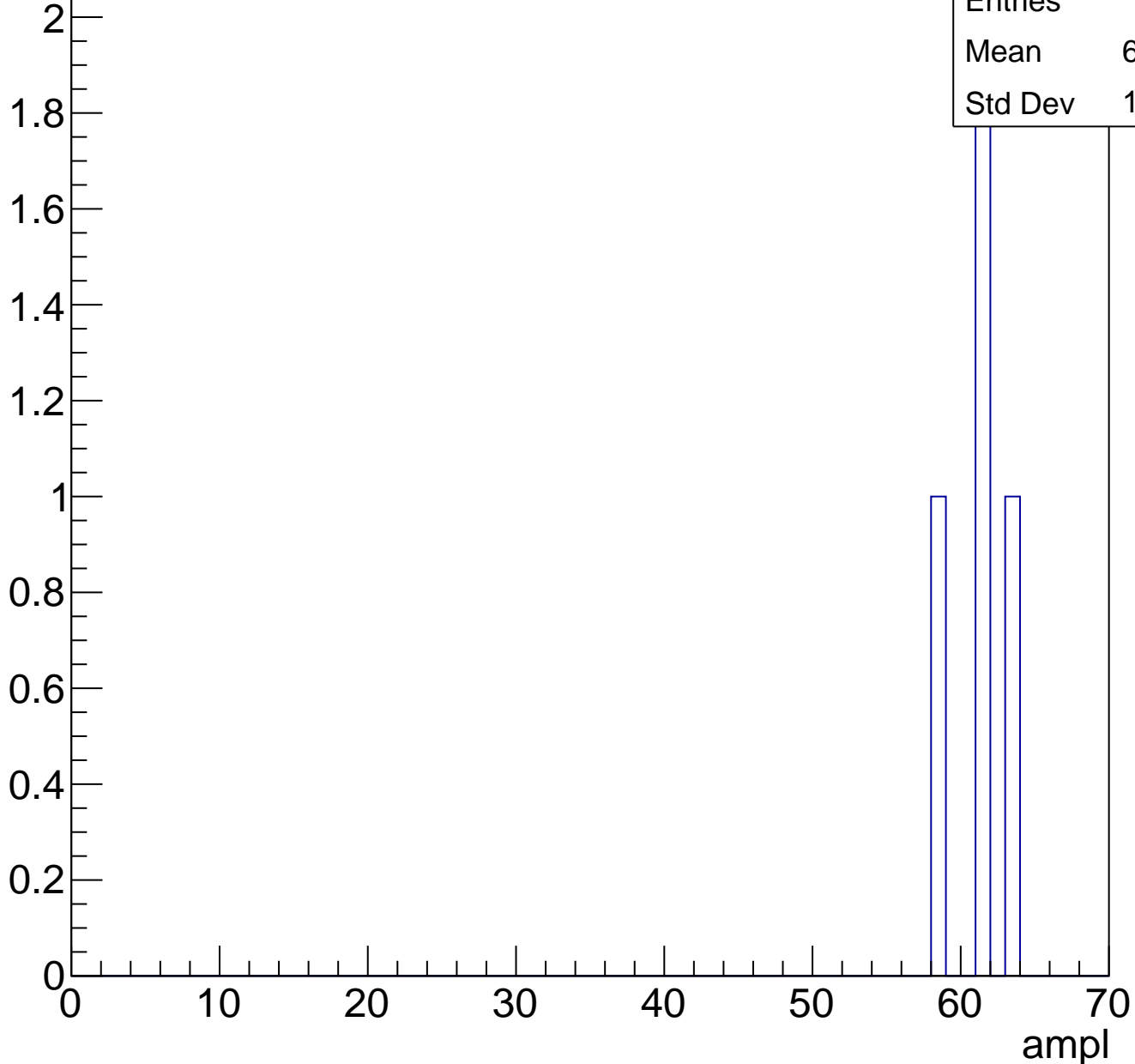
Entries	34
Mean	59.06
Std Dev	10.53



B1L101S, U23-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch50, adc0

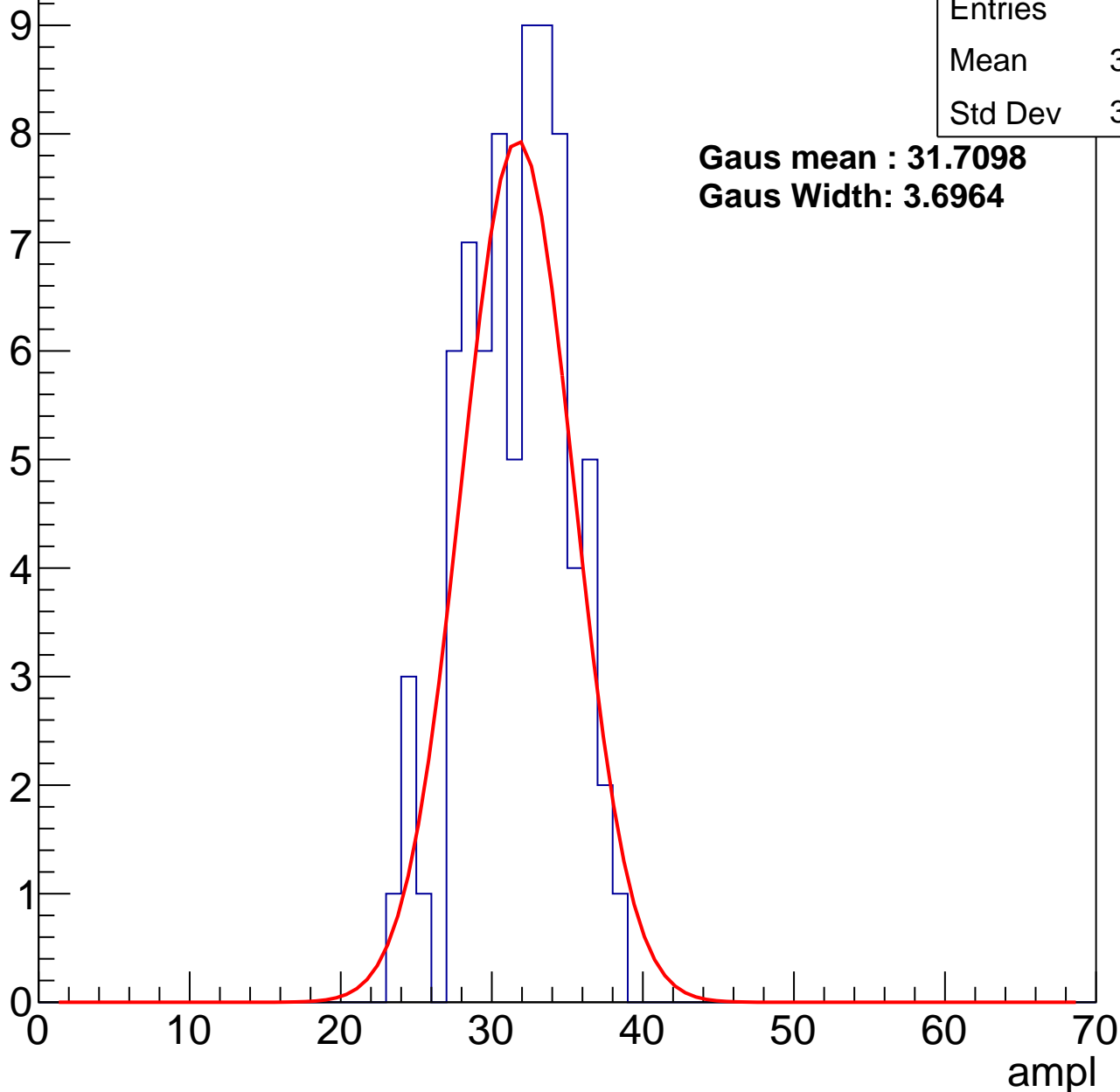
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	31.15
Std Dev	3.393

Gaus mean : 31.7098

Gaus Width: 3.6964



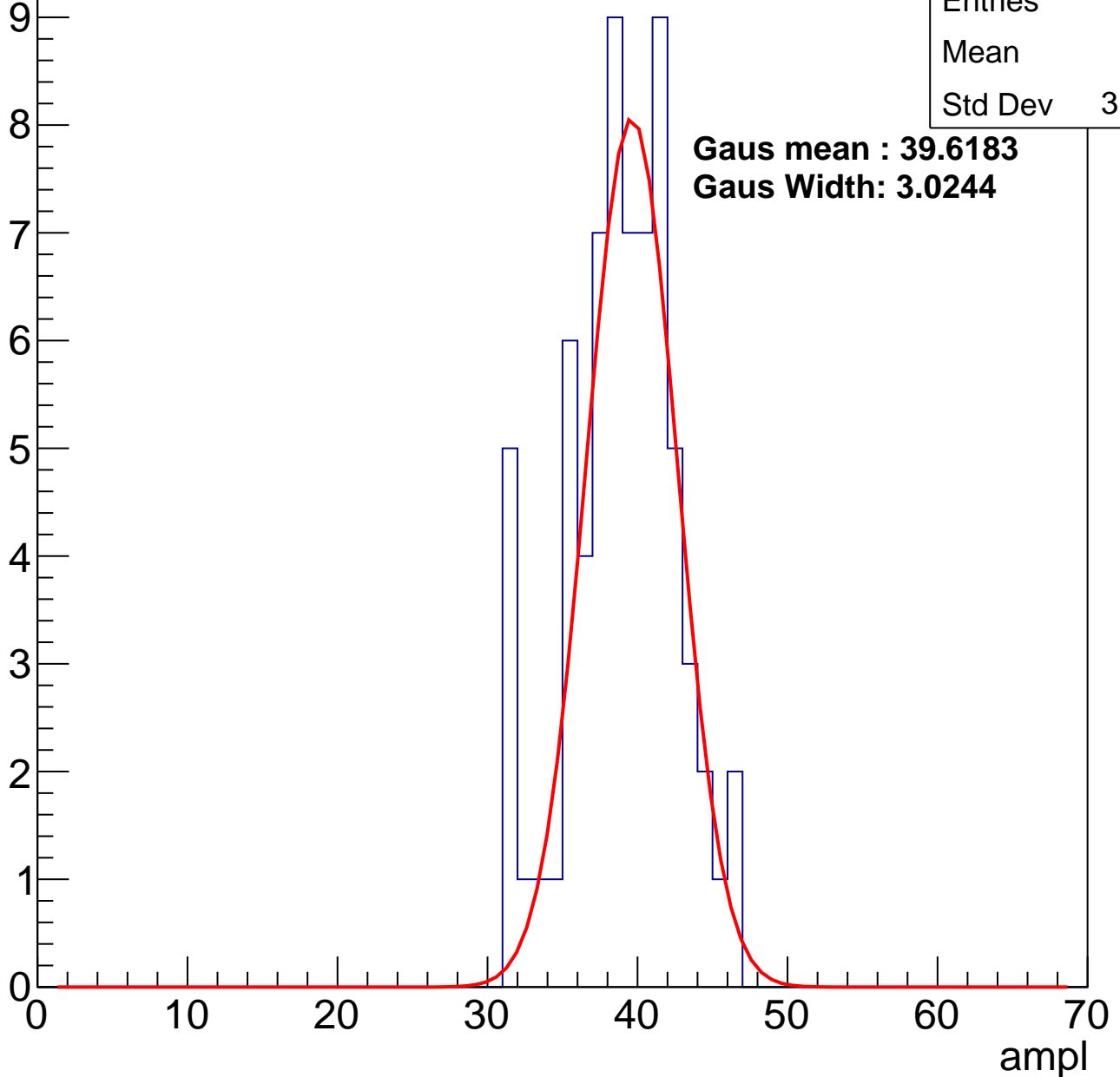
B1L101S, U23-ch50, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	38.5
Std Dev	3.589

Gaus mean : 39.6183
Gaus Width: 3.0244



B1L101S, U23-ch50, adc2

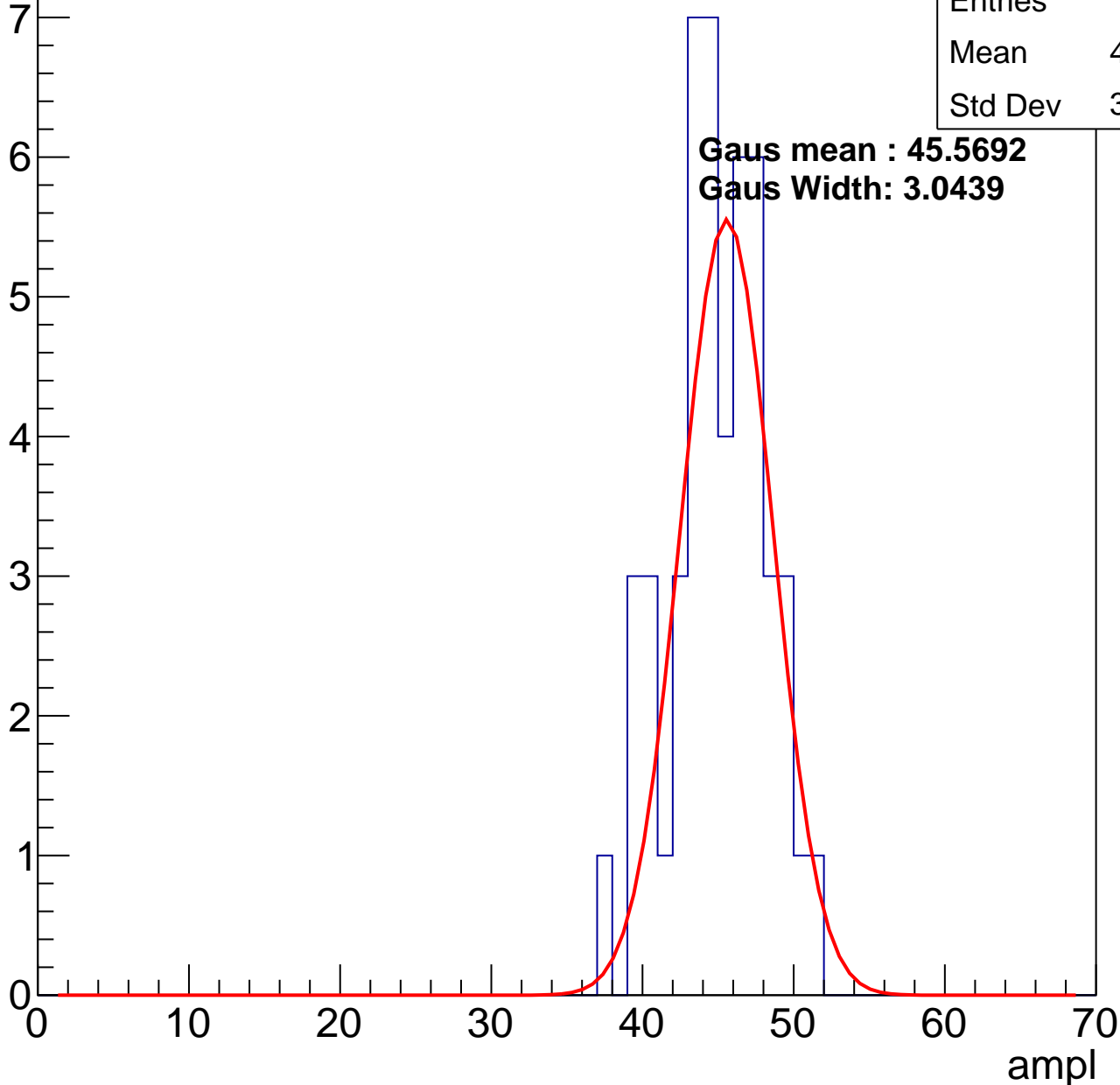
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	44.49
Std Dev	3.124

Gaus mean : 45.5692

Gaus Width: 3.0439

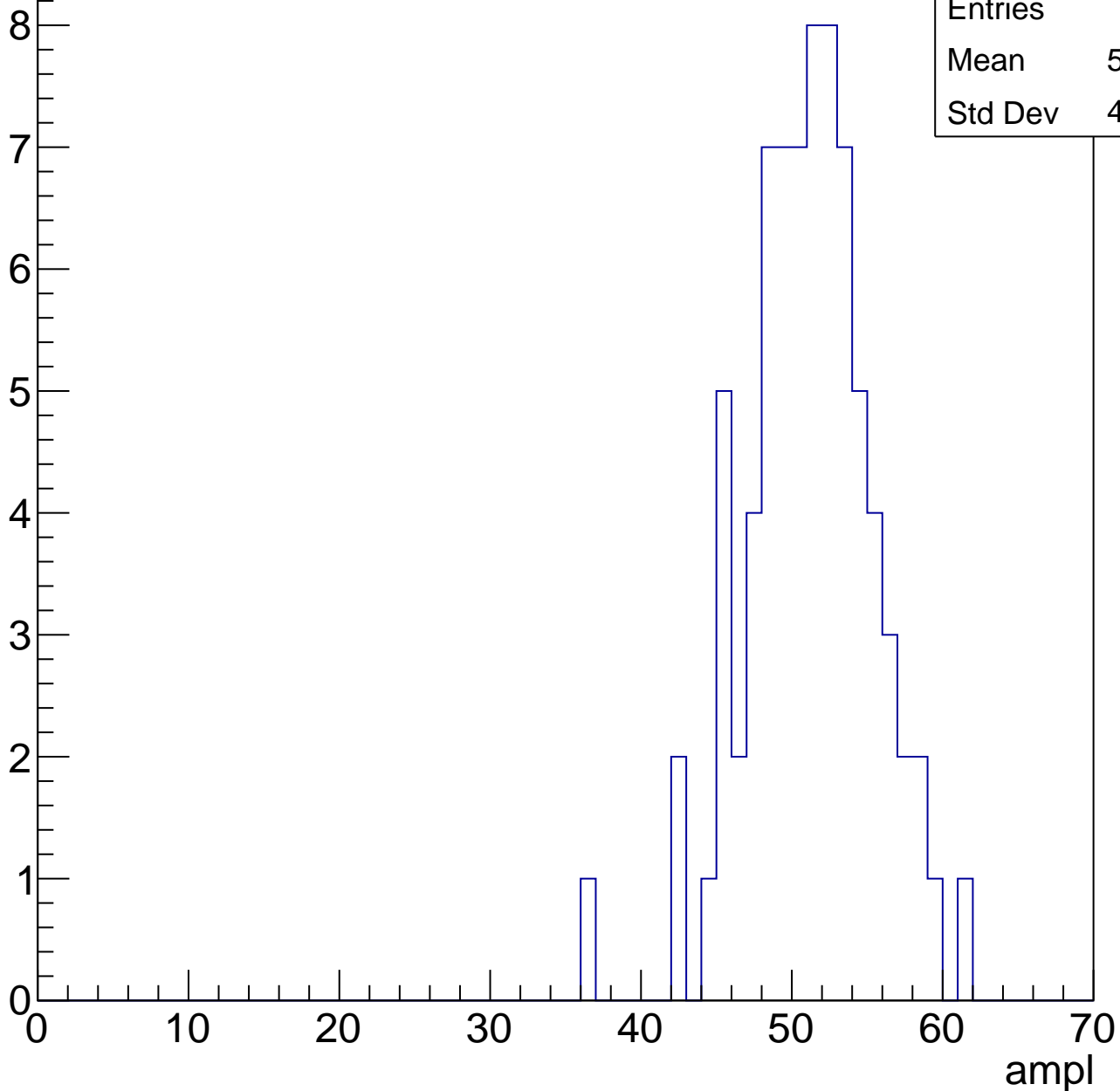


B1L101S, U23-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	50.66
Std Dev	4.223

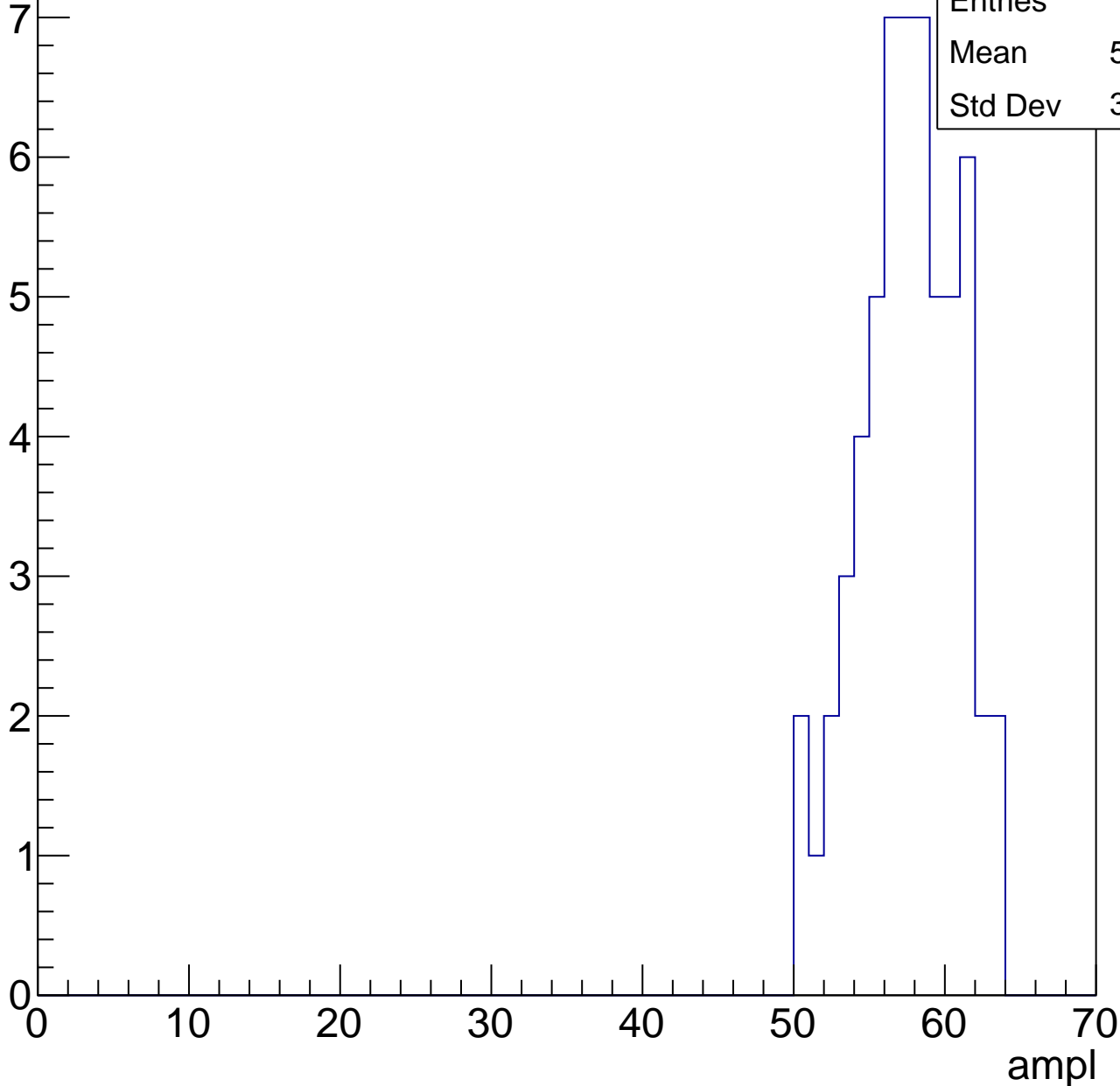


B1L101S, U23-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	57.12
Std Dev	3.174

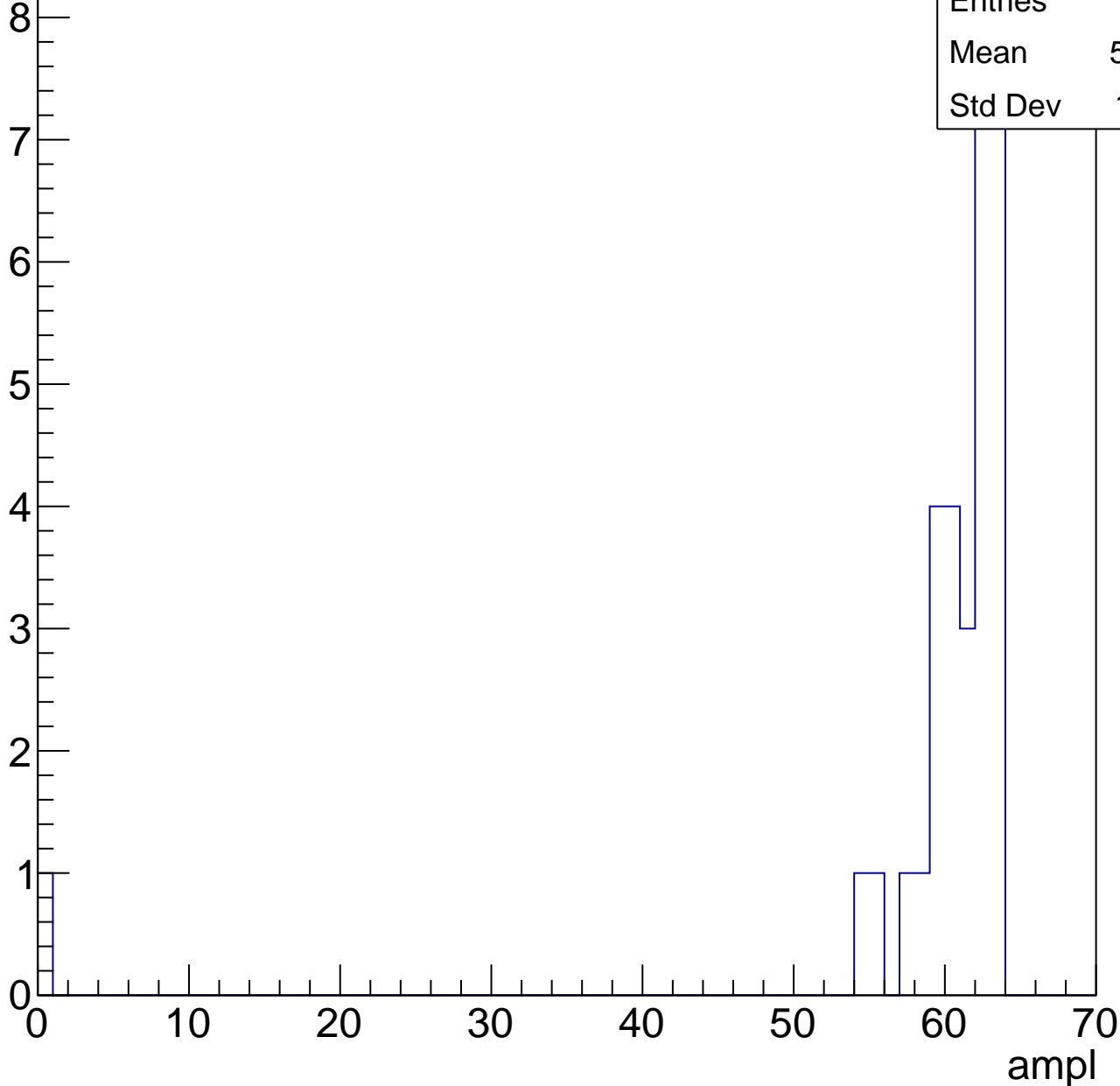


B1L101S, U23-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.84
Std Dev	10.81



B1L101S, U23-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U23-ch51, adc0

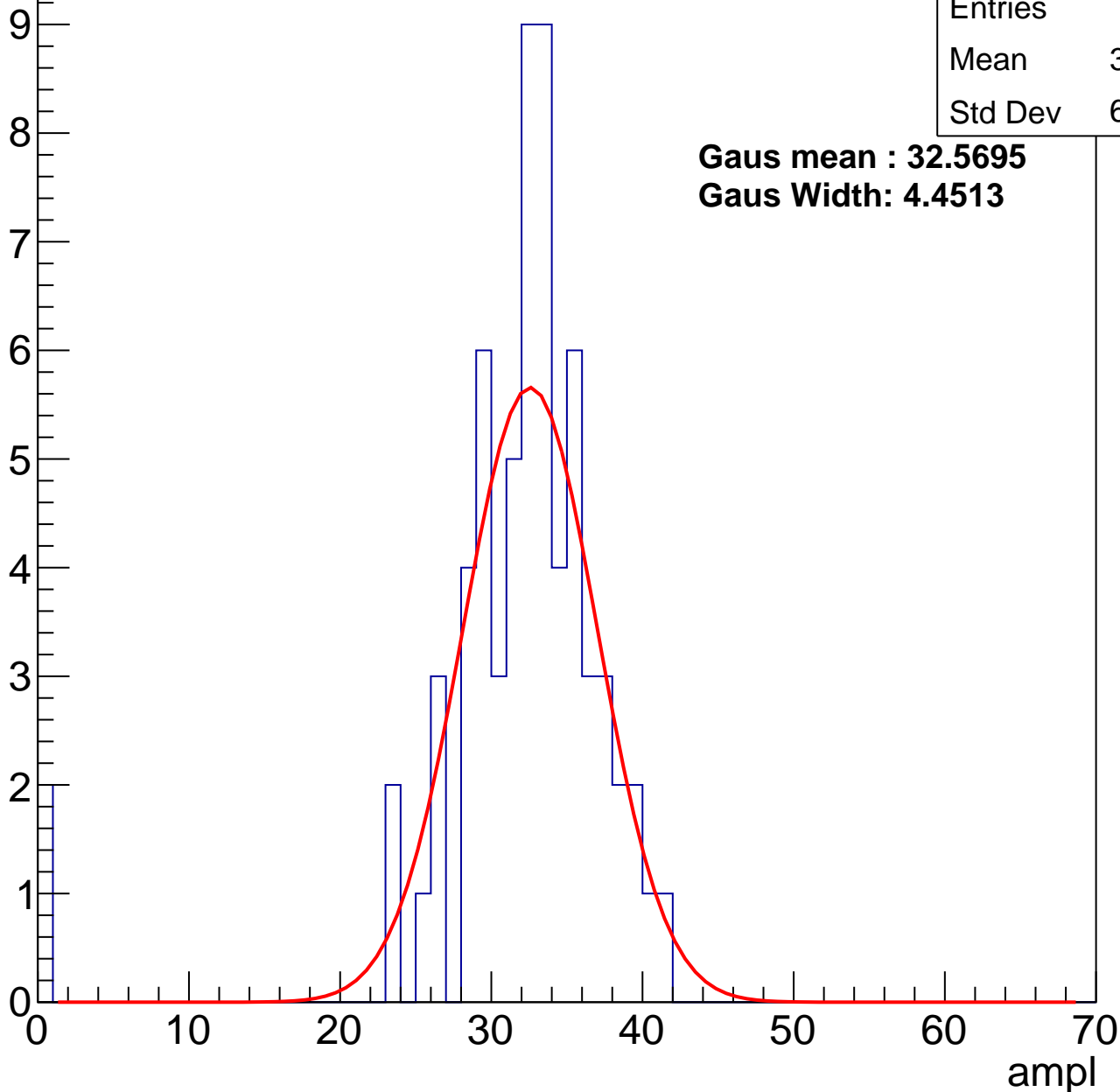
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	31.29
Std Dev	6.726

Gaus mean : 32.5695

Gaus Width: 4.4513



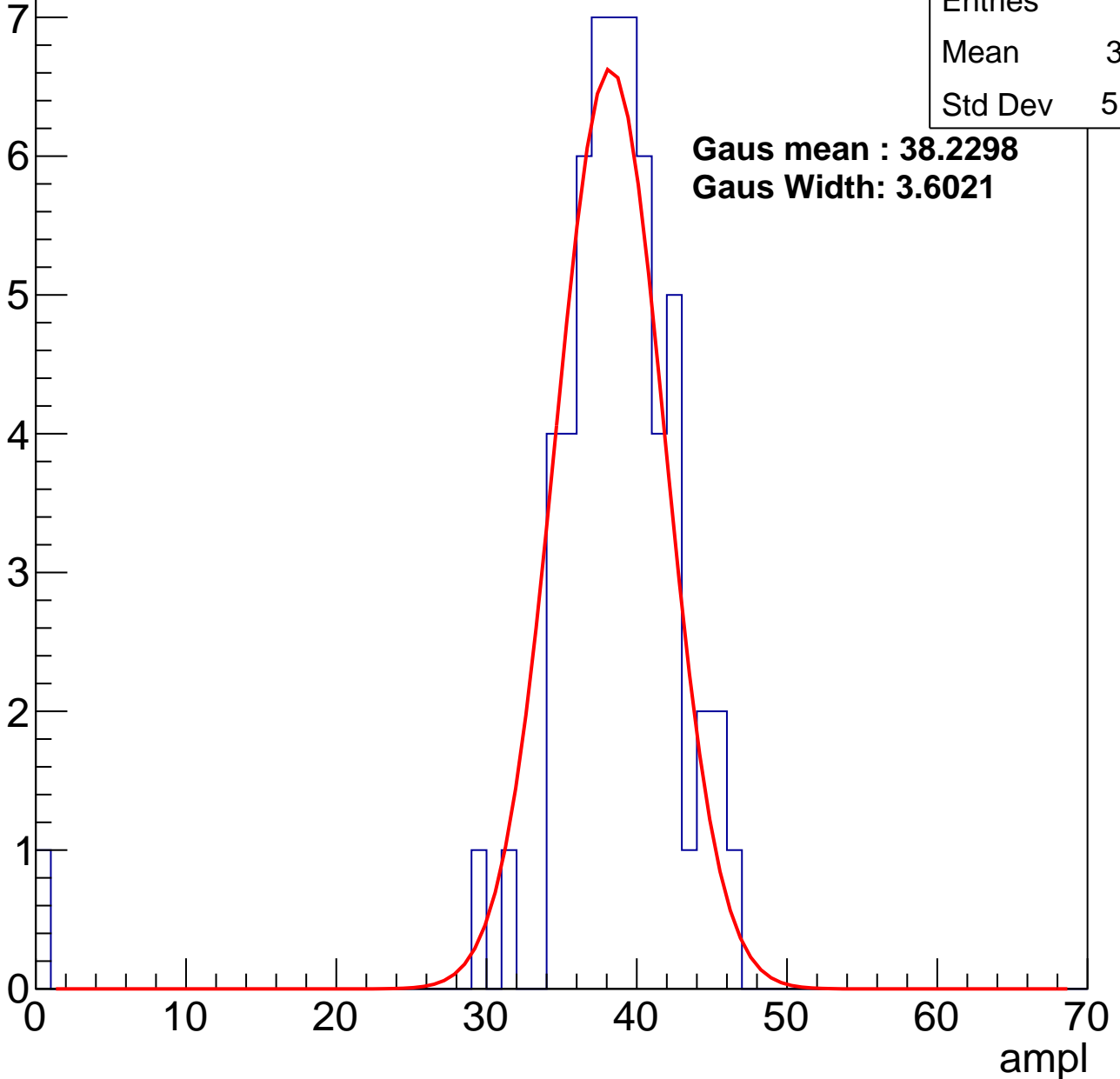
B1L101S, U23-ch51, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.81
Std Dev	5.979

Gaus mean : 38.2298
Gaus Width: 3.6021



B1L101S, U23-ch51, adc2

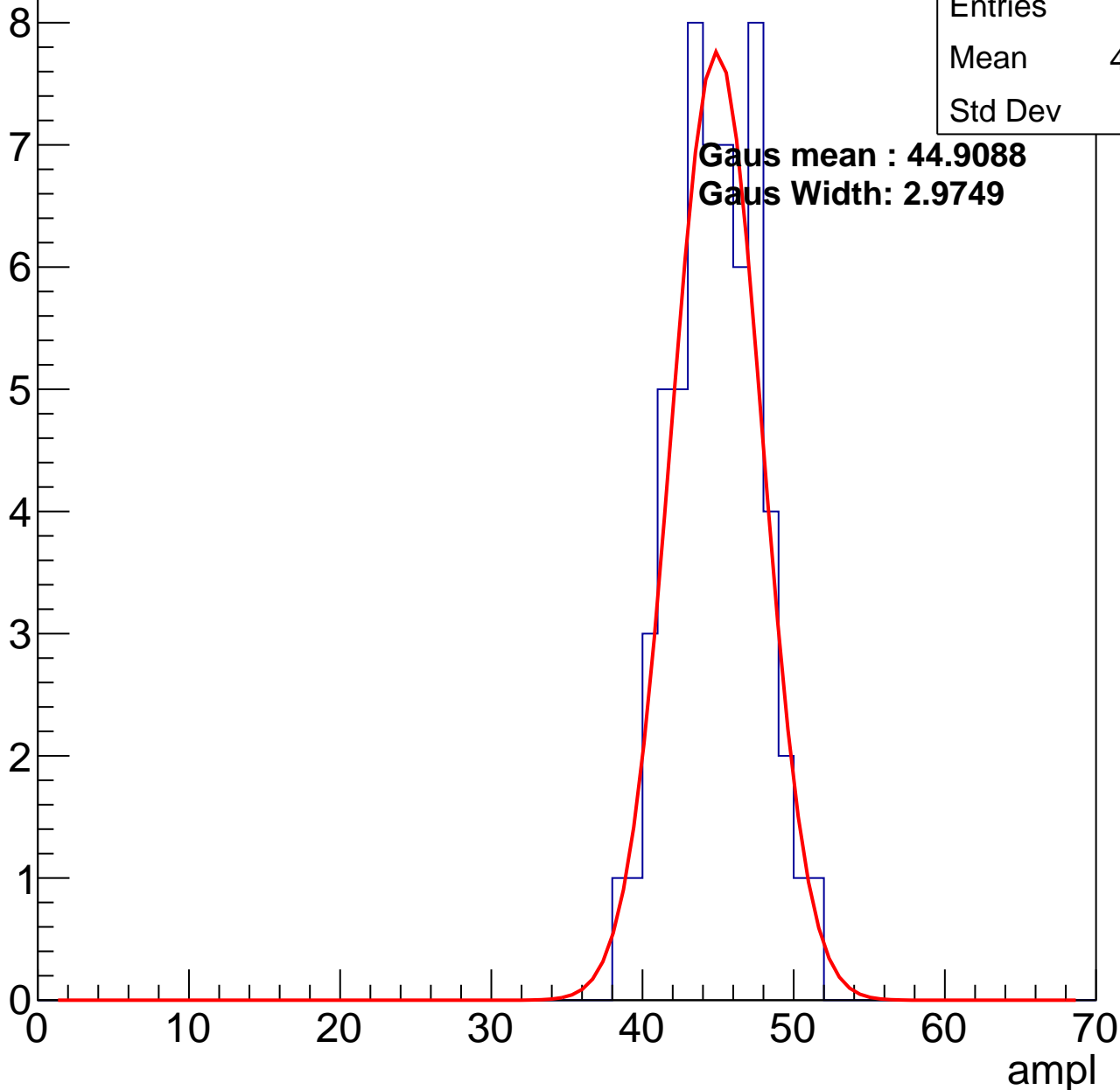
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.44
Std Dev	2.83

Gaus mean : 44.9088

Gaus Width: 2.9749



B1L101S, U23-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	51.73
Std Dev	3.637

Entry

10

8

6

4

2

0

0

10

20

30

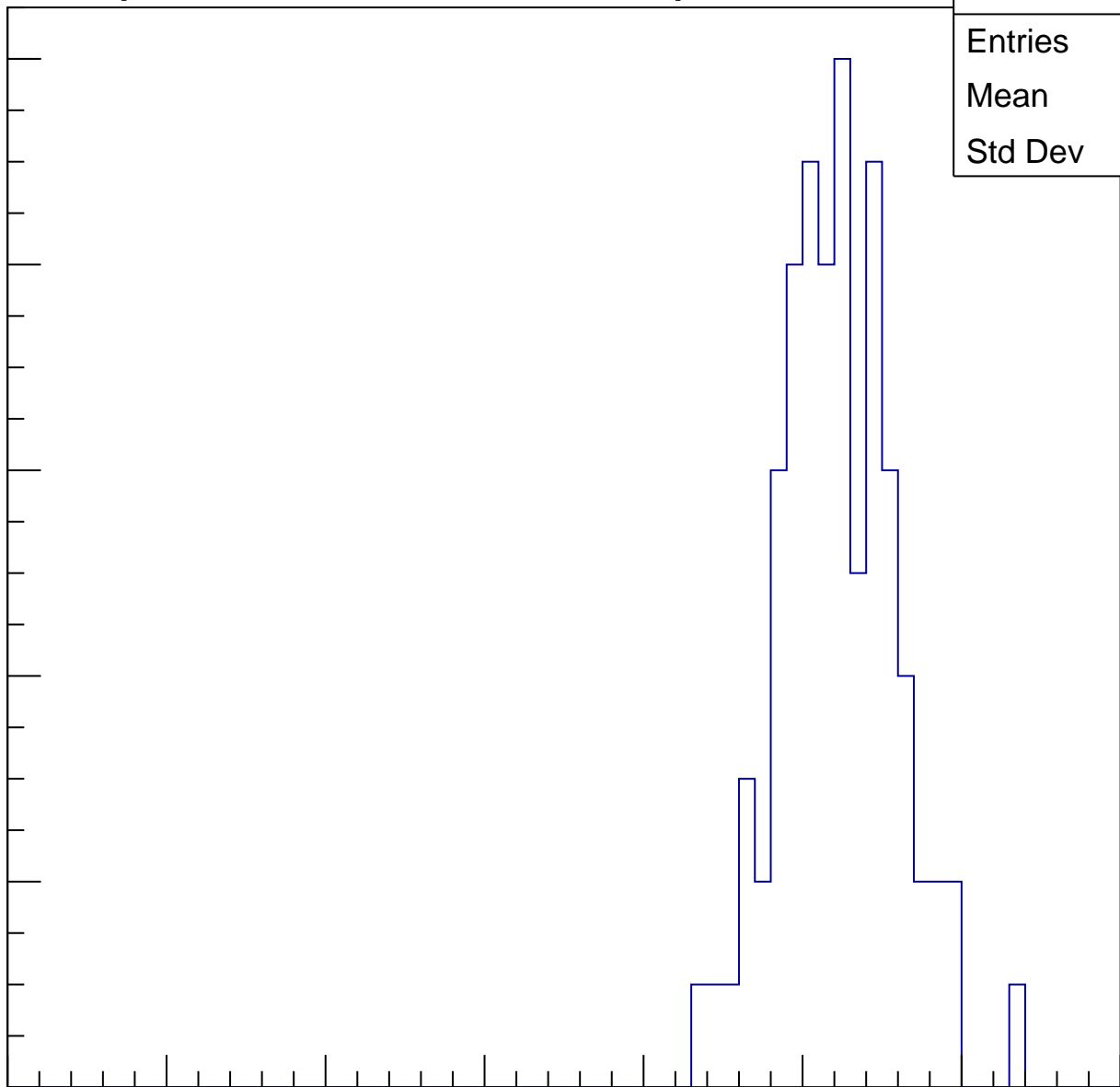
40

50

60

ampl

70

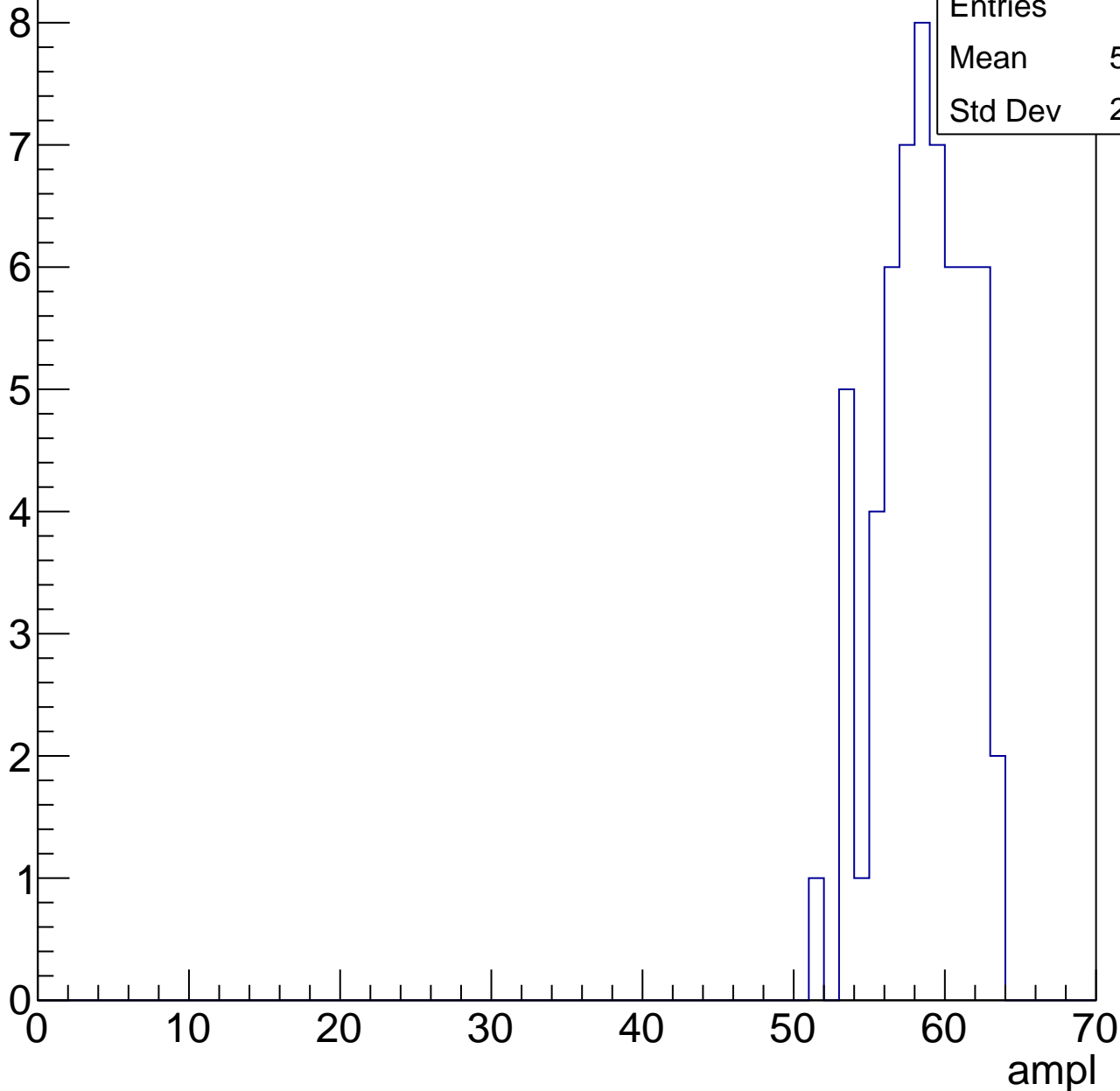


B1L101S, U23-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	58.07
Std Dev	2.875

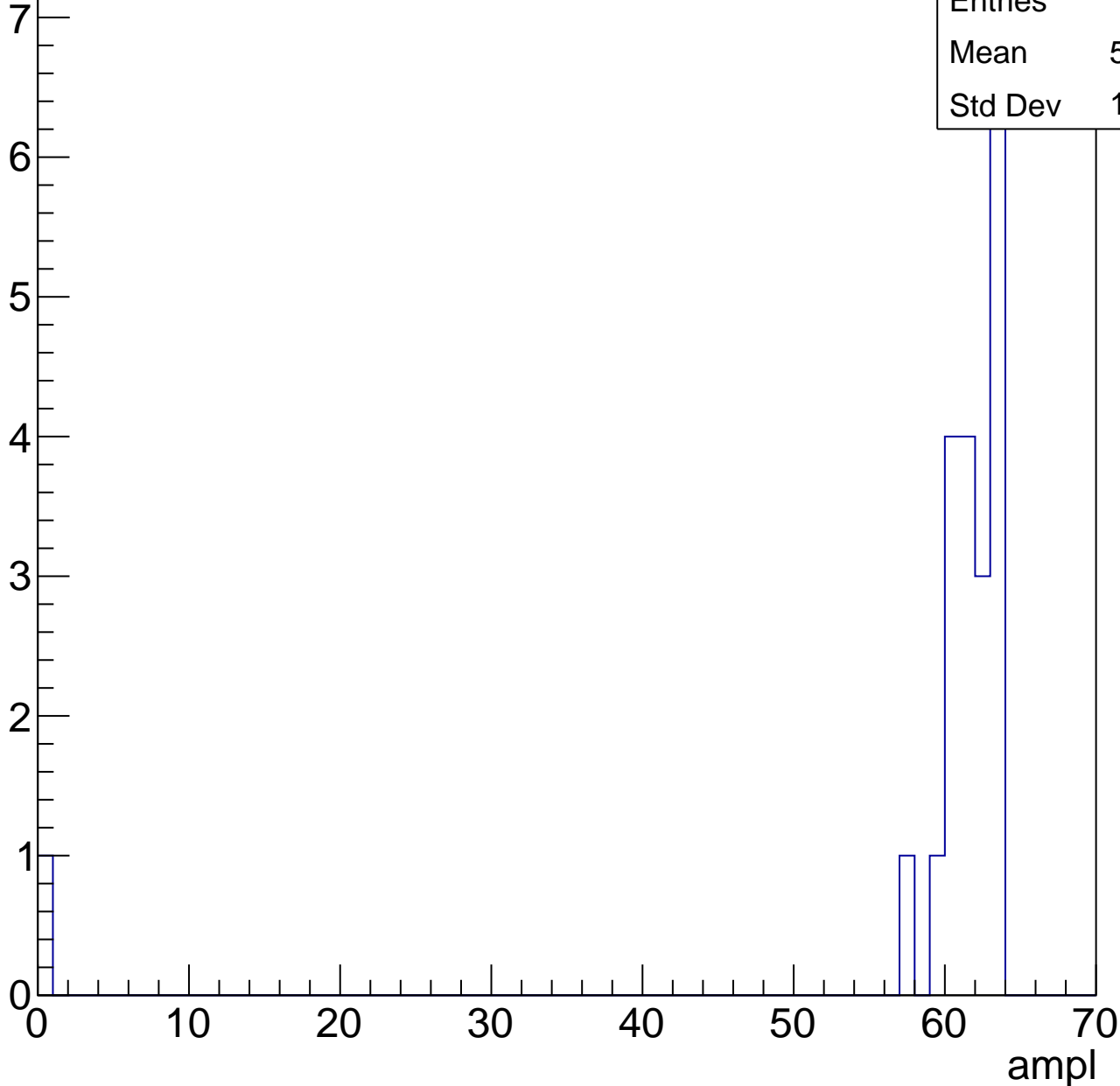


B1L101S, U23-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.43
Std Dev	13.16



B1L101S, U23-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch52, adc0

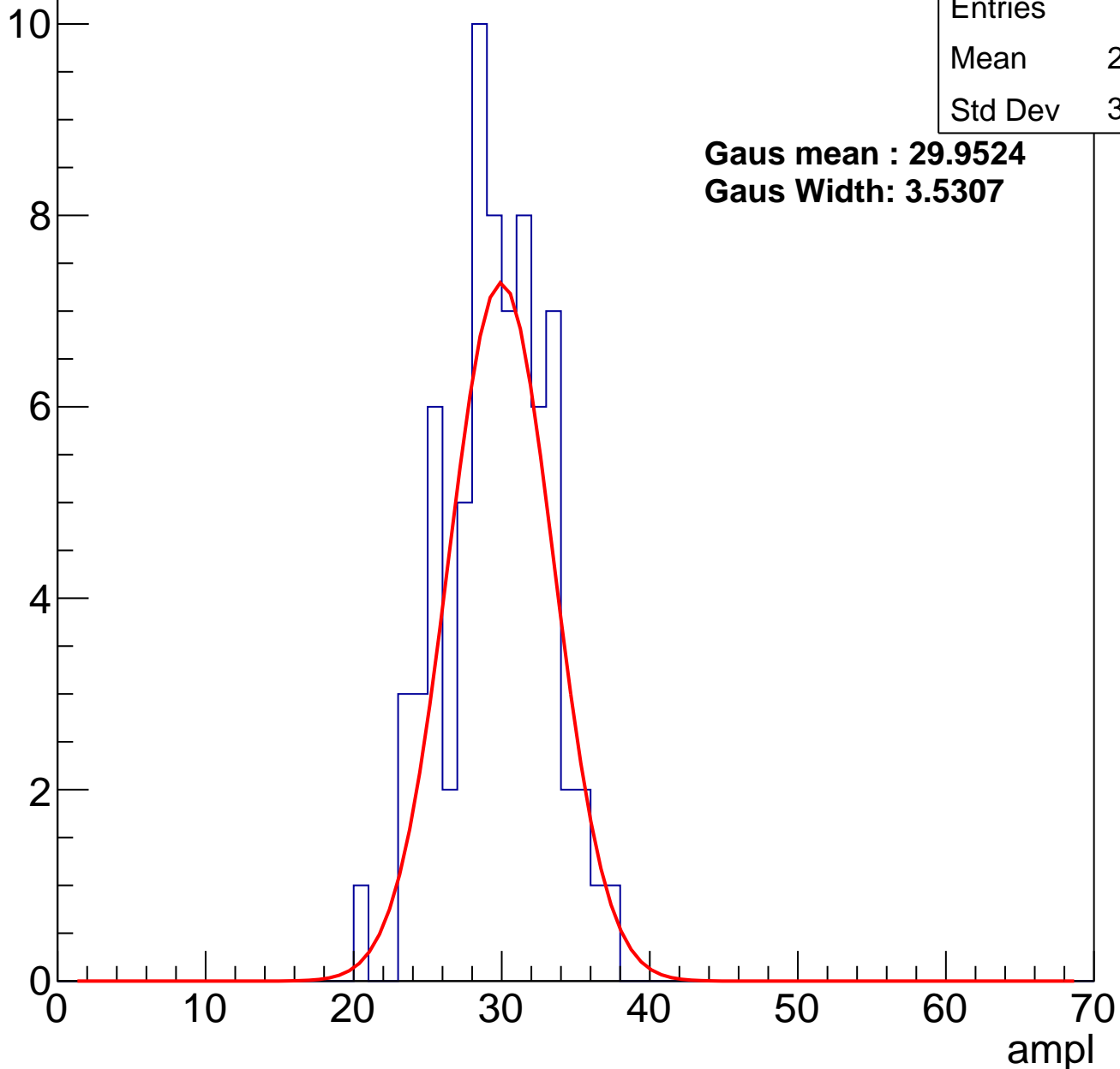
calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.19
Std Dev	3.426

Gaus mean : 29.9524

Gaus Width: 3.5307

Entry



B1L101S, U23-ch52, adc1

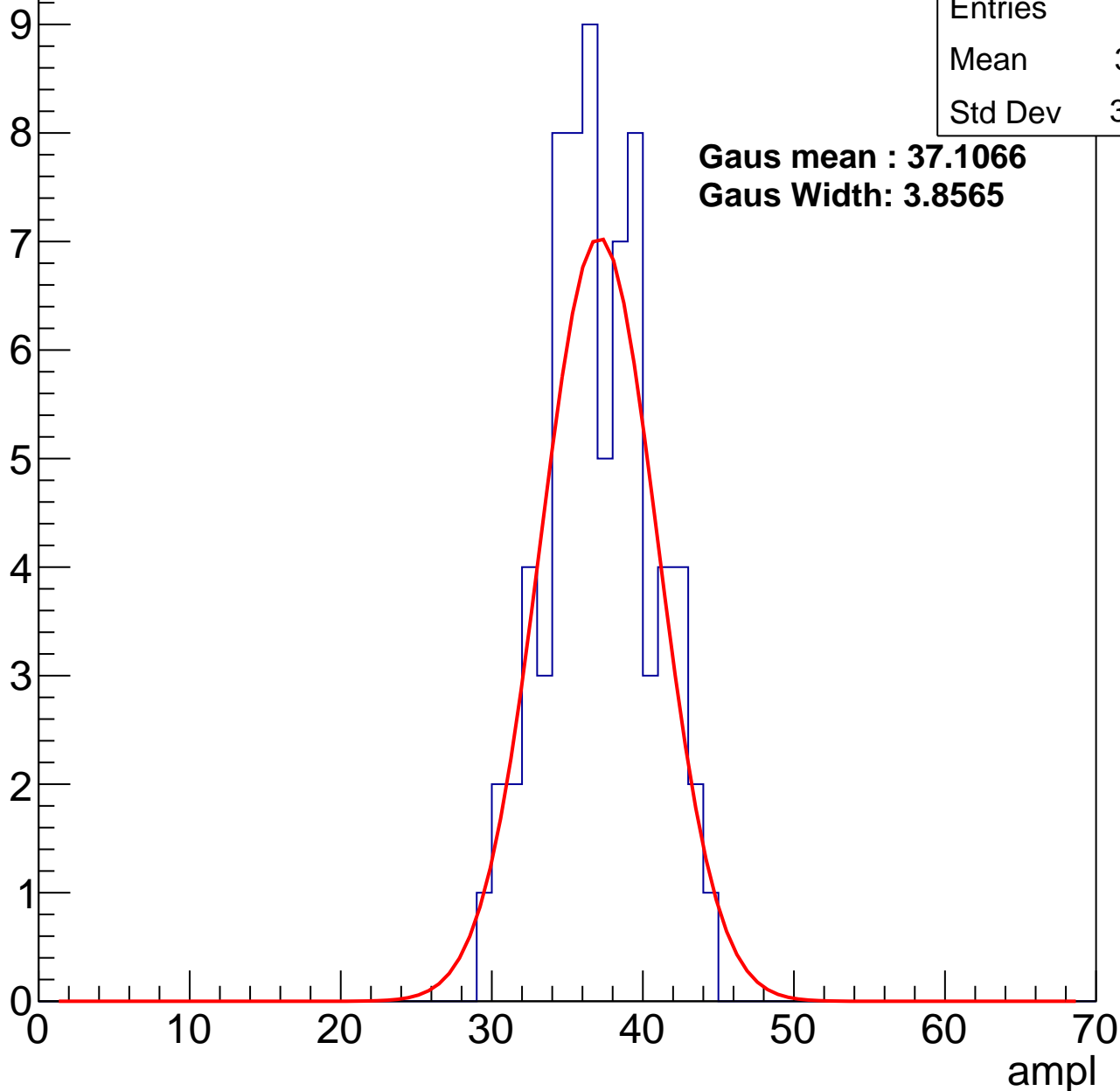
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.61
Std Dev	3.429

Gaus mean : 37.1066

Gaus Width: 3.8565



B1L101S, U23-ch52, adc2

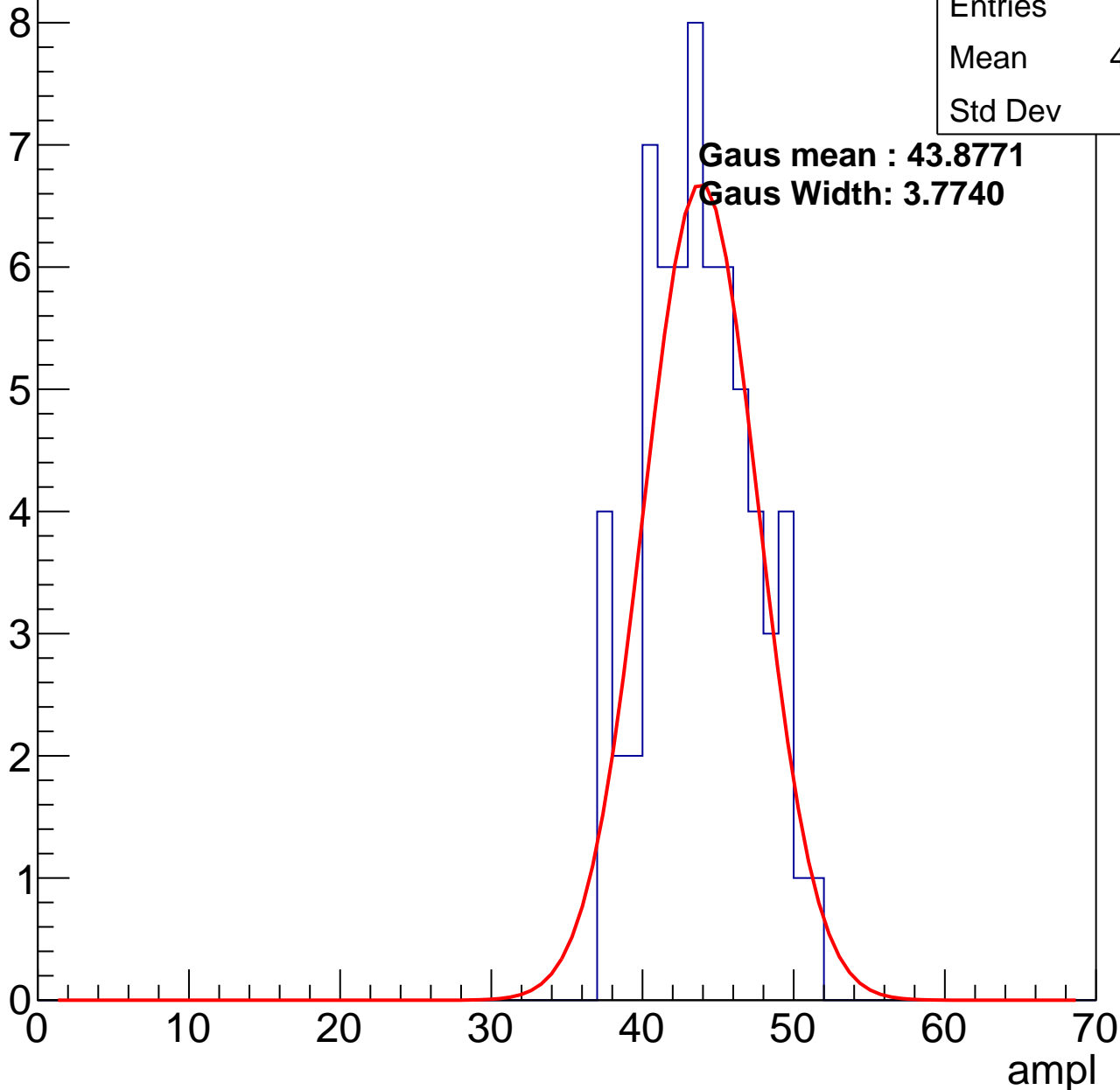
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.34
Std Dev	3.47

Gaus mean : 43.8771

Gaus Width: 3.7740

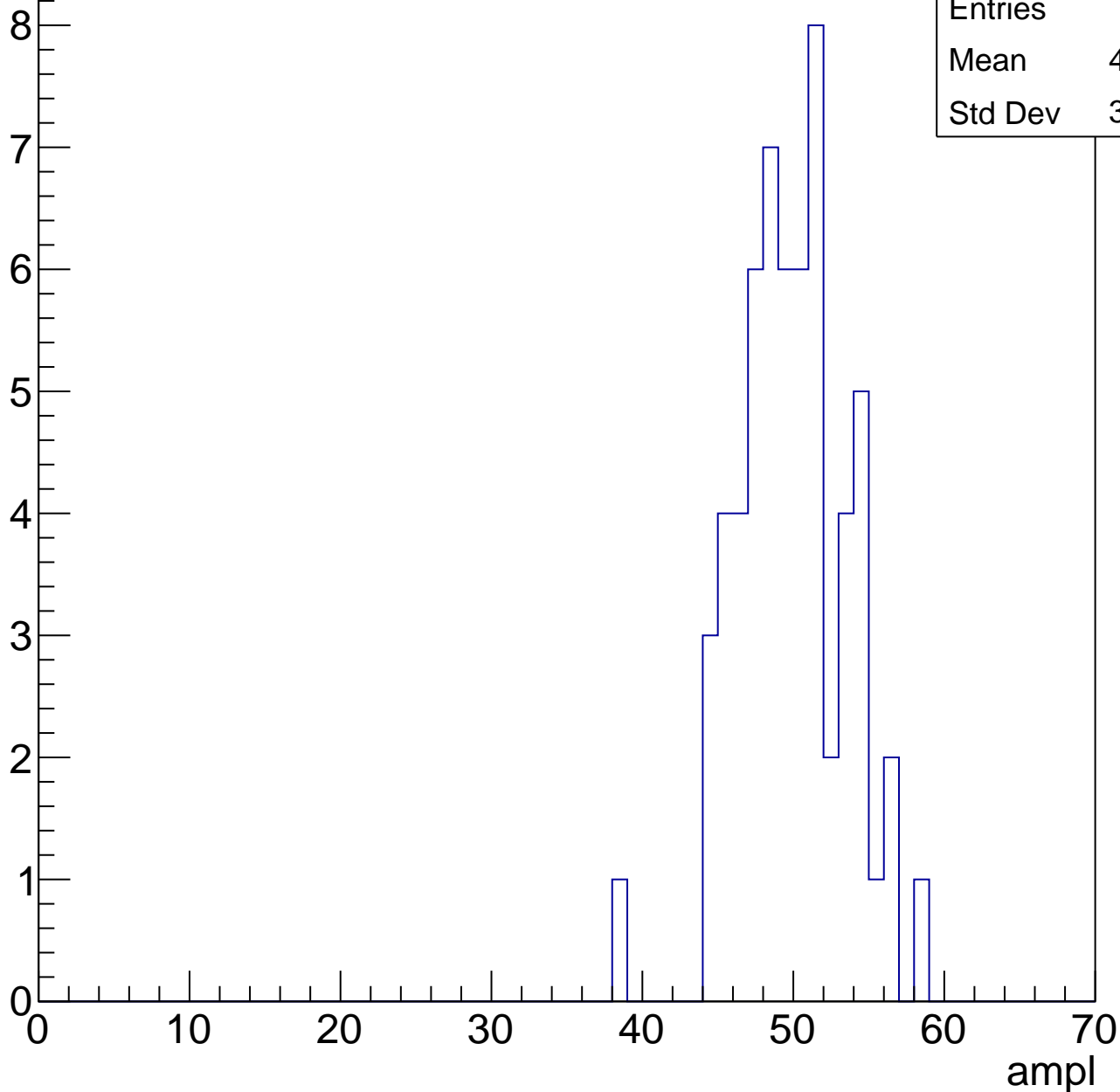


B1L101S, U23-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.42
Std Dev	3.598

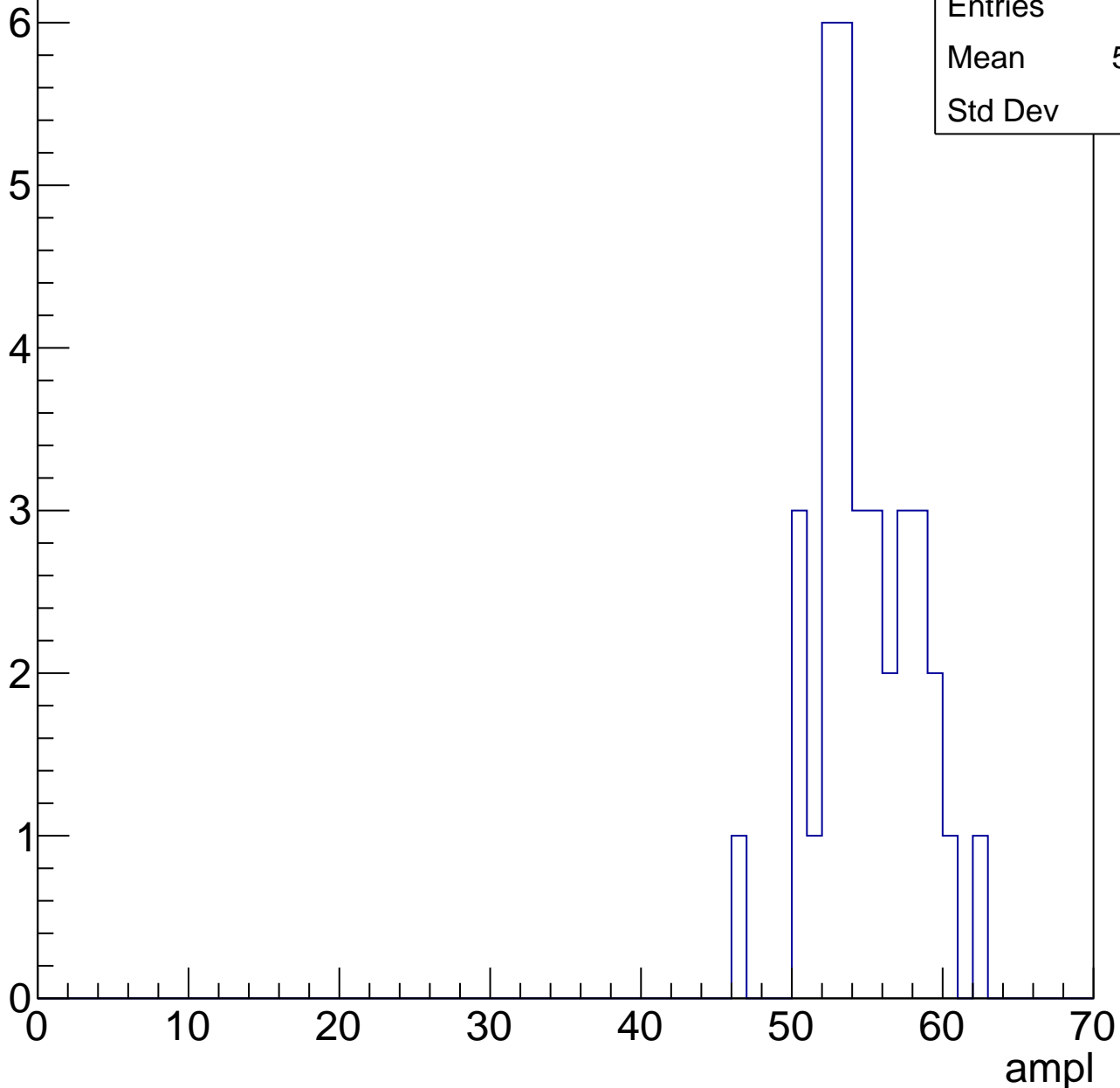


B1L101S, U23-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	54.31
Std Dev	3.31



B1L101S, U23-ch52, adc5

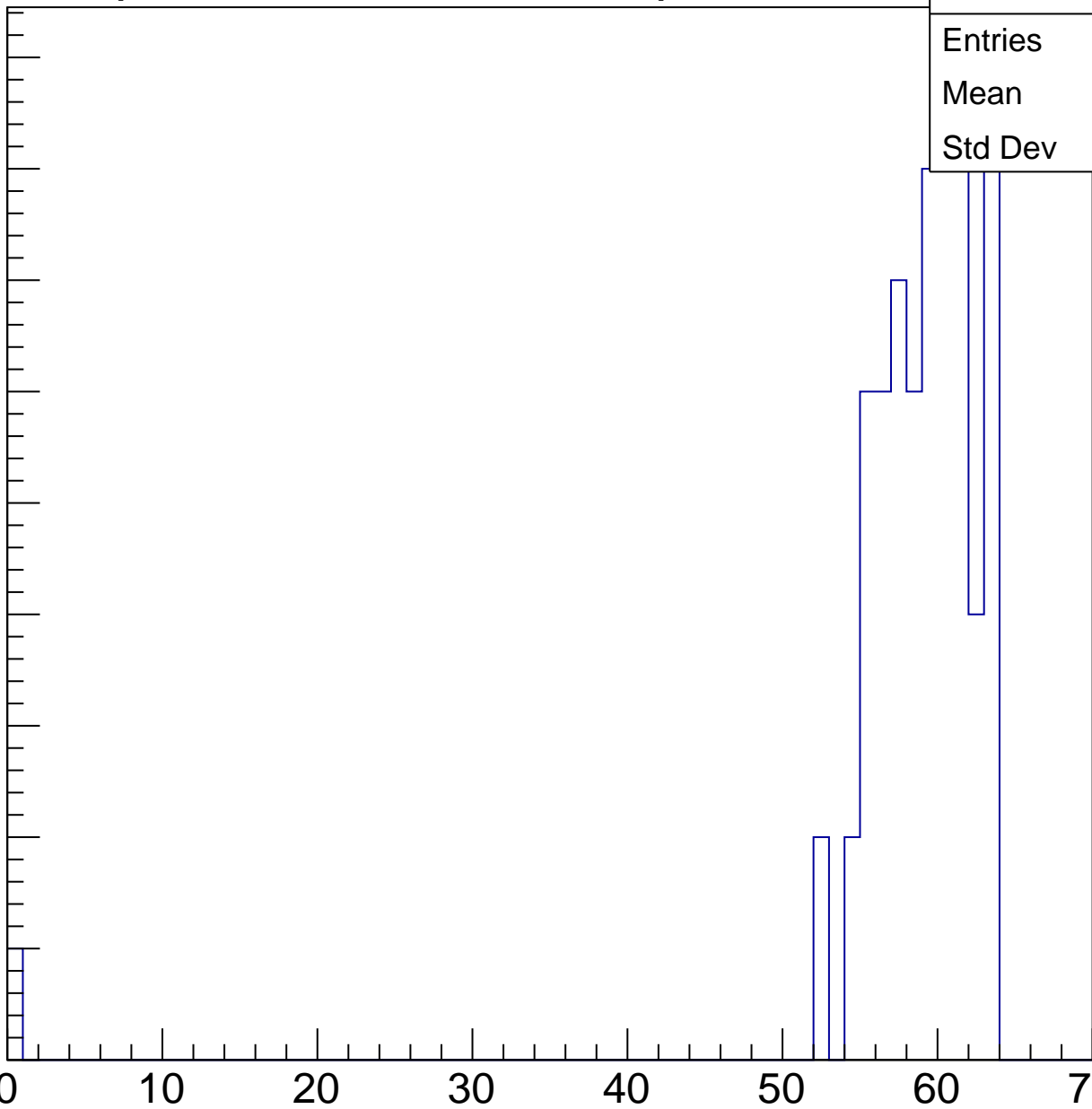
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	67
Mean	57.87
Std Dev	7.661

ampl

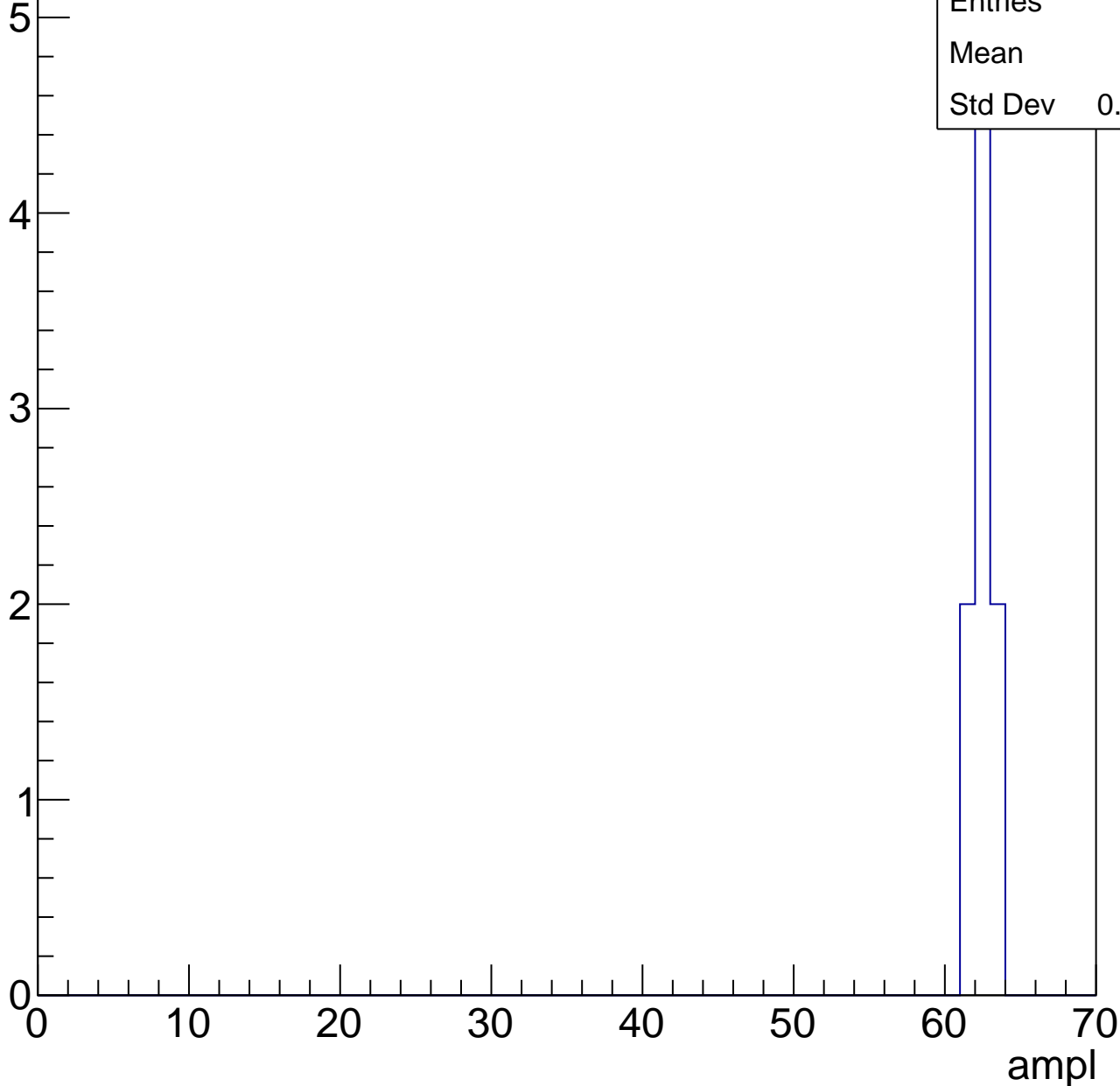


B1L101S, U23-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	62
Std Dev	0.6667



B1L101S, U23-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch53, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	30.54
Std Dev	4.604

Gaus mean : 31.9347

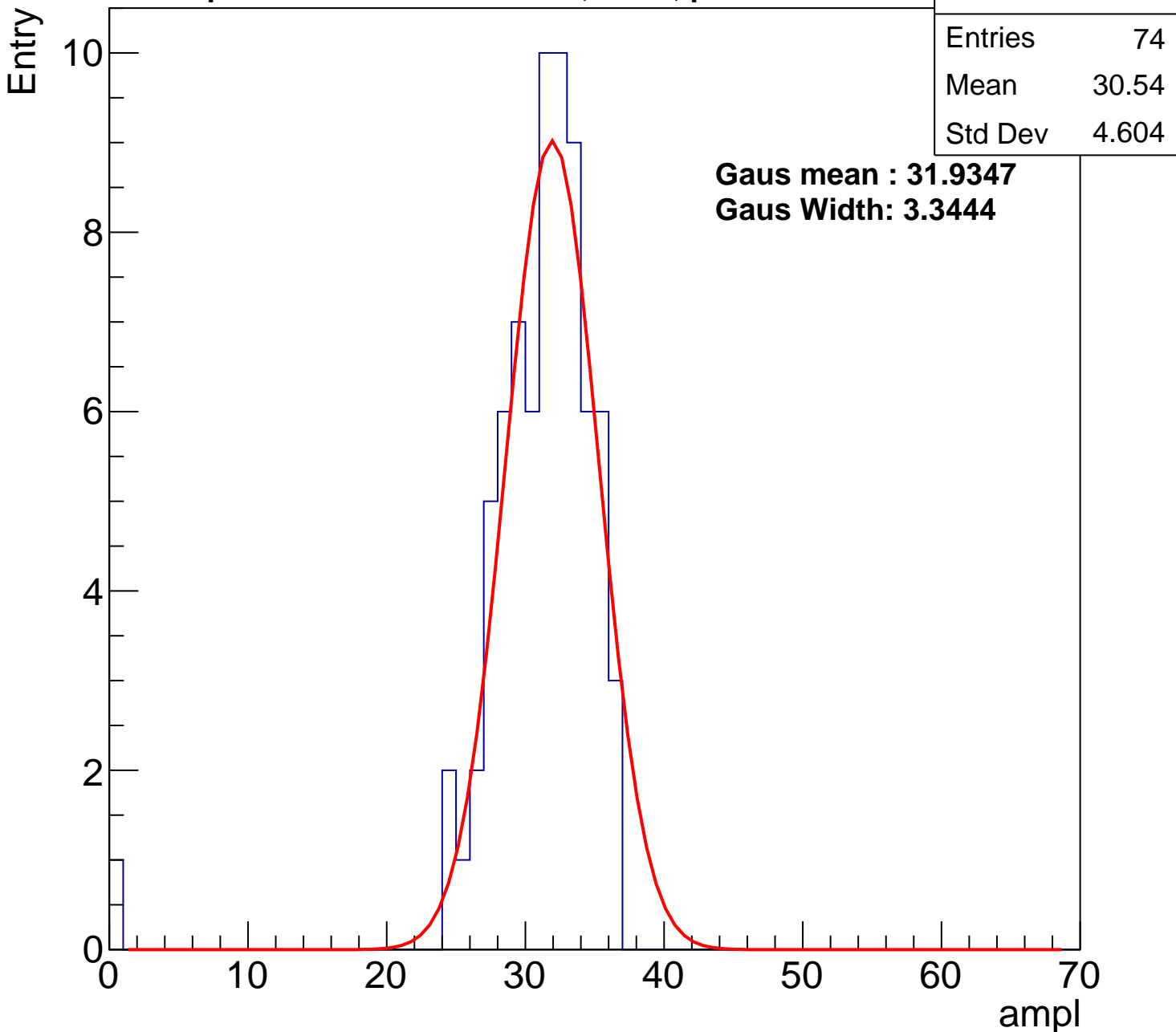
Gaus Width: 3.3444

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch53, adc1

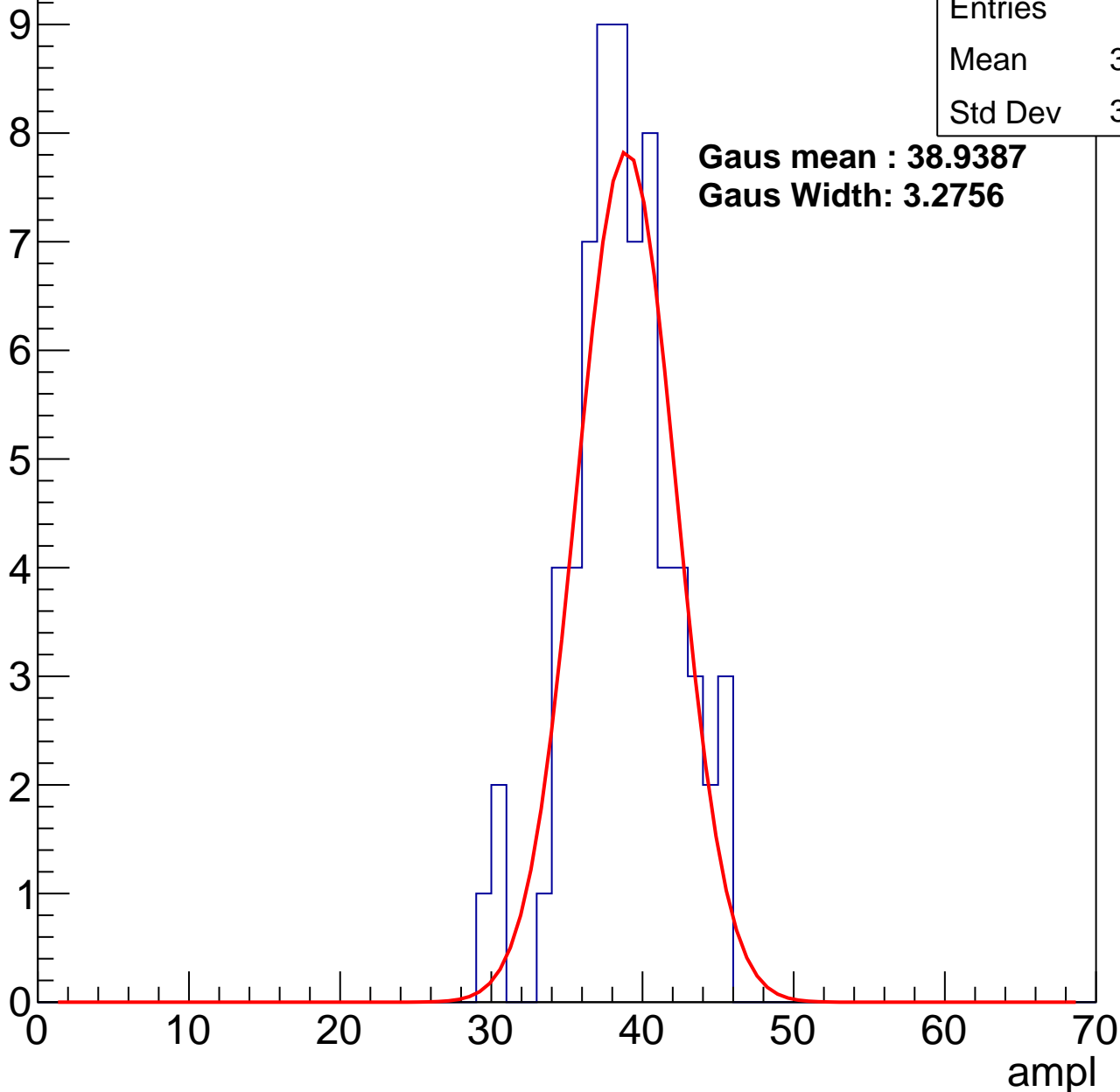
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	38.26
Std Dev	3.428

Gaus mean : 38.9387

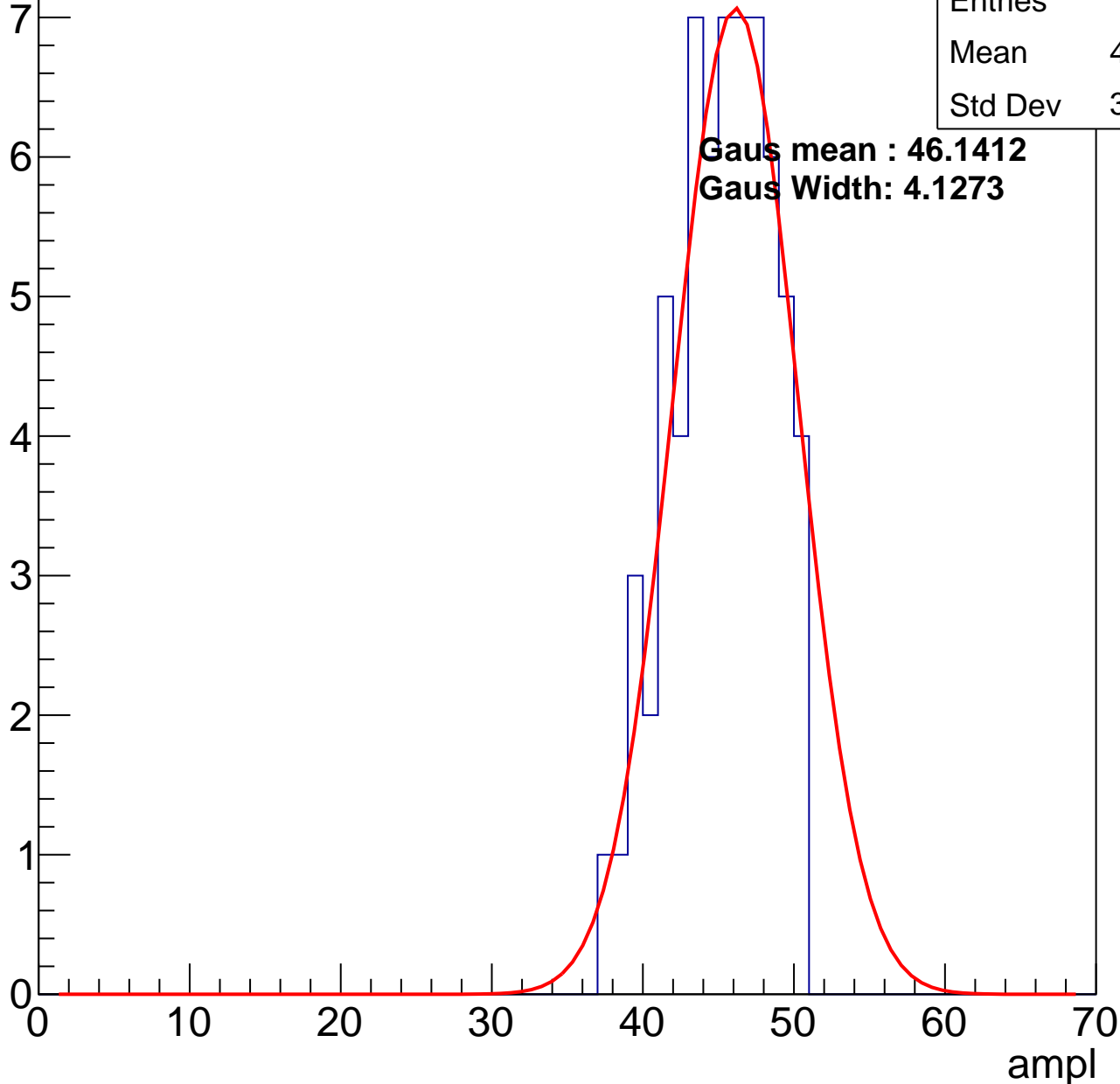
Gaus Width: 3.2756



B1L101S, U23-ch53, adc2

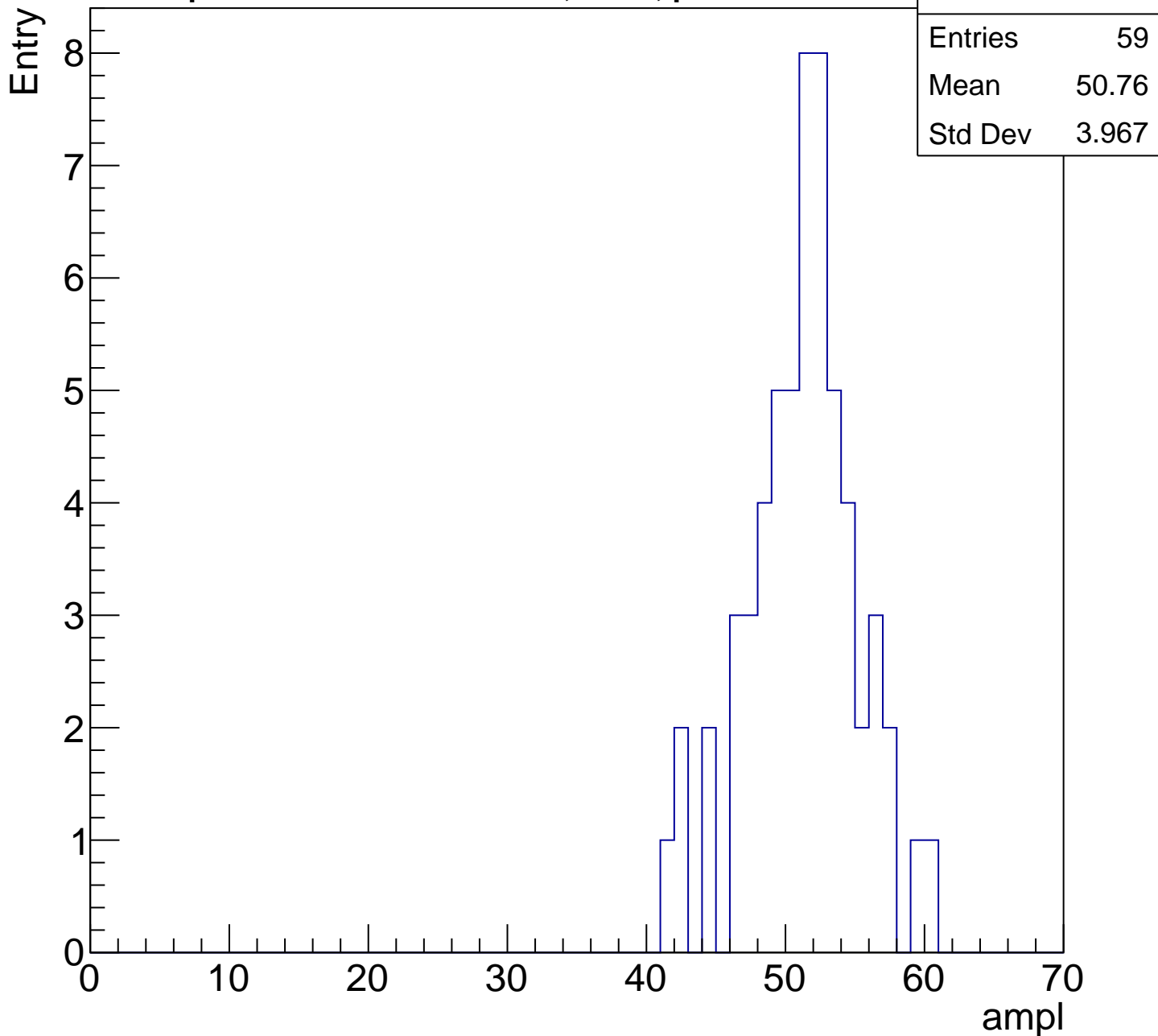
calib_packv5_042523_0143.root, FC#0, port D2

Entry



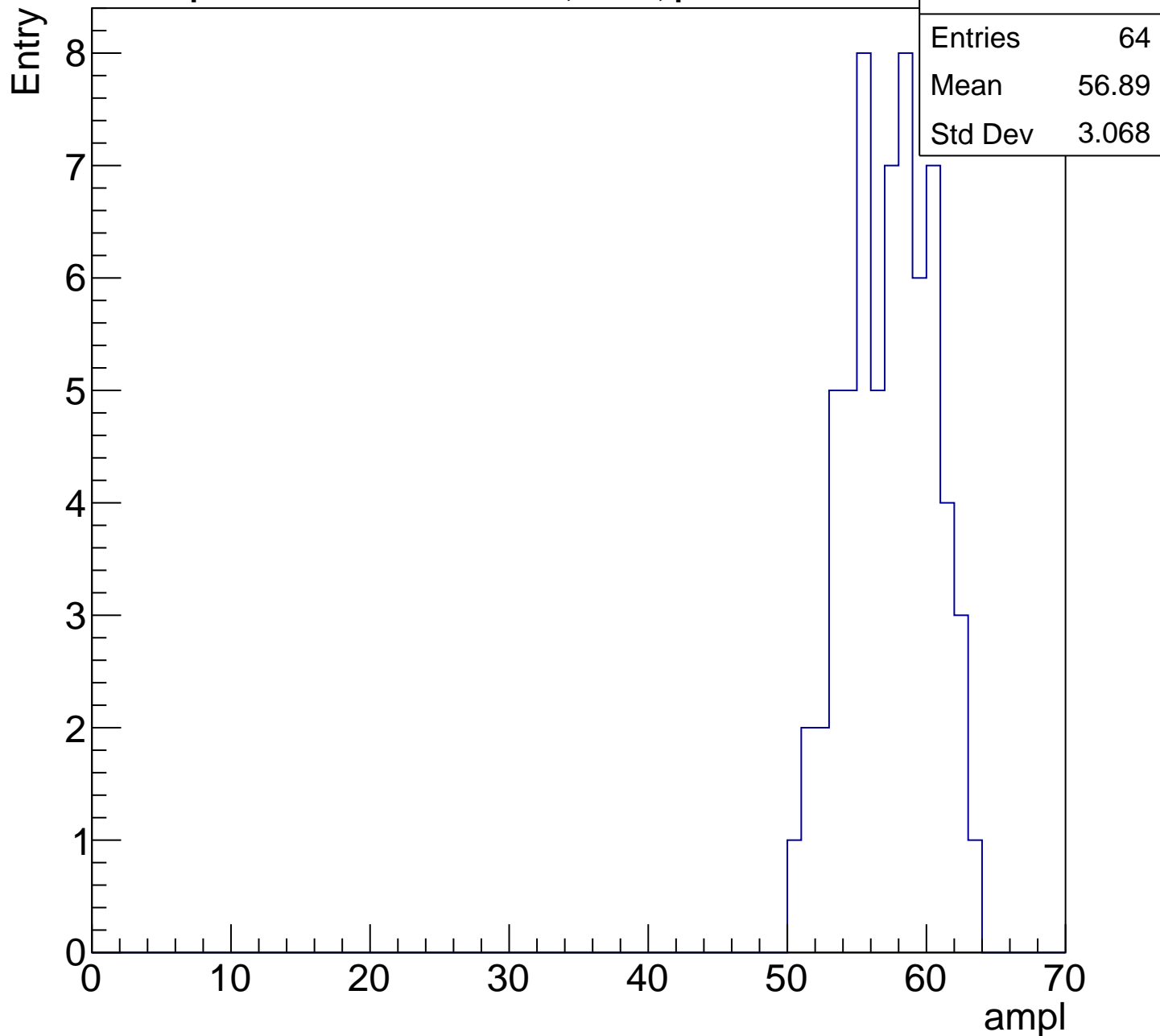
B1L101S, U23-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch53, adc4

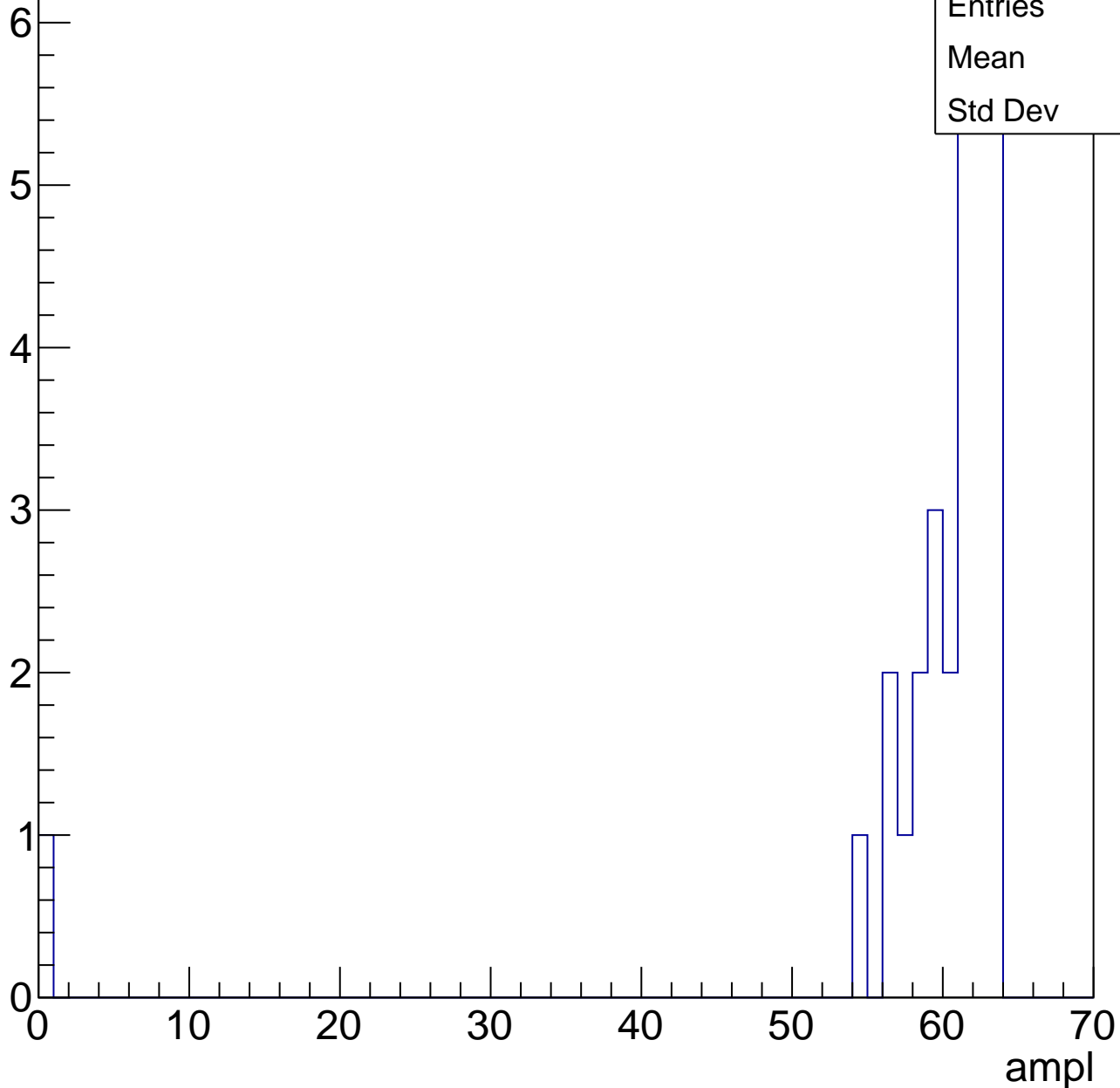
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	12
Std Dev	12

B1L101S, U23-ch54, adc0

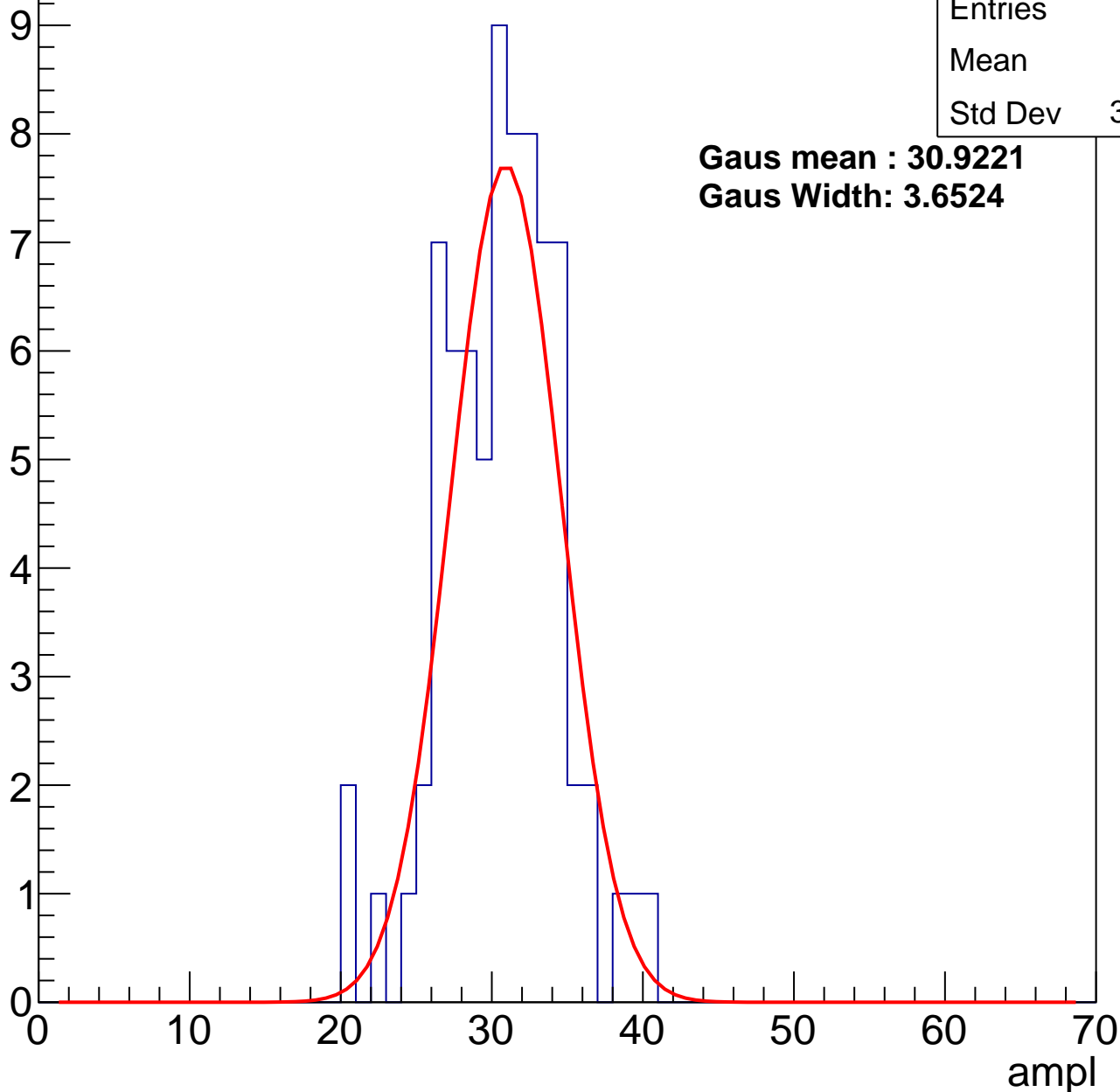
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.2
Std Dev	3.846

Gaus mean : 30.9221

Gaus Width: 3.6524



B1L101S, U23-ch54, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	58
Mean	37.05
Std Dev	3.636

Gaus mean : 37.6396

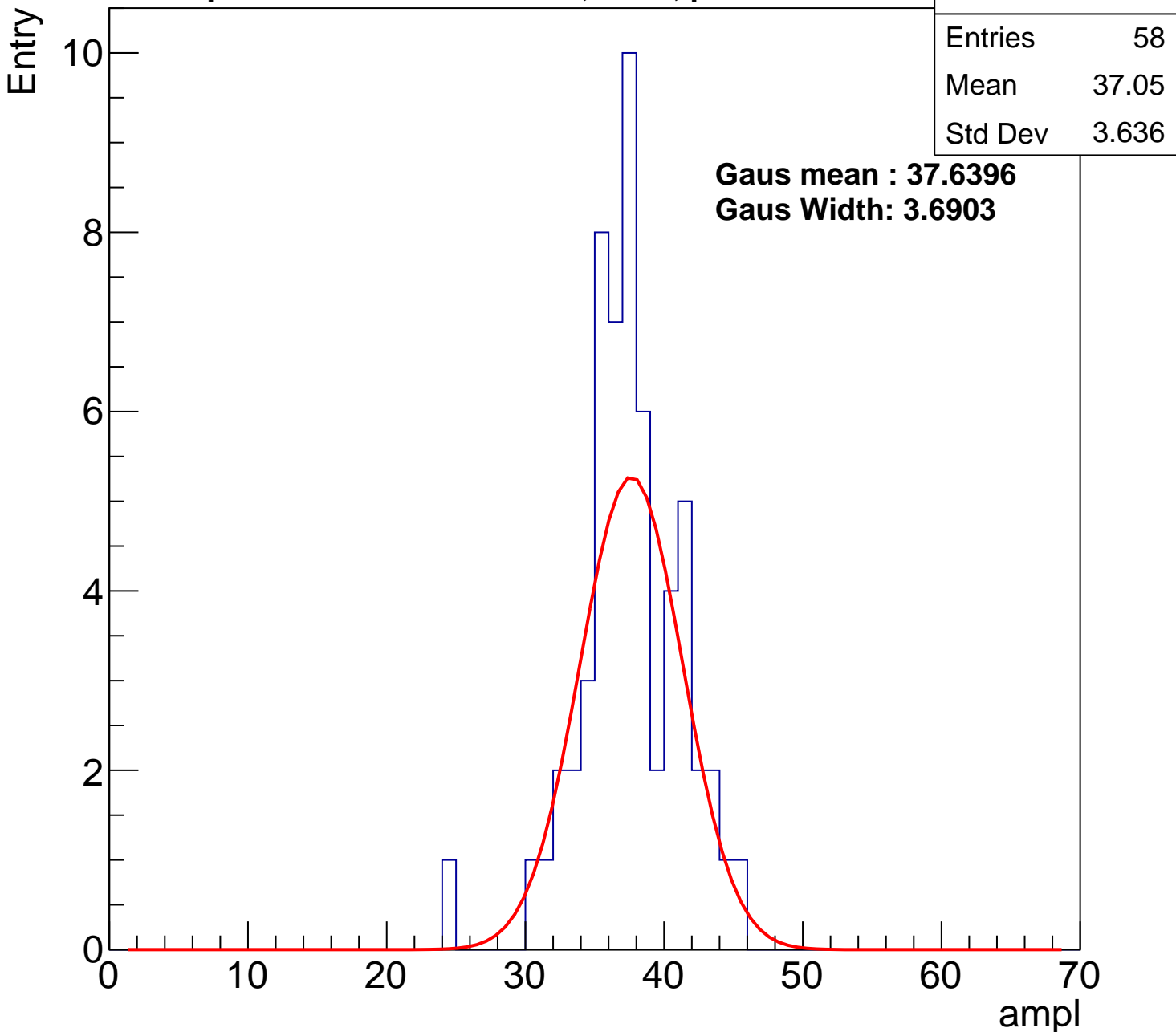
Gaus Width: 3.6903

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch54, adc2

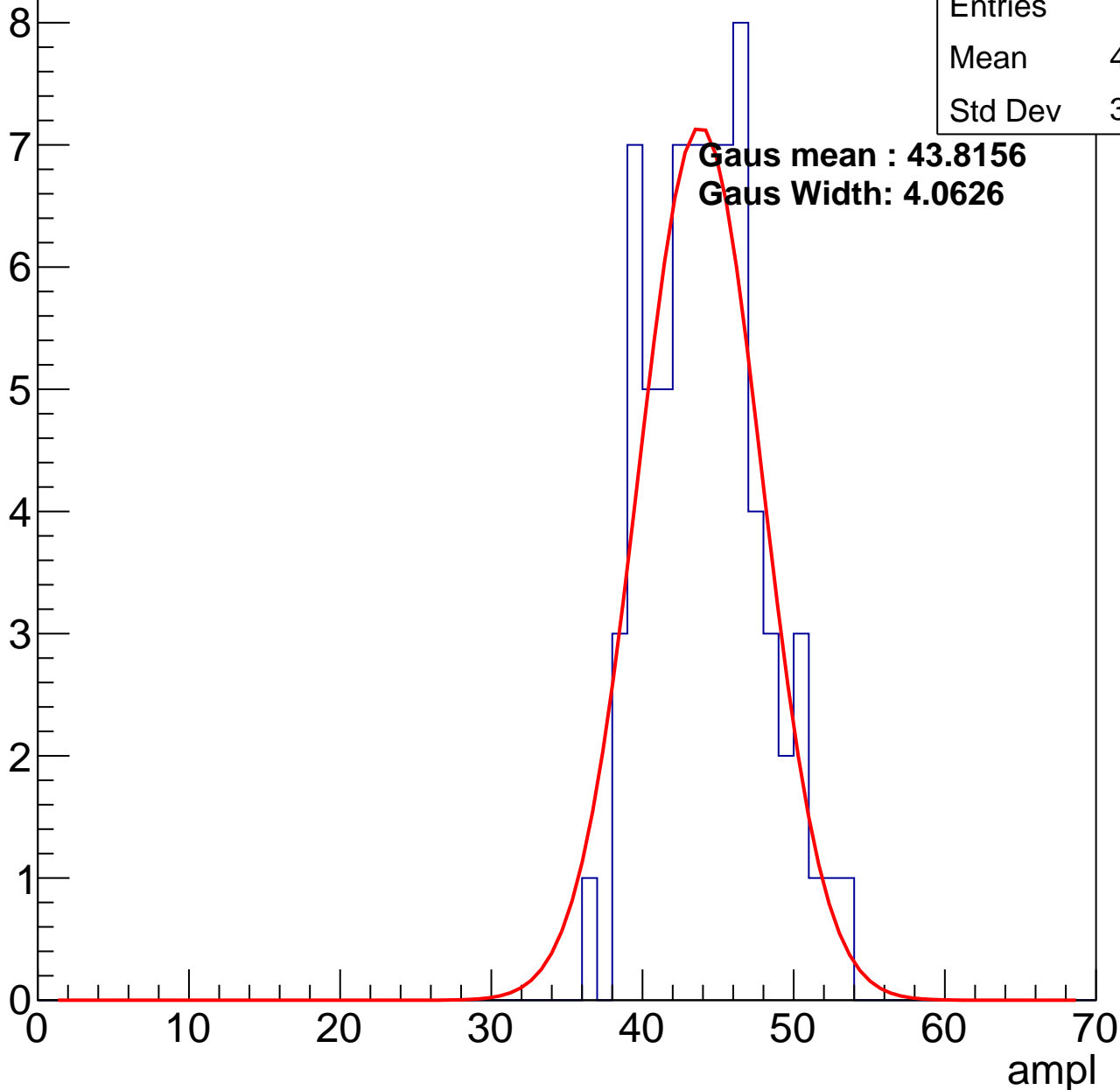
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.75
Std Dev	3.685

Gaus mean : 43.8156

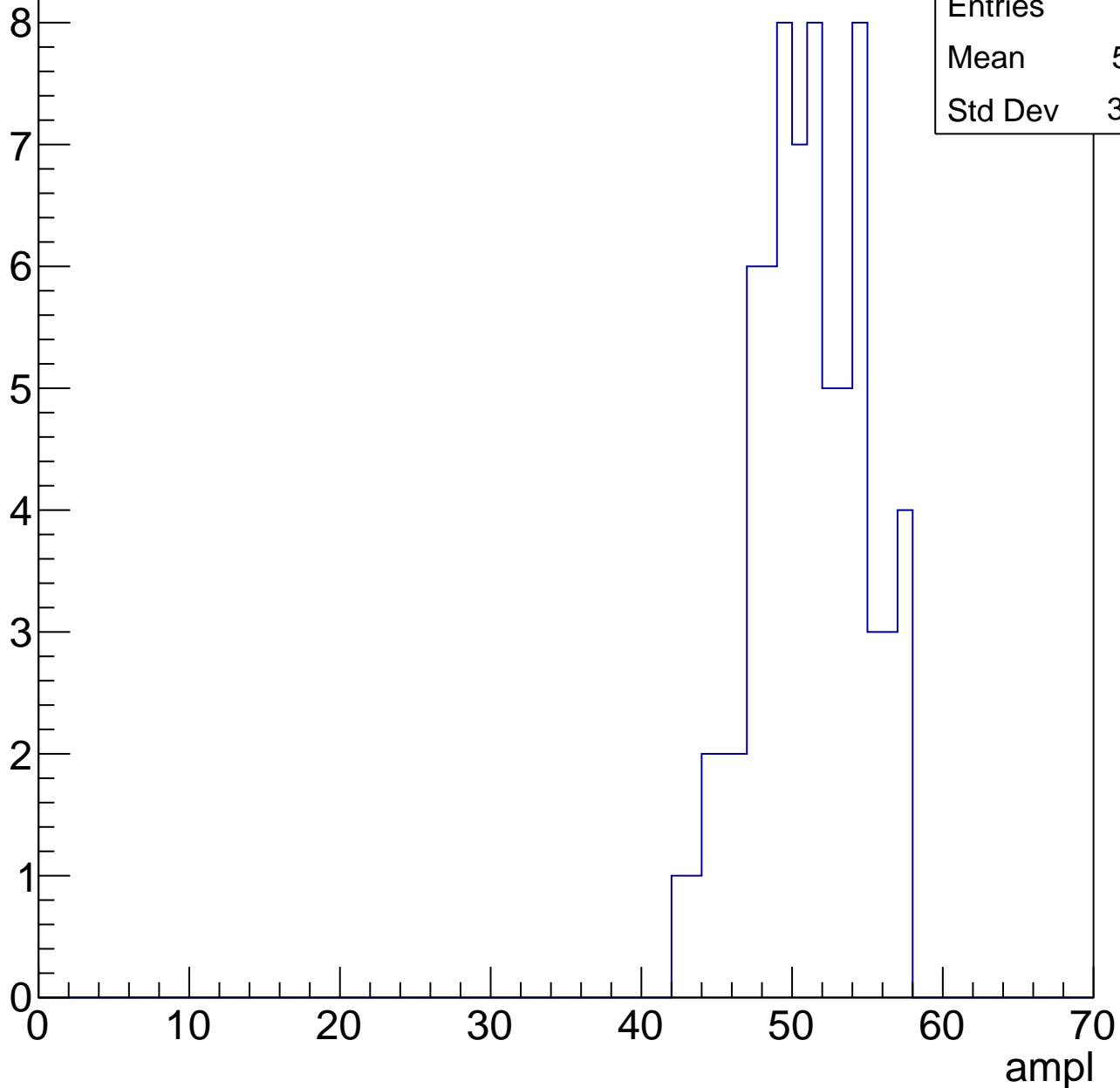
Gaus Width: 4.0626



B1L101S, U23-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

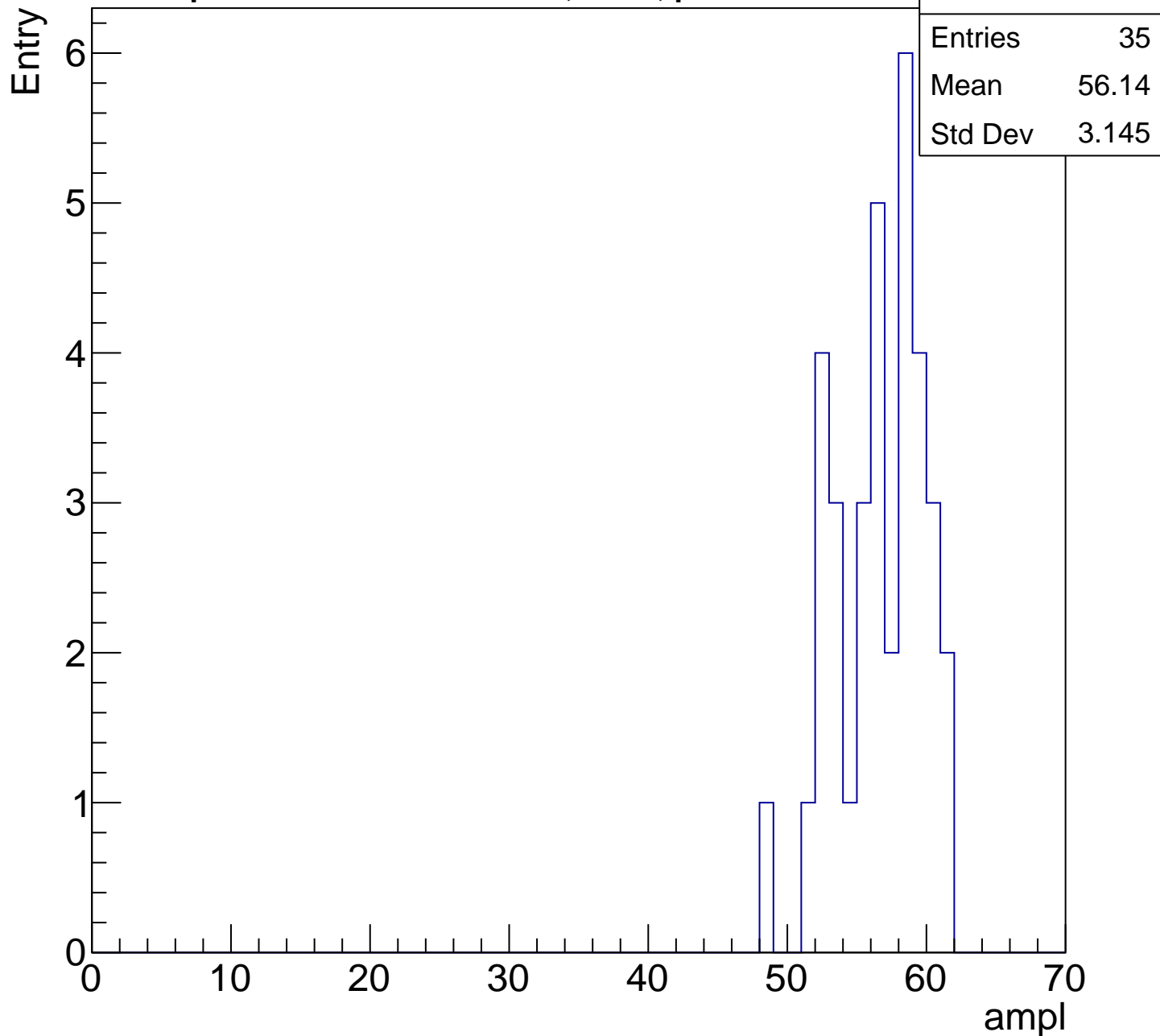
Entry



Entries	71
Mean	50.61
Std Dev	3.574

B1L101S, U23-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

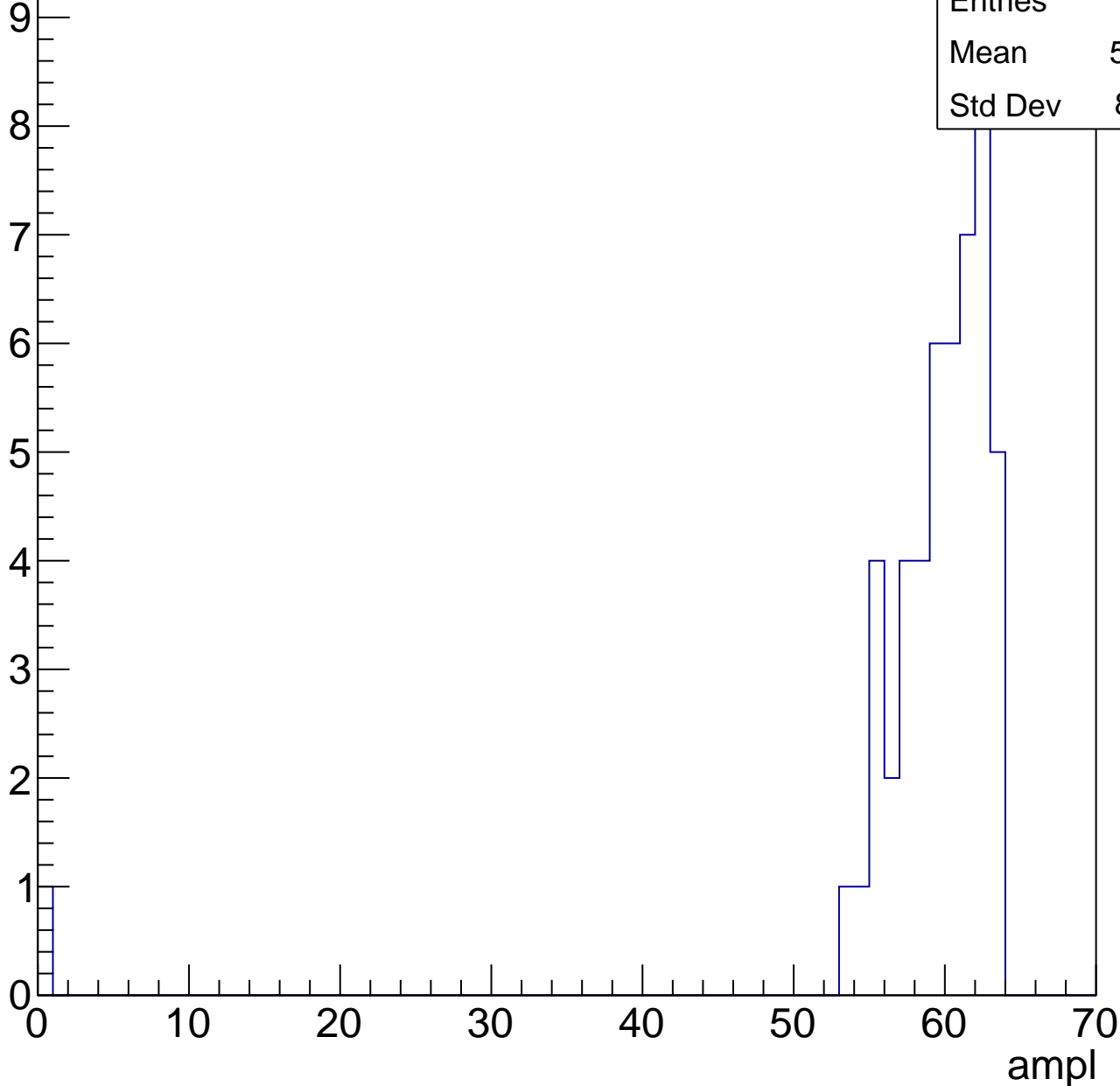


B1L101S, U23-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

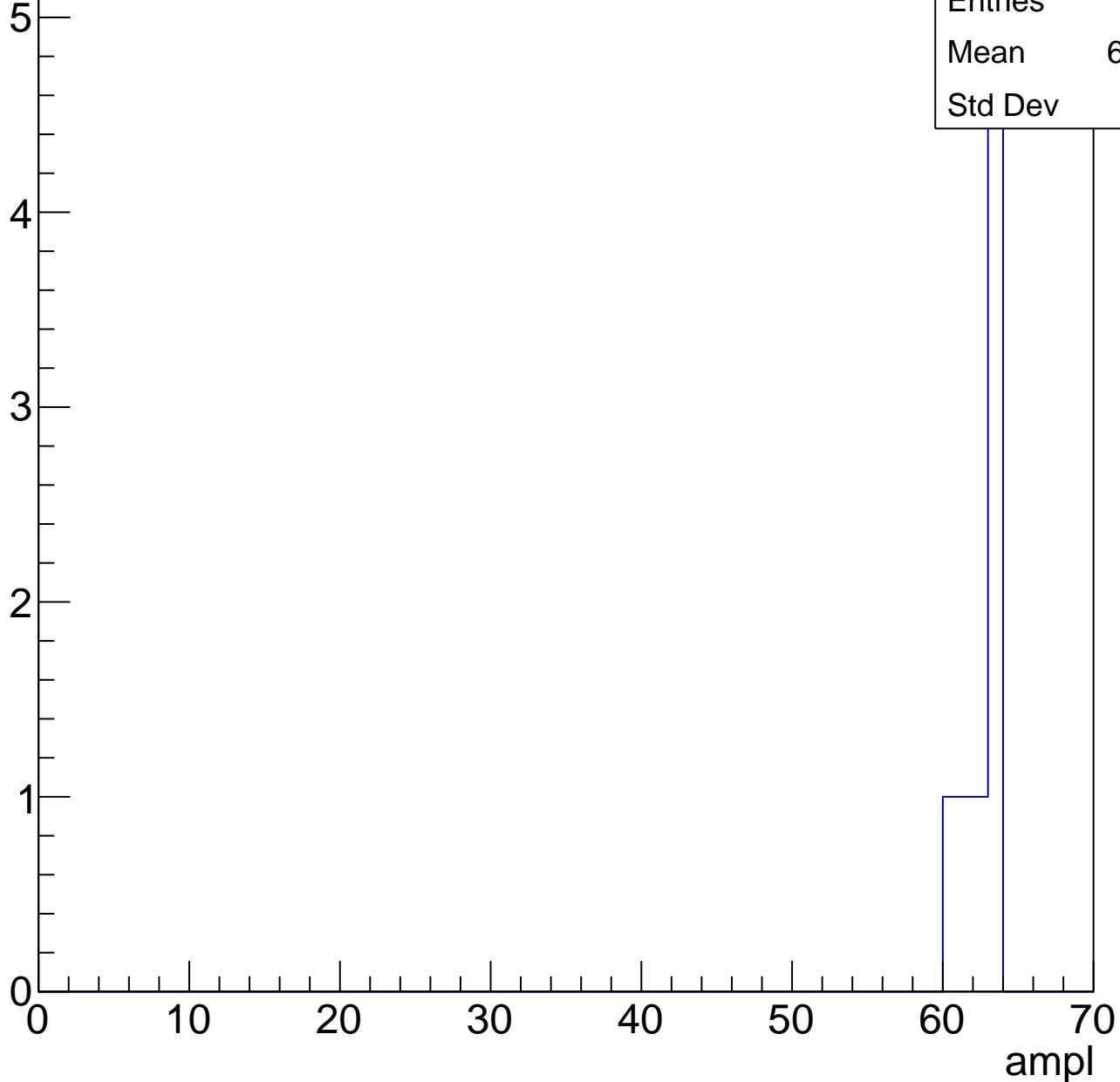
Entries	50
Mean	58.26
Std Dev	8.731



B1L101S, U23-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch55, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	29.03
Std Dev	4.836

Gaus mean : 30.2681

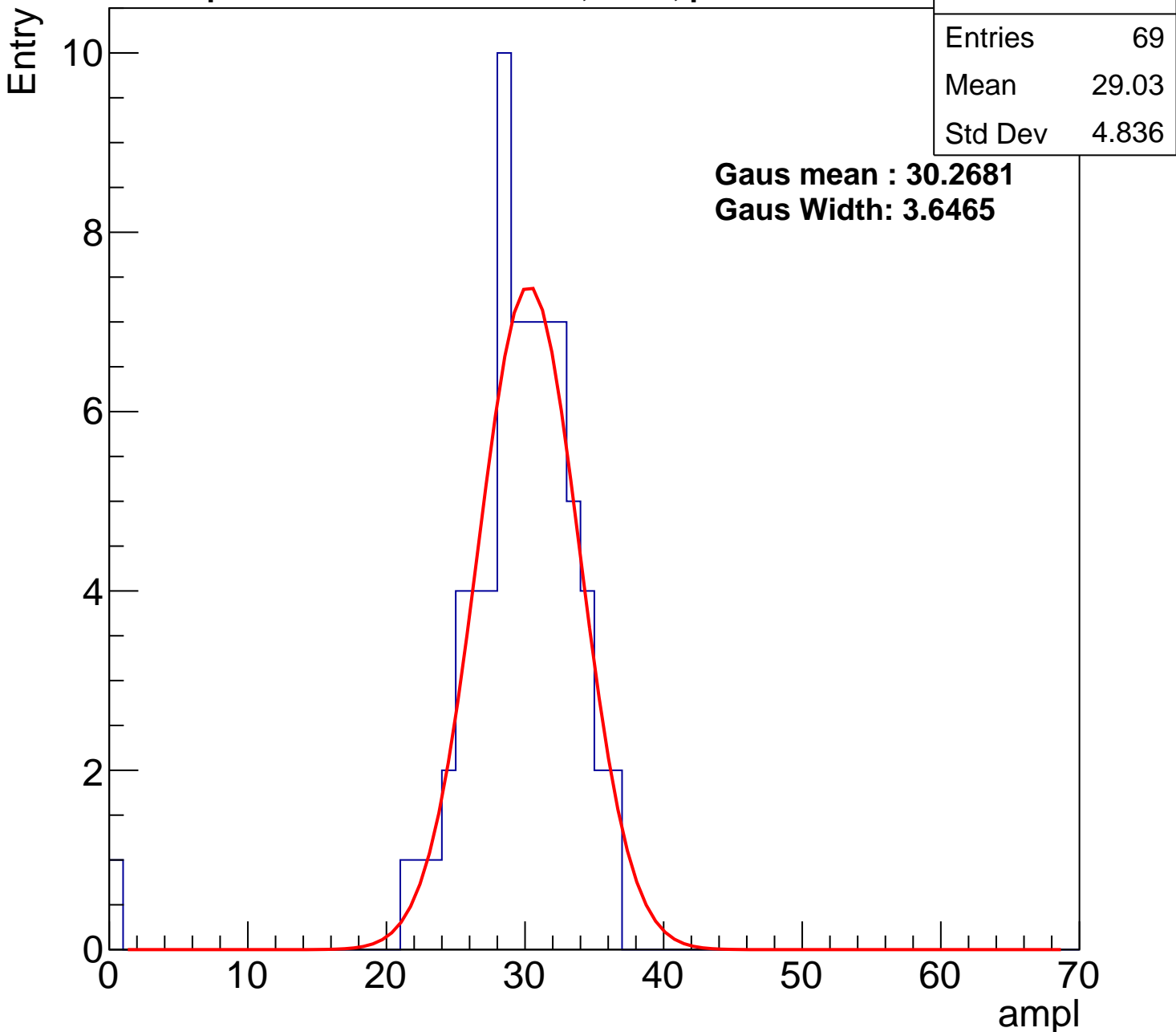
Gaus Width: 3.6465

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch55, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	38
Std Dev	3.8

Gaus mean : 38.3976

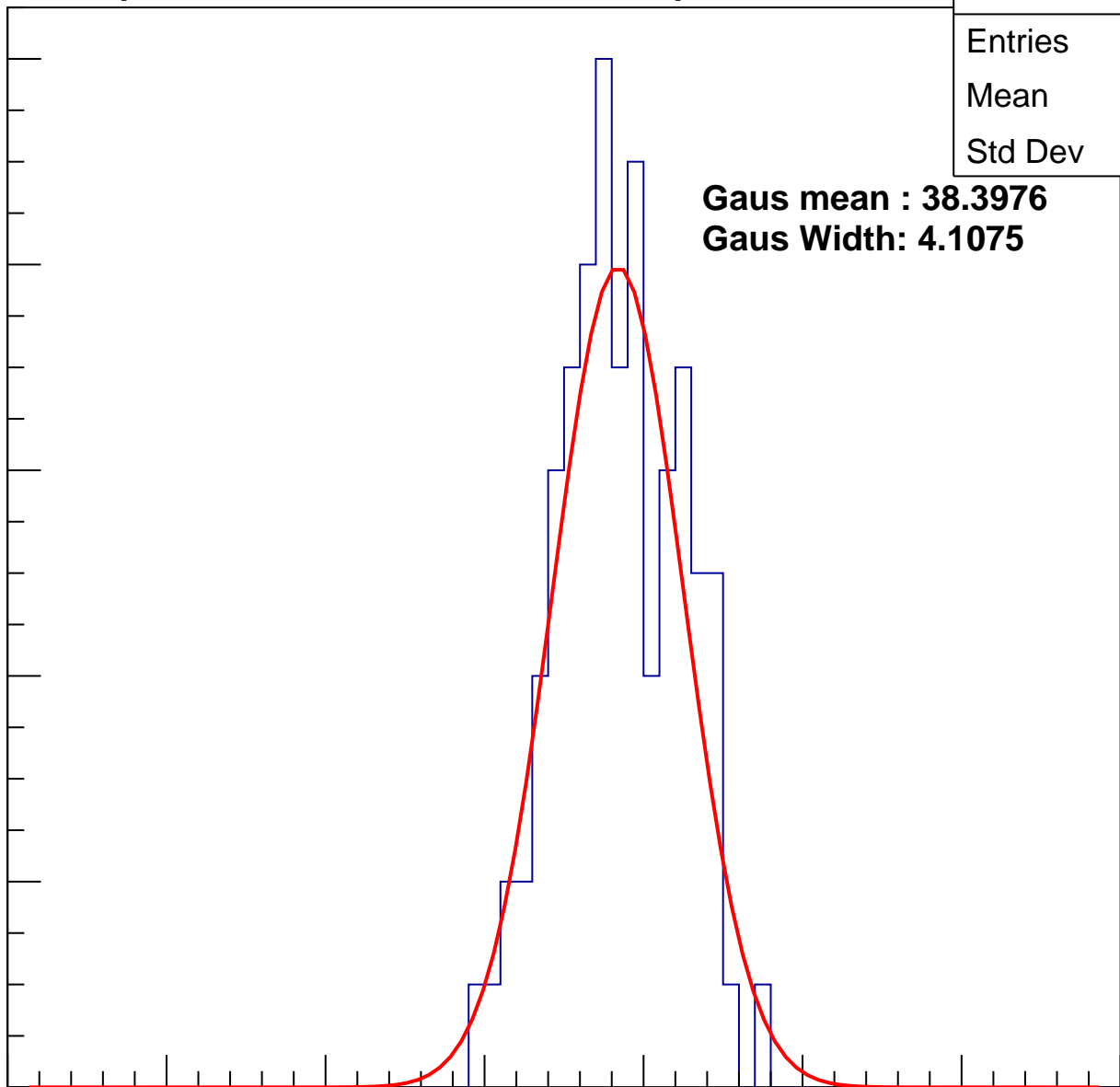
Gaus Width: 4.1075

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch55, adc2

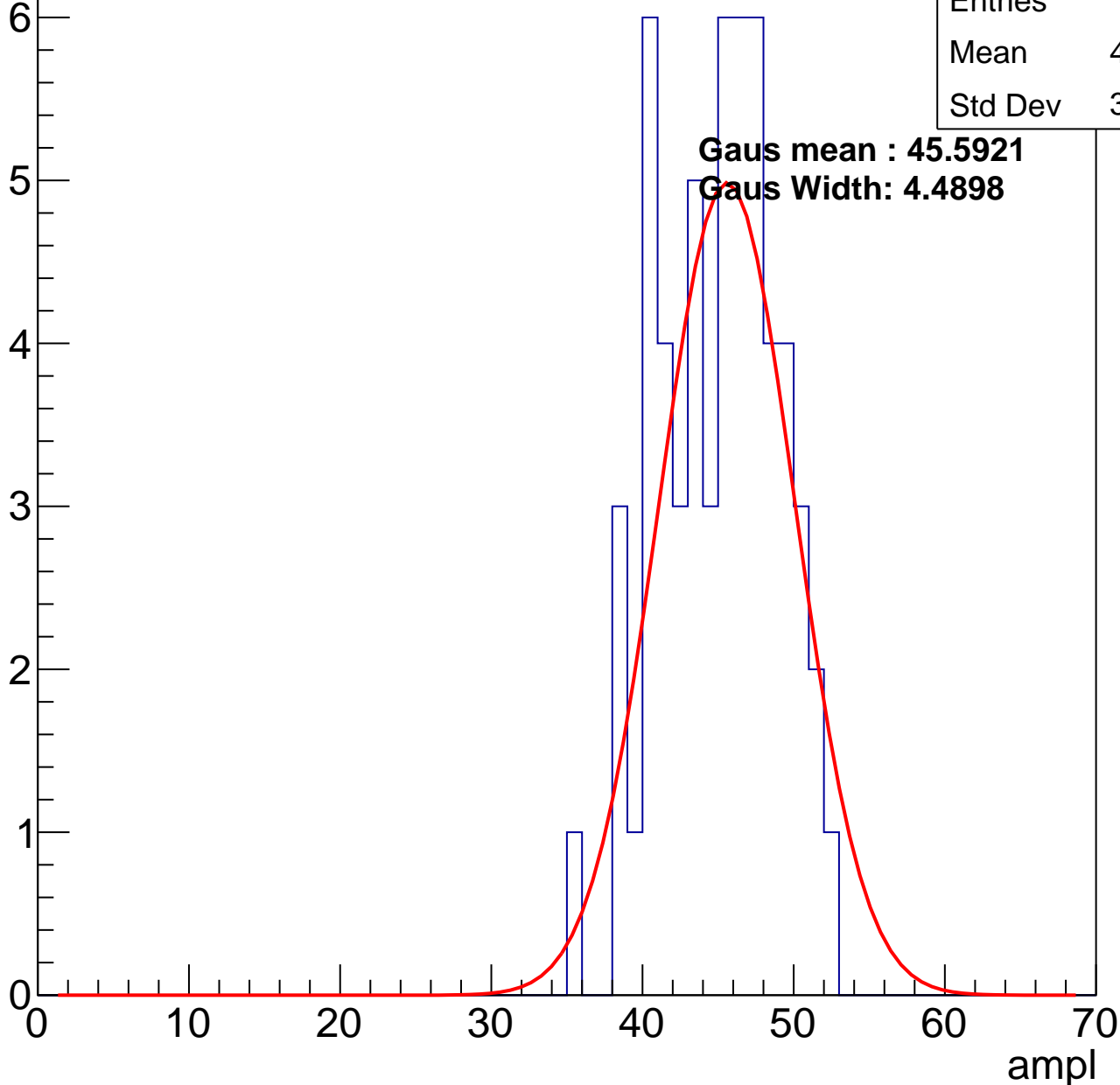
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.57
Std Dev	3.856

Gaus mean : 45.5921

Gaus Width: 4.4898

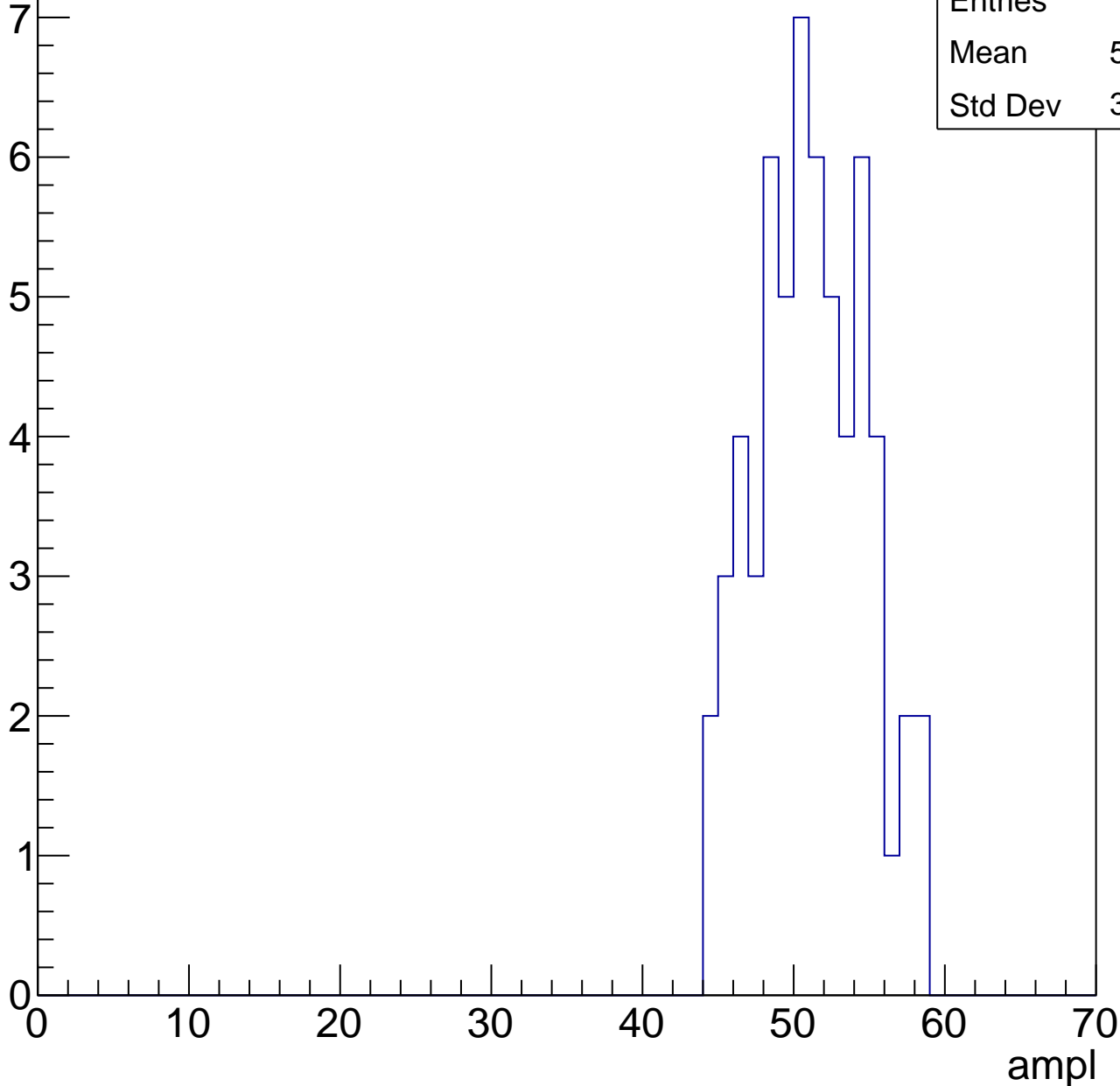


B1L101S, U23-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

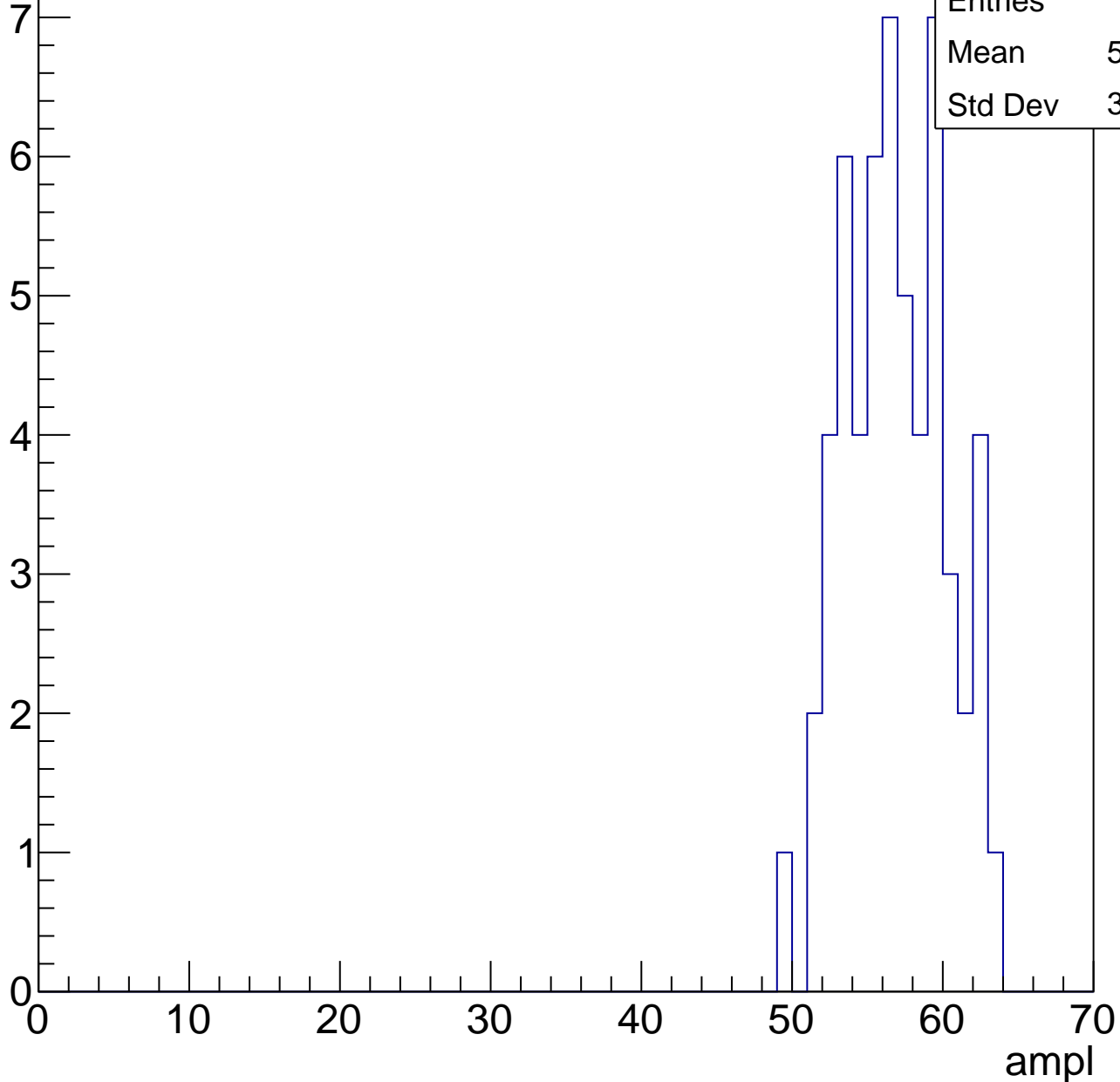
Entries	60
Mean	50.65
Std Dev	3.563



B1L101S, U23-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.85
Std Dev	9.524

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L101S, U23-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

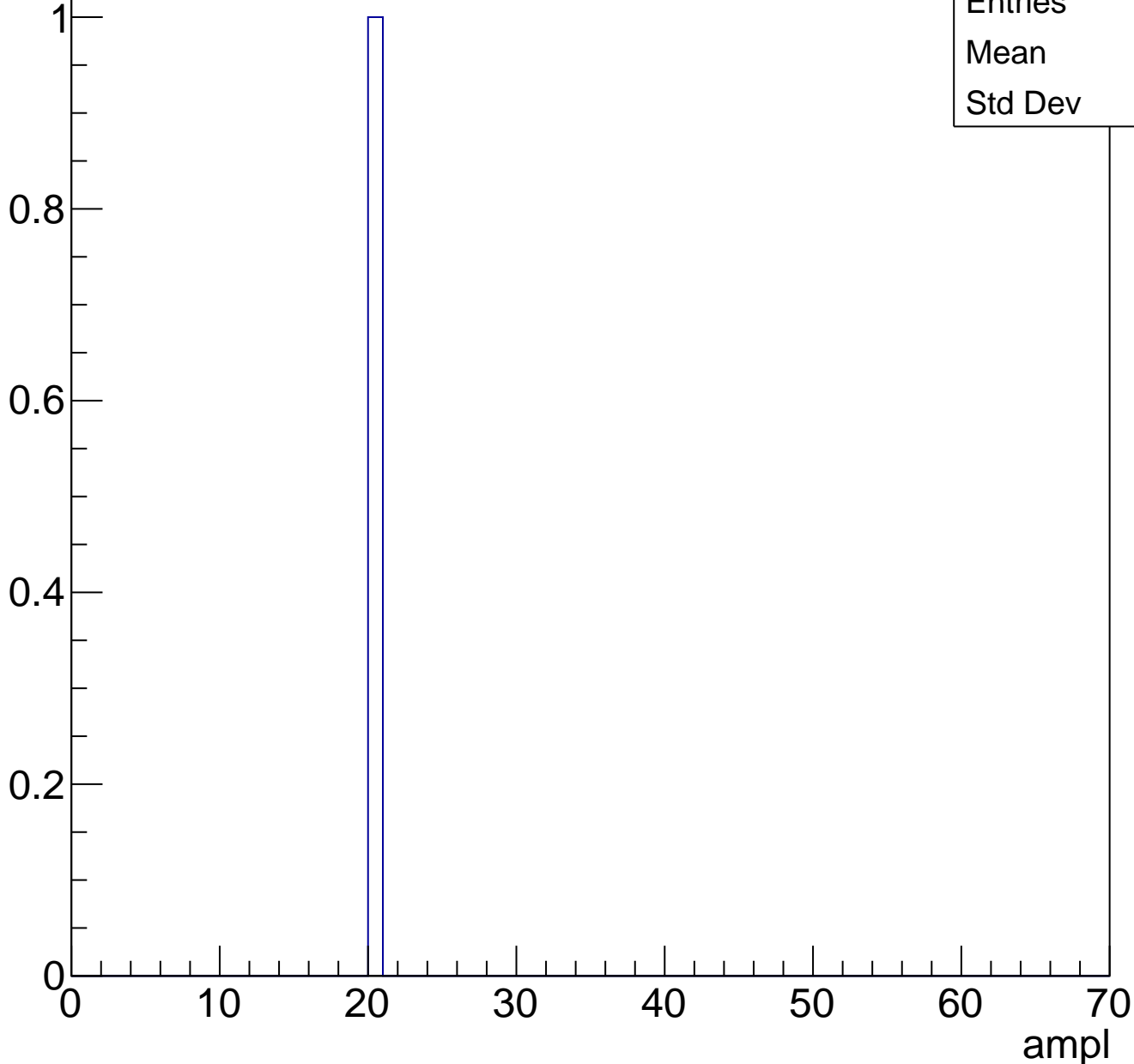
Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U23-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	20
Std Dev	0

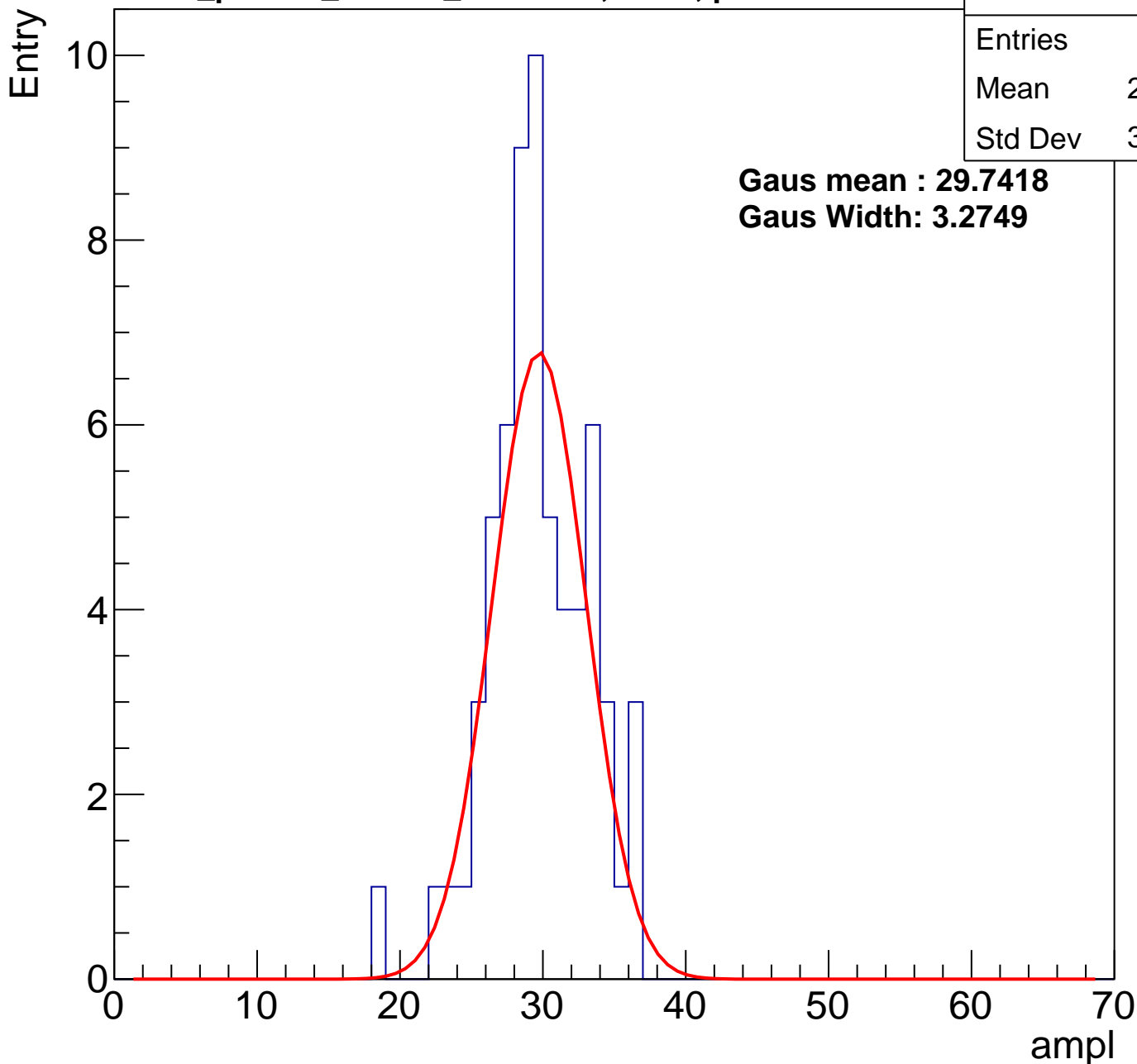
B1L101S, U23-ch56, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	29.22
Std Dev	3.484

Gaus mean : 29.7418

Gaus Width: 3.2749



B1L101S, U23-ch56, adc1

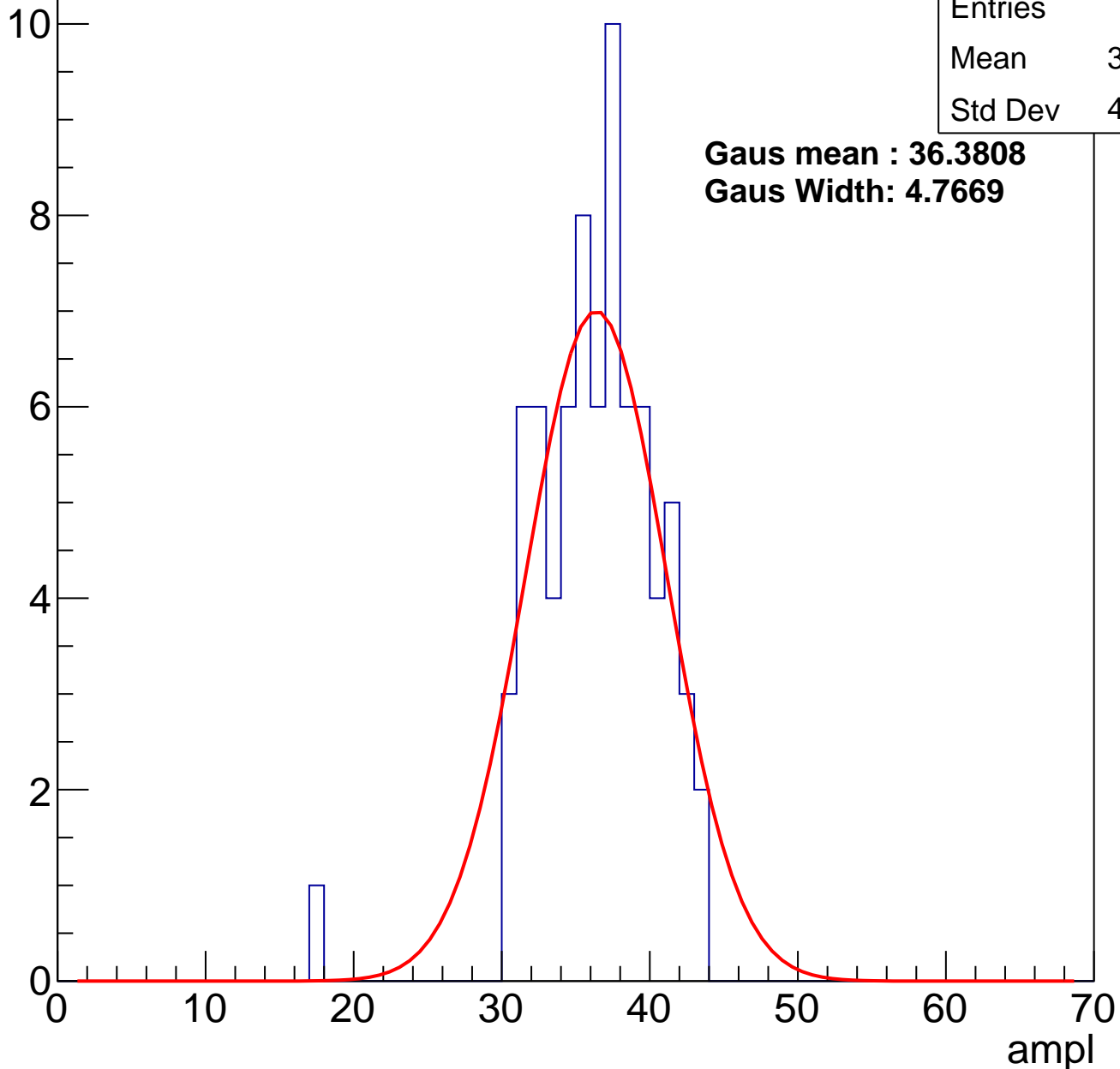
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	35.87
Std Dev	4.079

Gaus mean : 36.3808

Gaus Width: 4.7669

Entry



B1L101S, U23-ch56, adc2

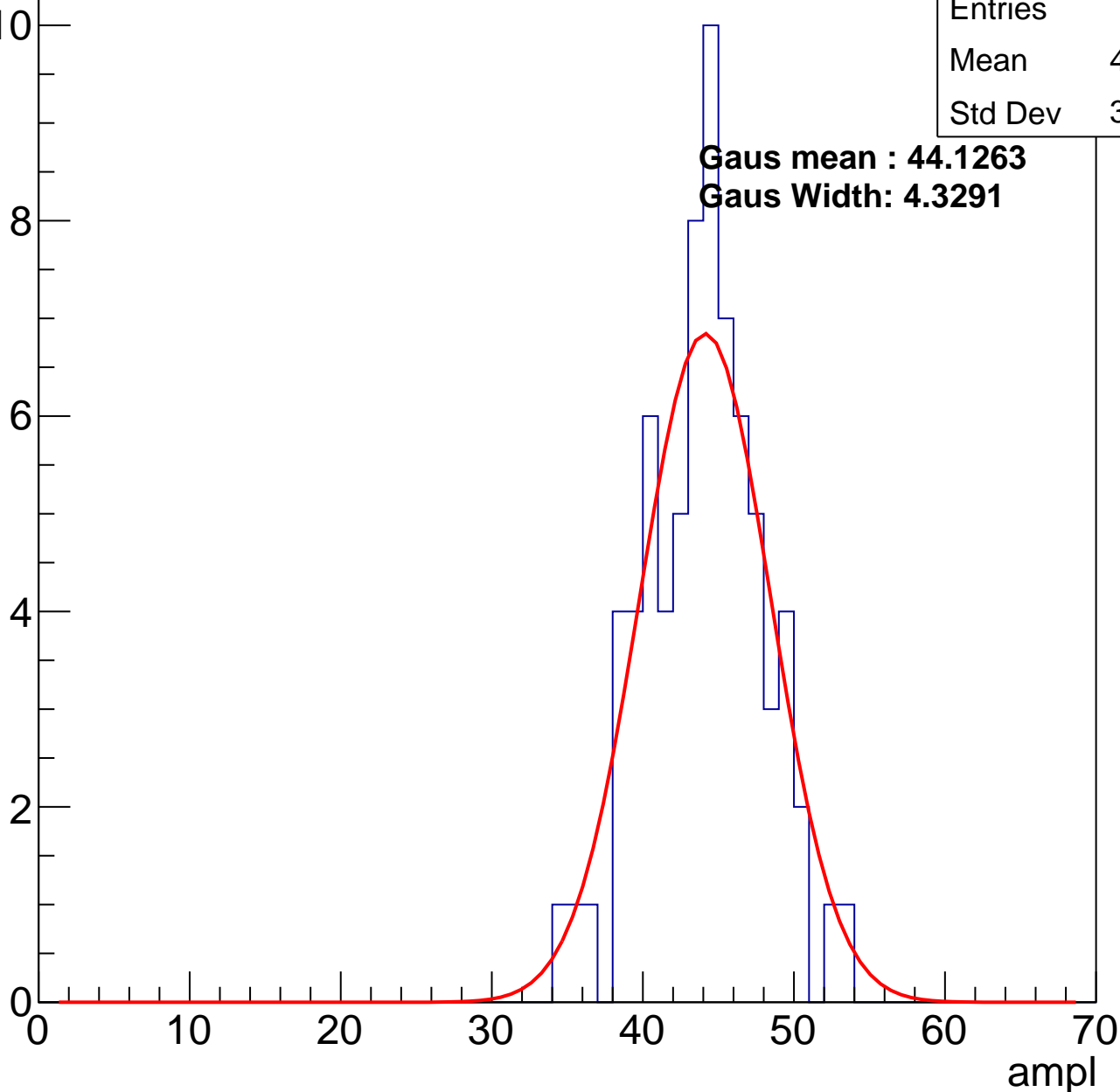
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.59
Std Dev	3.842

Gaus mean : 44.1263

Gaus Width: 4.3291

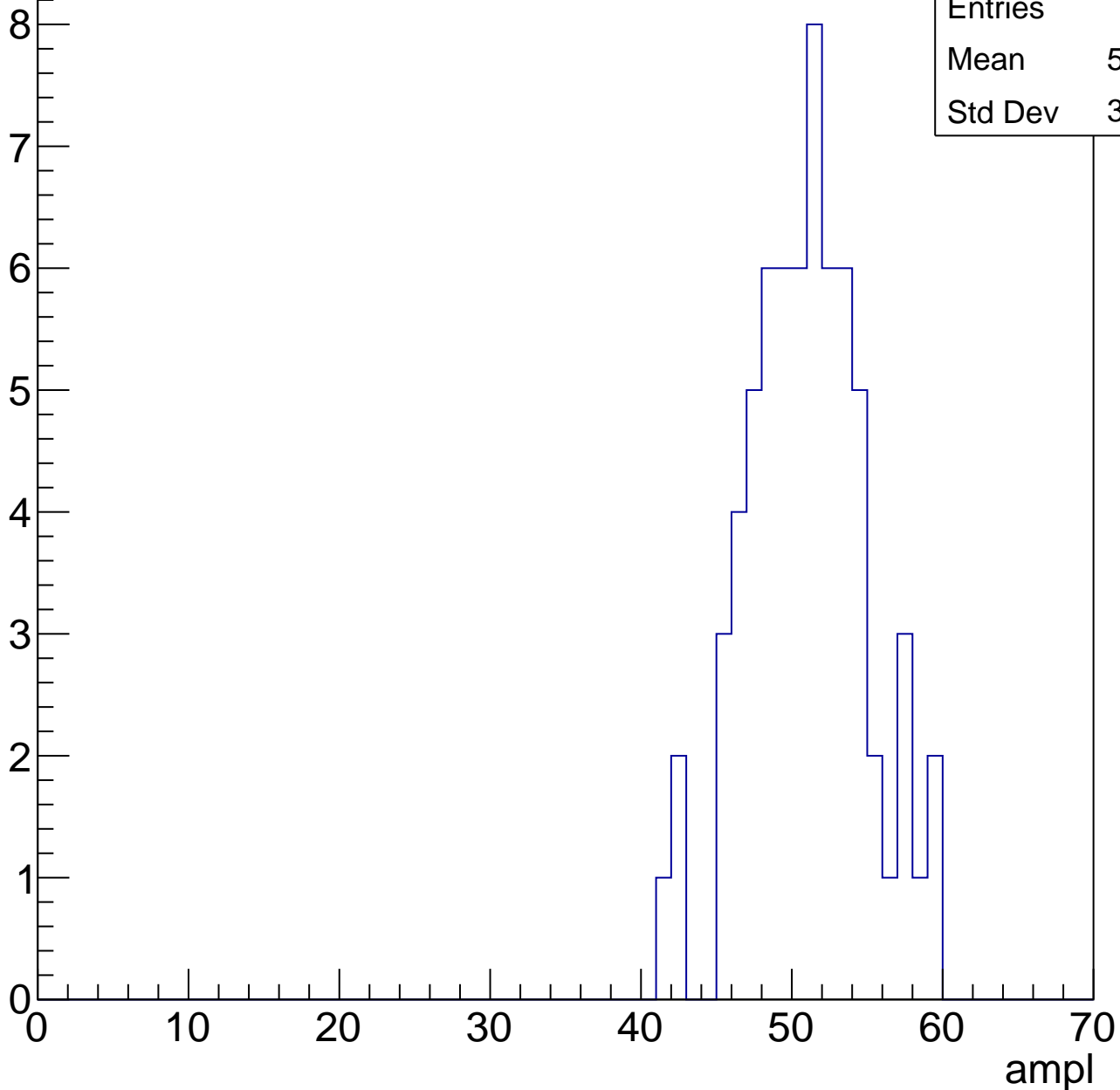


B1L101S, U23-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.48
Std Dev	3.926

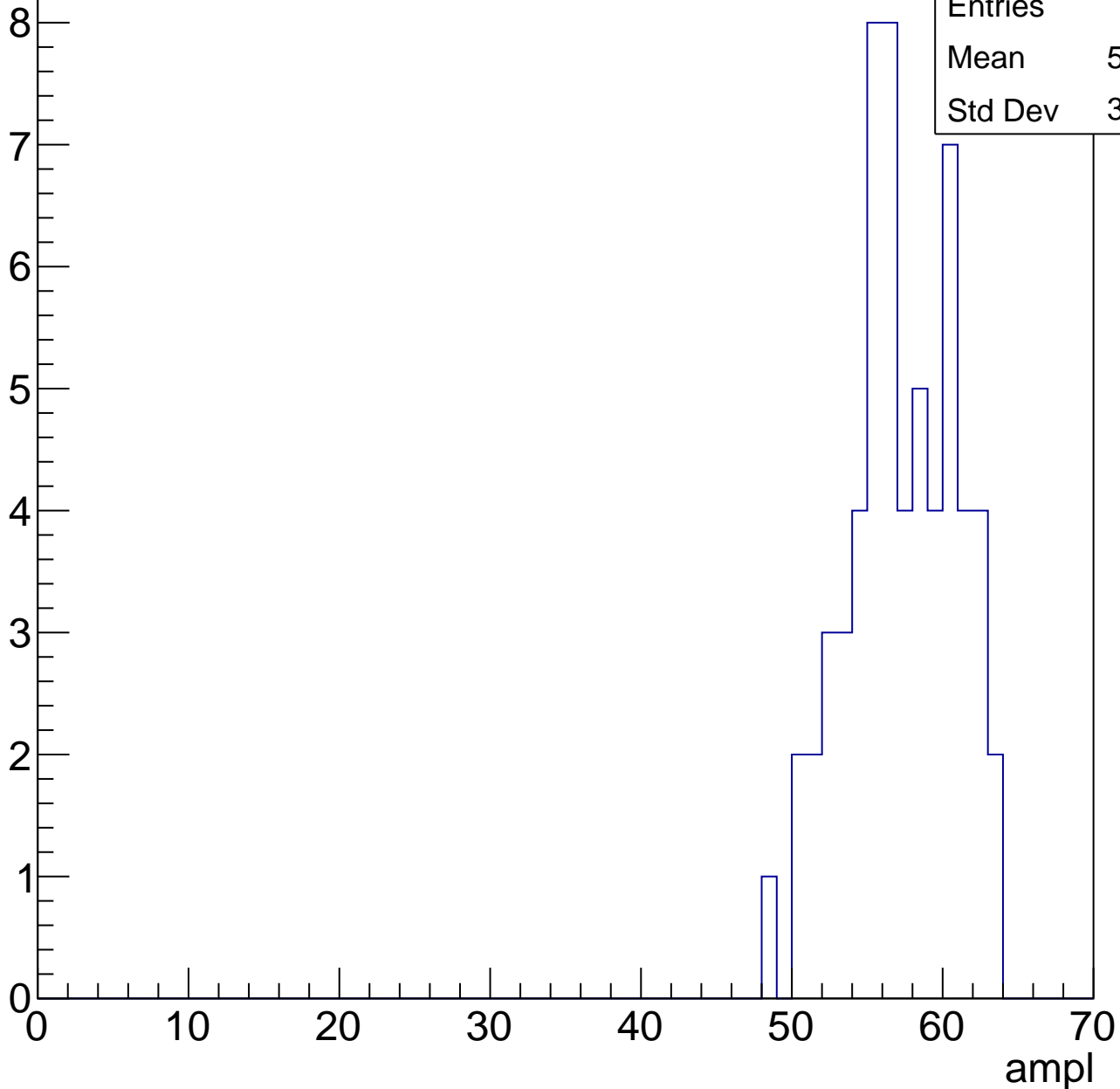


B1L101S, U23-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.74
Std Dev	3.543

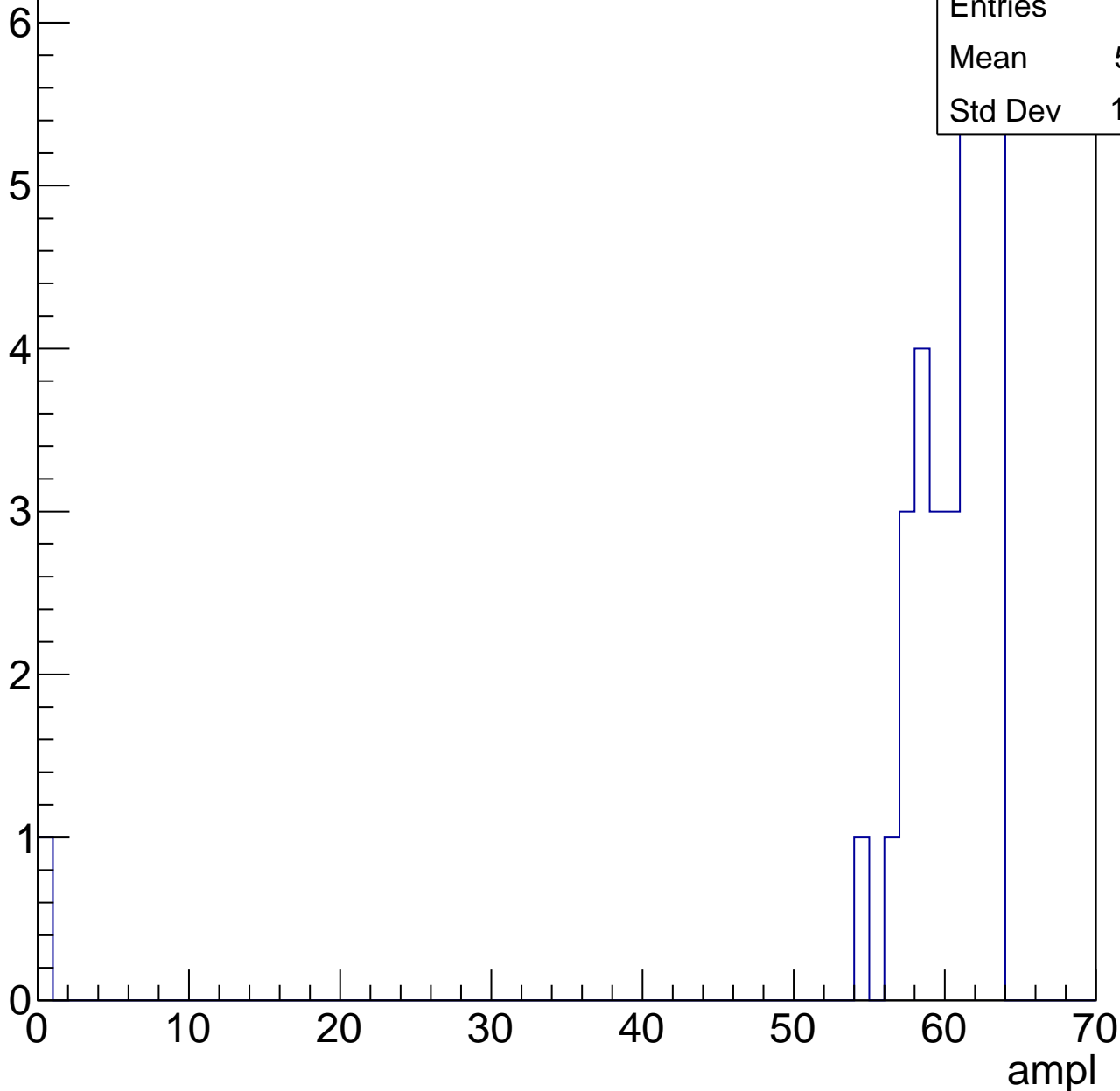


B1L101S, U23-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.41
Std Dev	10.43



B1L101S, U23-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch57, adc0

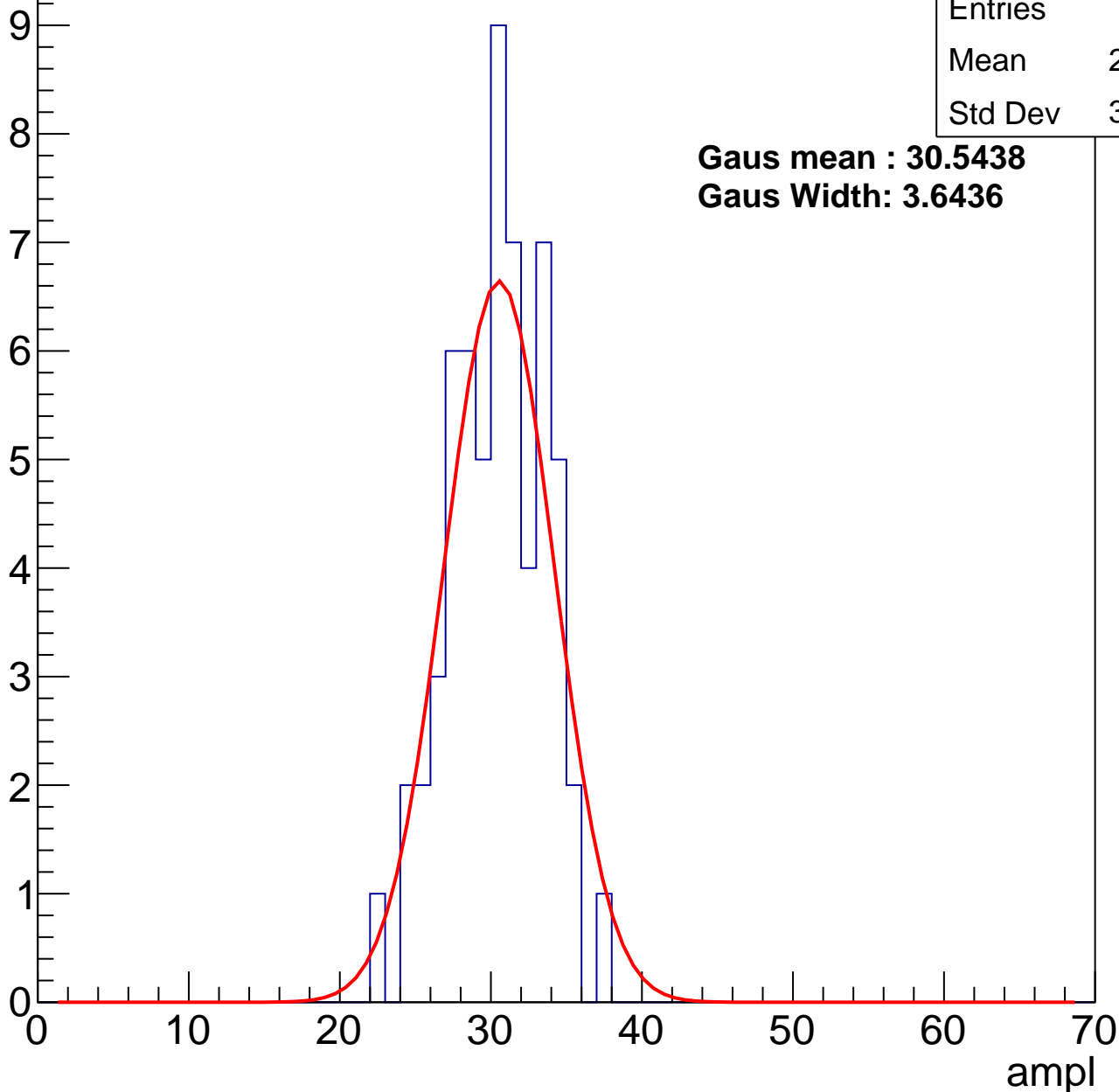
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.93
Std Dev	3.114

Gaus mean : 30.5438

Gaus Width: 3.6436



B1L101S, U23-ch57, adc1

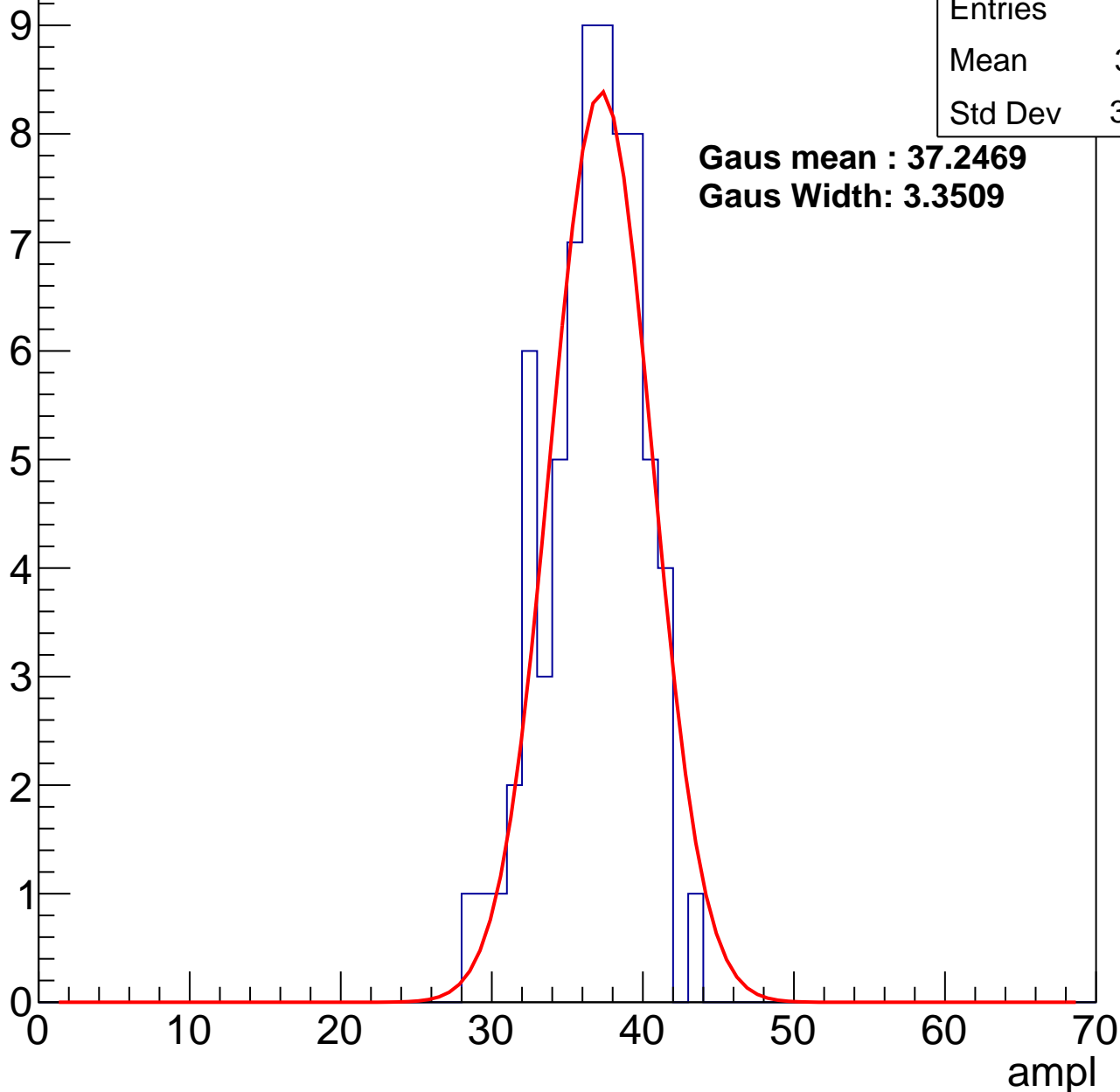
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	36.21
Std Dev	3.135

Gaus mean : 37.2469

Gaus Width: 3.3509



B1L101S, U23-ch57, adc2

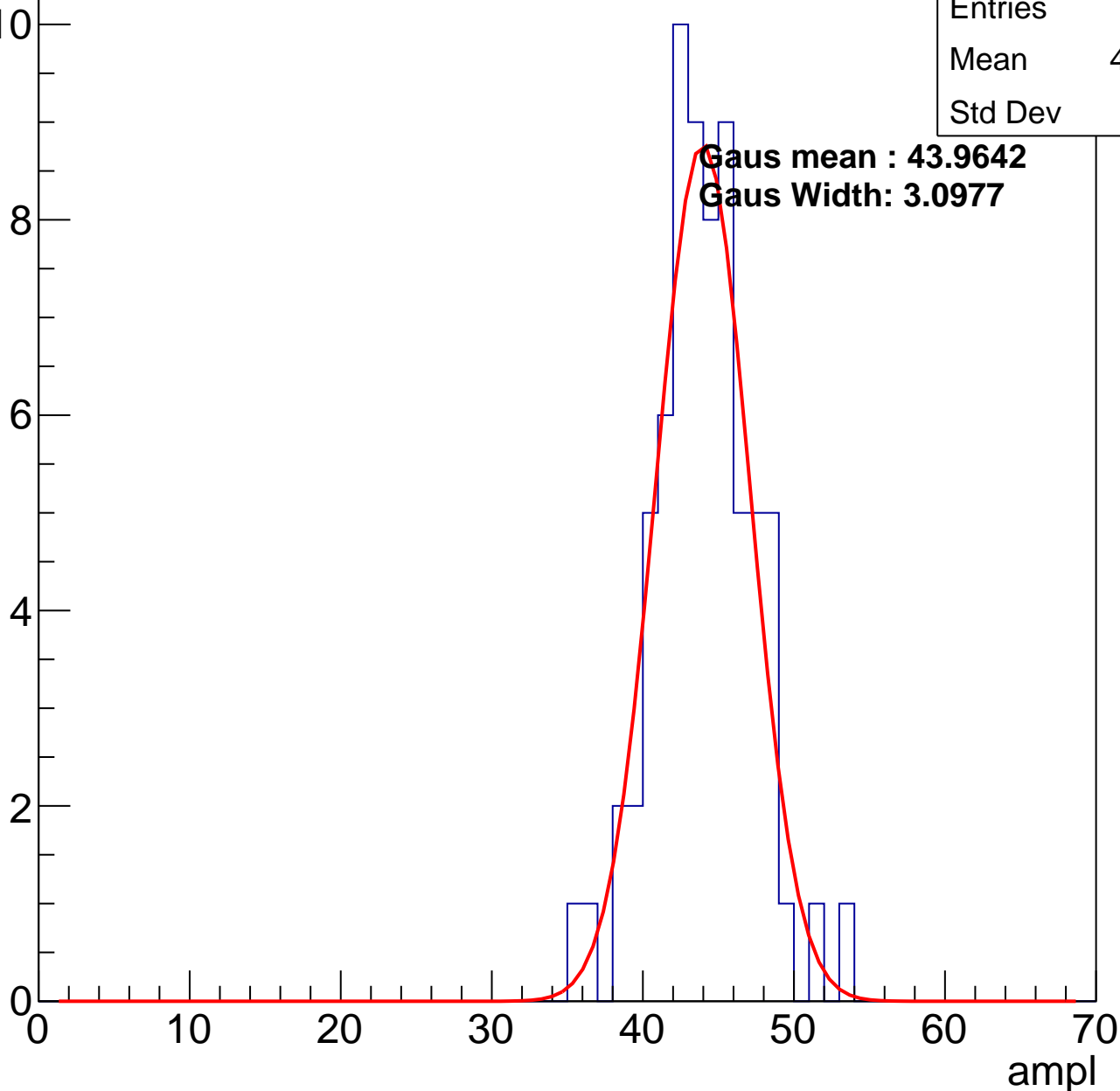
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.56
Std Dev	3.24

Gaus mean : 43.9642

Gaus Width: 3.0977

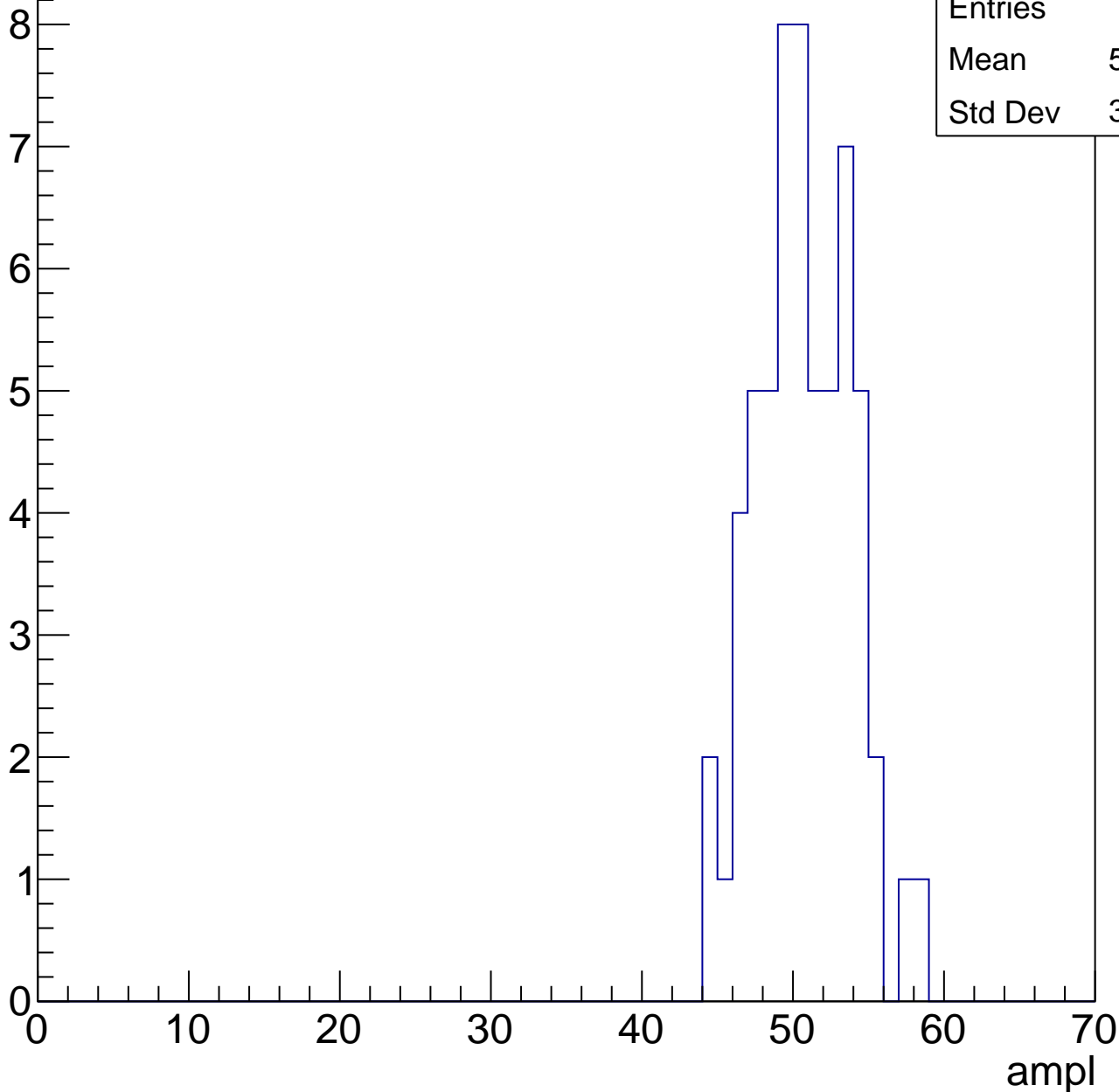


B1L101S, U23-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

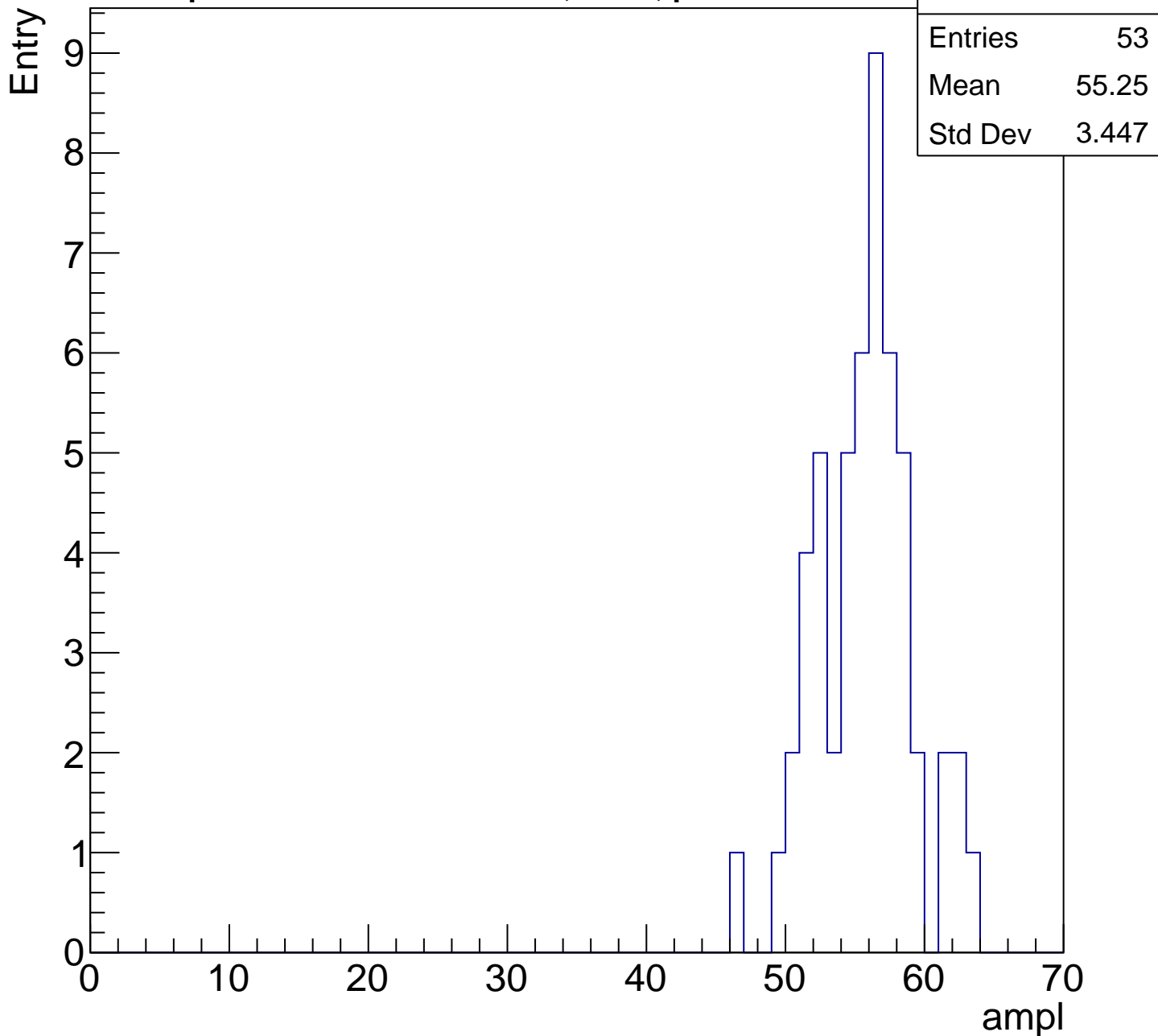
Entry

Entries	59
Mean	50.25
Std Dev	3.084



B1L101S, U23-ch57, adc4

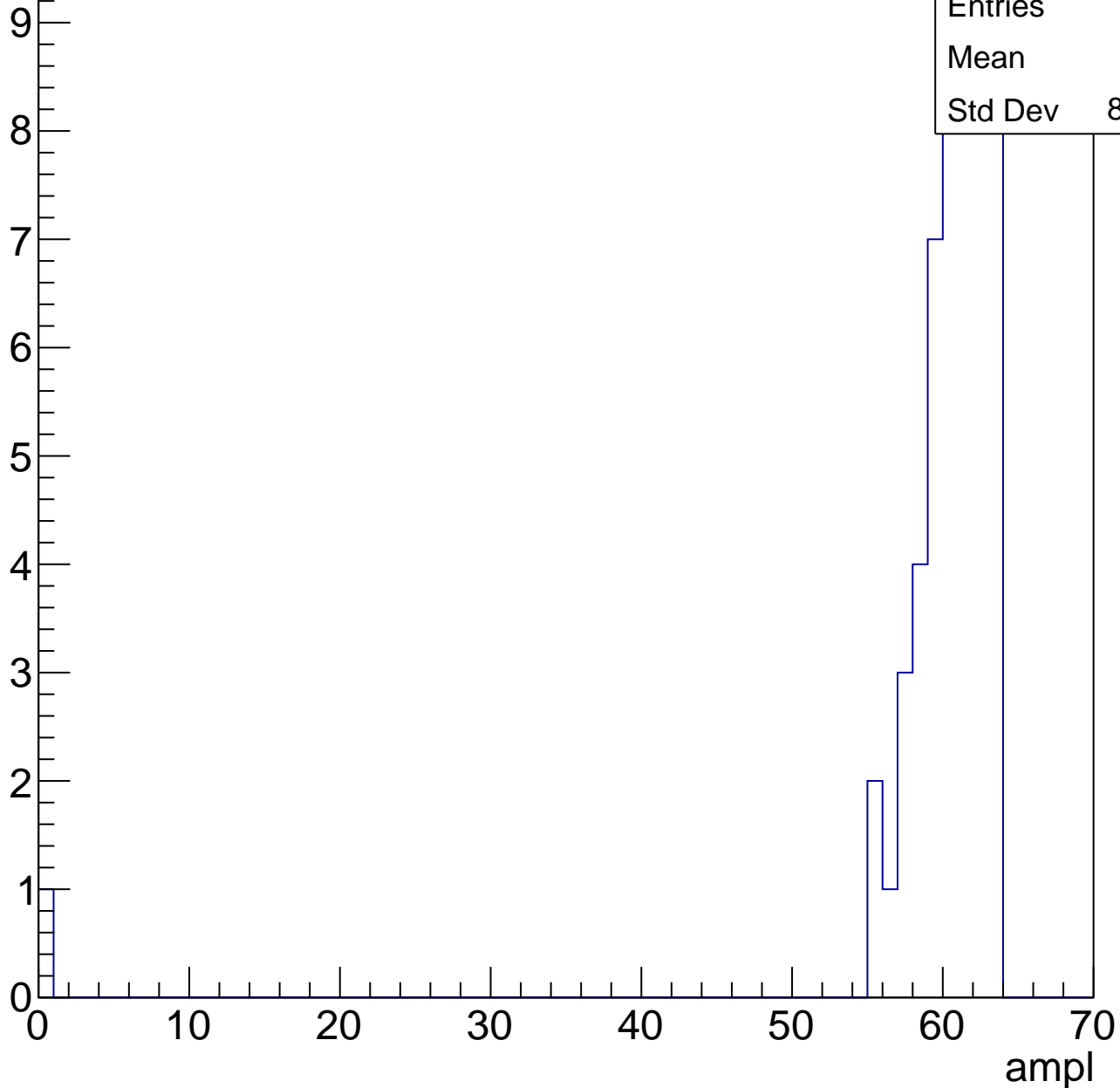
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

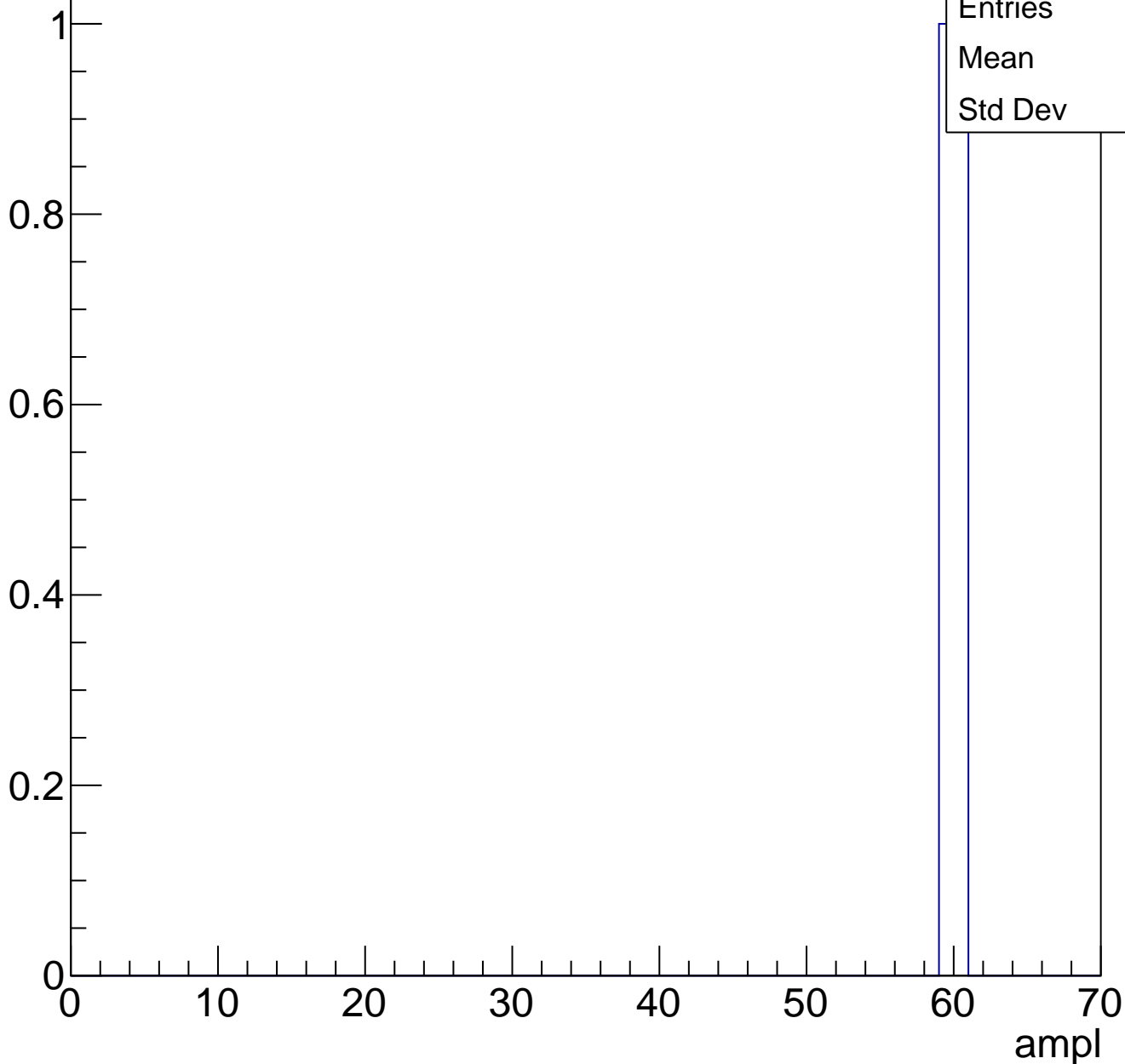
Entry



B1L101S, U23-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch58, adc0

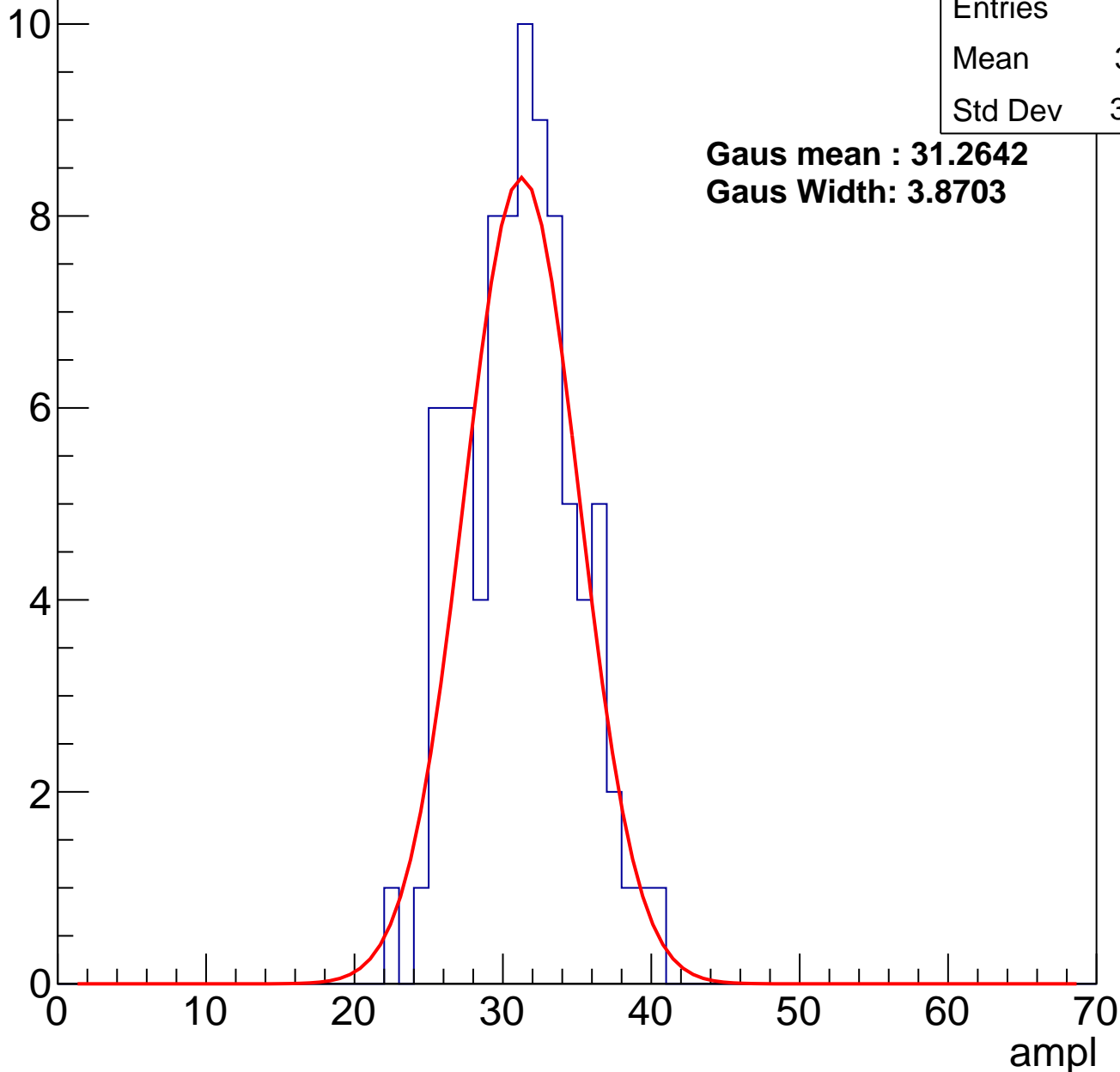
calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	30.71
Std Dev	3.738

Gaus mean : 31.2642

Gaus Width: 3.8703

Entry



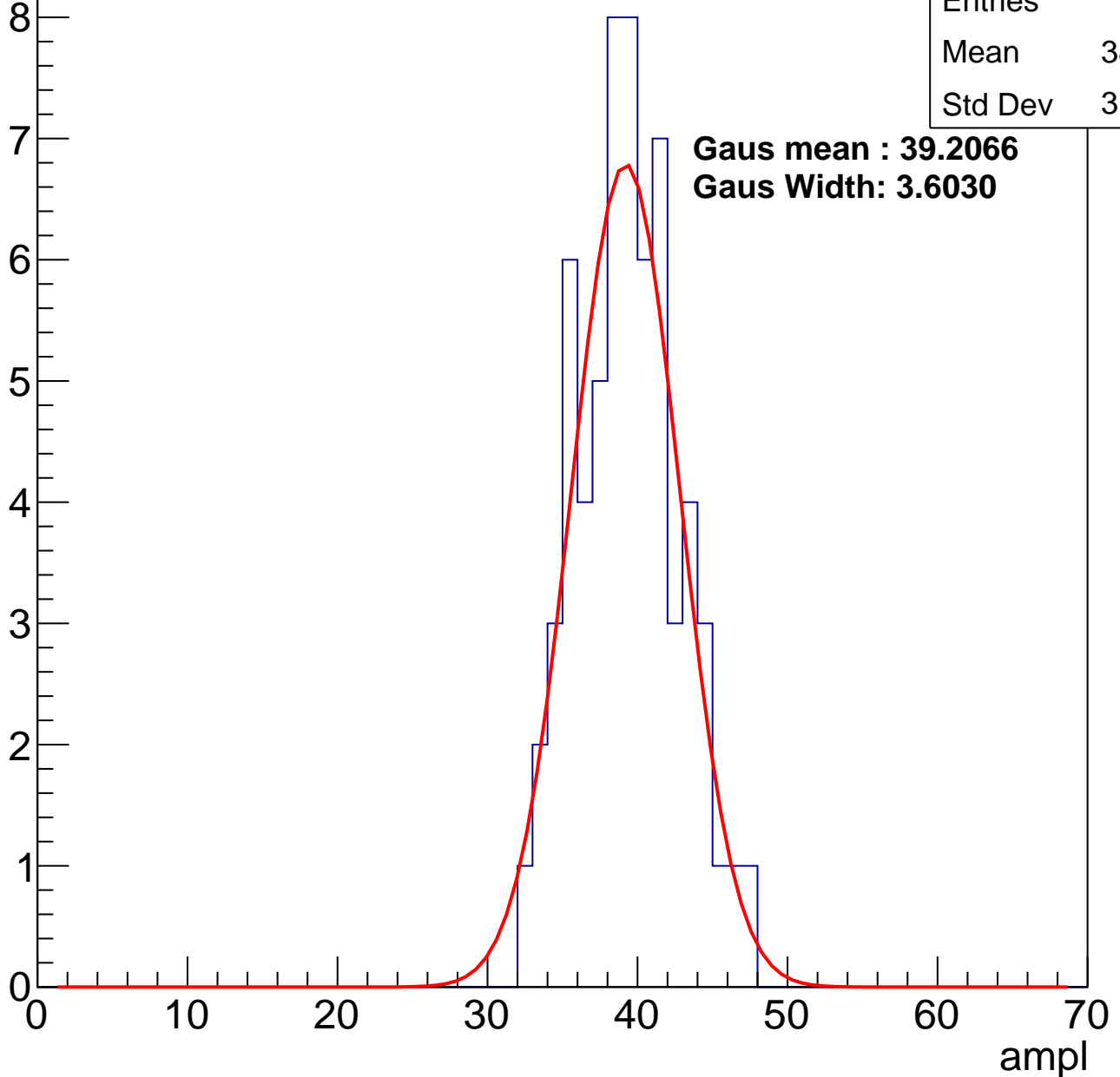
B1L101S, U23-ch58, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.89
Std Dev	3.343

Gaus mean : 39.2066
Gaus Width: 3.6030



B1L101S, U23-ch58, adc2

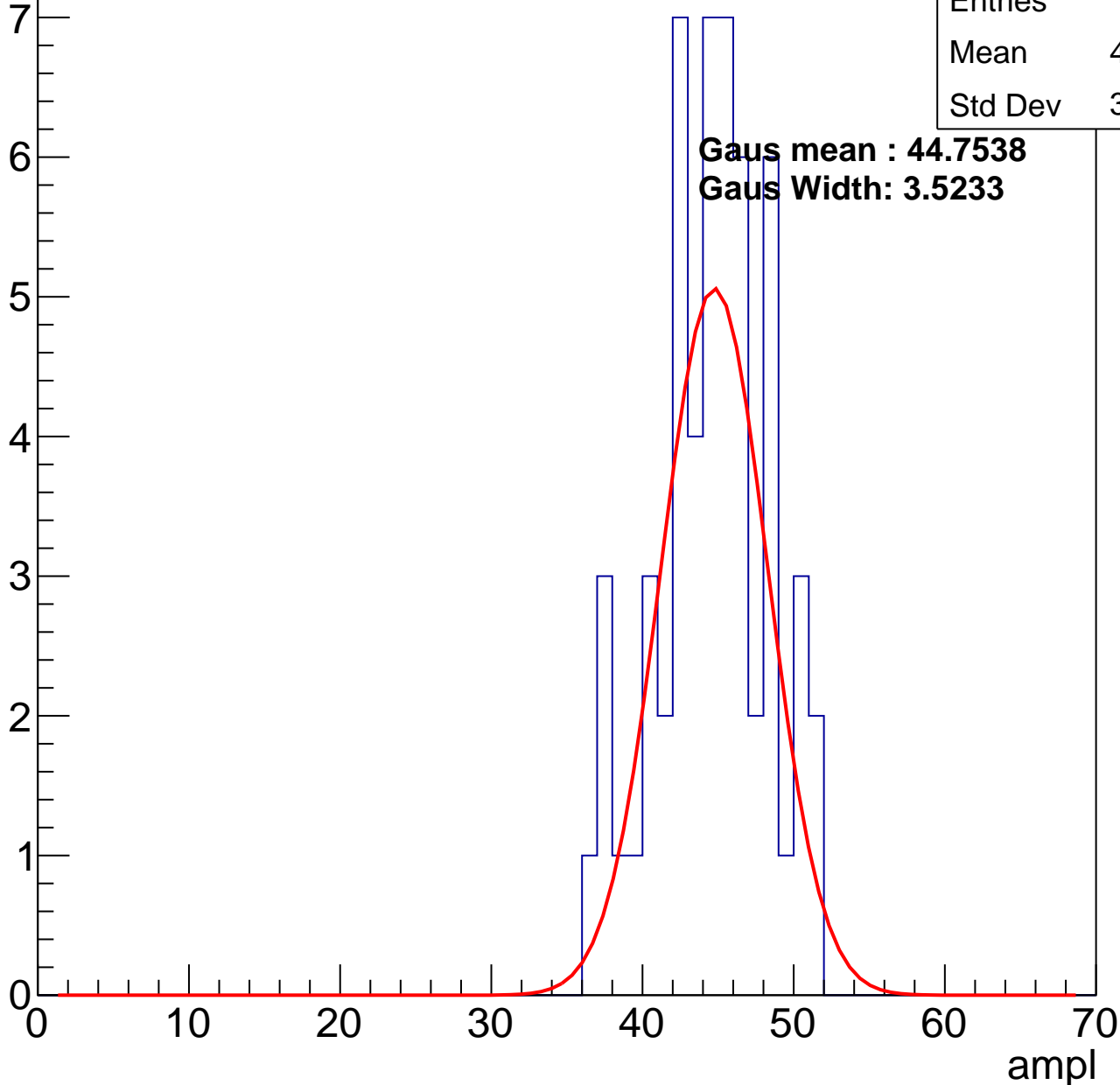
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	44.18
Std Dev	3.645

Gaus mean : 44.7538

Gaus Width: 3.5233

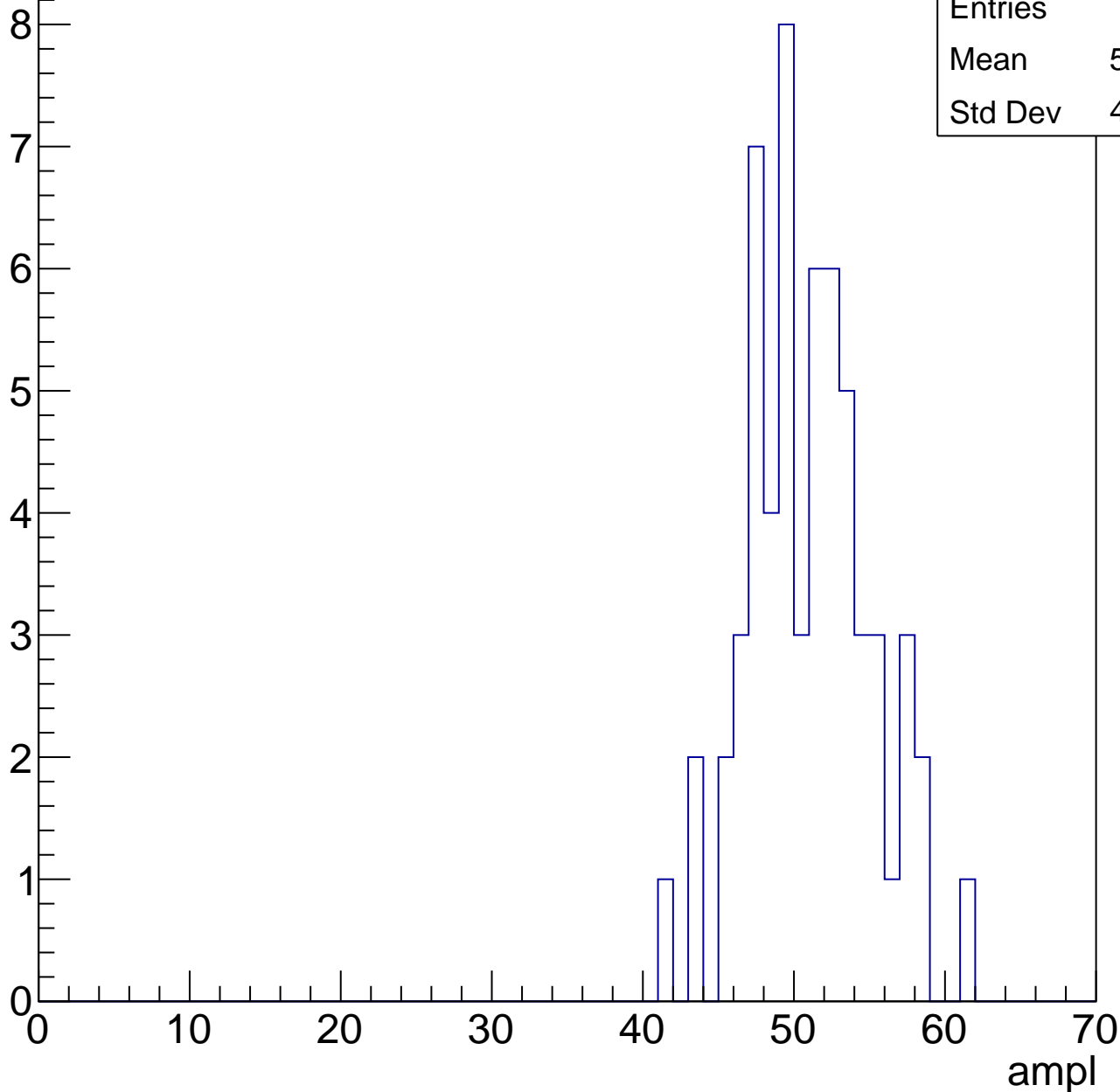


B1L101S, U23-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.53
Std Dev	4.035

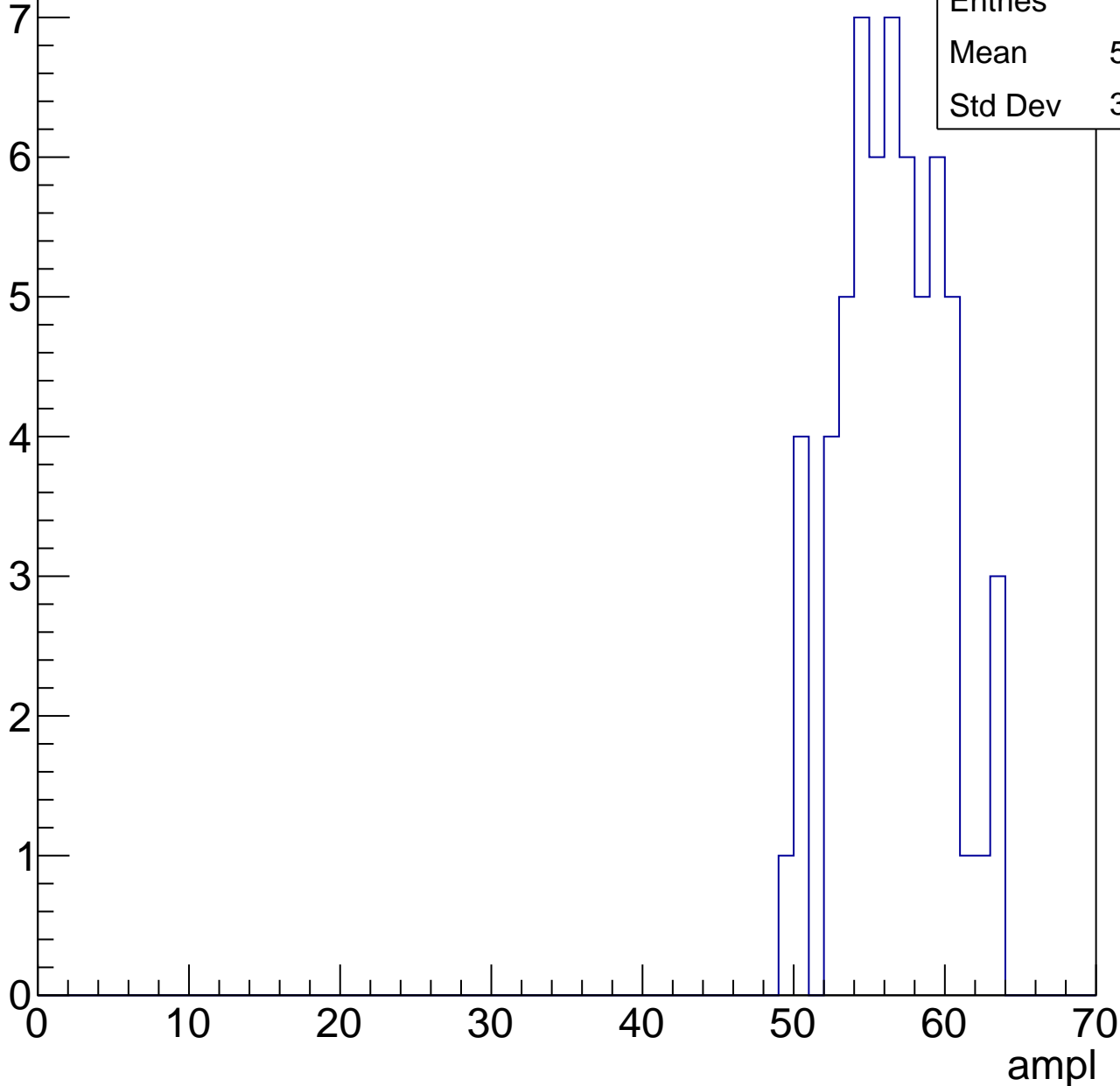


B1L101S, U23-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.07
Std Dev	3.397

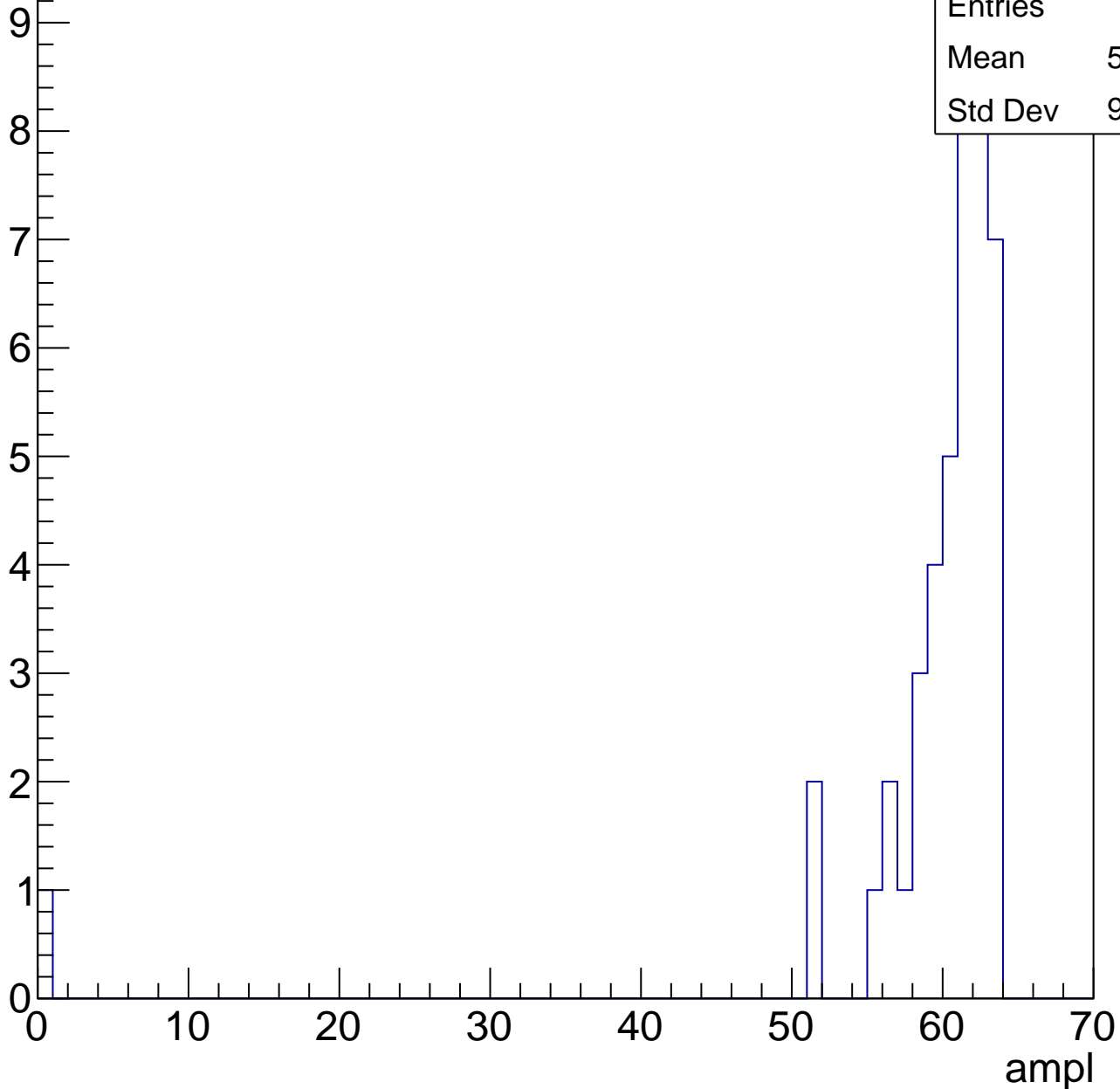


B1L101S, U23-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.67
Std Dev	9.494



B1L101S, U23-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch59, adc0

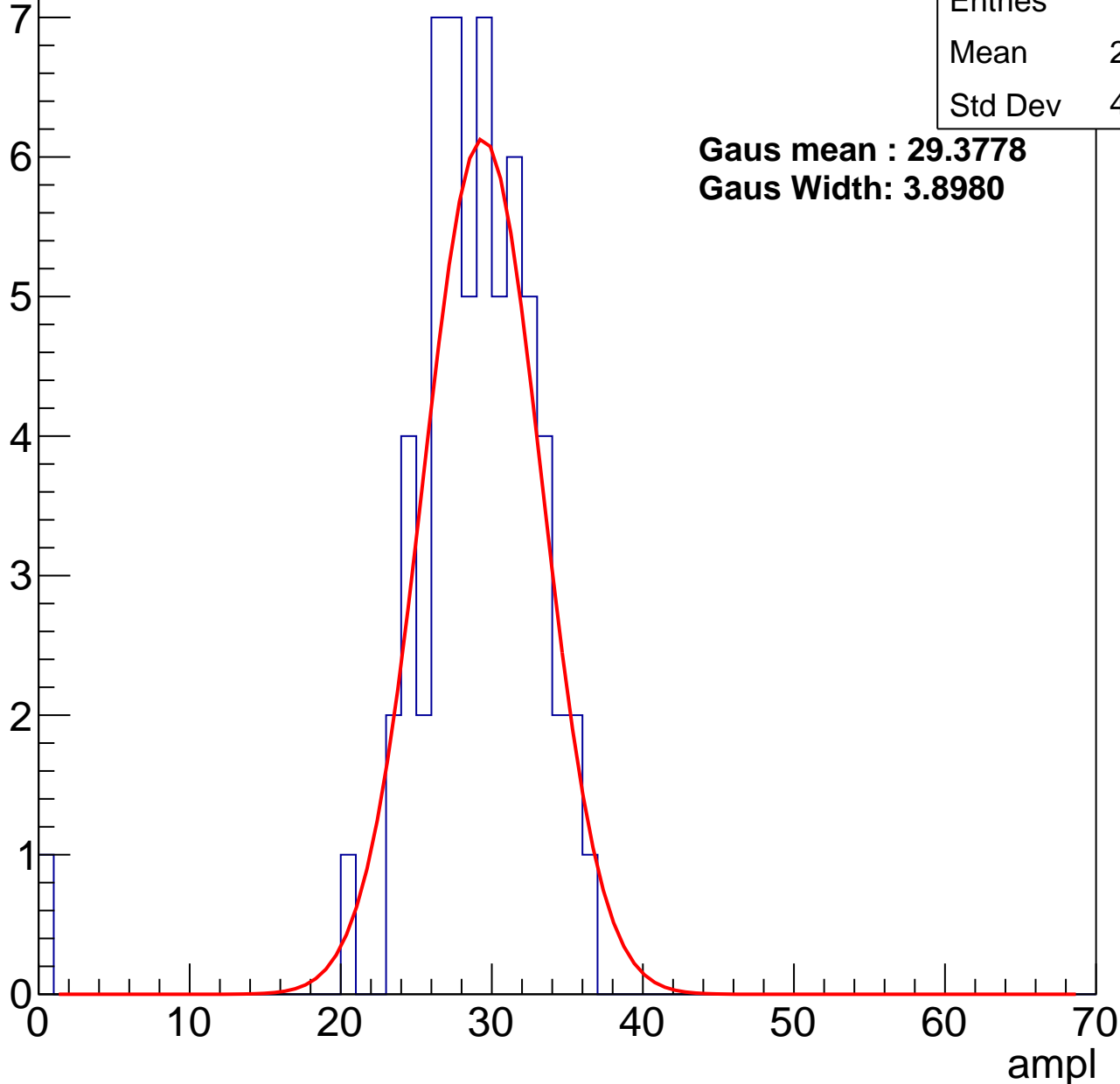
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	28.33
Std Dev	4.965

Gaus mean : 29.3778

Gaus Width: 3.8980



B1L101S, U23-ch59, adc1

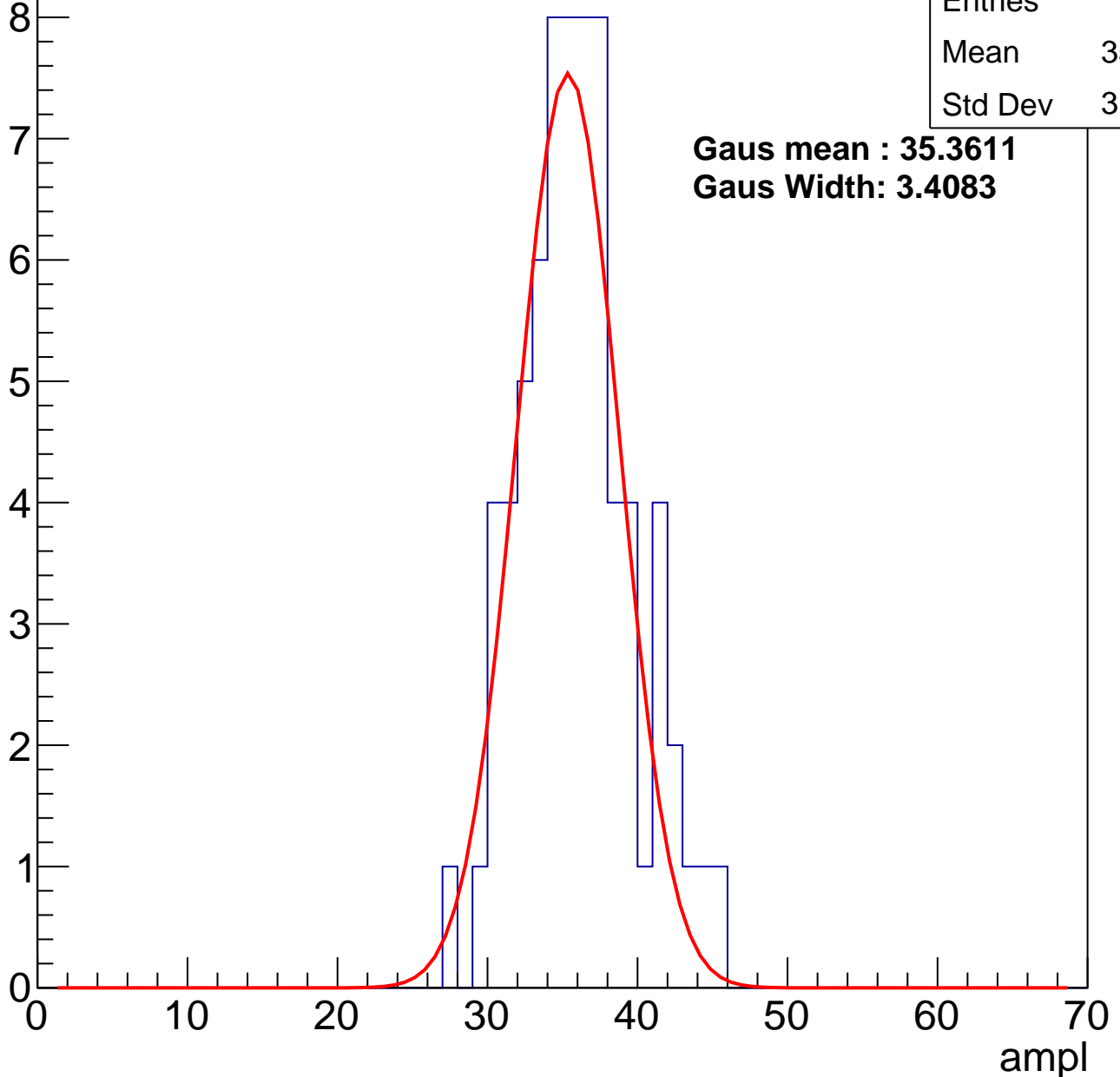
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.52
Std Dev	3.703

Gaus mean : 35.3611

Gaus Width: 3.4083



B1L101S, U23-ch59, adc2

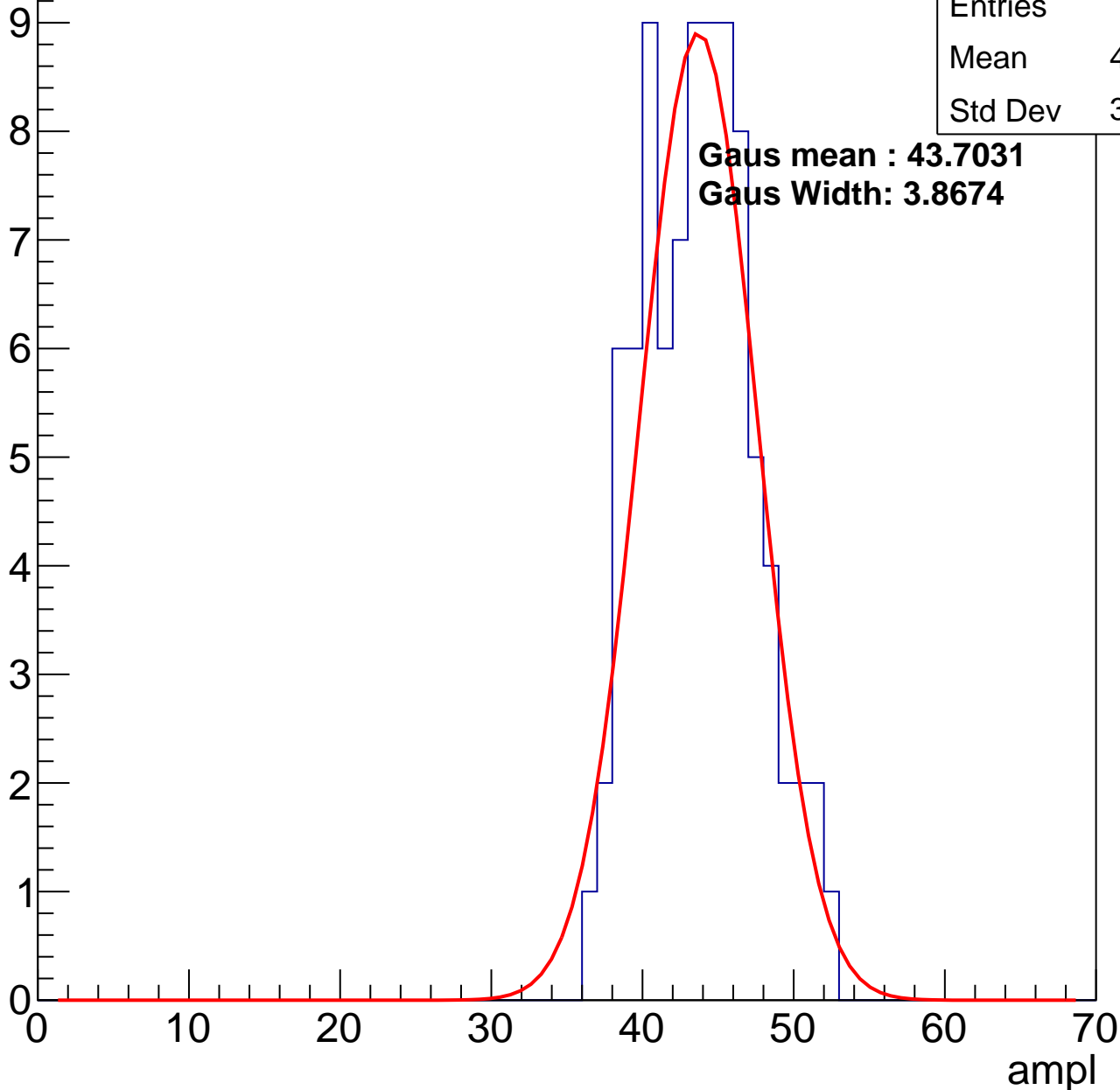
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	43.26
Std Dev	3.613

Gaus mean : 43.7031

Gaus Width: 3.8674

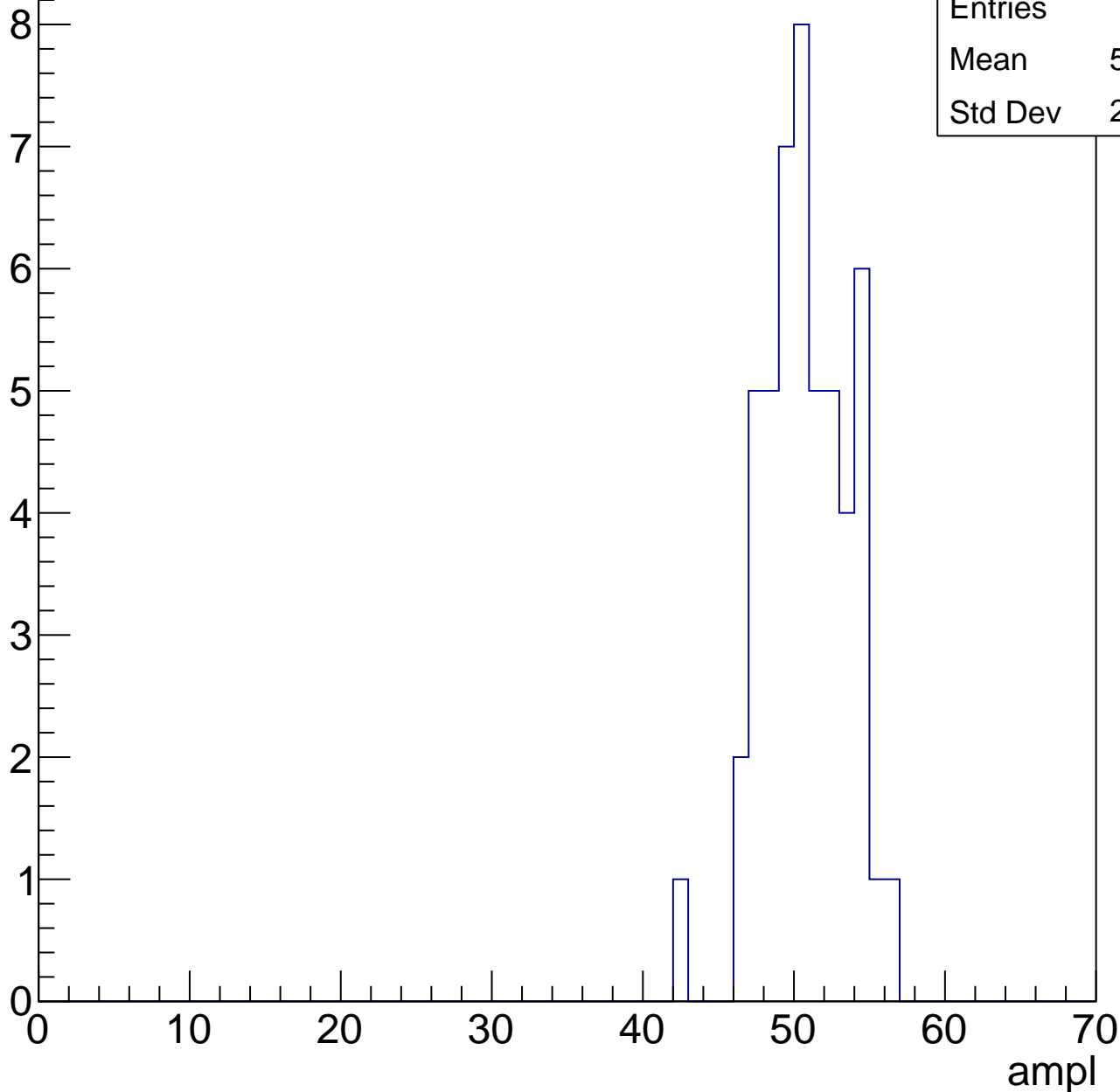


B1L101S, U23-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	50.28
Std Dev	2.764

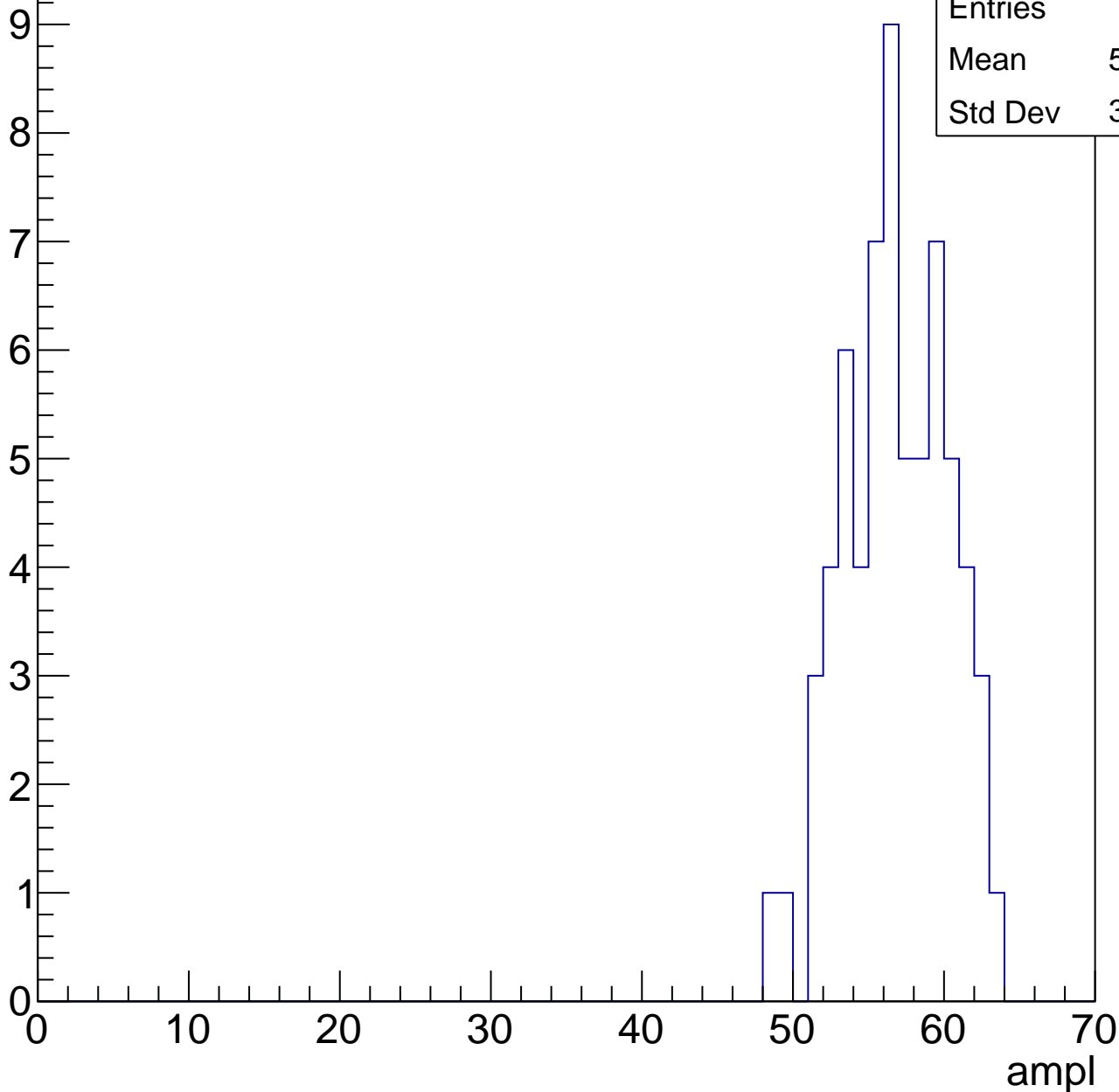


B1L101S, U23-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	56.34
Std Dev	3.389

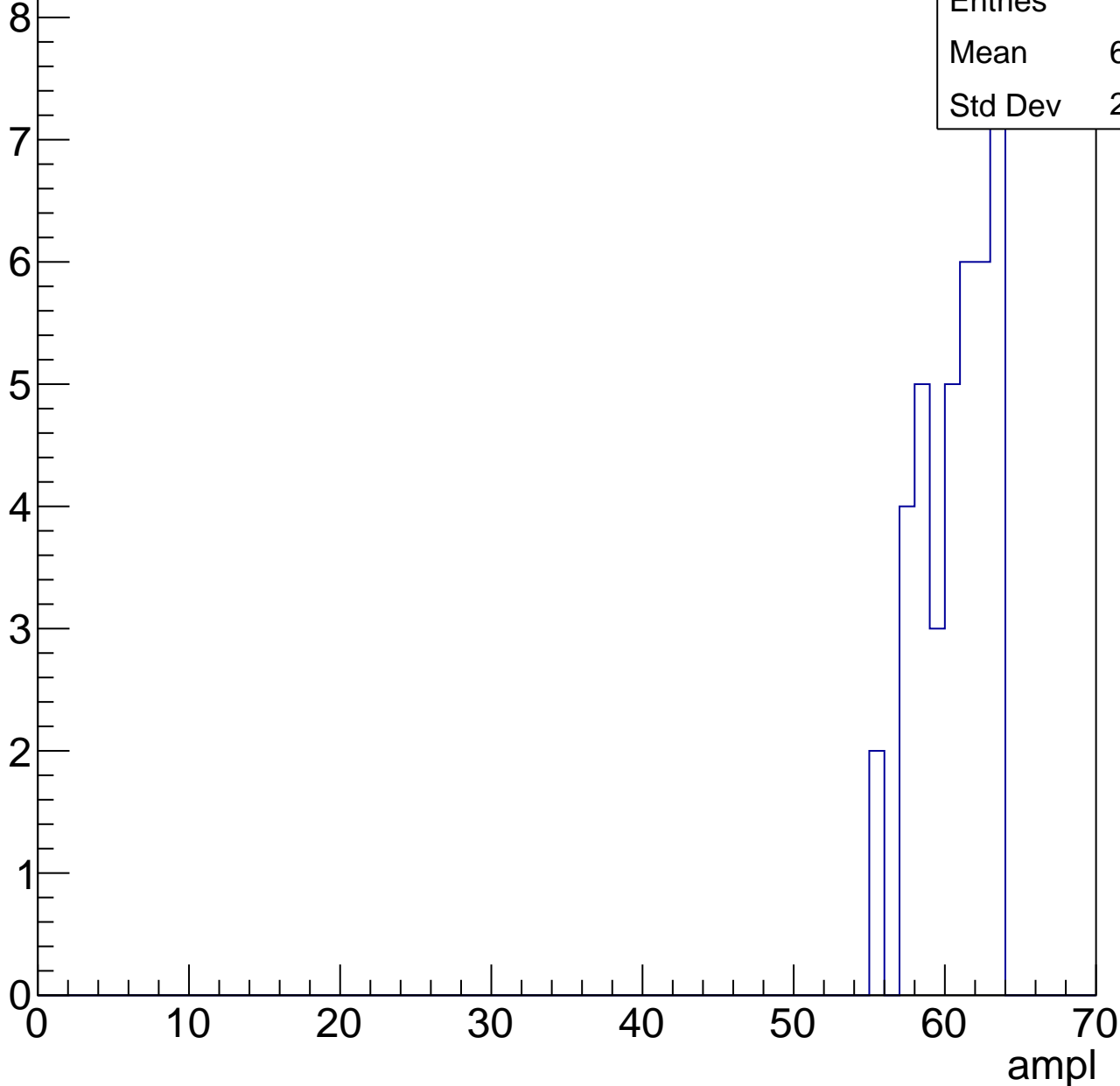


B1L101S, U23-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

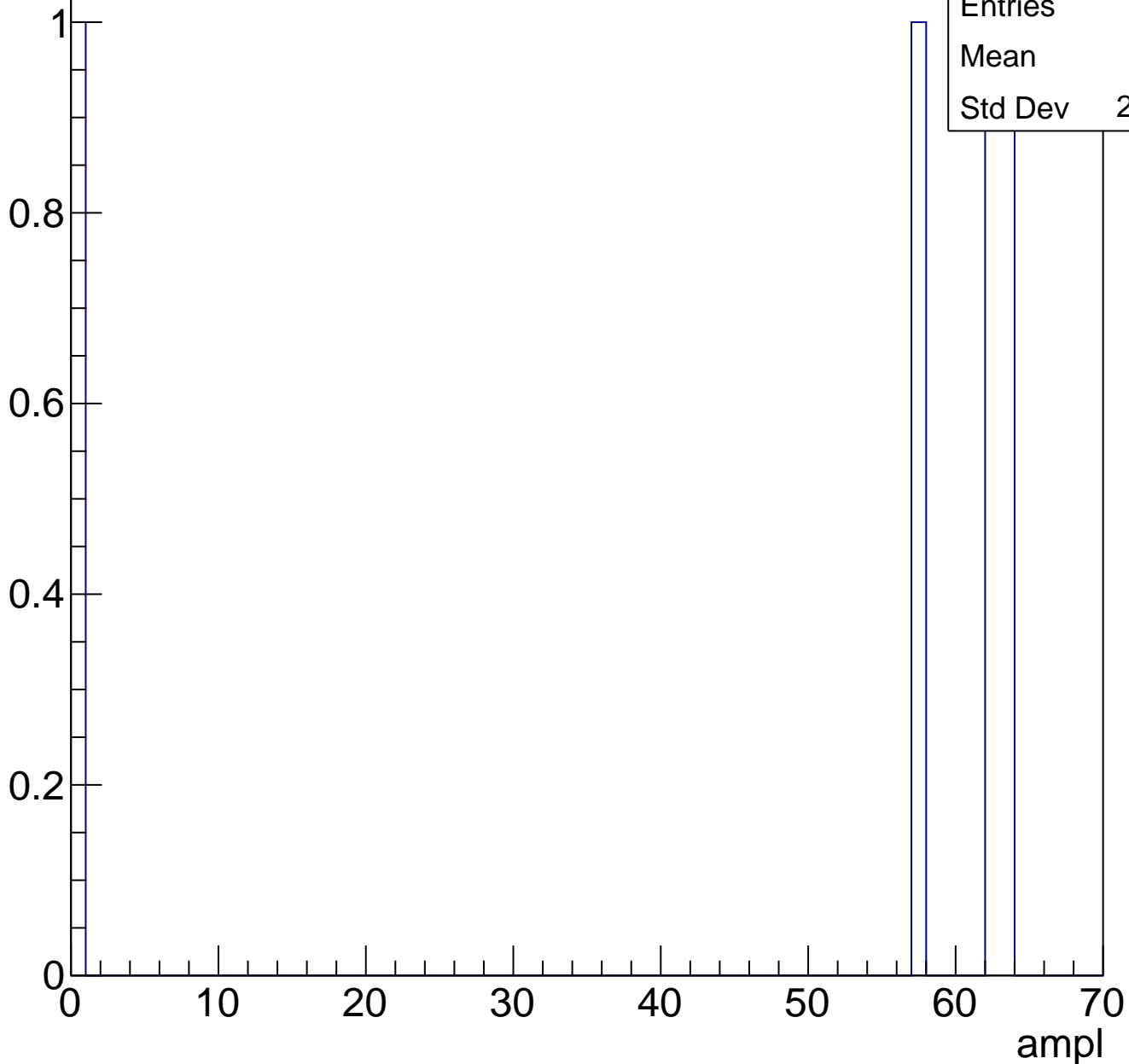
Entries	39
Mean	60.18
Std Dev	2.319



B1L101S, U23-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch60, adc0

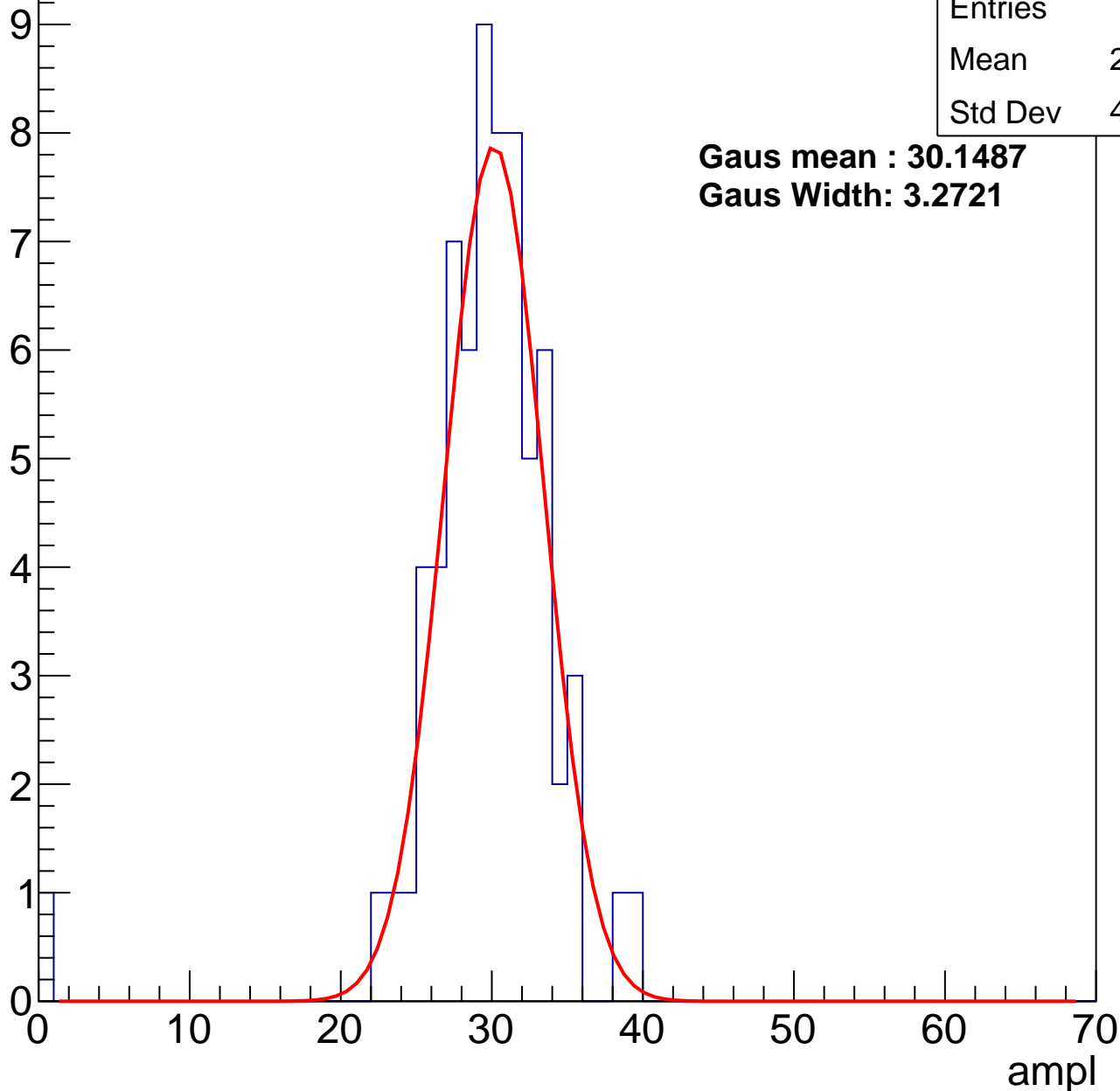
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.22
Std Dev	4.853

Gaus mean : 30.1487

Gaus Width: 3.2721

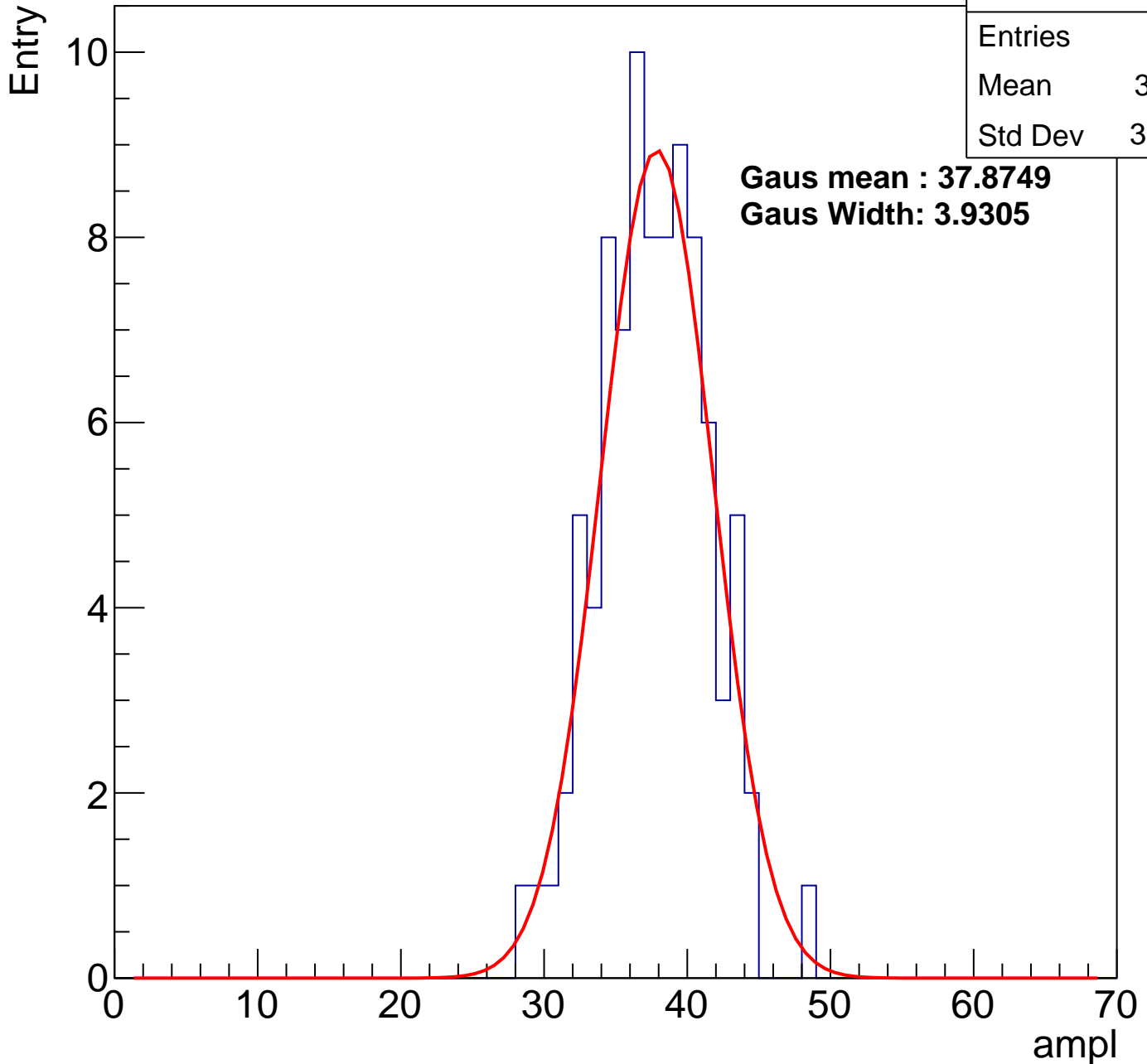


B1L101S, U23-ch60, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	37.21
Std Dev	3.743

Gaus mean : 37.8749
Gaus Width: 3.9305



B1L101S, U23-ch60, adc2

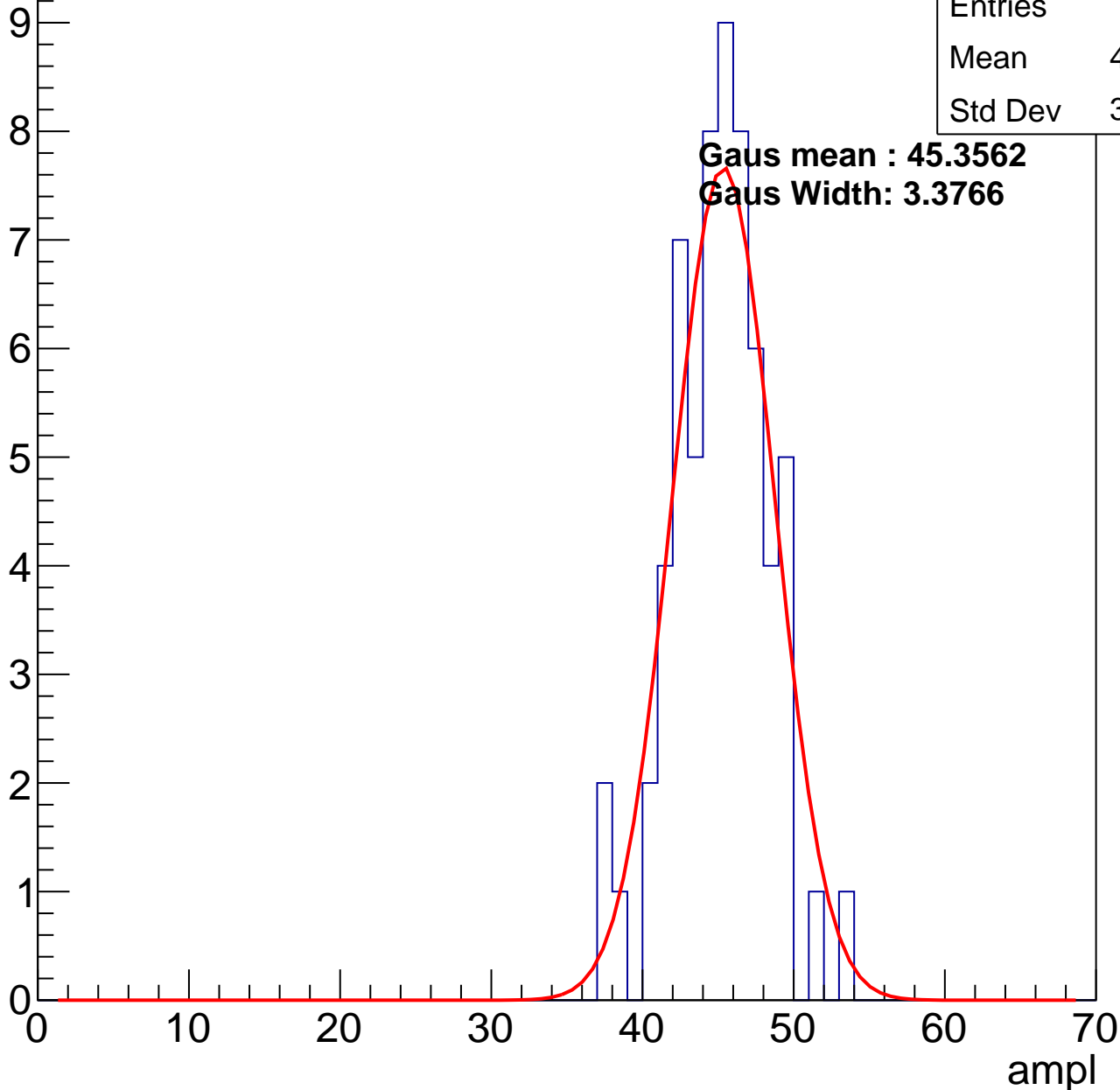
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.65
Std Dev	3.143

Gaus mean : 45.3562

Gaus Width: 3.3766

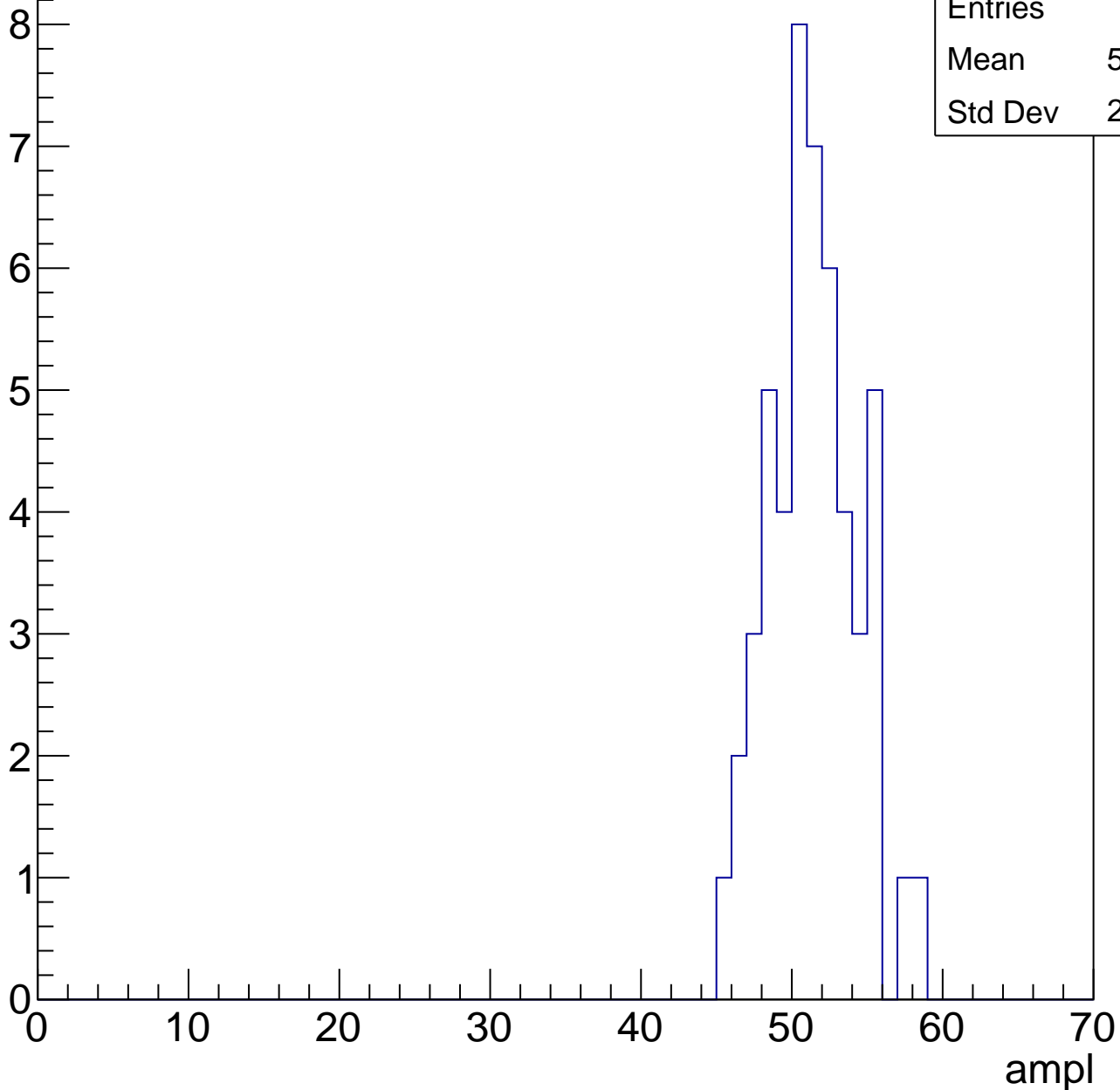


B1L101S, U23-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	50.94
Std Dev	2.887

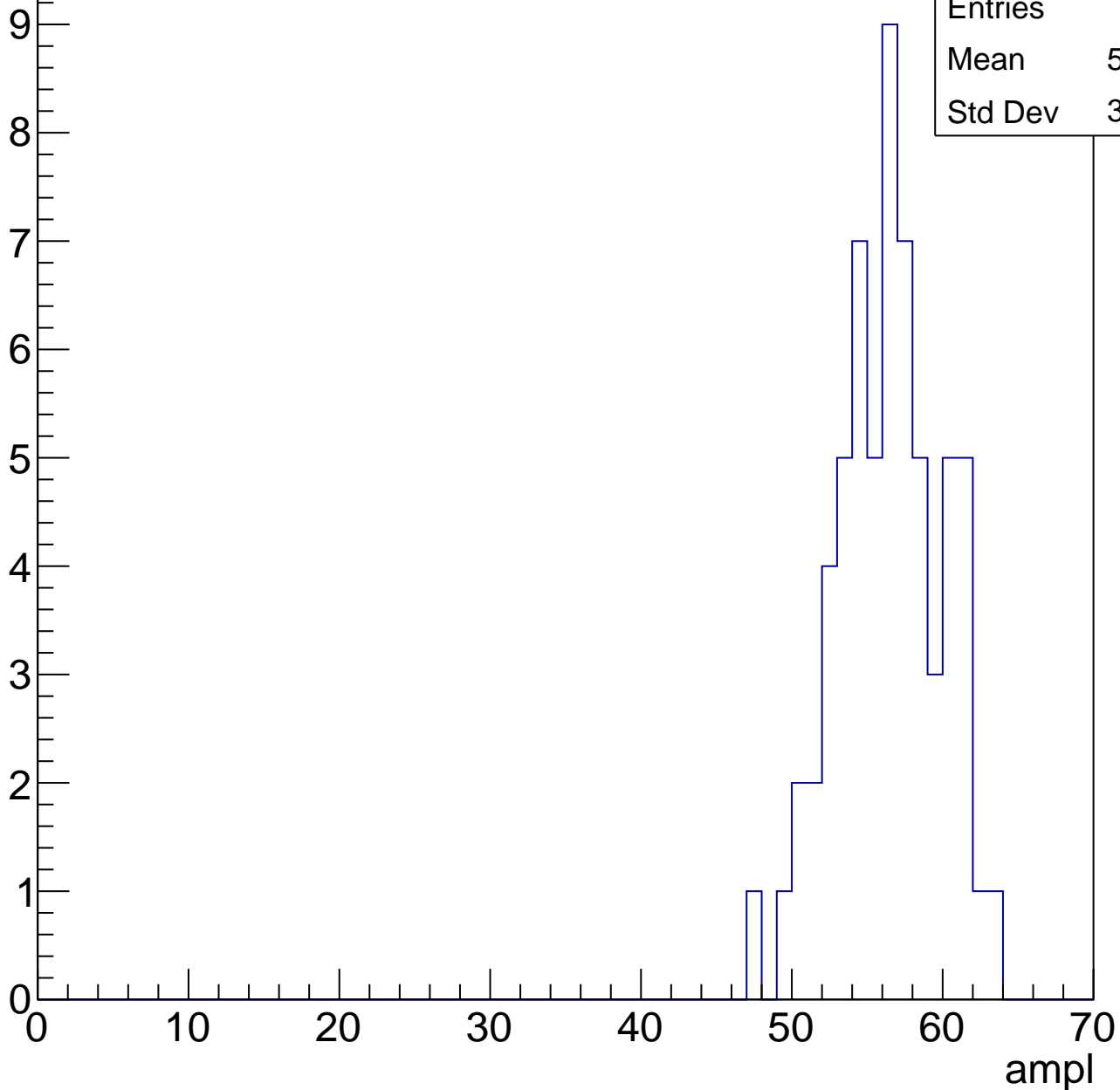


B1L101S, U23-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.94
Std Dev	3.422

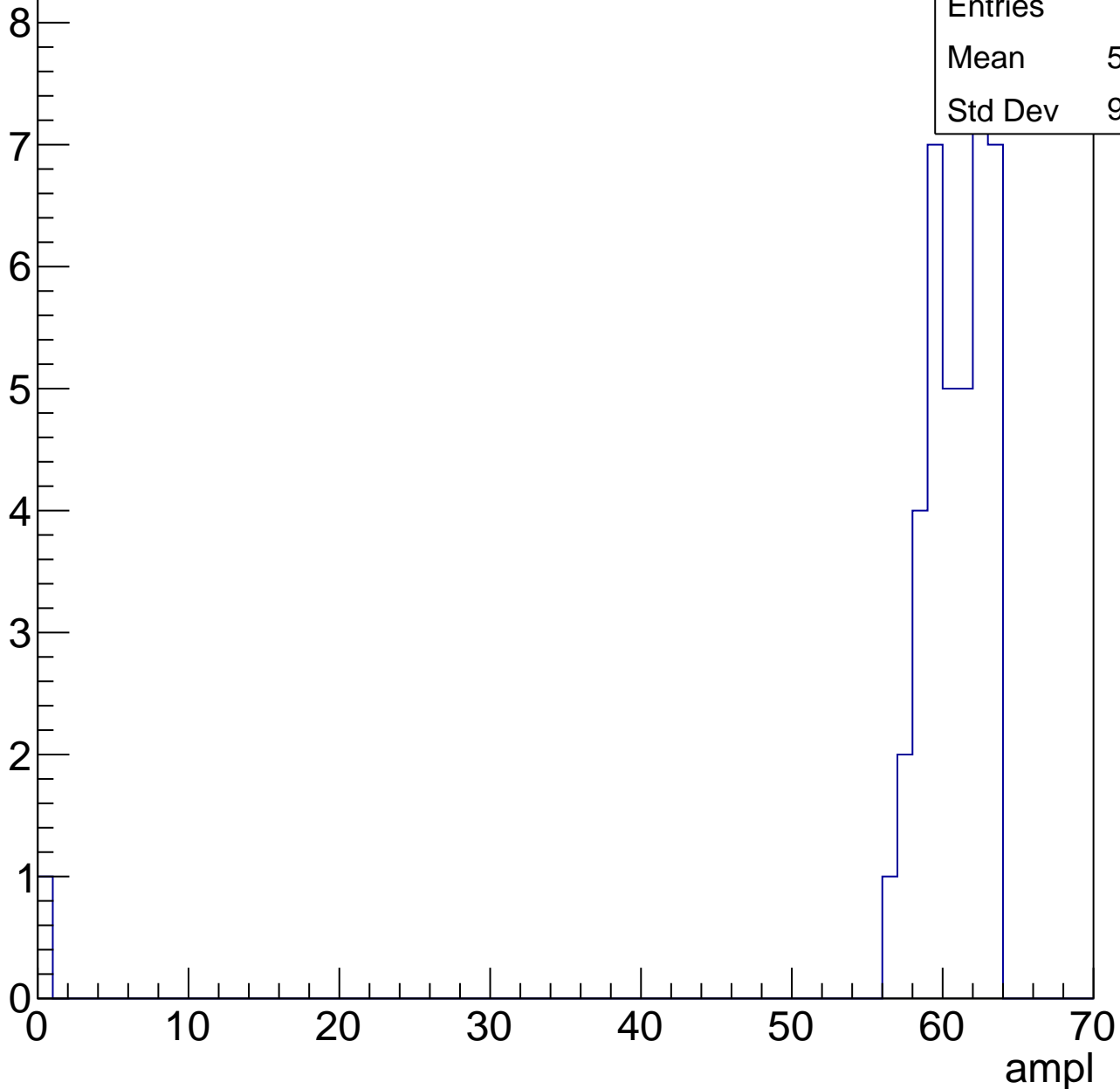


B1L101S, U23-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.92
Std Dev	9.632



B1L101S, U23-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U23-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch61, adc0

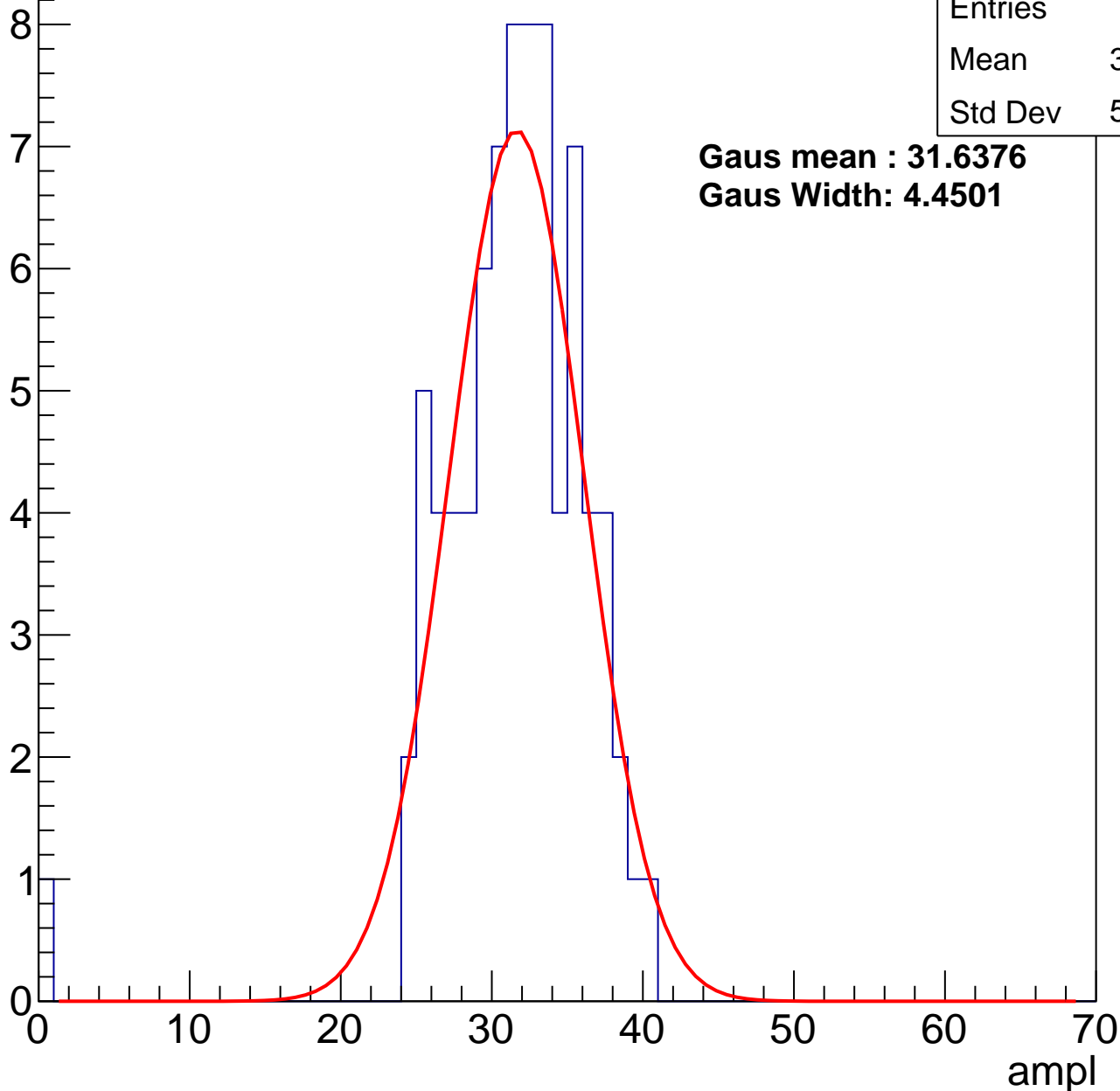
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.96
Std Dev	5.178

Gaus mean : 31.6376

Gaus Width: 4.4501



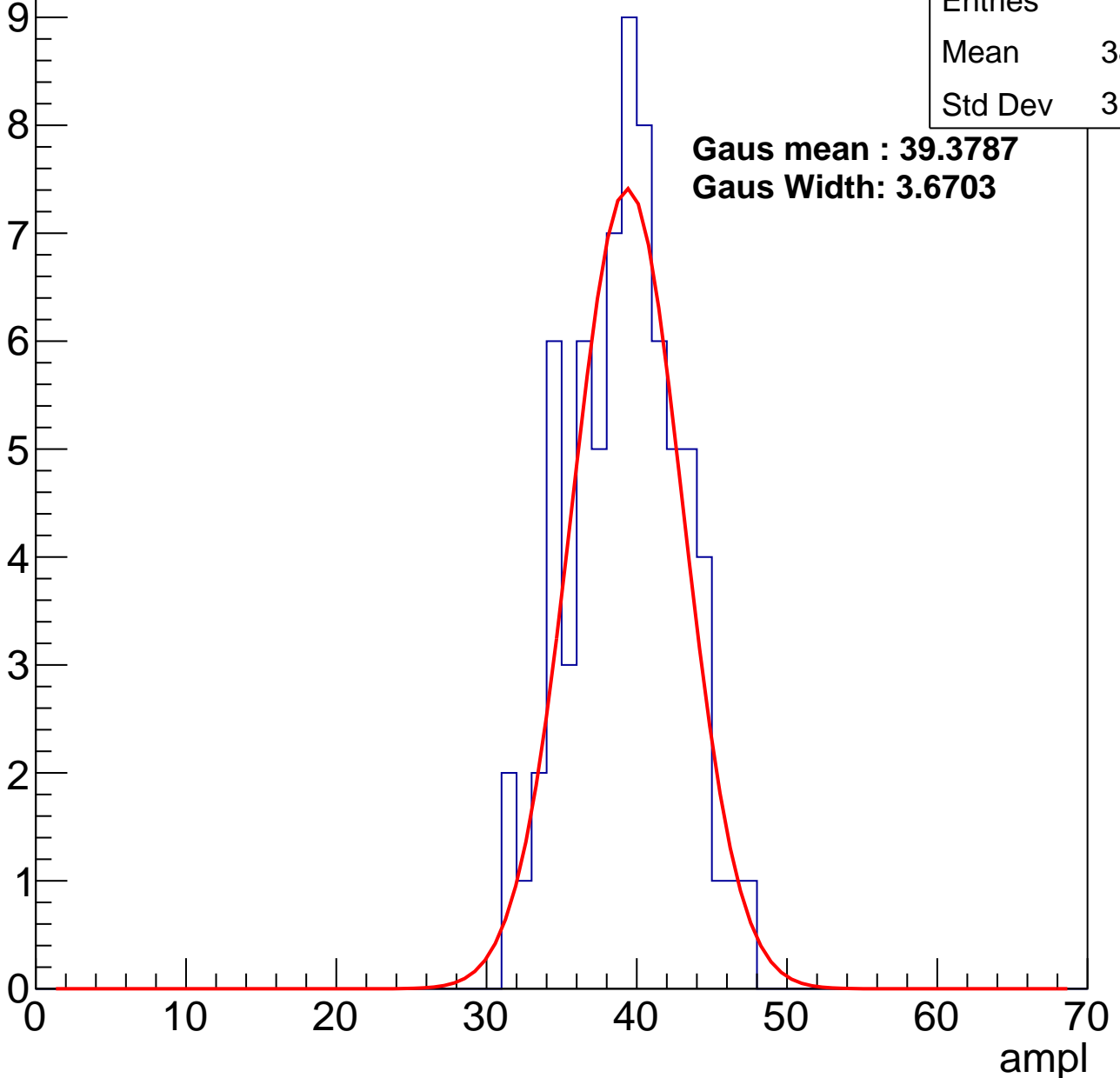
B1L101S, U23-ch61, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	38.78
Std Dev	3.595

Gaus mean : 39.3787
Gaus Width: 3.6703



B1L101S, U23-ch61, adc2

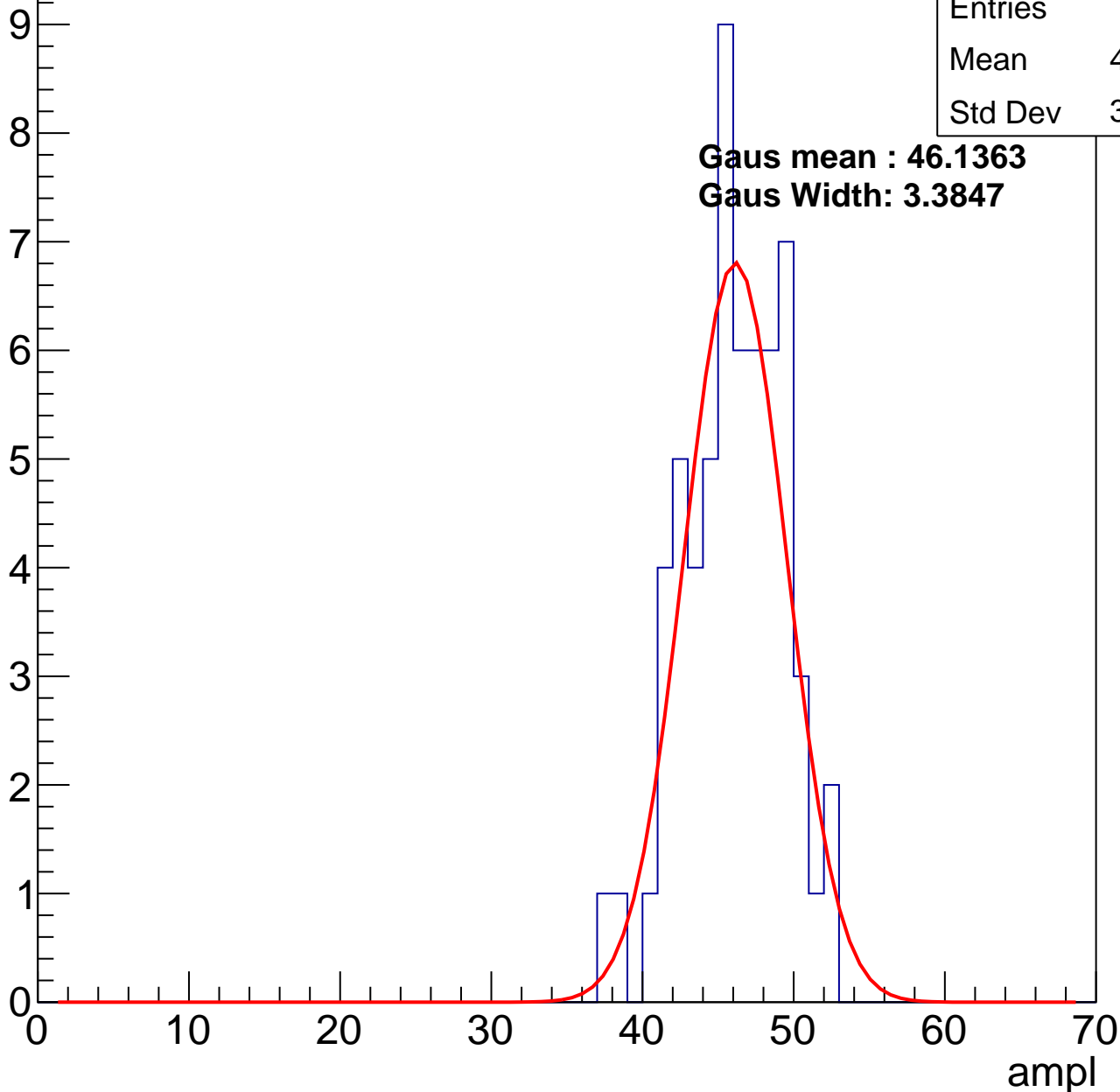
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	45.57
Std Dev	3.272

Gaus mean : 46.1363

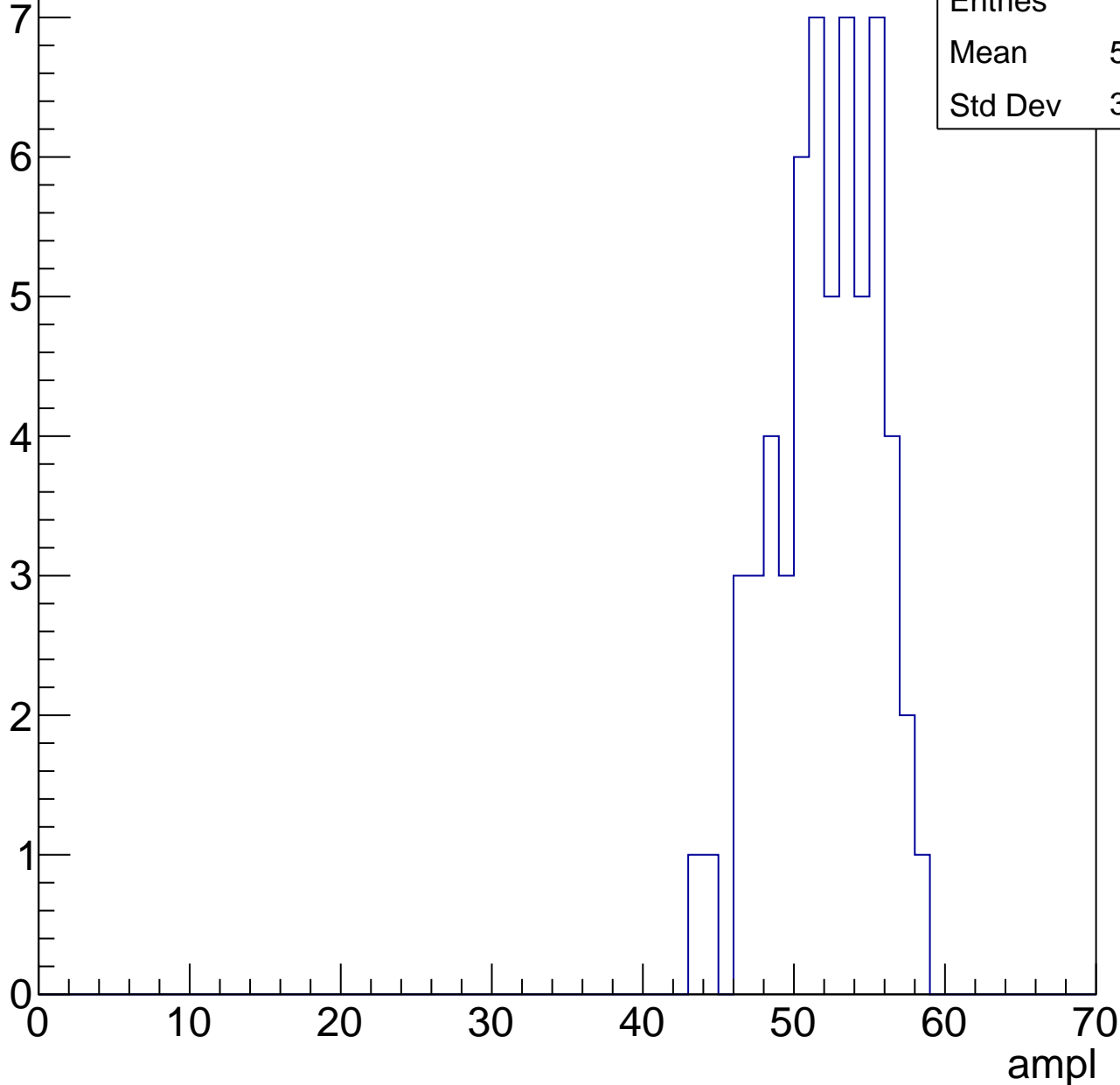
Gaus Width: 3.3847



B1L101S, U23-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

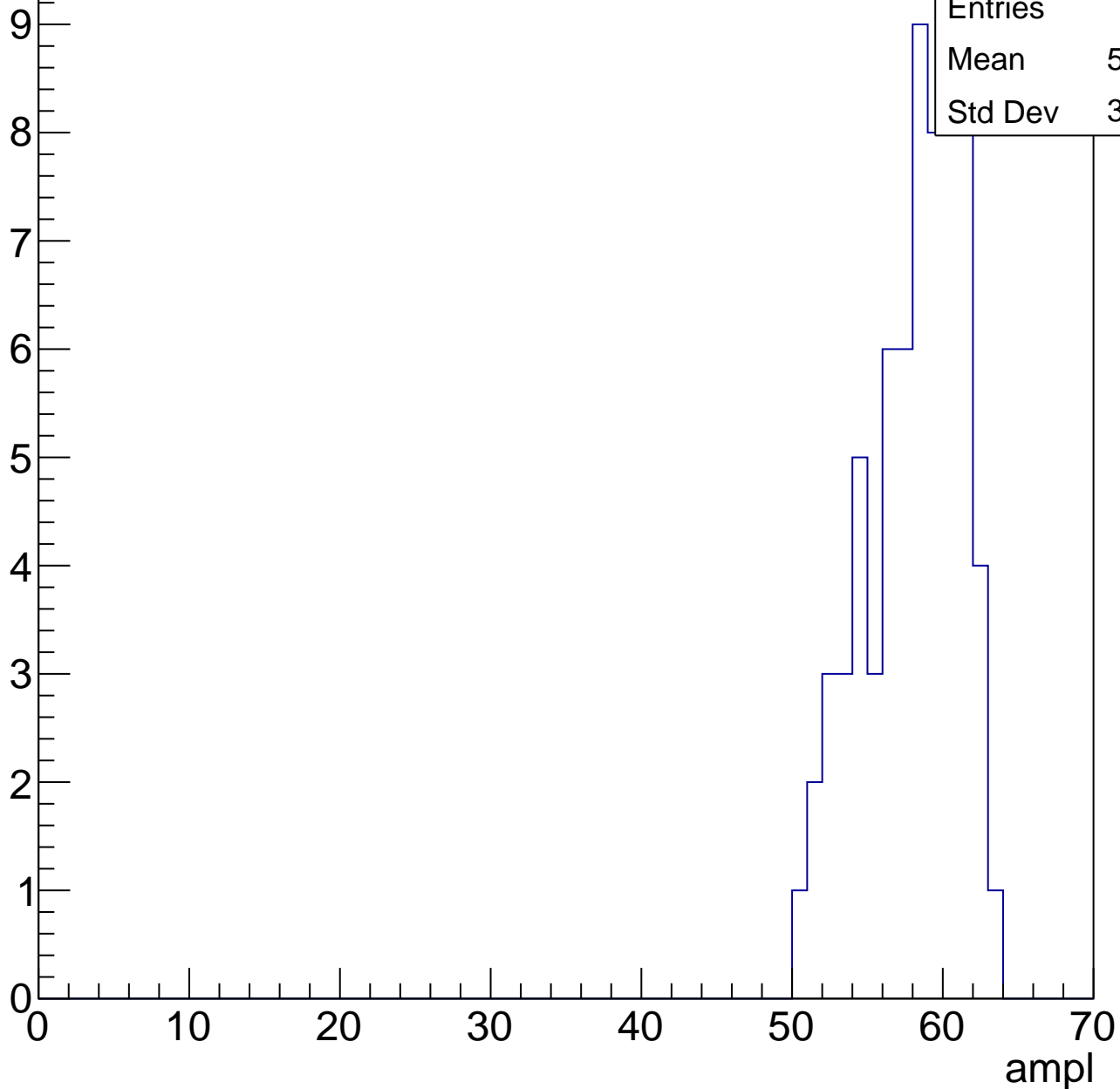


Entries	59
Mean	51.59
Std Dev	3.396

B1L101S, U23-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

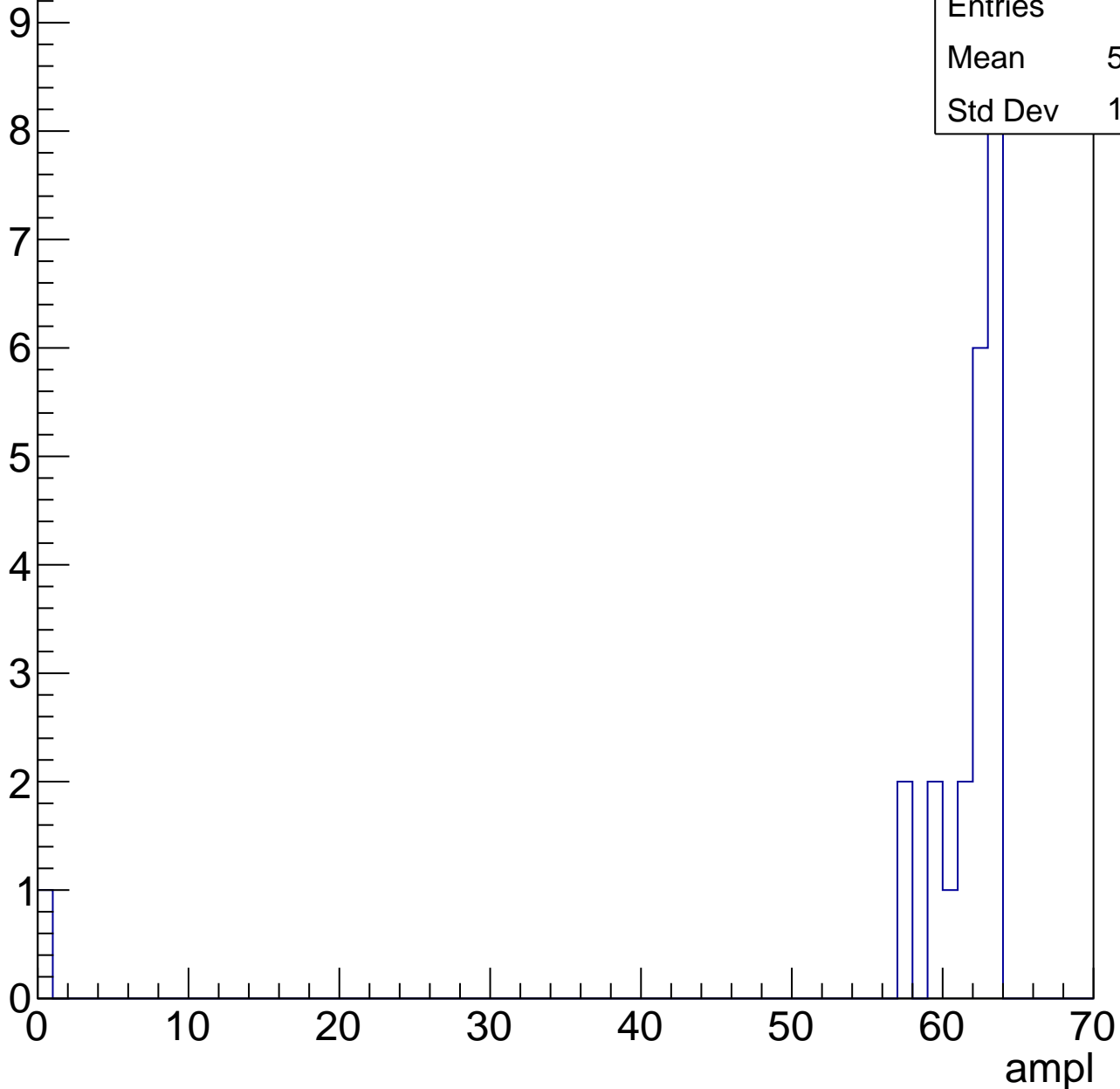


B1L101S, U23-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.83
Std Dev	12.68



B1L101S, U23-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

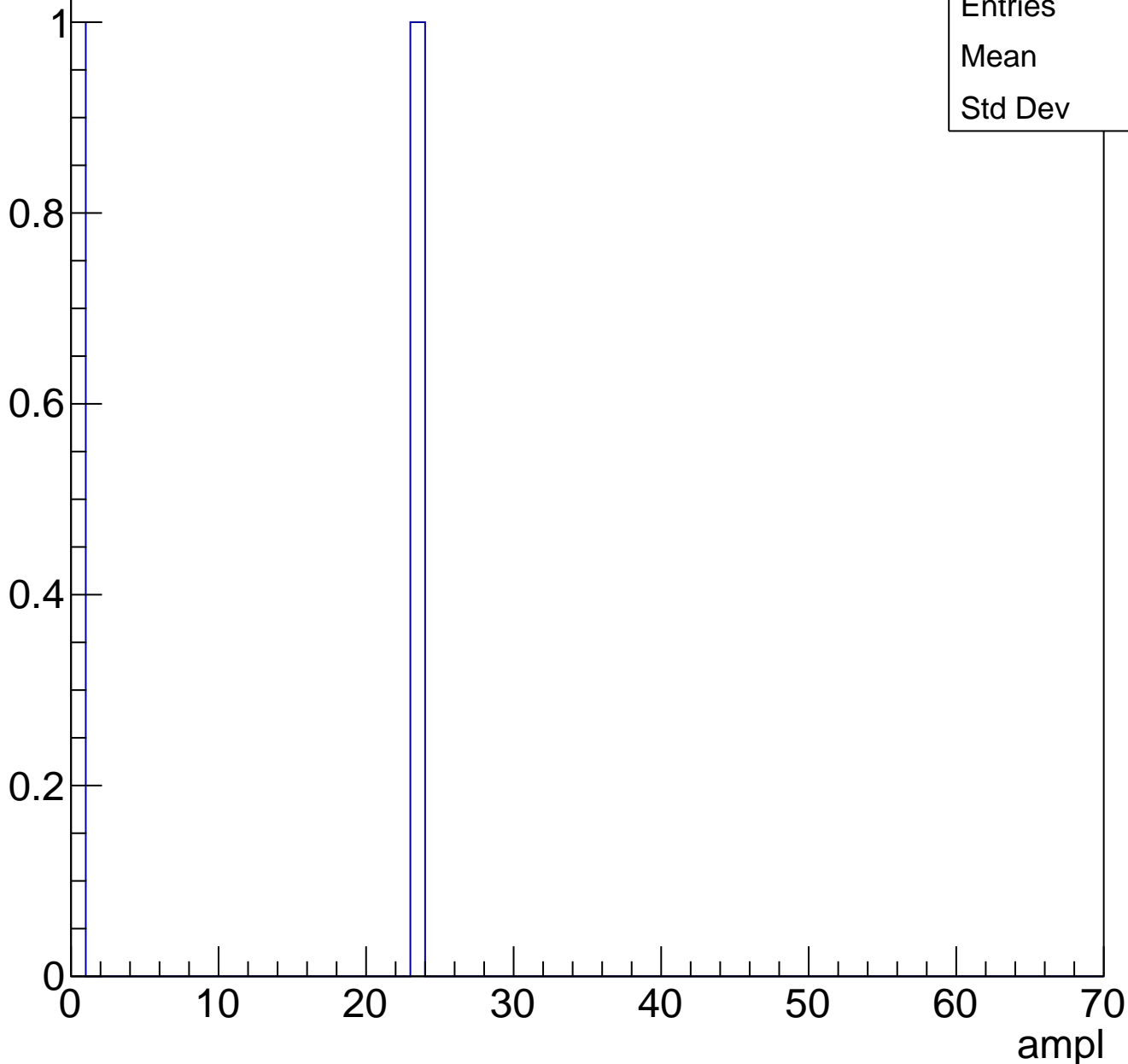


Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L101S, U23-ch62, adc0

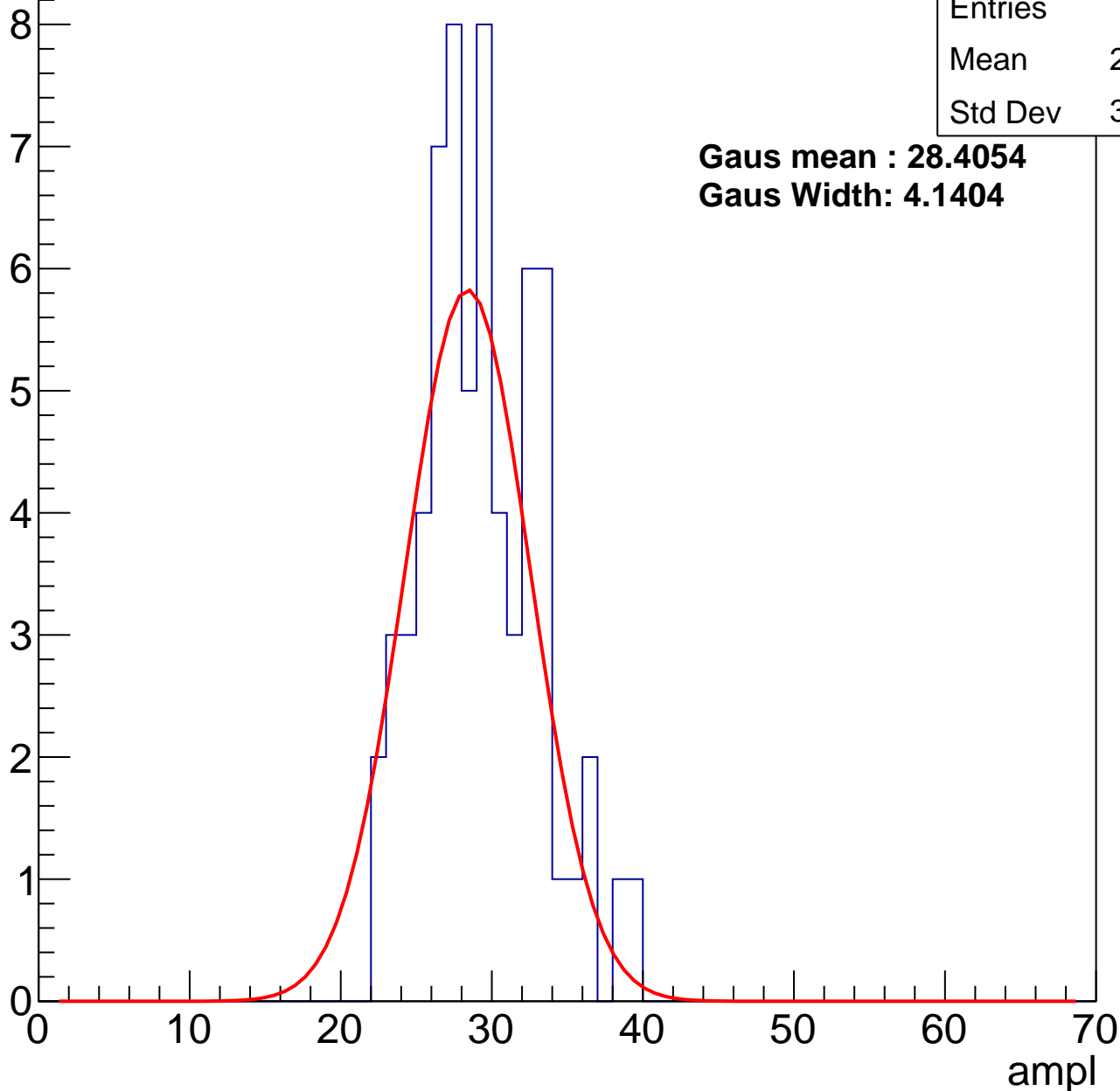
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.86
Std Dev	3.823

Gaus mean : 28.4054

Gaus Width: 4.1404



B1L101S, U23-ch62, adc1

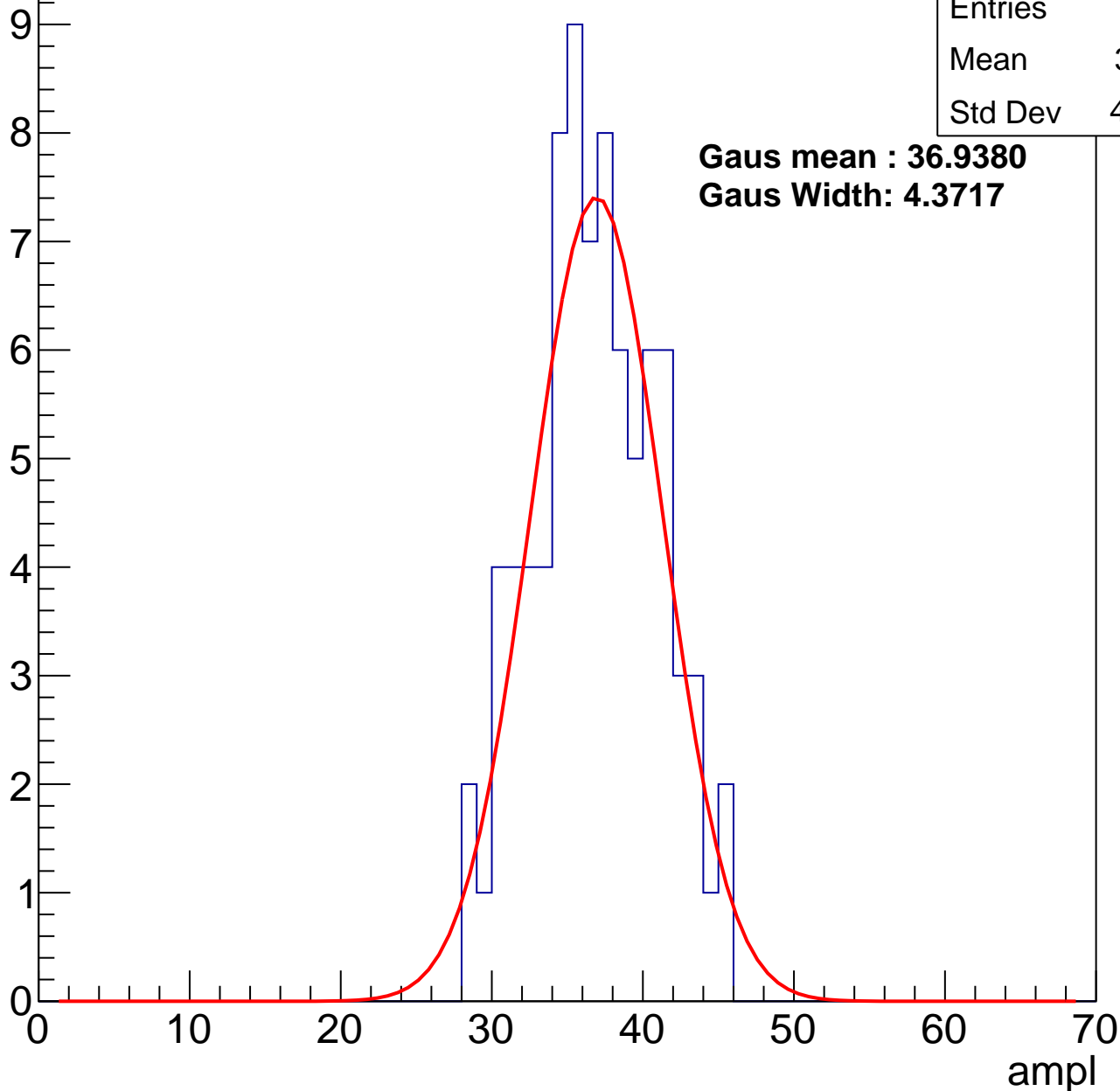
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	36.41
Std Dev	4.036

Gaus mean : 36.9380

Gaus Width: 4.3717



B1L101S, U23-ch62, adc2

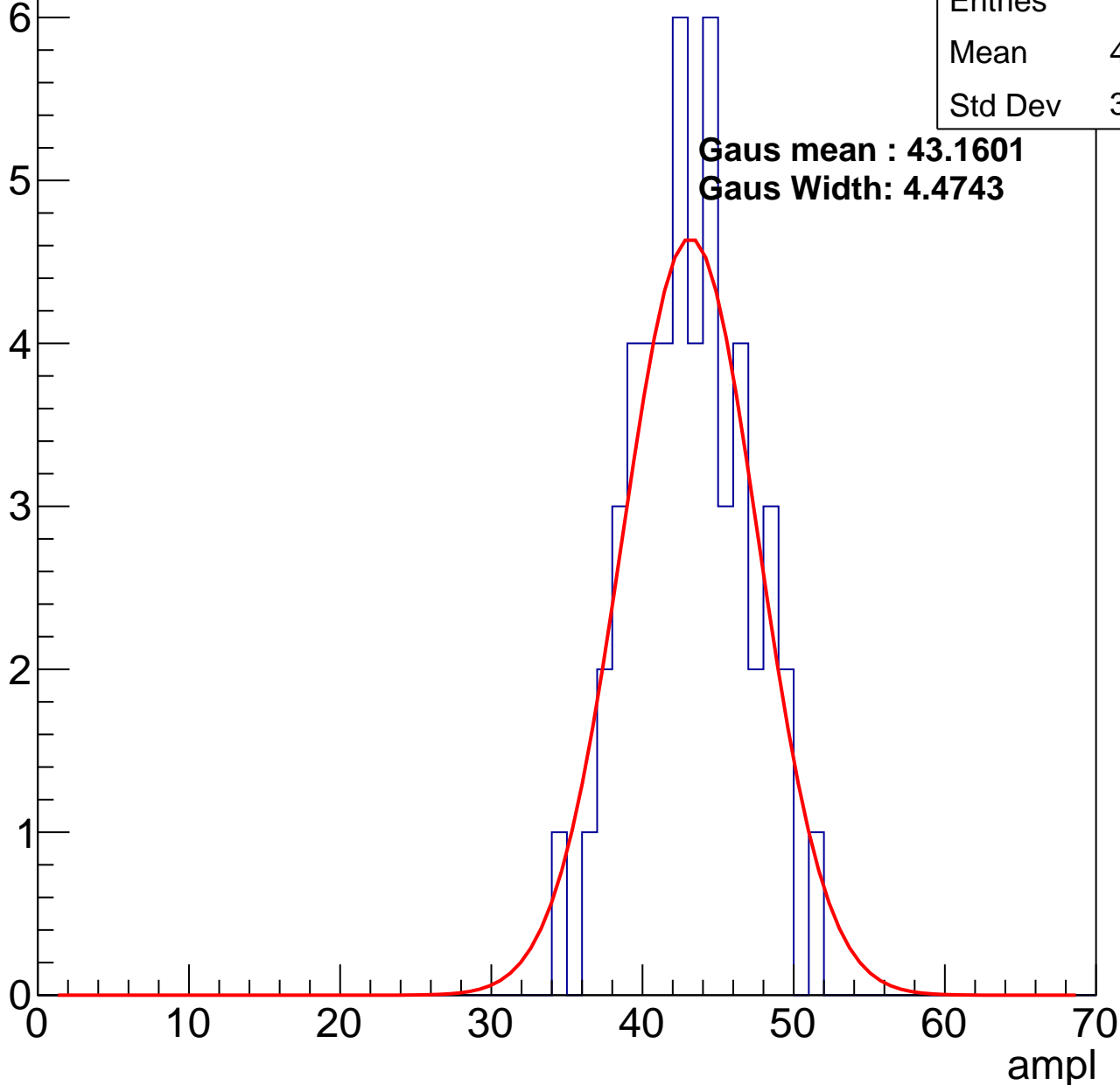
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.64
Std Dev	3.714

Gaus mean : 43.1601

Gaus Width: 4.4743

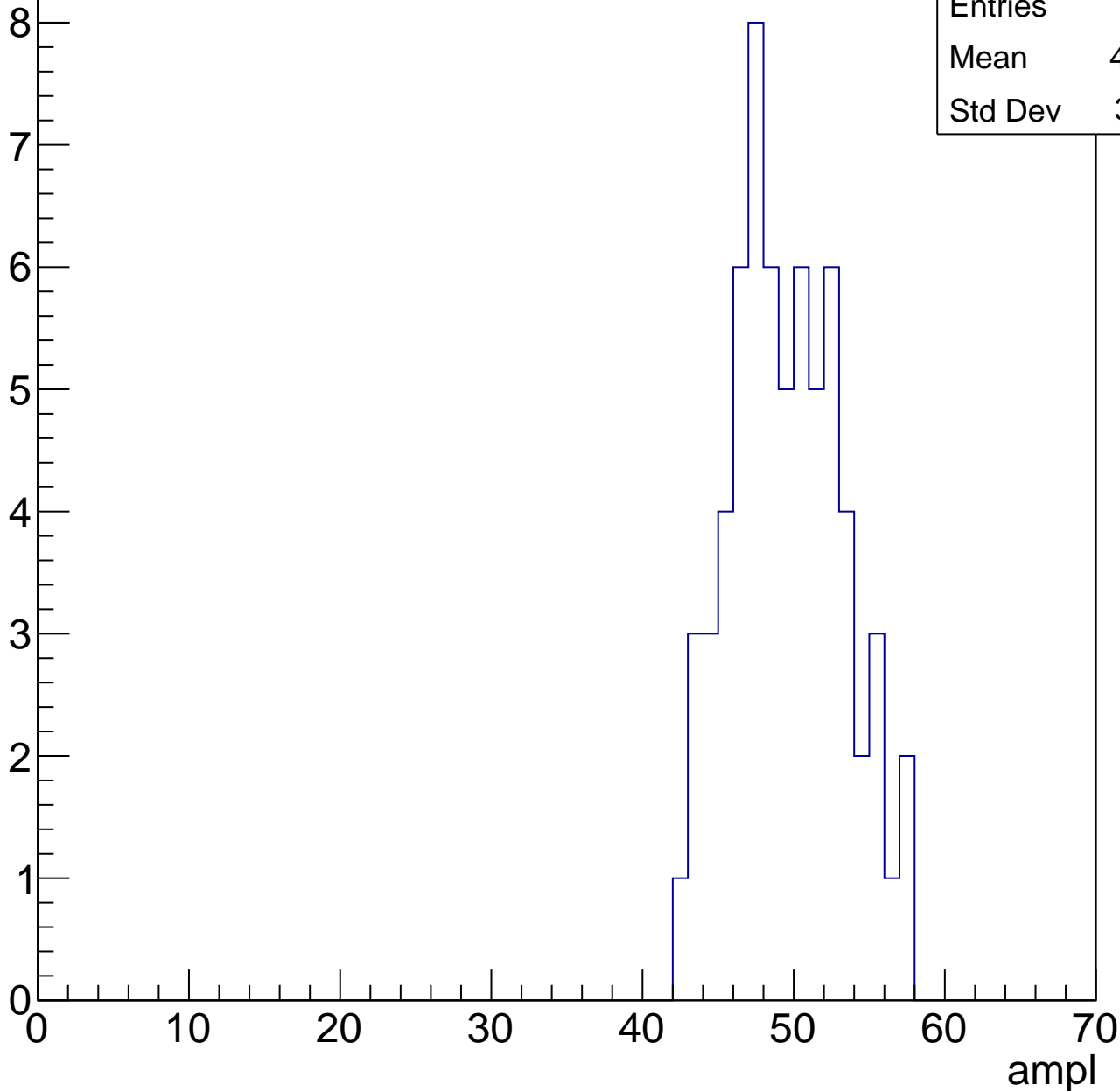


B1L101S, U23-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.08
Std Dev	3.651

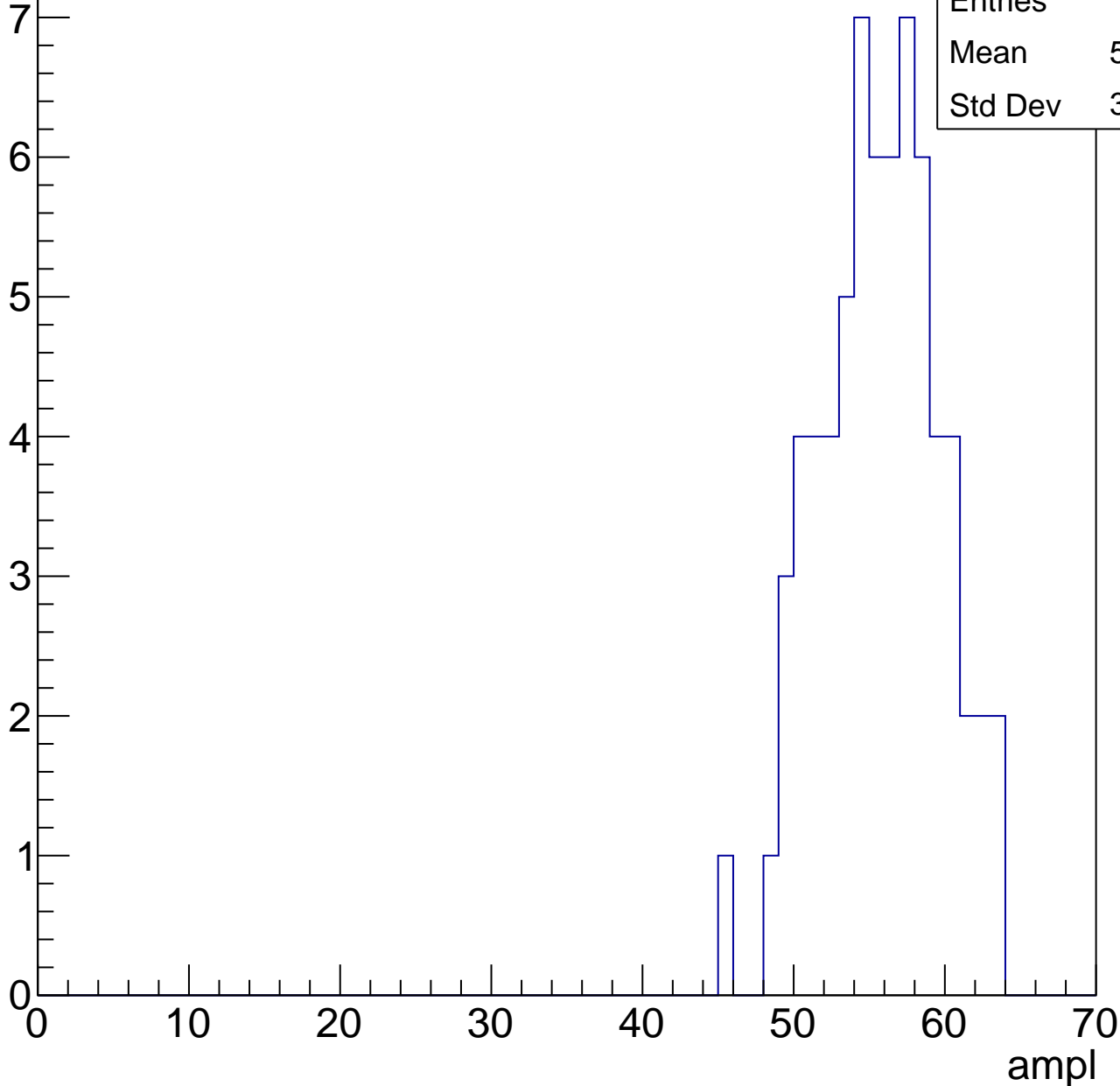


B1L101S, U23-ch62, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	55.24
Std Dev	3.892



B1L101S, U23-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

5

4

3

2

1

0

Entries	33
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Mean	59.33
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Std Dev	2.613
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ampl

0

10

20

30

40

50

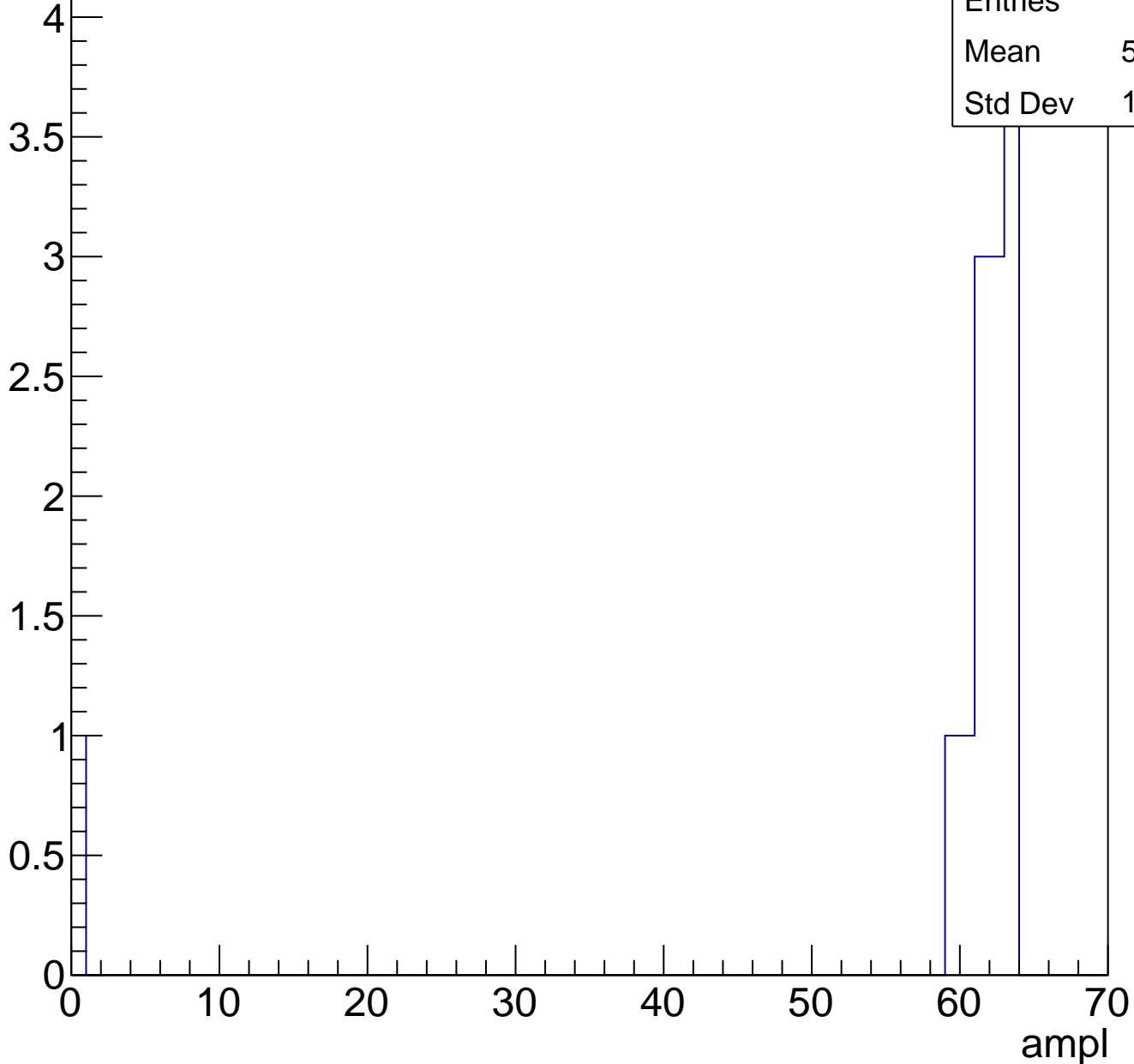
60

70

B1L101S, U23-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

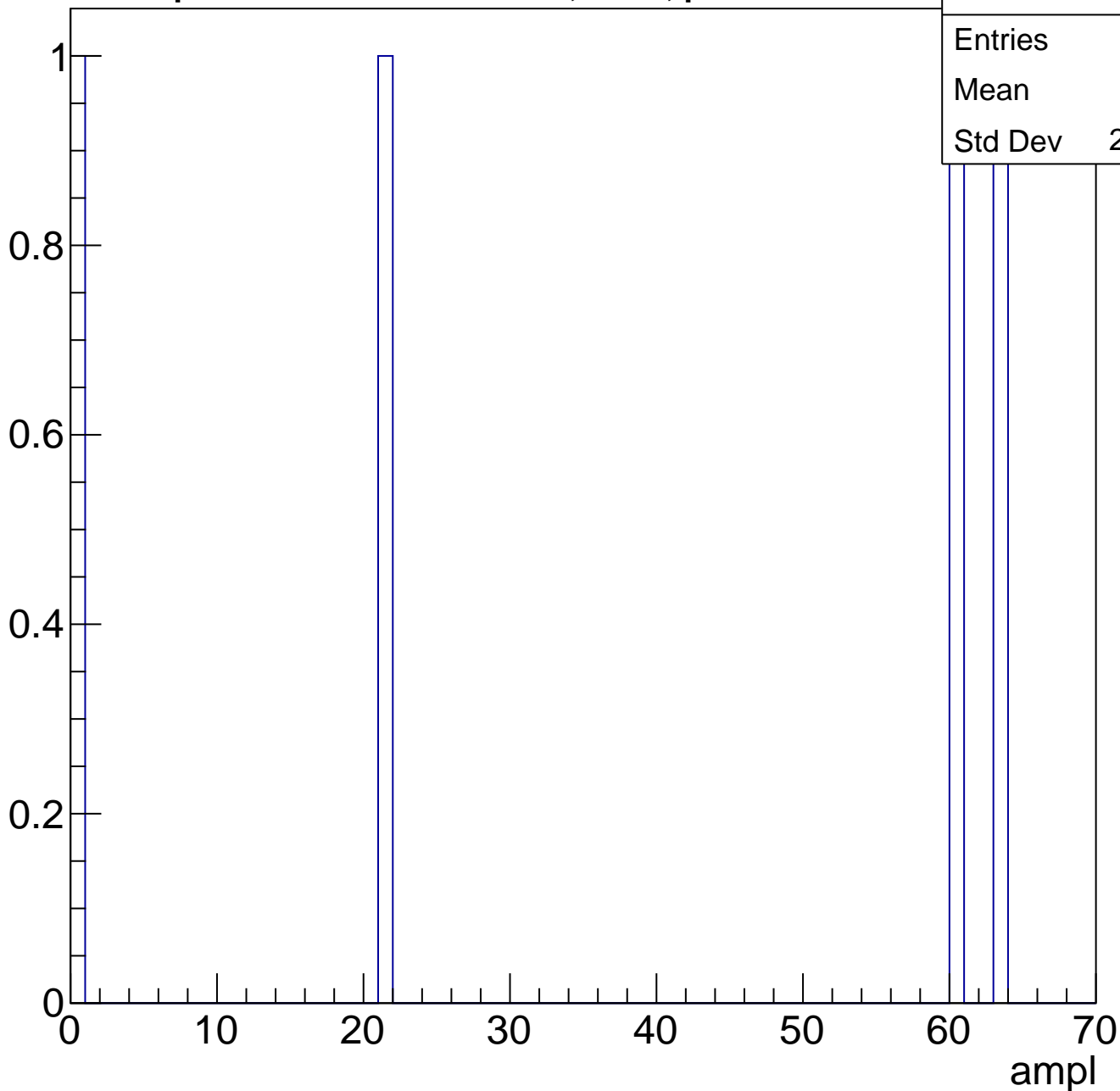
Entry



B1L101S, U23-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch63, adc0

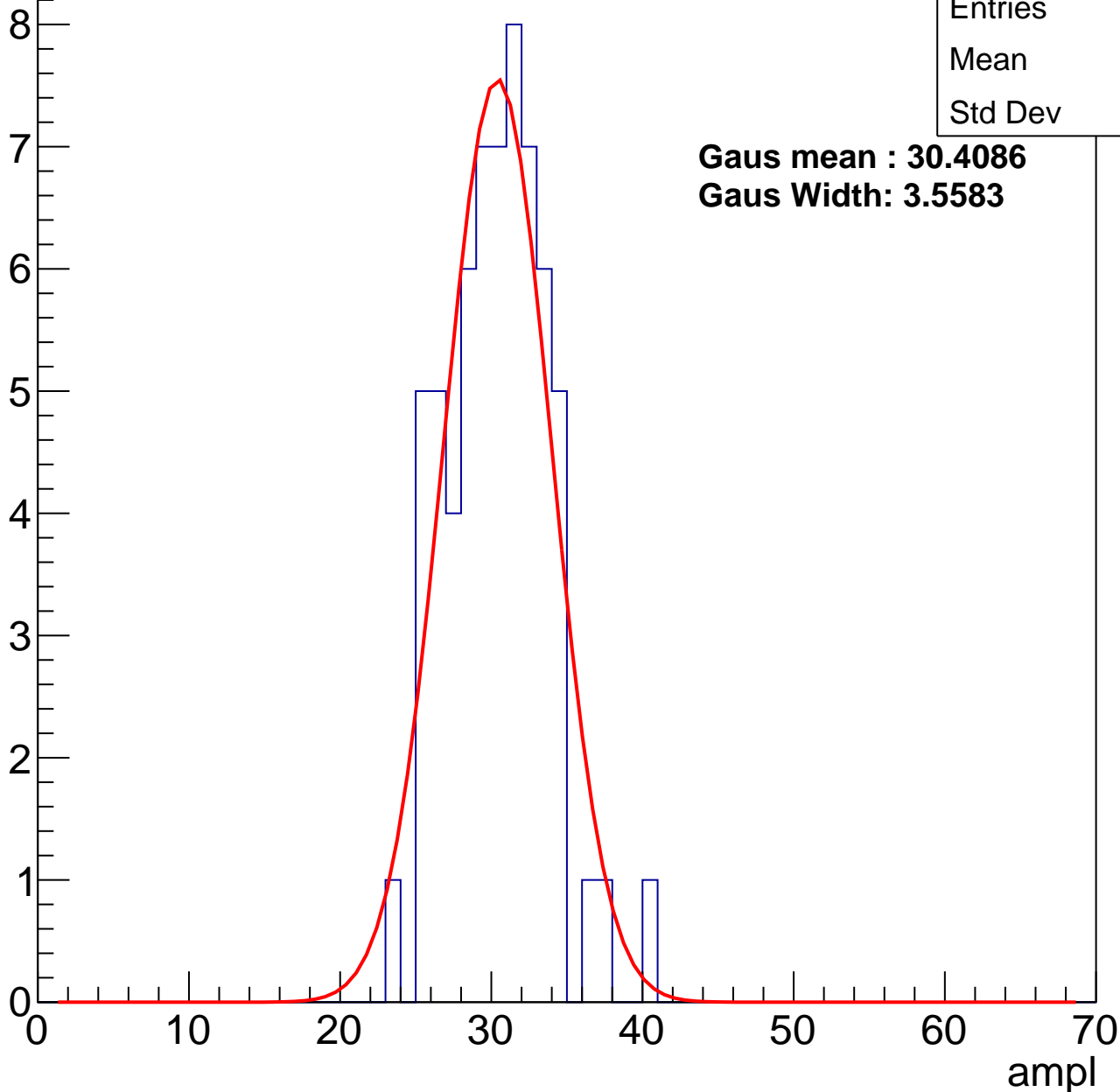
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30
Std Dev	3.25

Gaus mean : 30.4086

Gaus Width: 3.5583

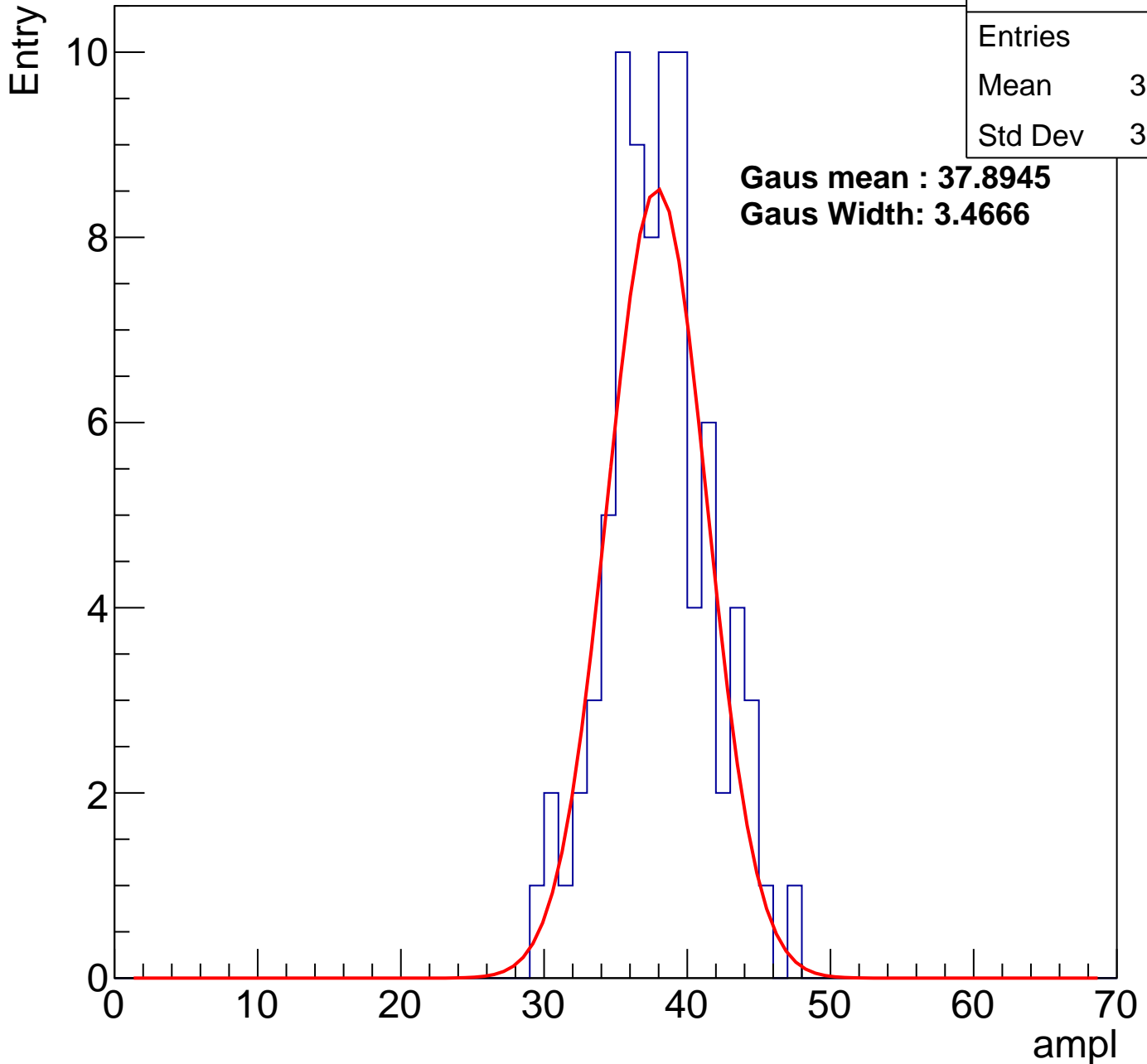


B1L101S, U23-ch63, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	37.55
Std Dev	3.582

Gaus mean : 37.8945
Gaus Width: 3.4666



B1L101S, U23-ch63, adc2

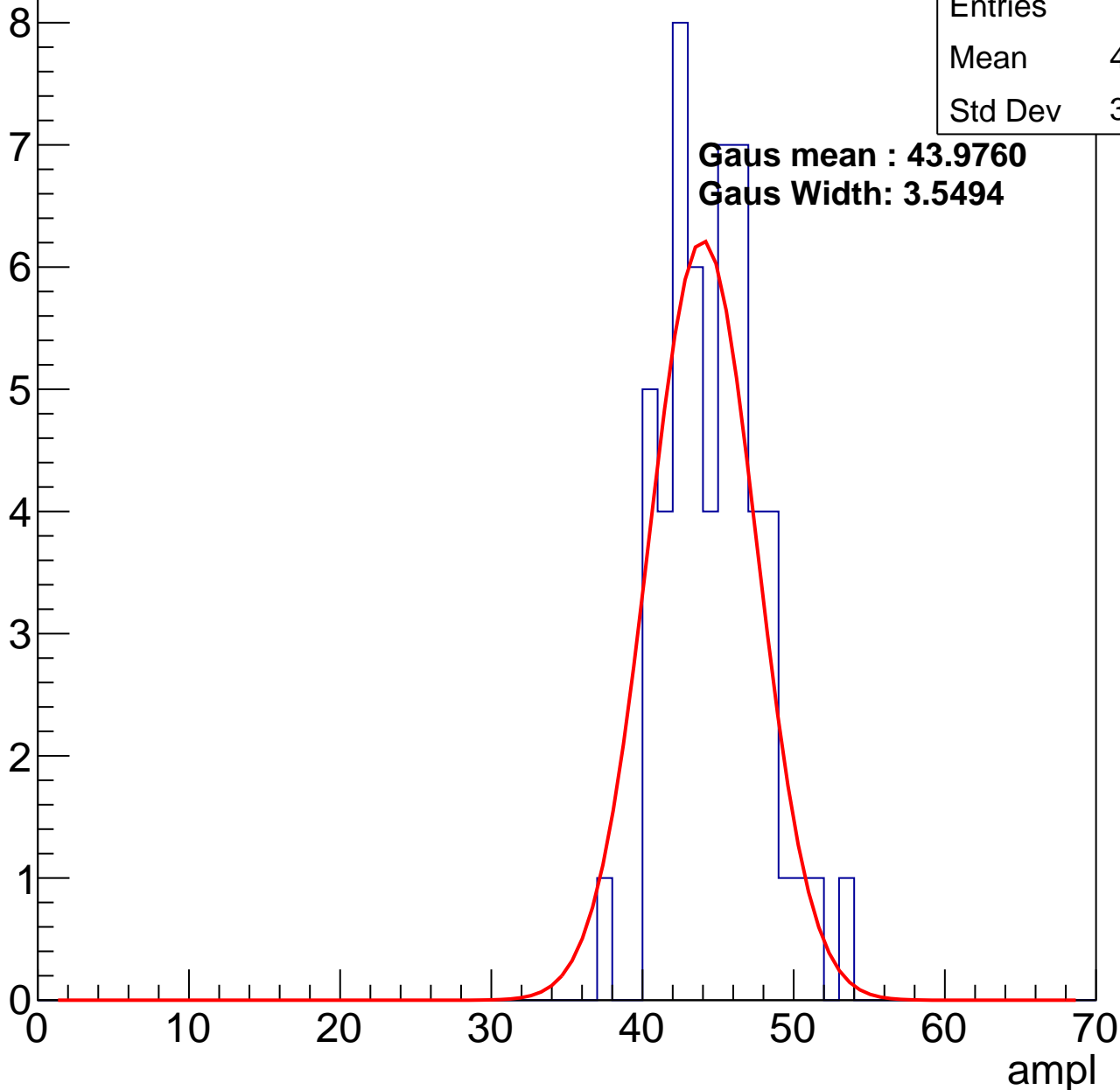
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.28
Std Dev	3.118

Gaus mean : 43.9760

Gaus Width: 3.5494

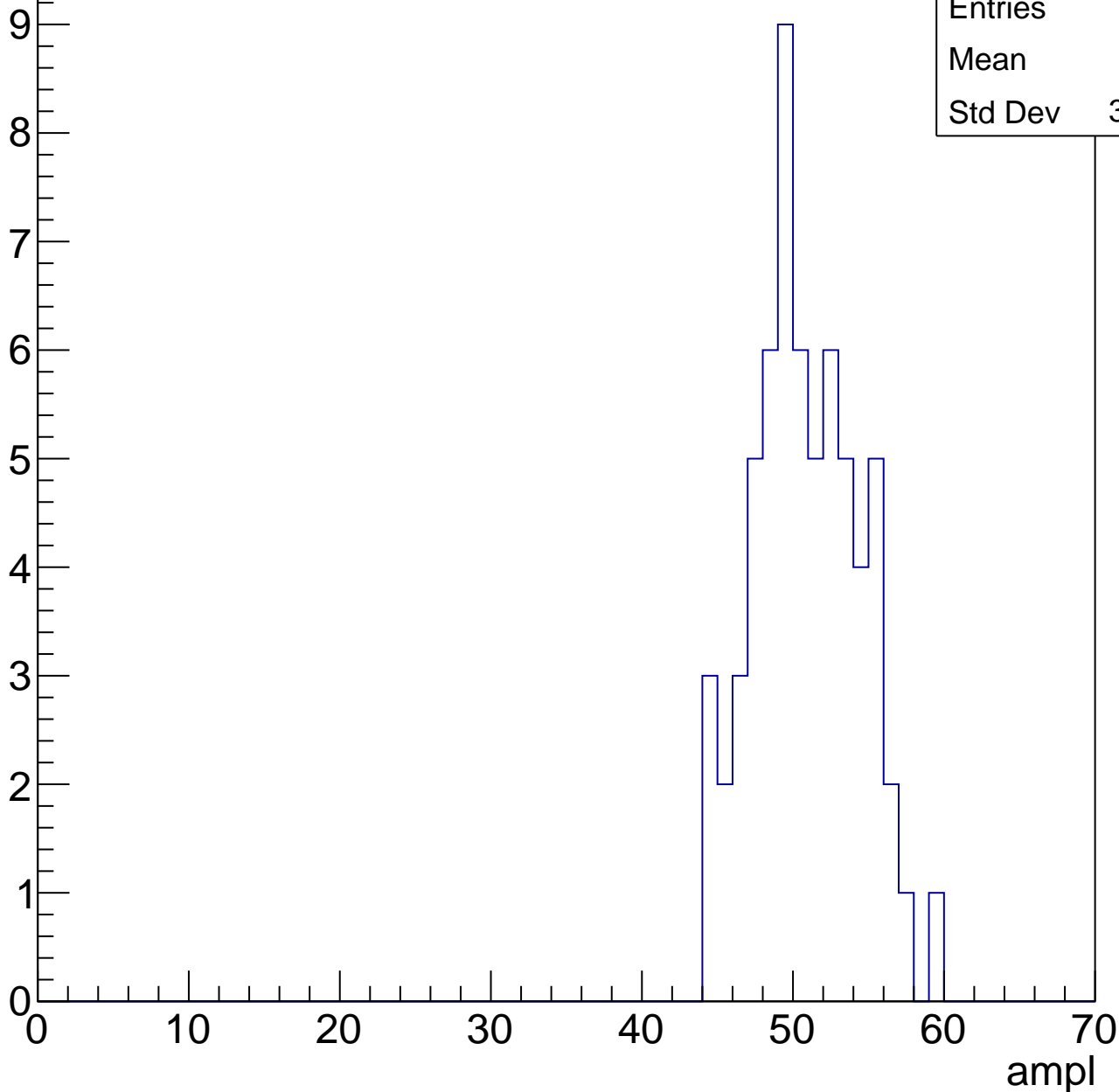


B1L101S, U23-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.4
Std Dev	3.425

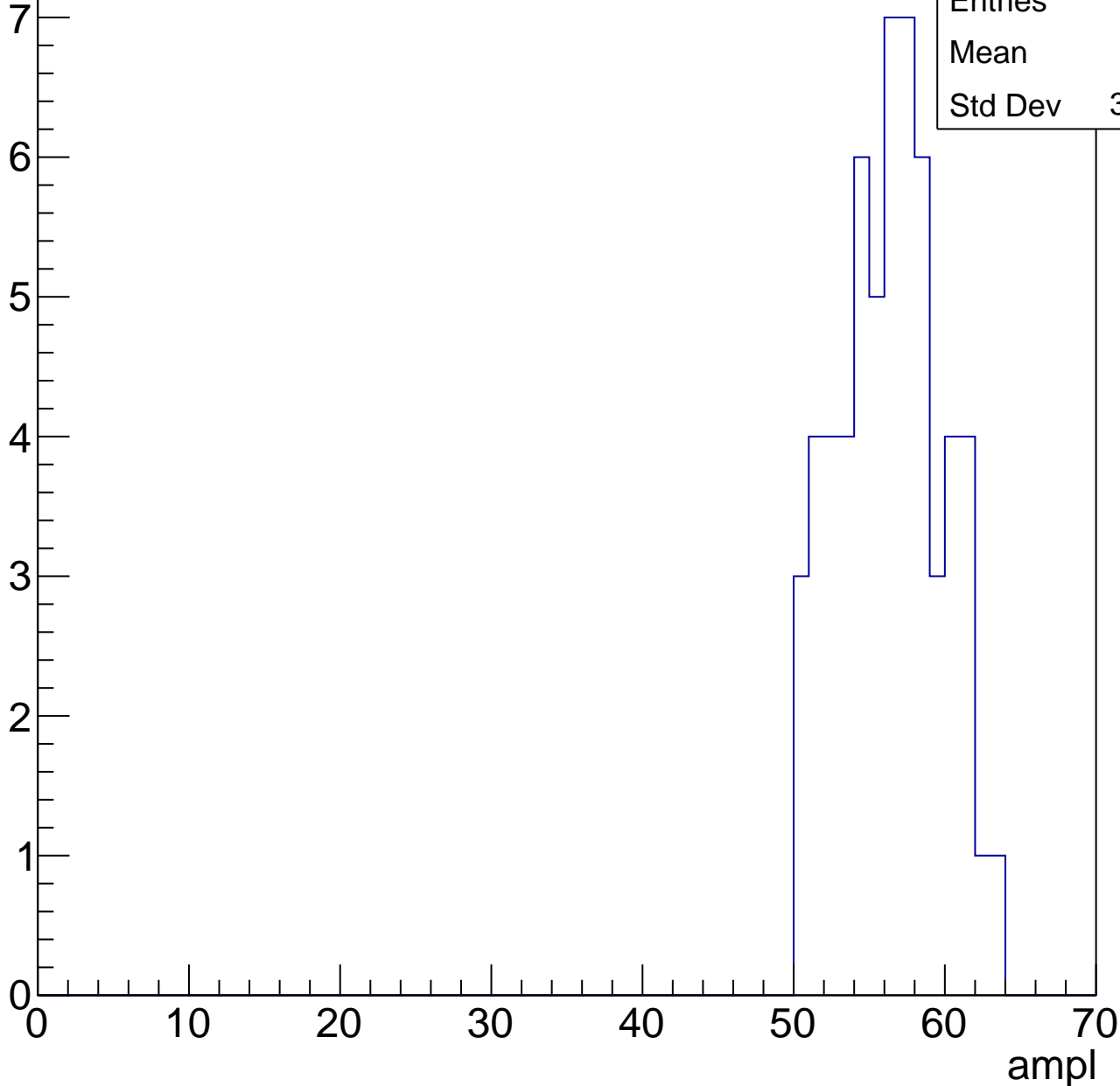


B1L101S, U23-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.9
Std Dev	3.302

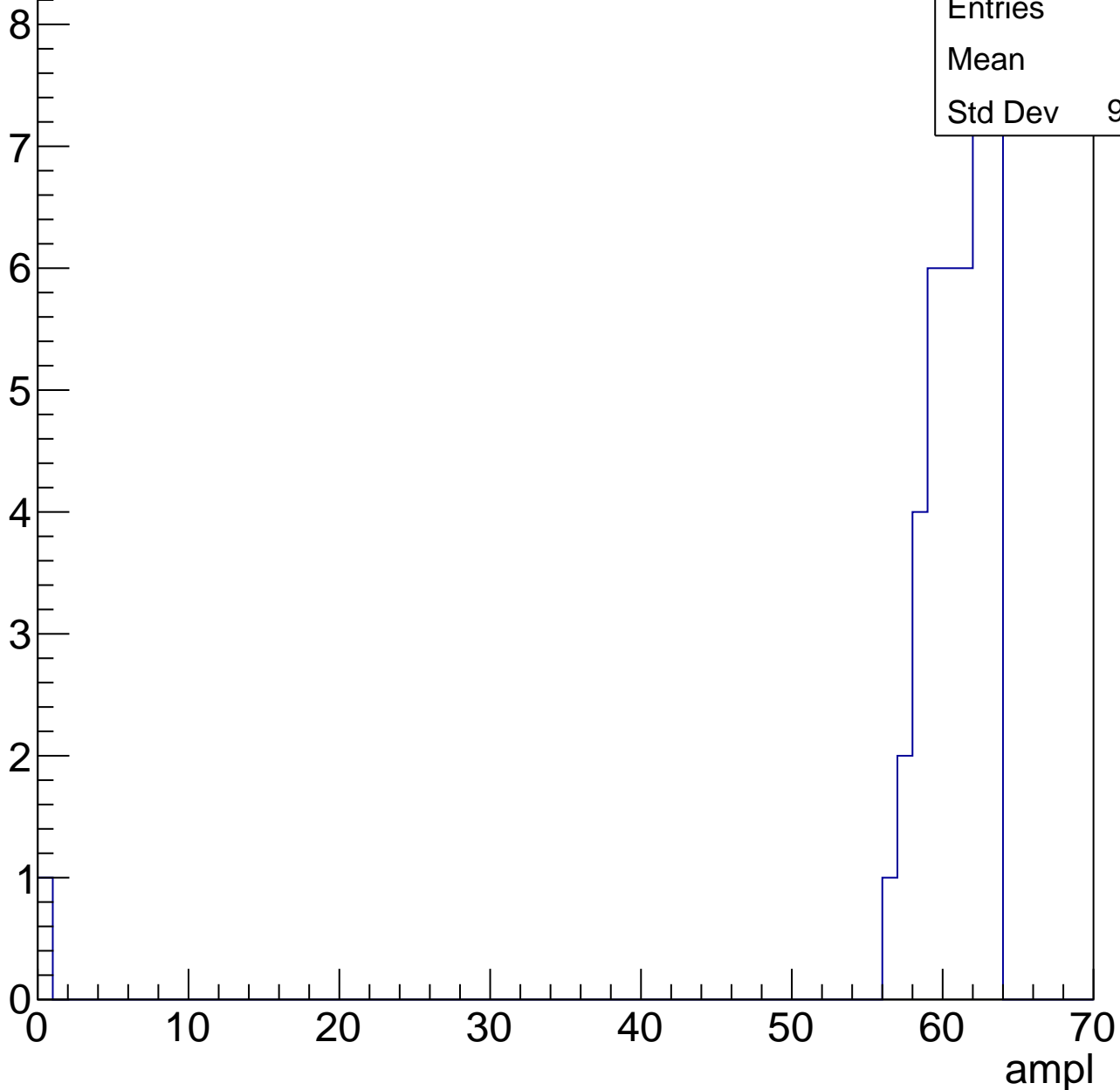


B1L101S, U23-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.1
Std Dev	9.426



B1L101S, U23-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch64, adc0

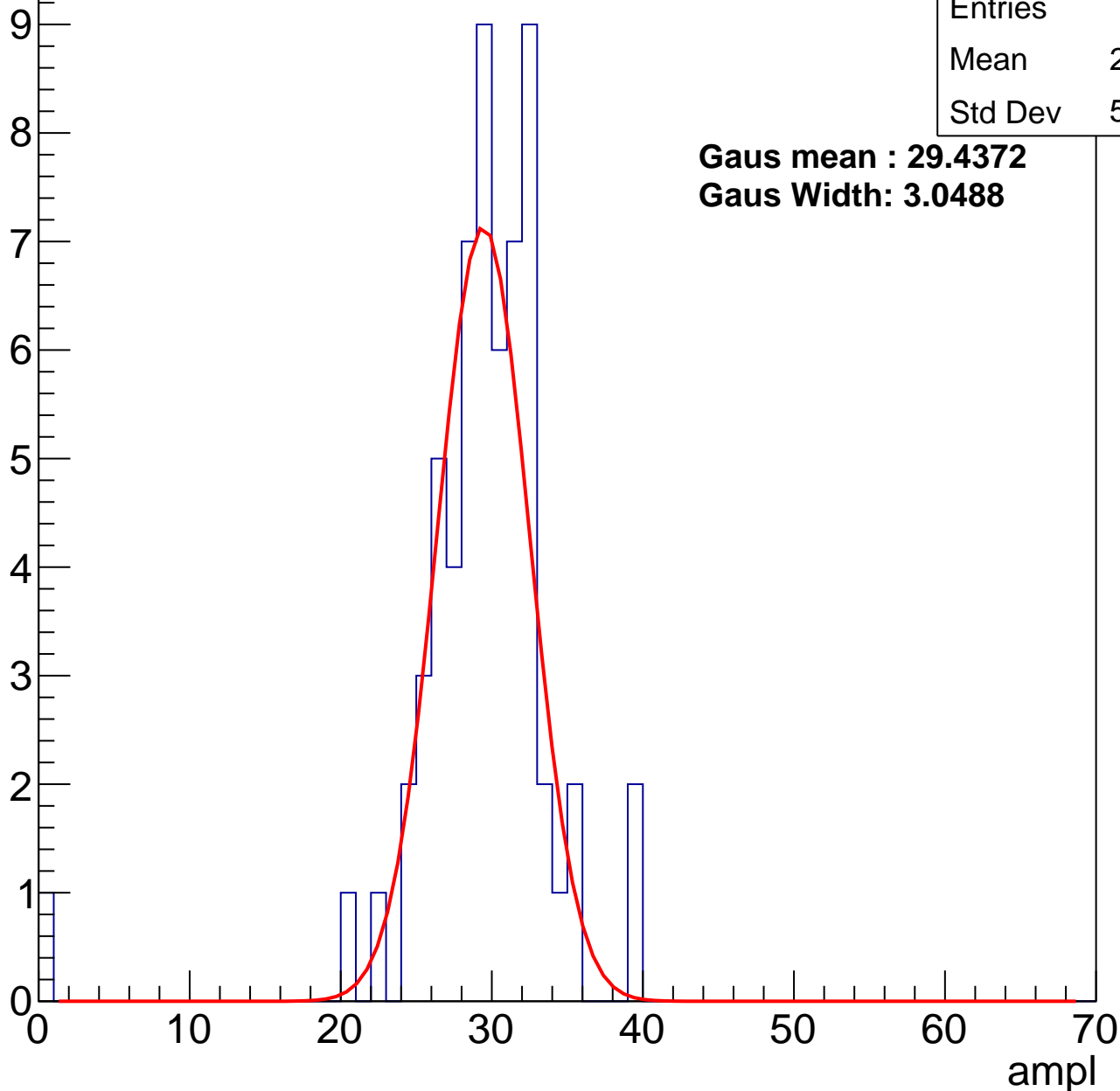
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.92
Std Dev	5.049

Gaus mean : 29.4372

Gaus Width: 3.0488



B1L101S, U23-ch64, adc1

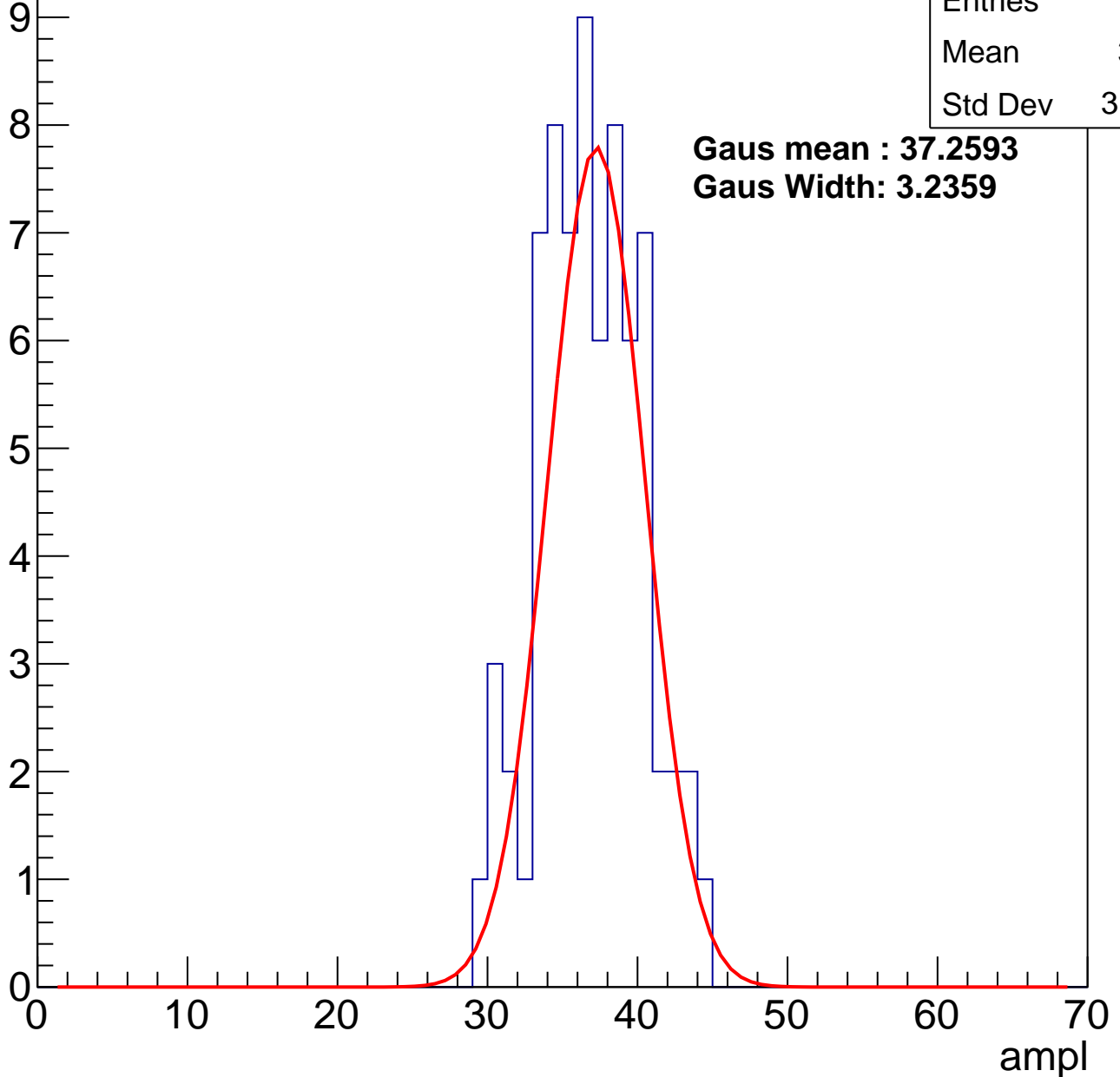
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.4
Std Dev	3.336

Gaus mean : 37.2593

Gaus Width: 3.2359



B1L101S, U23-ch64, adc2

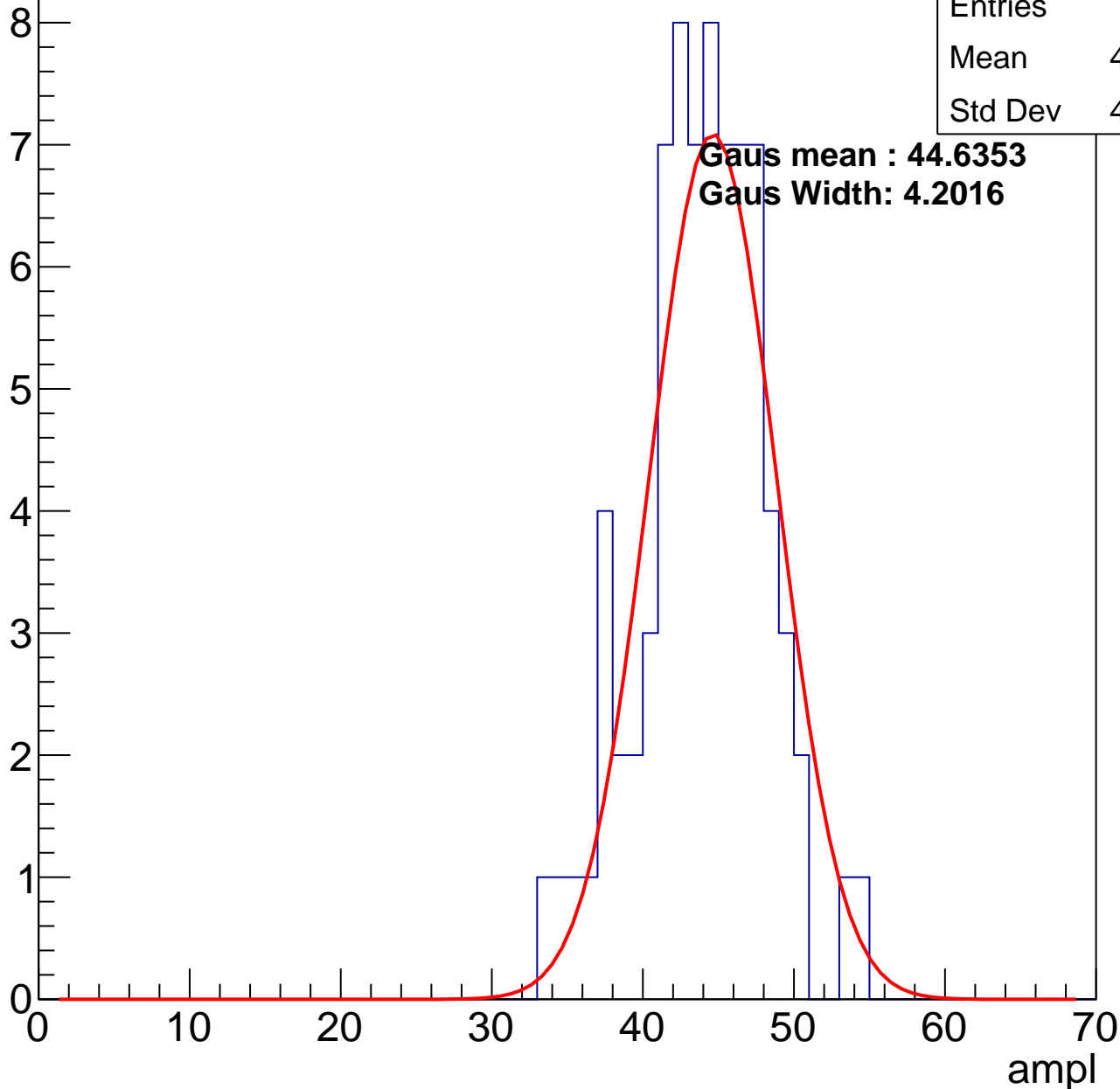
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.48
Std Dev	4.114

Gaus mean : 44.6353

Gaus Width: 4.2016

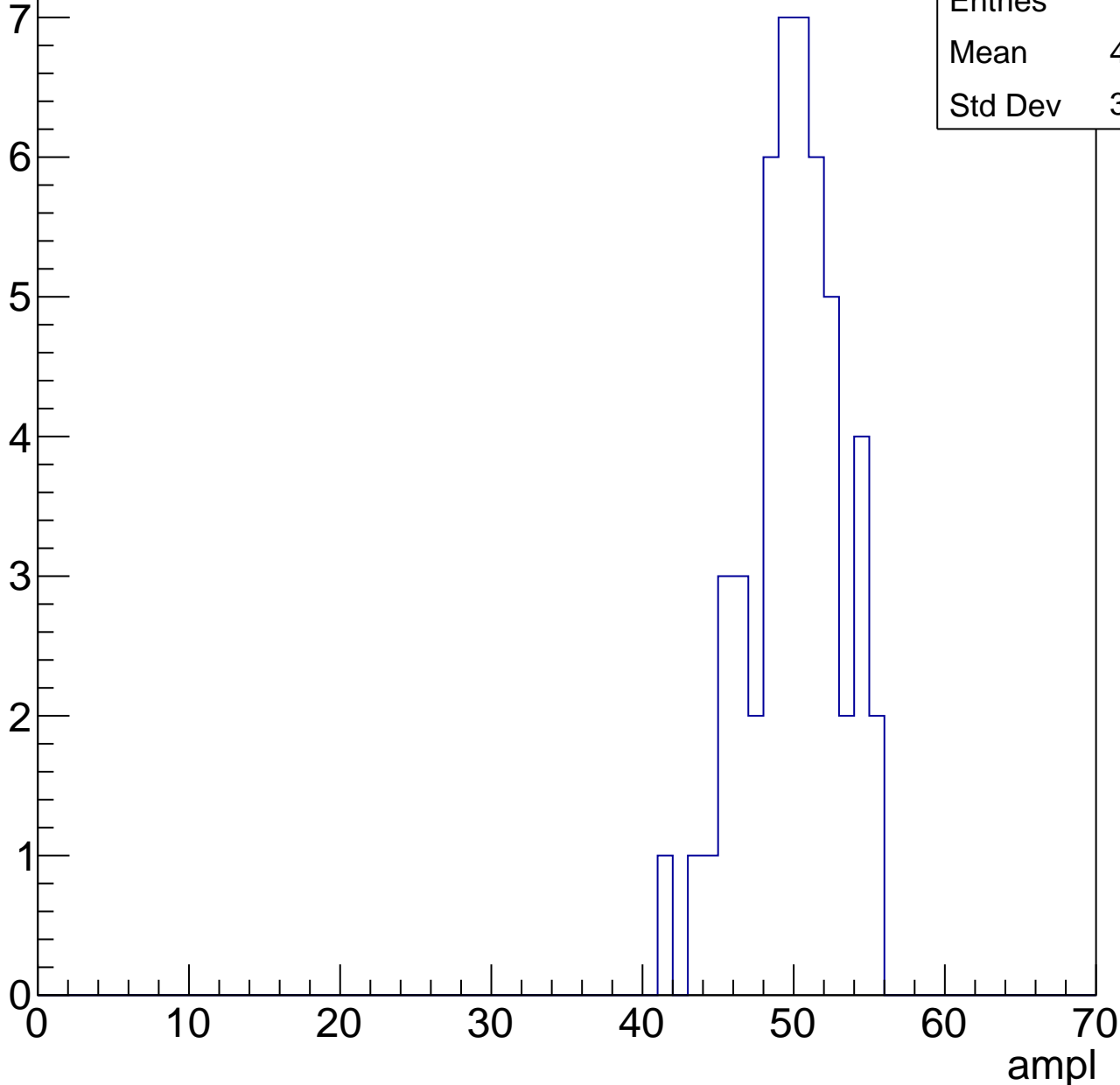


B1L101S, U23-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	49.48
Std Dev	3.106

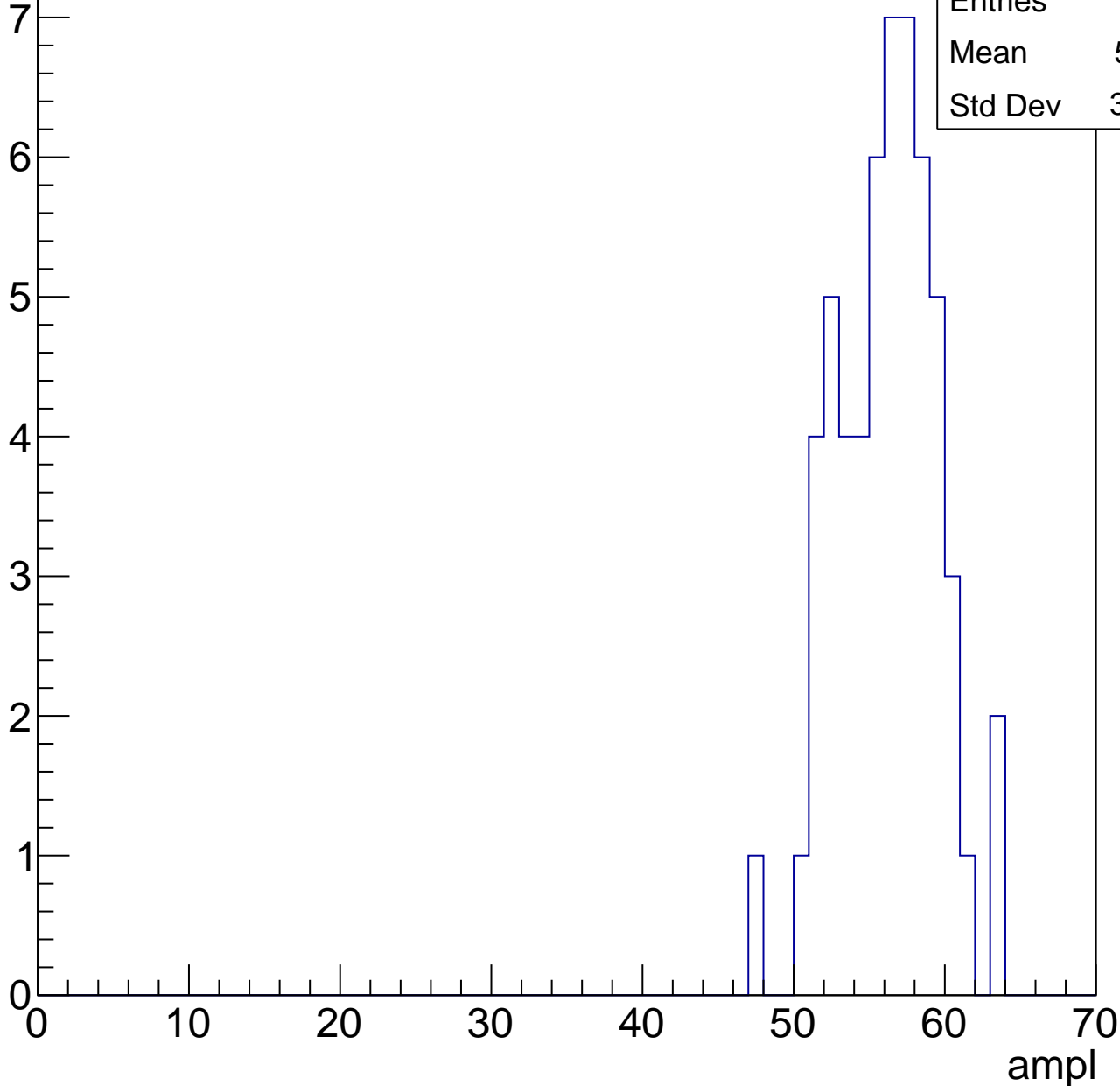


B1L101S, U23-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.71
Std Dev	3.266

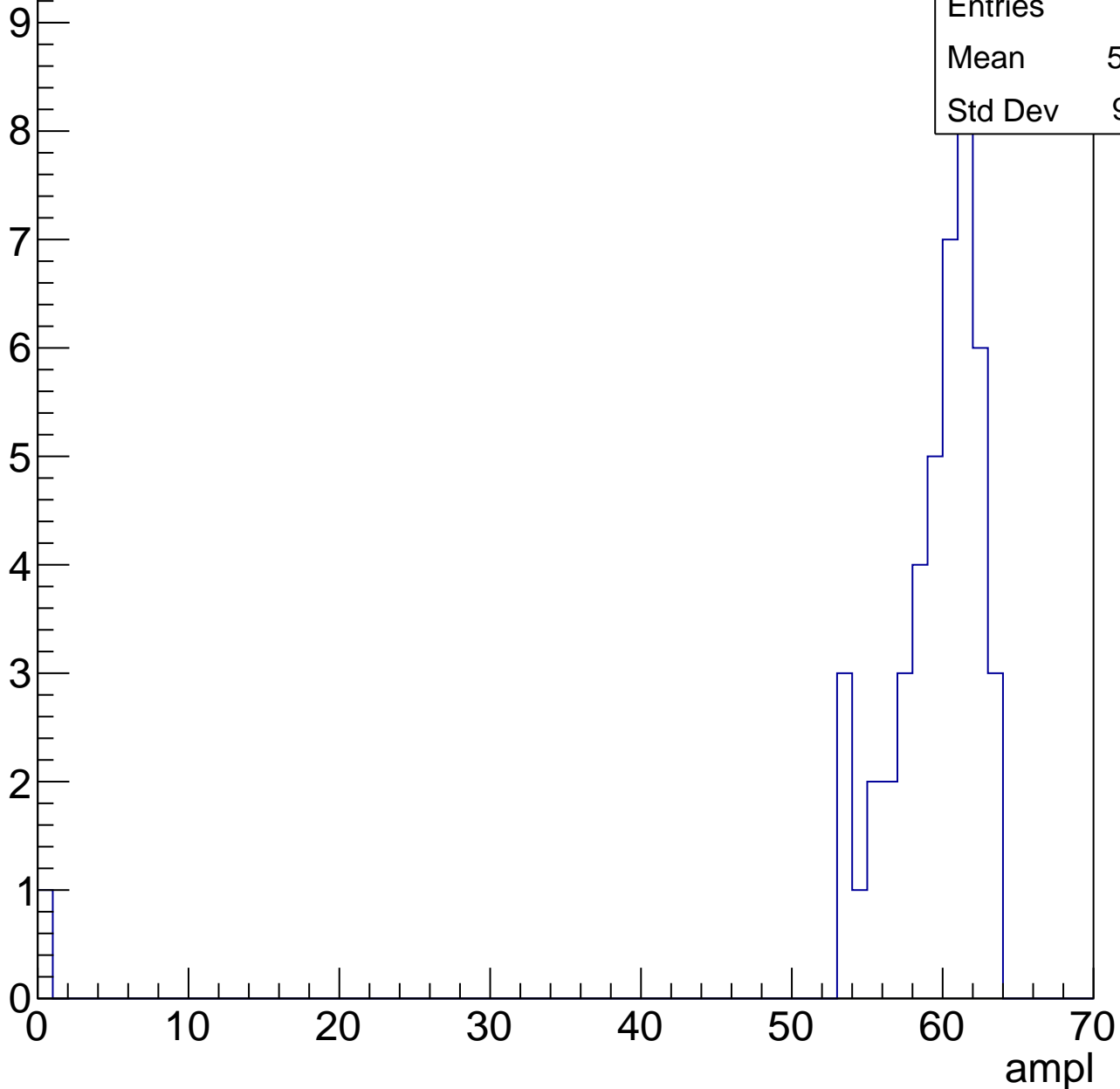


B1L101S, U23-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.89
Std Dev	9.051

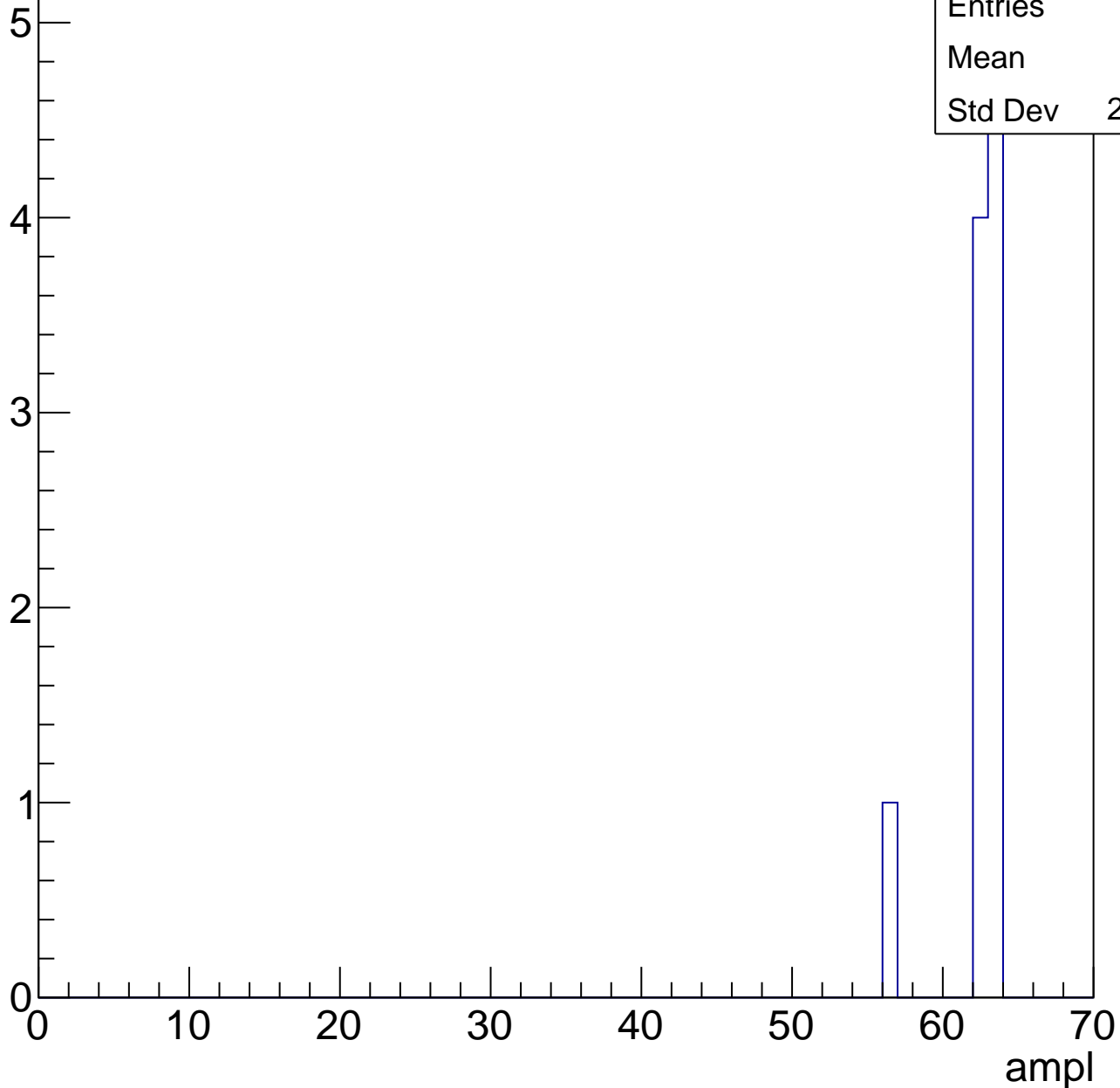


B1L101S, U23-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	61.9
Std Dev	2.022



B1L101S, U23-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch65, adc0

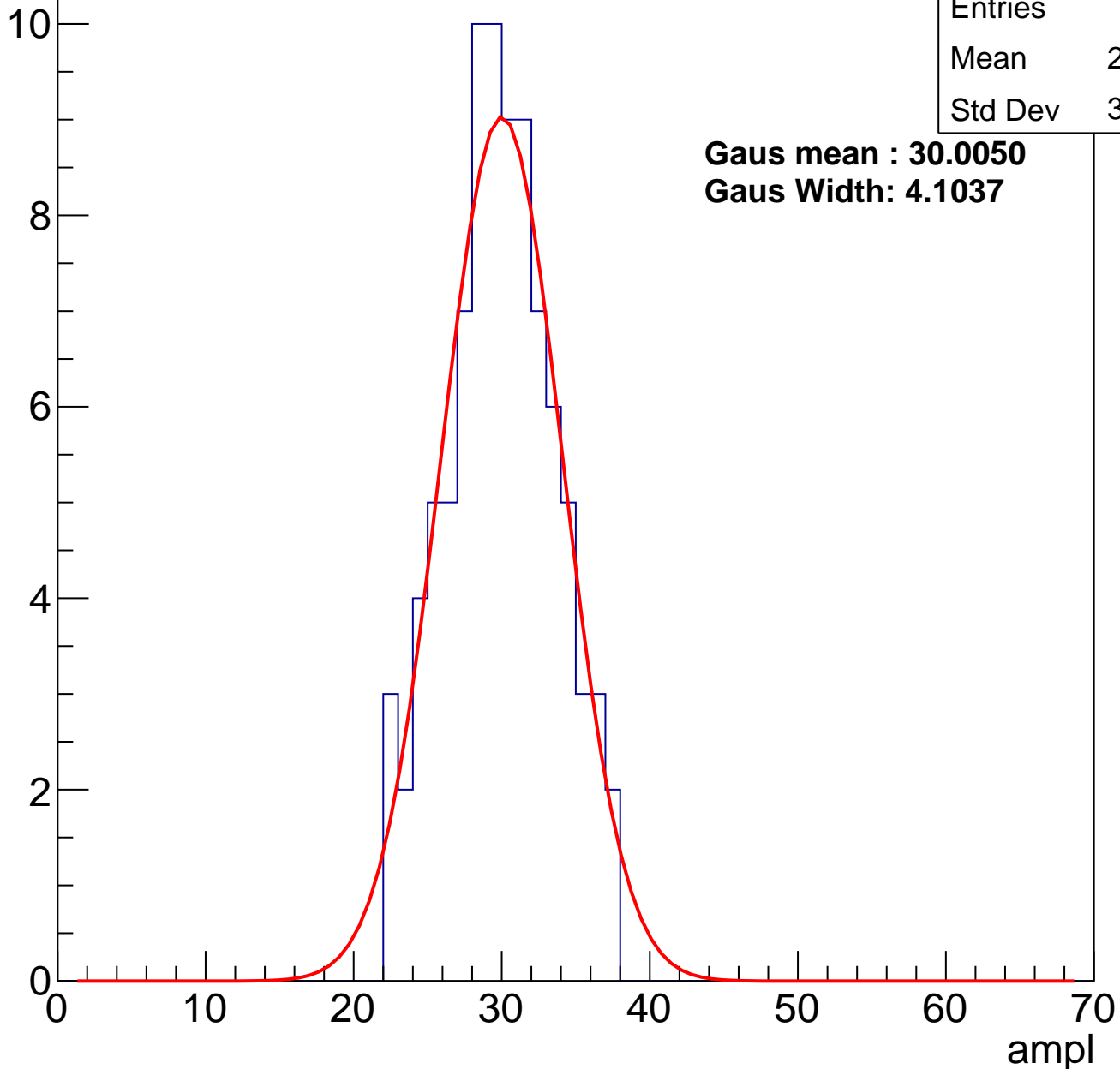
calib_packv5_042523_0143.root, FC#0, port D2

Entries	90
Mean	29.44
Std Dev	3.615

Gaus mean : 30.0050

Gaus Width: 4.1037

Entry



B1L101S, U23-ch65, adc1

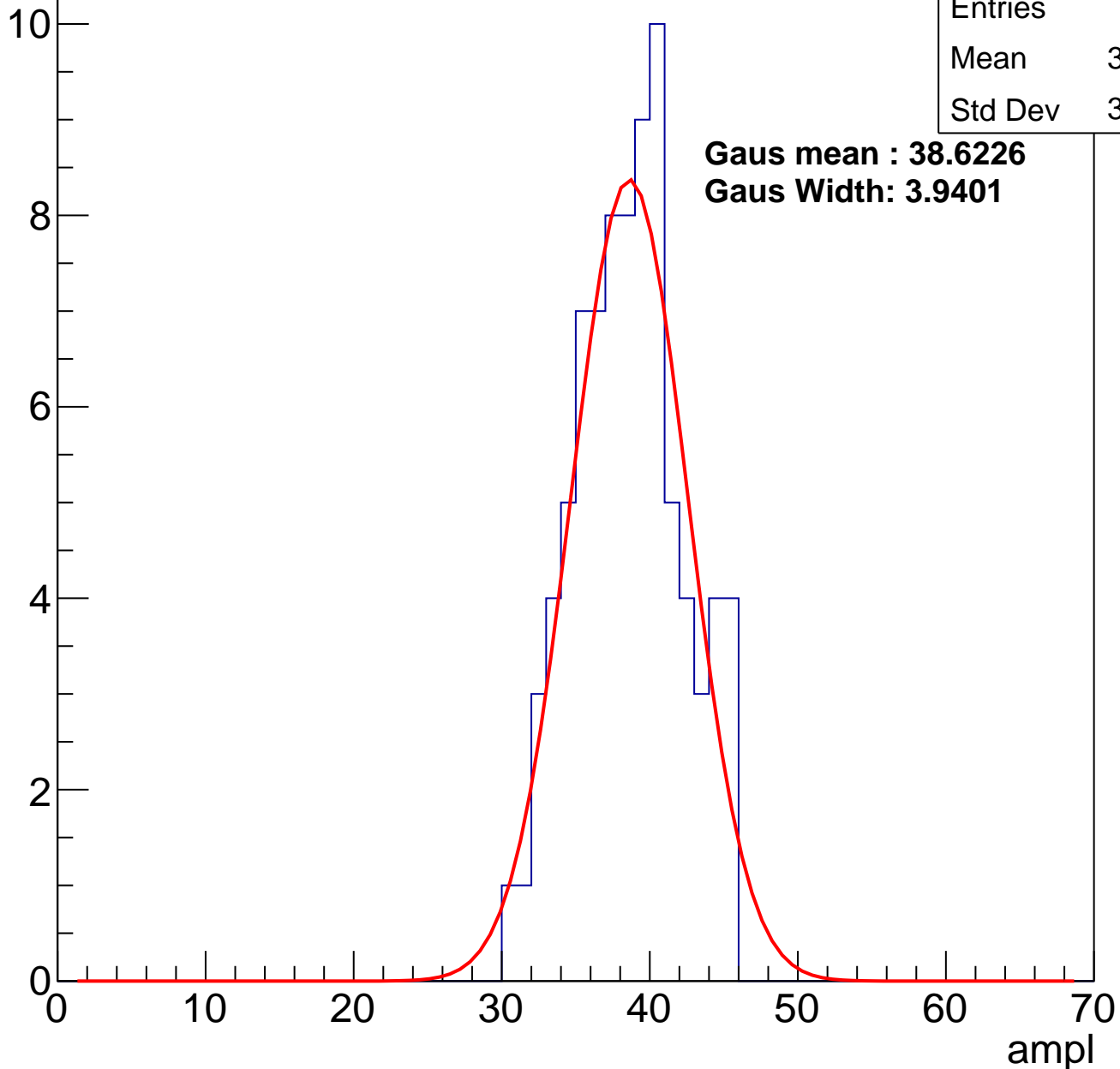
calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	38.13
Std Dev	3.593

Gaus mean : 38.6226

Gaus Width: 3.9401

Entry



B1L101S, U23-ch65, adc2

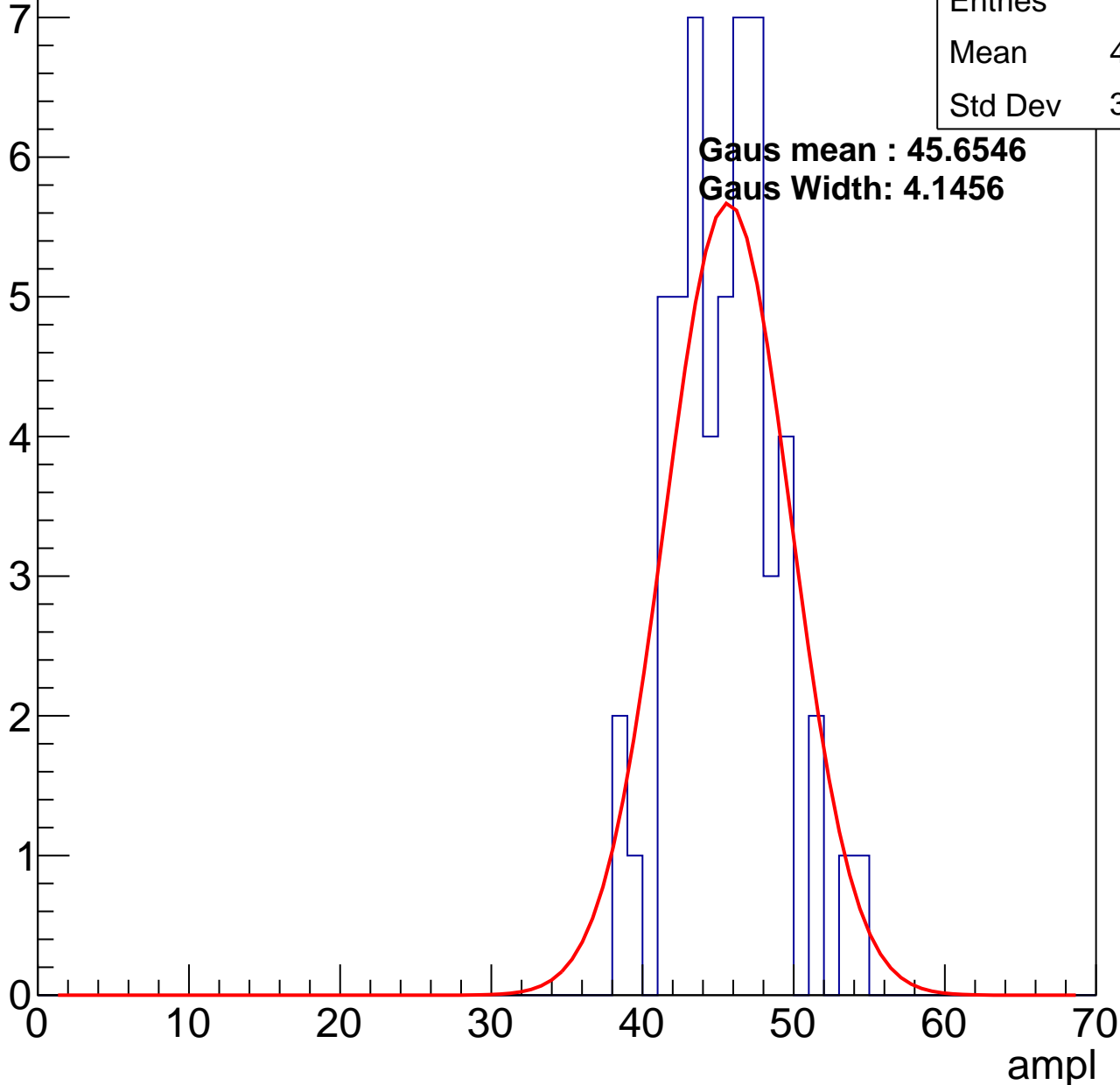
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	45.04
Std Dev	3.426

Gaus mean : 45.6546

Gaus Width: 4.1456

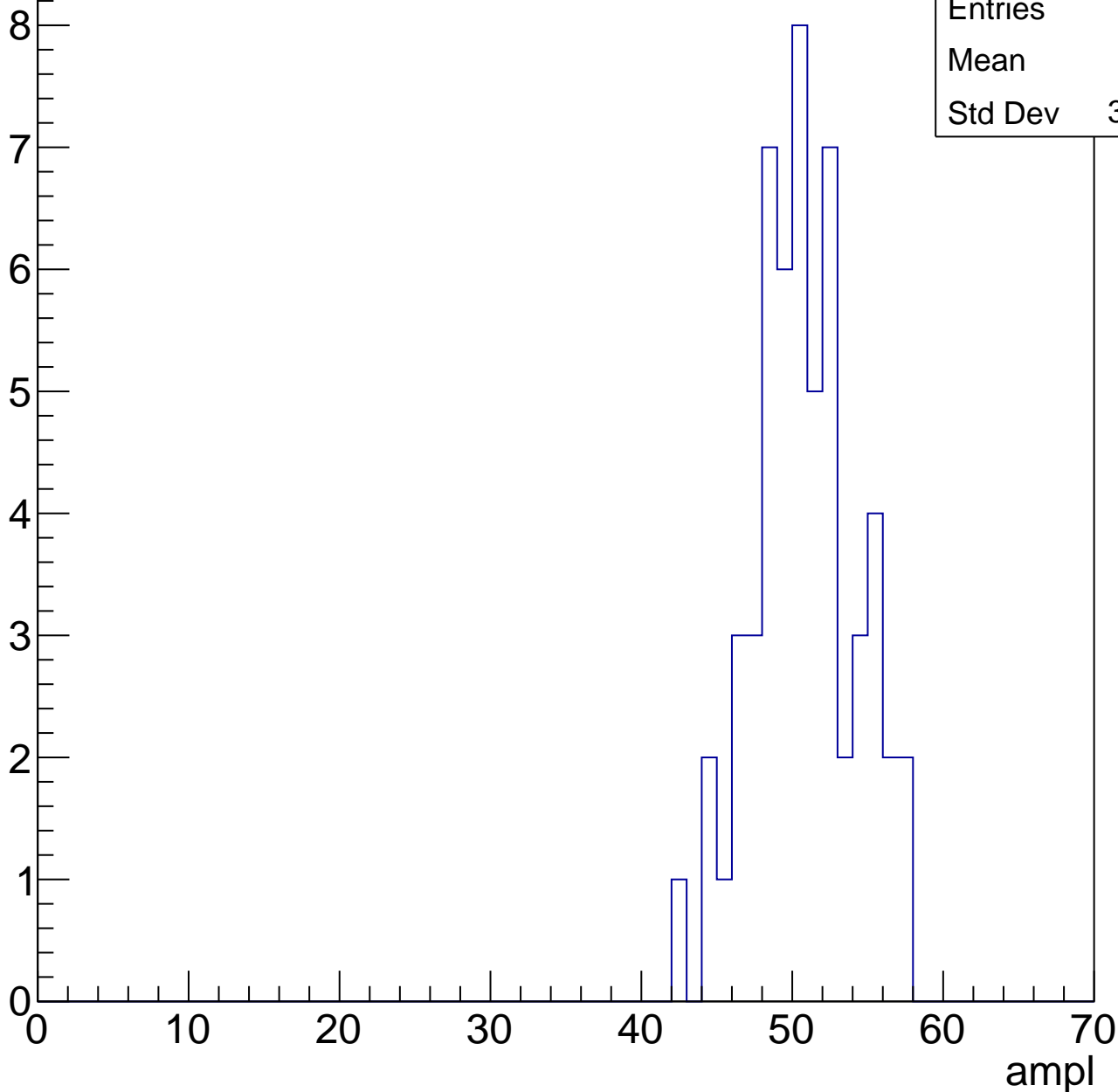


B1L101S, U23-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.3
Std Dev	3.364

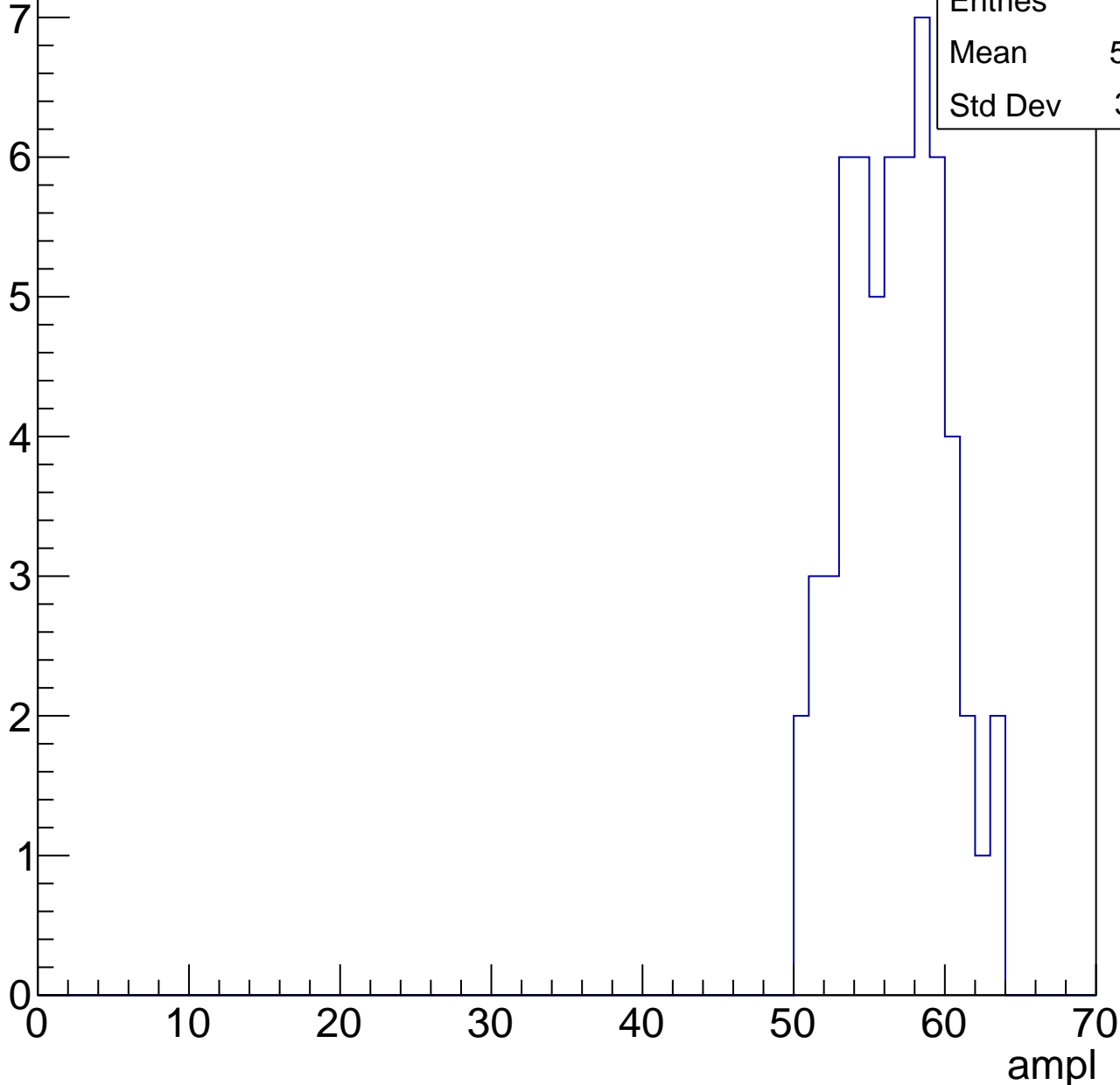


B1L101S, U23-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.17
Std Dev	3.221



B1L101S, U23-ch65, adc5

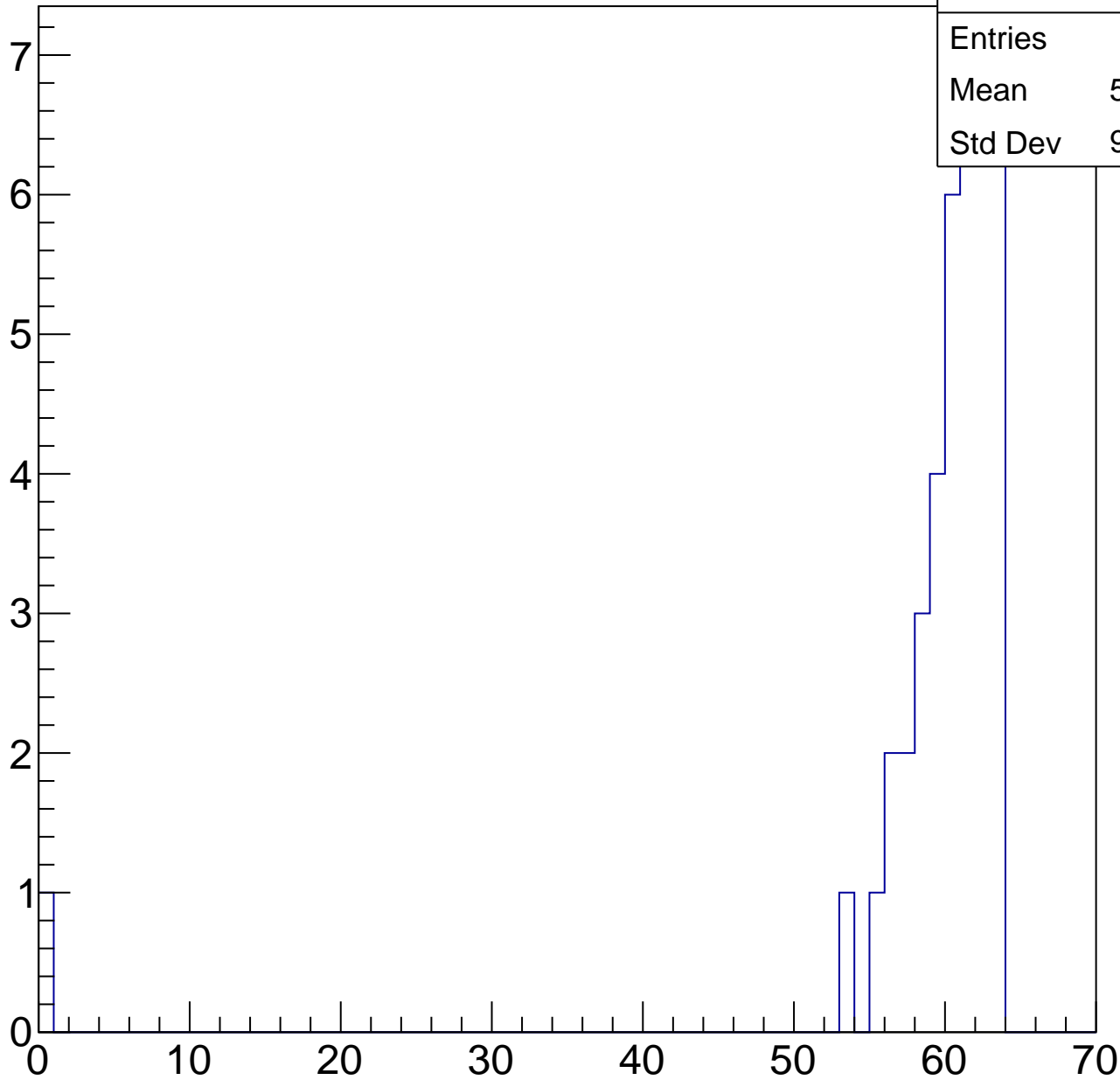
calib_packv5_042523_0143.root, FC#0, port D2

Entry

7
6
5
4
3
2
1
0

Entries	41
Mean	58.68
Std Dev	9.585

ampl



B1L101S, U23-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch66, adc0

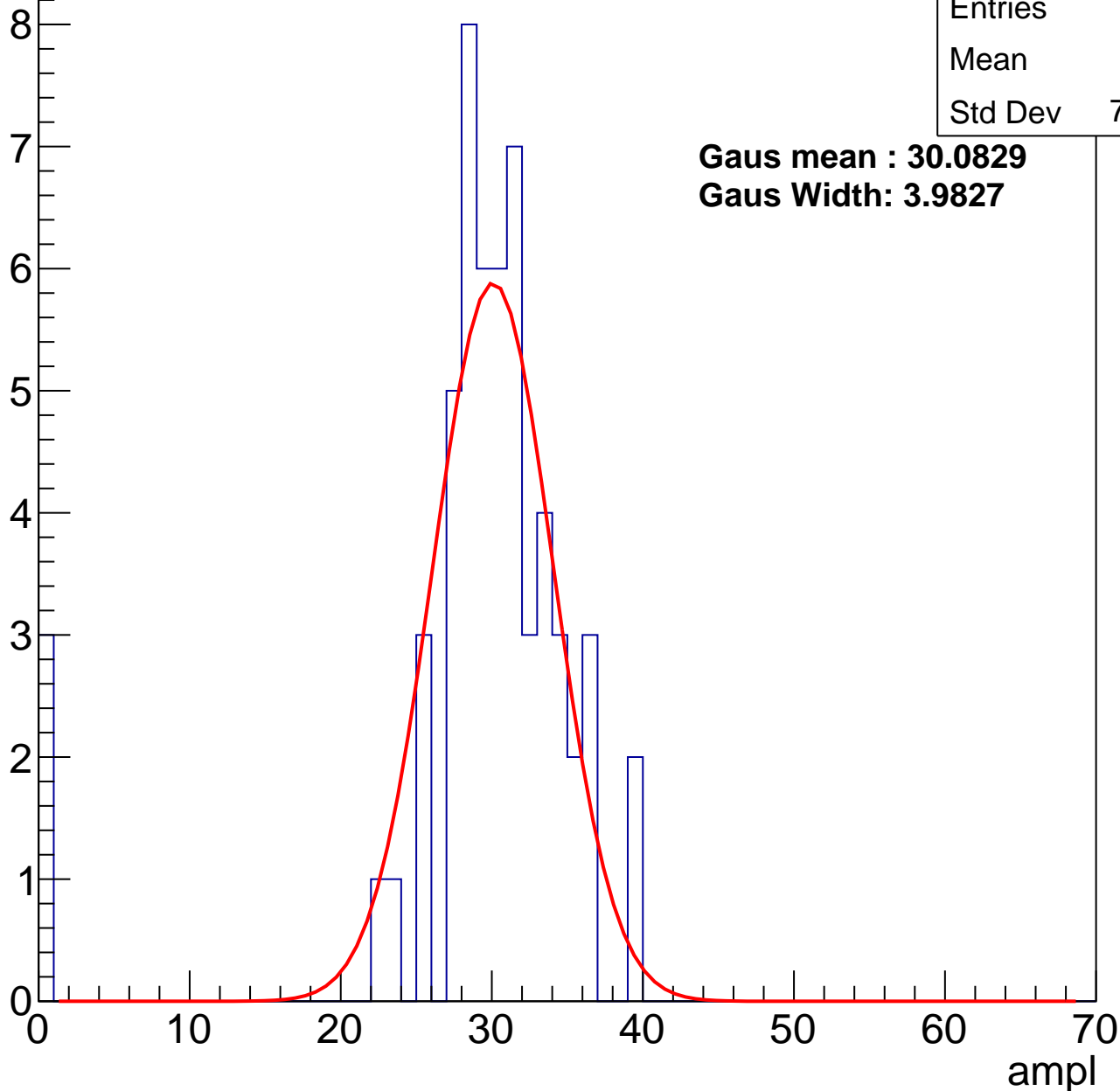
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	28.7
Std Dev	7.606

Gaus mean : 30.0829

Gaus Width: 3.9827



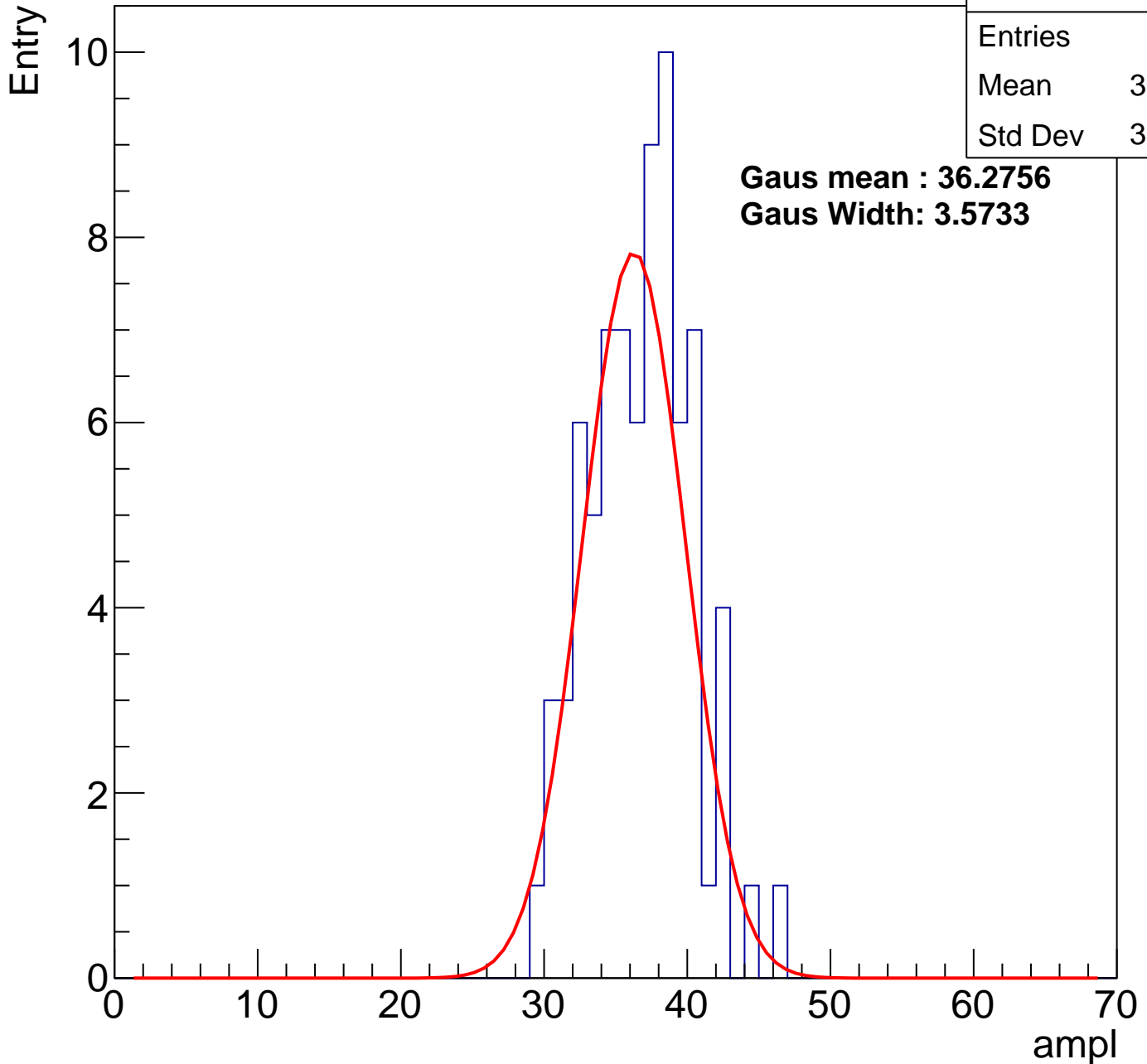
B1L101S, U23-ch66, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	36.29
Std Dev	3.512

Gaus mean : 36.2756

Gaus Width: 3.5733



B1L101S, U23-ch66, adc2

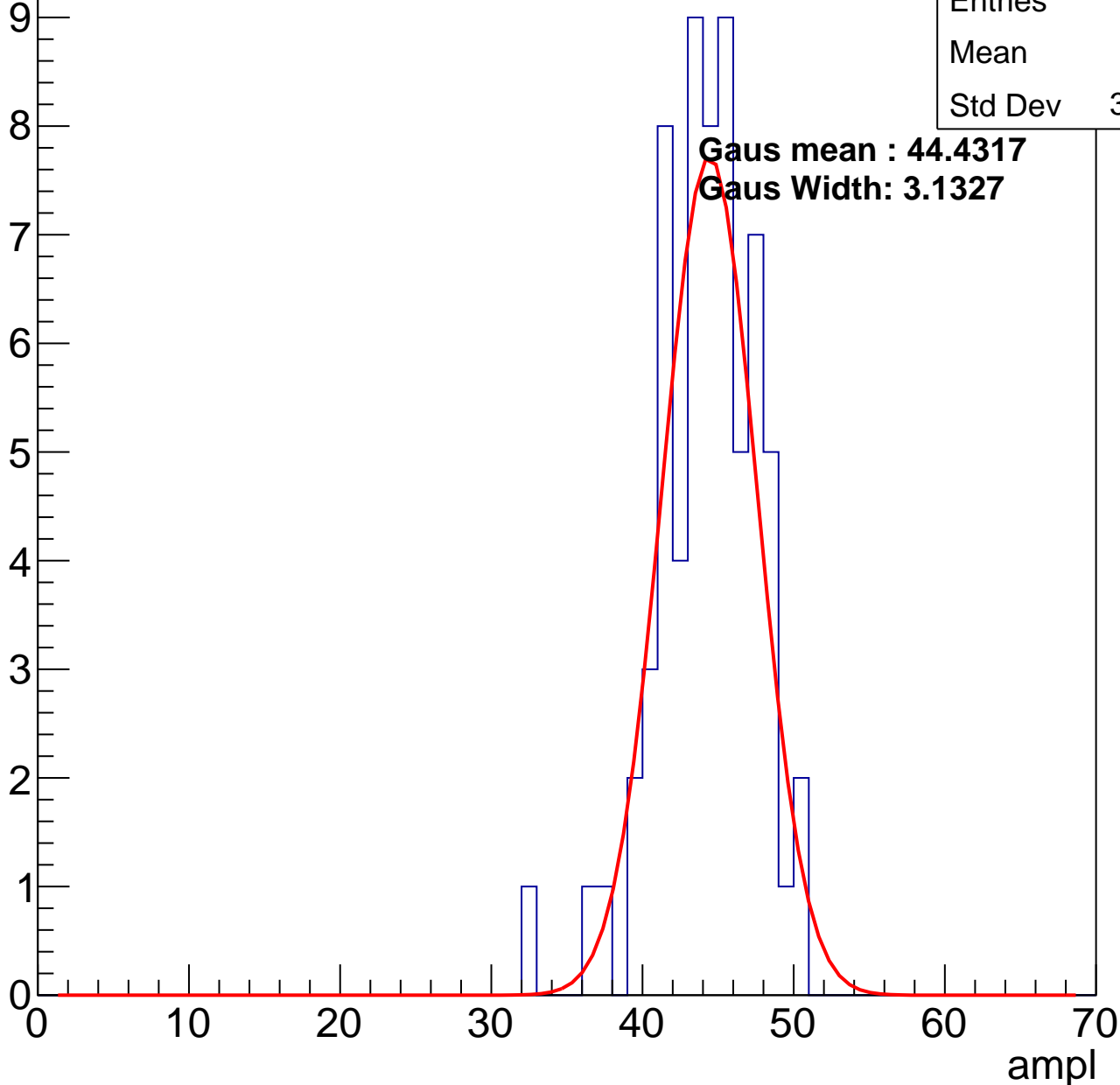
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.8
Std Dev	3.299

Gaus mean : 44.4317

Gaus Width: 3.1327

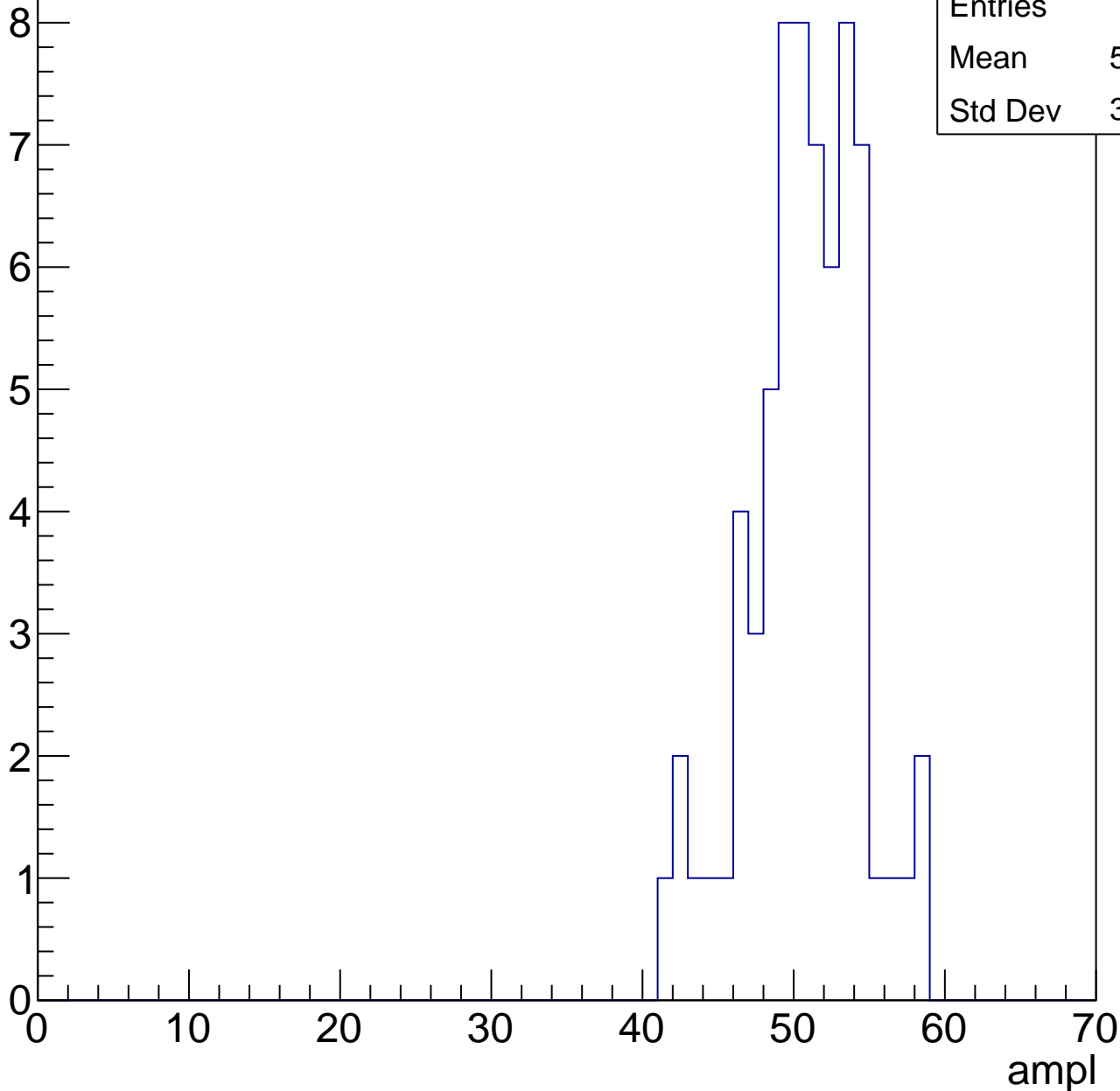


B1L101S, U23-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

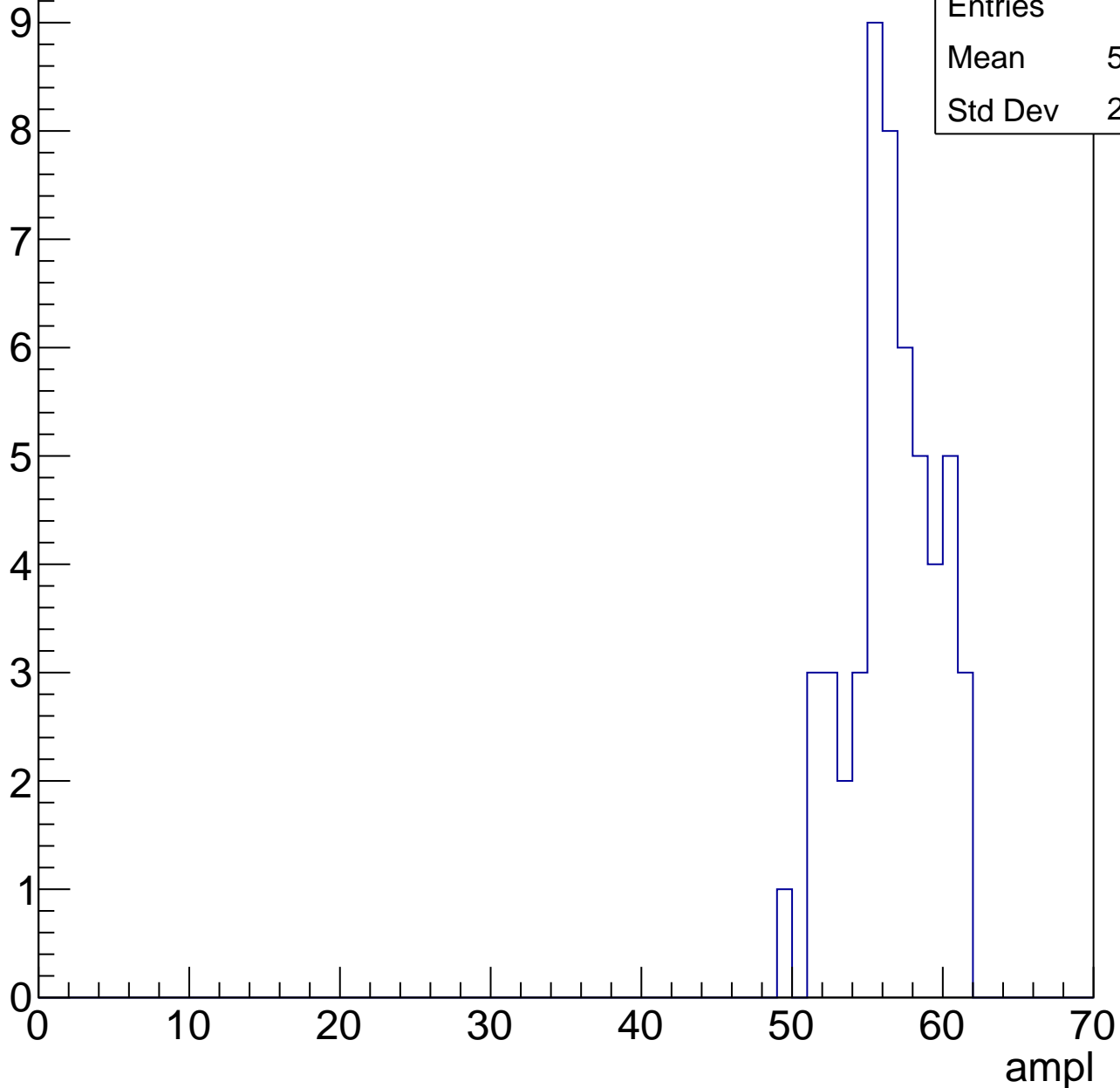
Entries	67
Mean	50.28
Std Dev	3.636



B1L101S, U23-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	52
Mean	56.15
Std Dev	2.865

B1L101S, U23-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	45
Mean	59.11
Std Dev	9.209

Entry

10

8

6

4

2

0

0

10

20

30

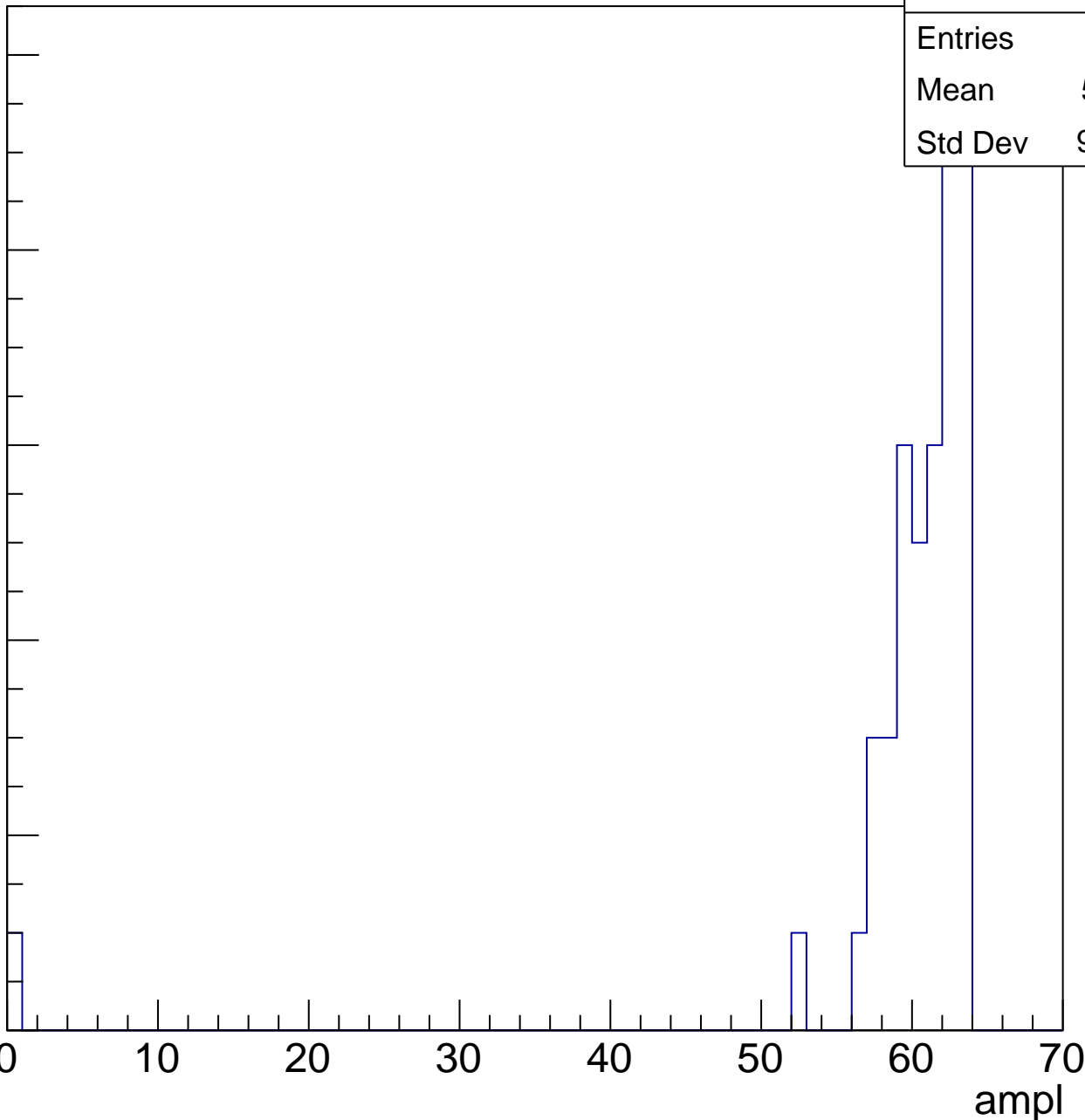
40

50

60

70

ampl



B1L101S, U23-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch67, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	94
Mean	32.45
Std Dev	5.061

Gaus mean : 33.1909

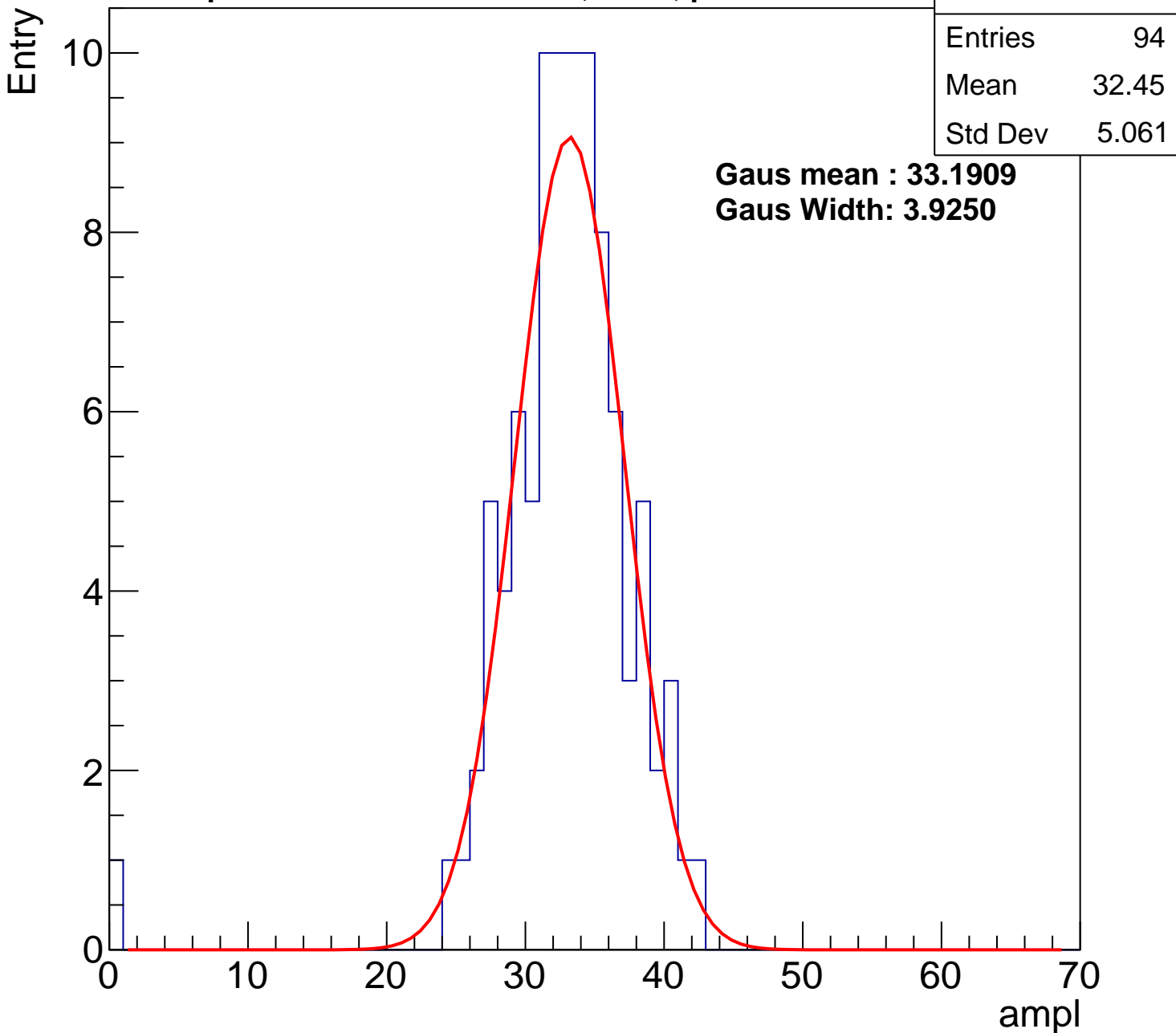
Gaus Width: 3.9250

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch67, adc1

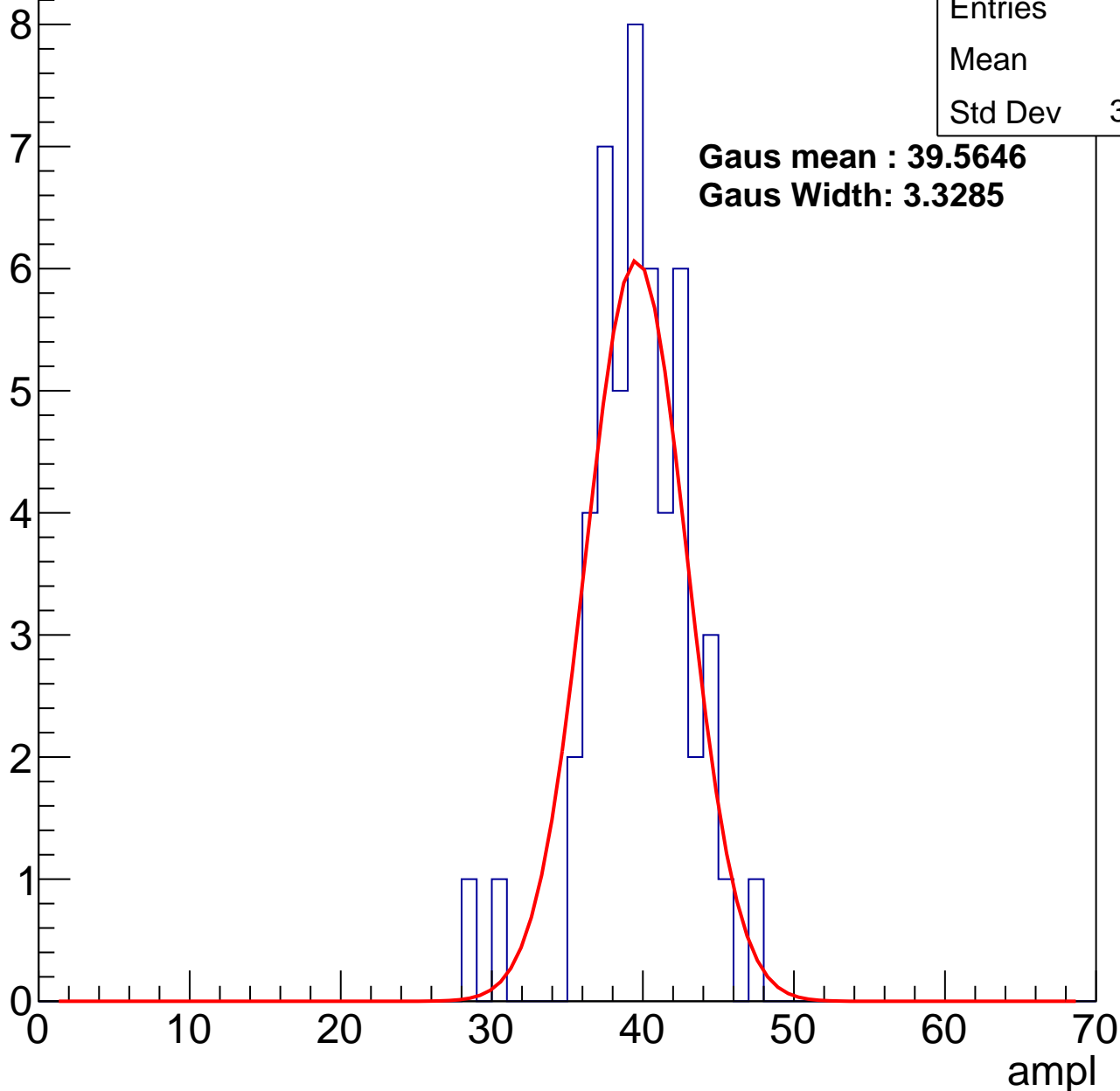
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	39.2
Std Dev	3.384

Gaus mean : 39.5646

Gaus Width: 3.3285



B1L101S, U23-ch67, adc2

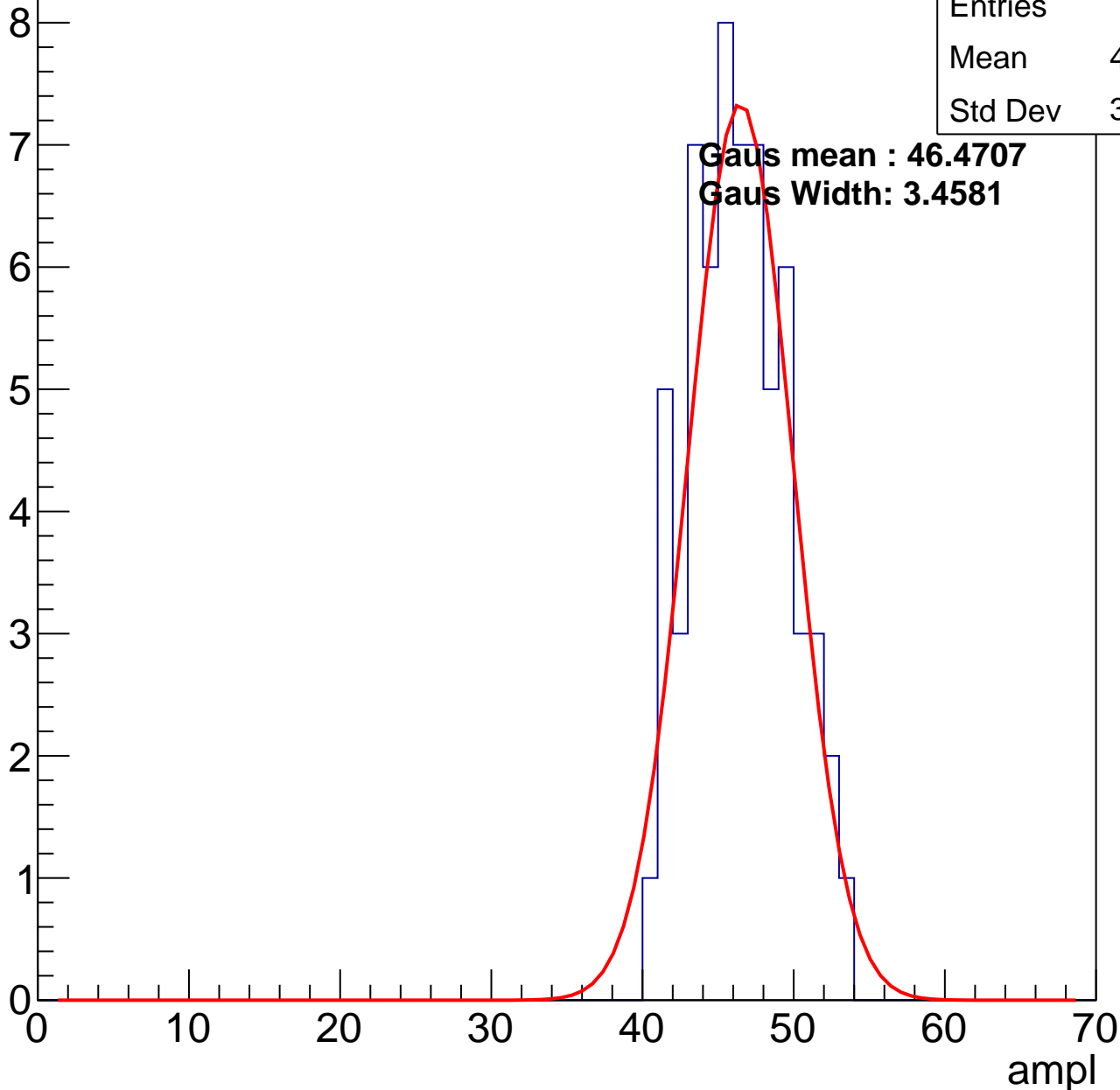
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	45.95
Std Dev	3.135

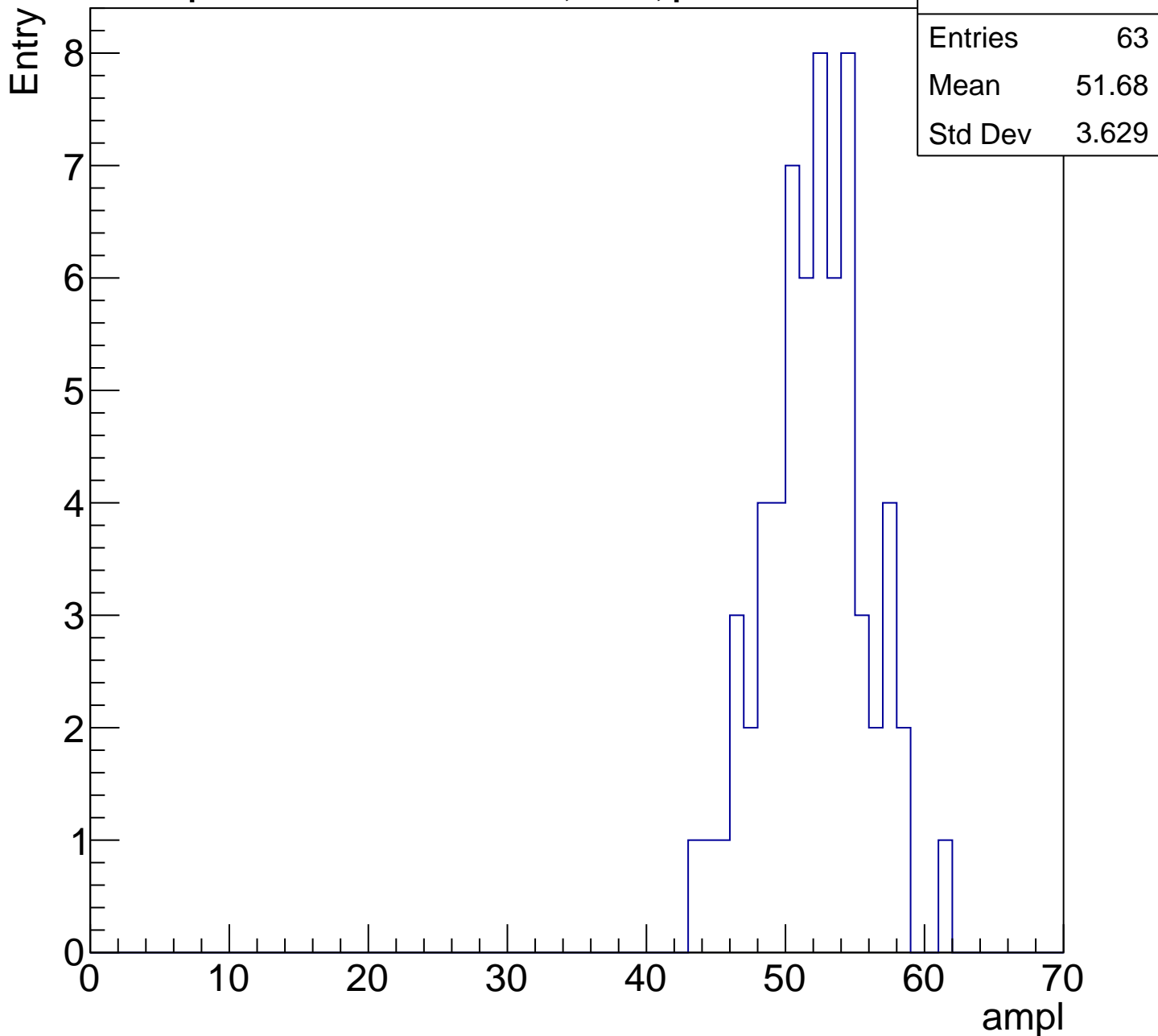
Gaus mean : 46.4707

Gaus Width: 3.4581



B1L101S, U23-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

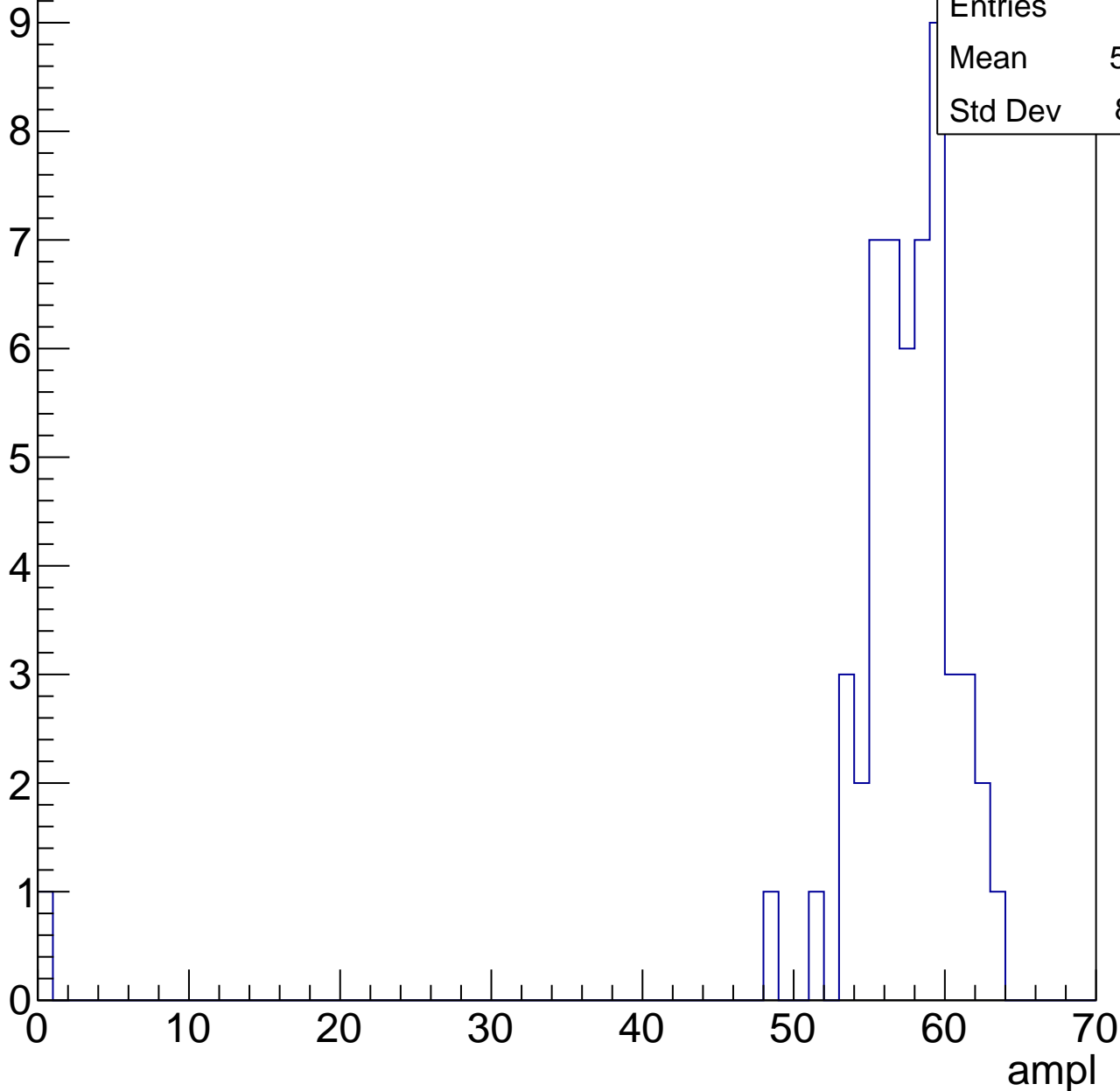


B1L101S, U23-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

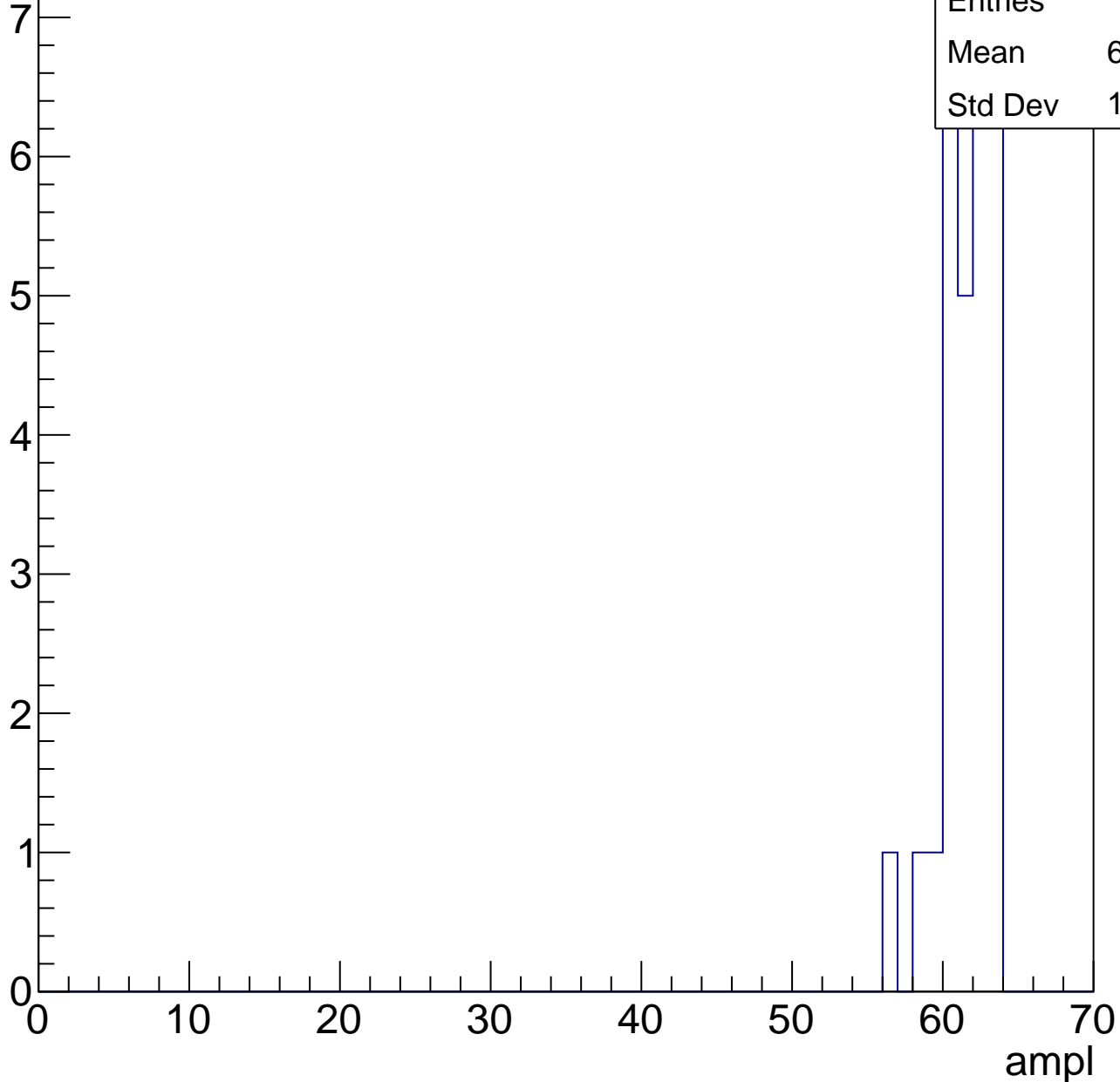
Entries	53
Mean	56.08
Std Dev	8.271



B1L101S, U23-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch68, adc0

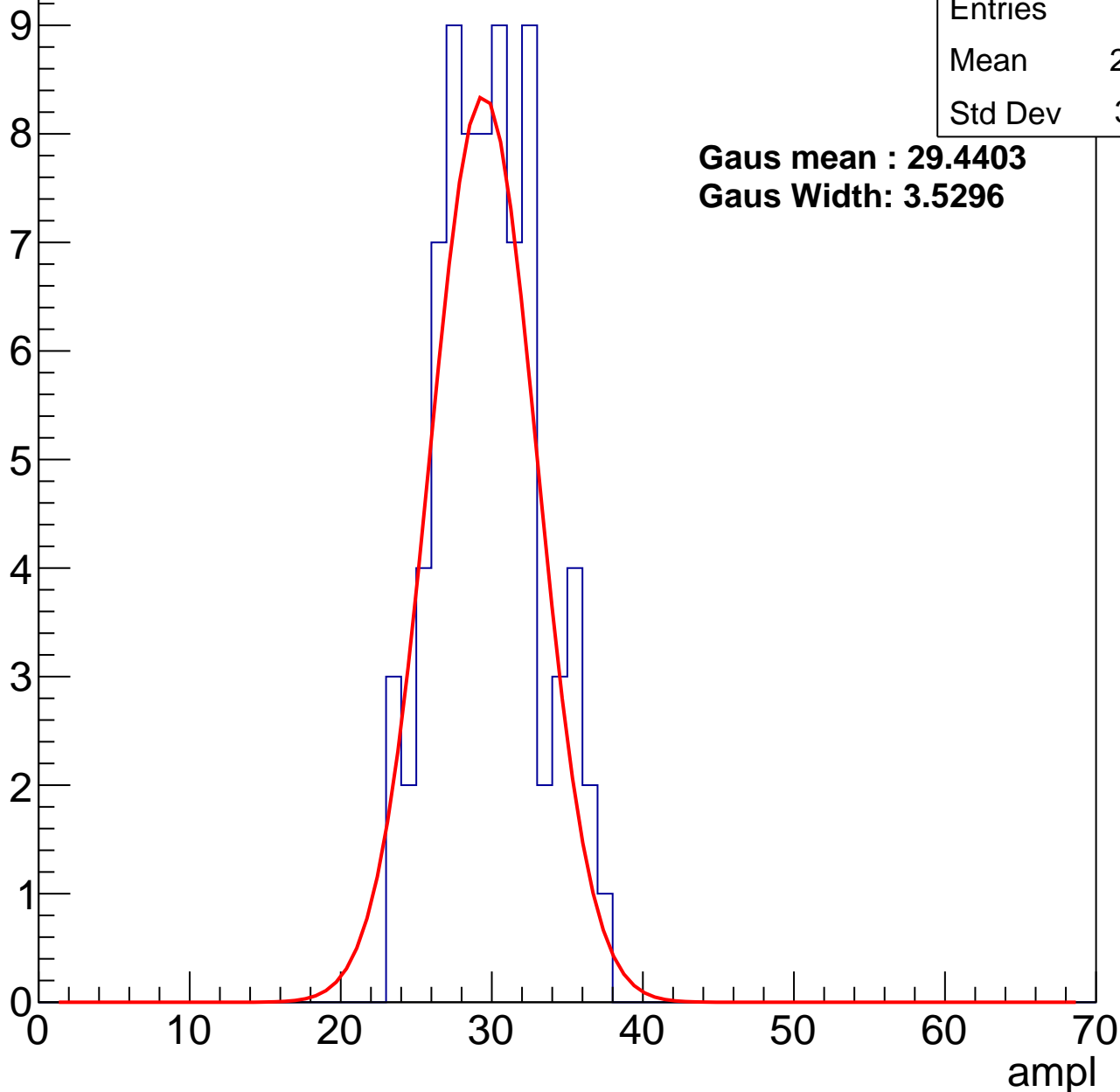
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.36
Std Dev	3.301

Gaus mean : 29.4403

Gaus Width: 3.5296



B1L101S, U23-ch68, adc1

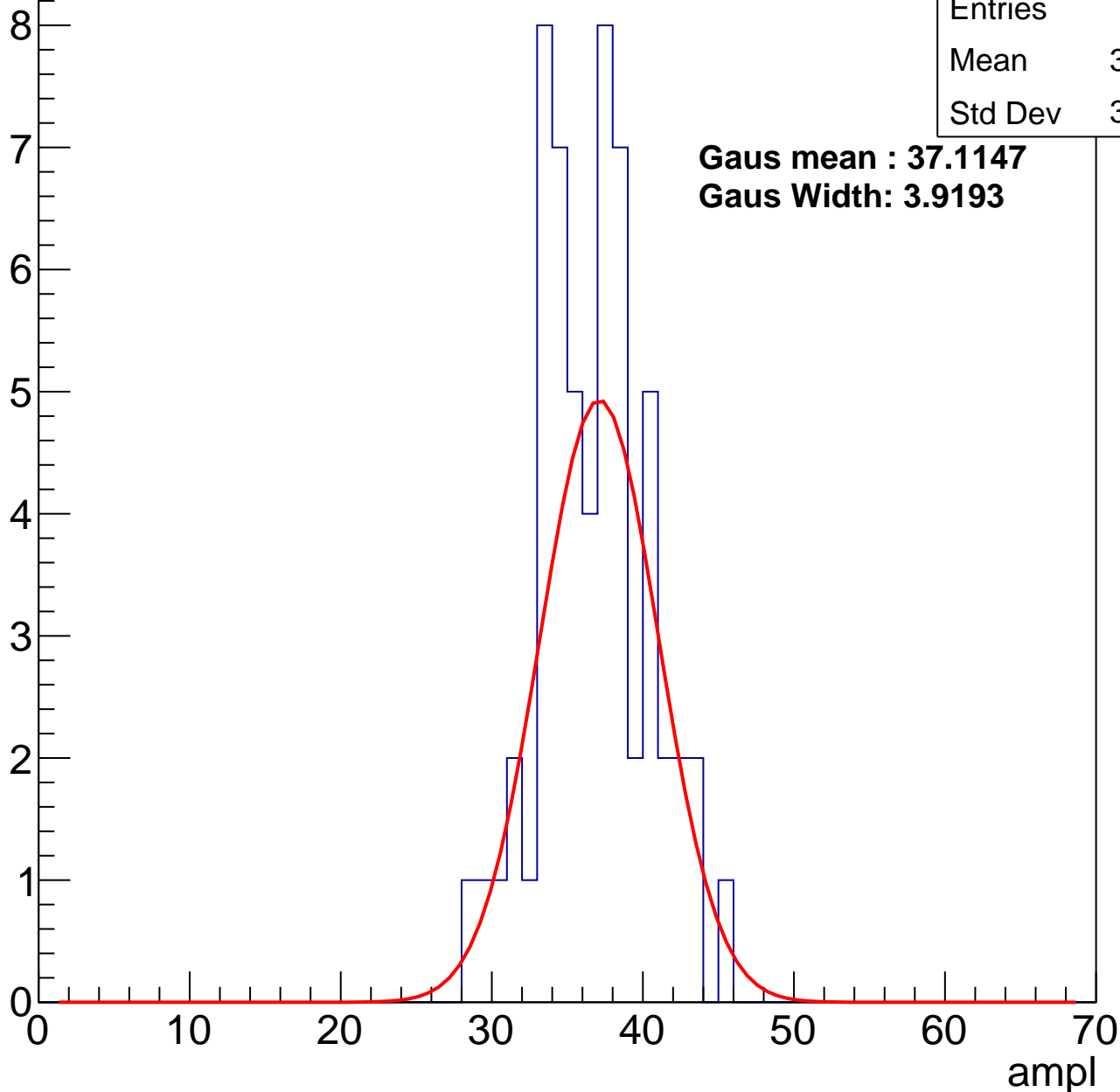
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.25
Std Dev	3.568

Gaus mean : 37.1147

Gaus Width: 3.9193



B1L101S, U23-ch68, adc2

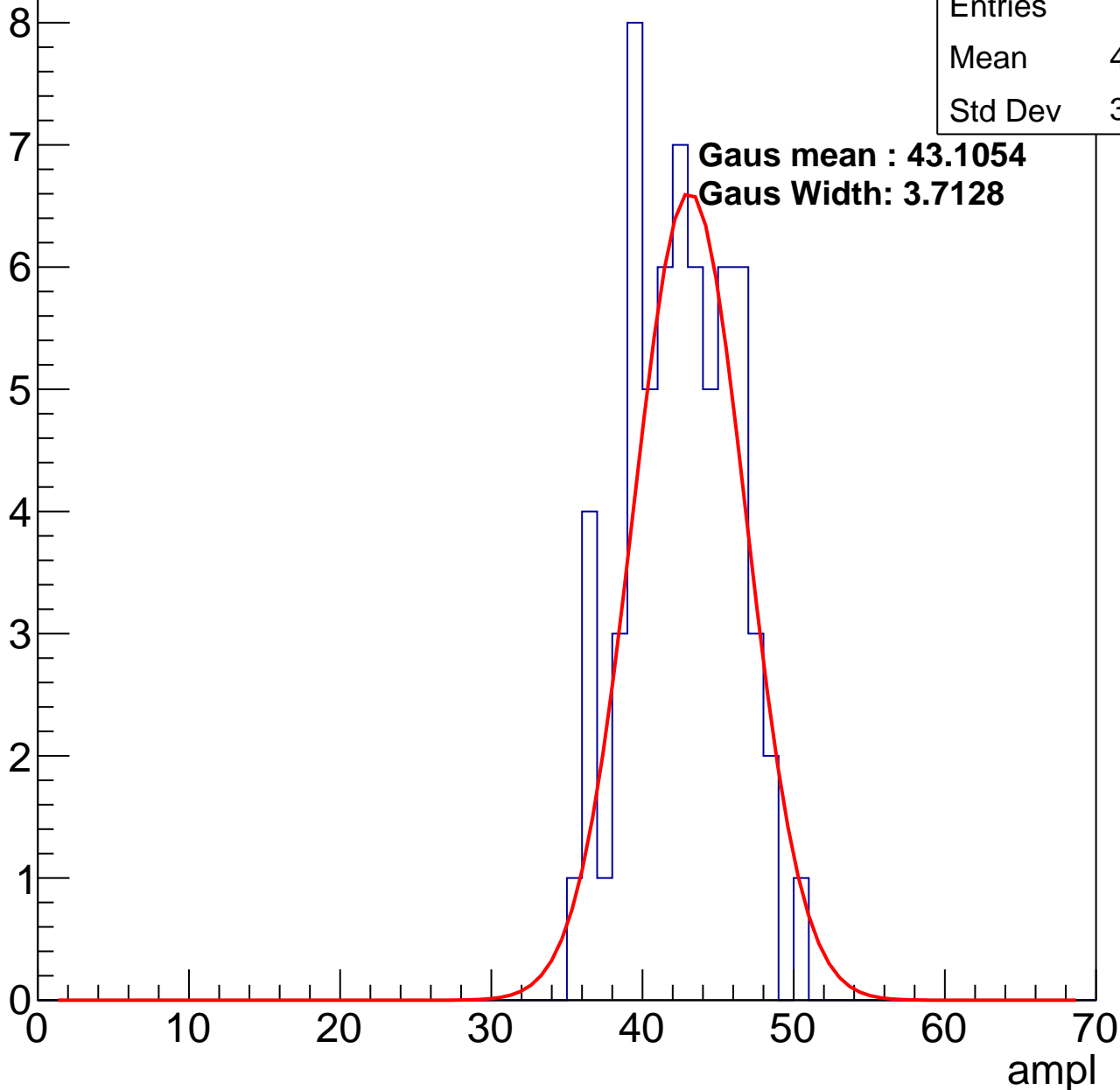
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.08
Std Dev	3.425

Gaus mean : 43.1054

Gaus Width: 3.7128

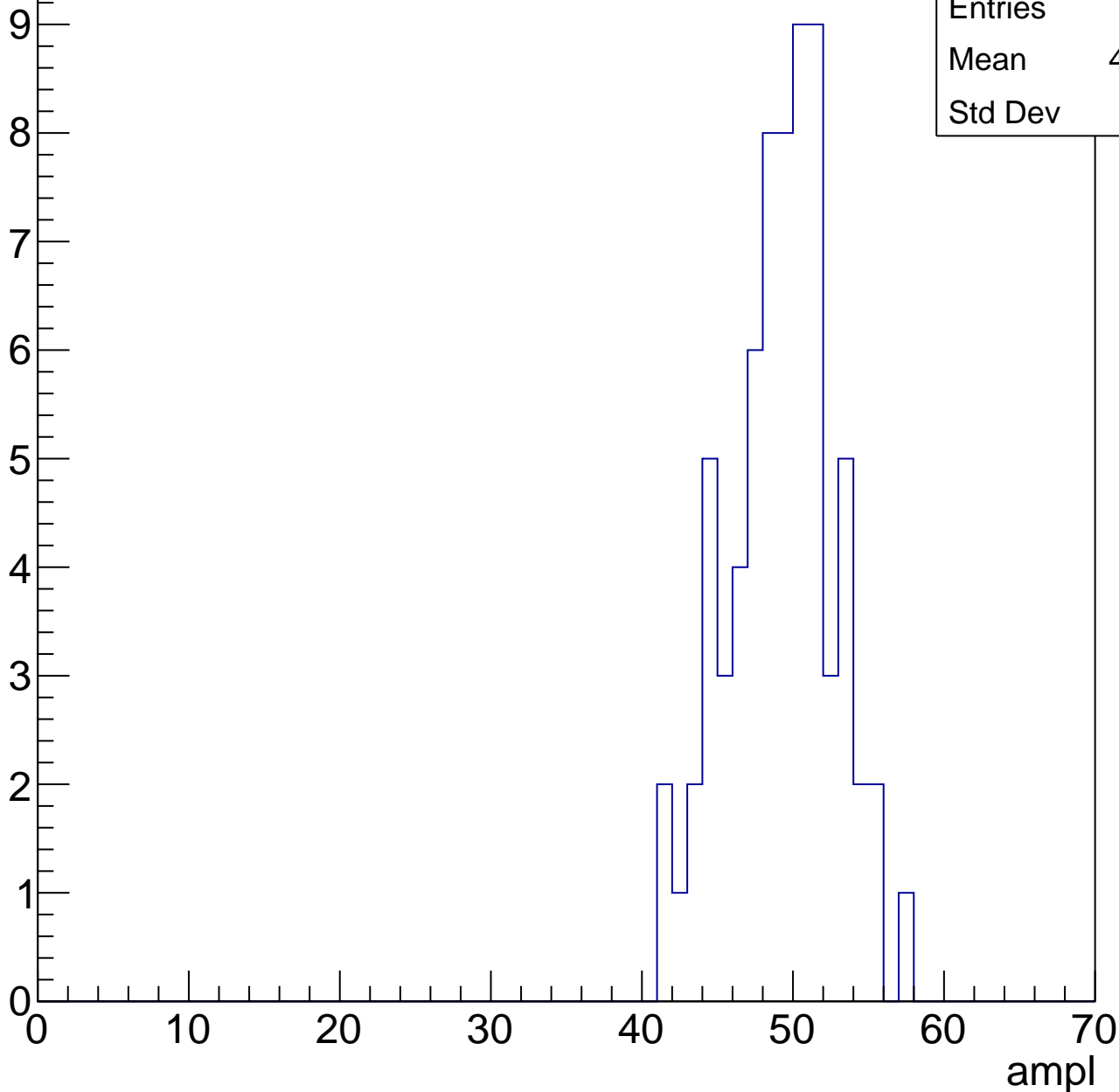


B1L101S, U23-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.74
Std Dev	3.43

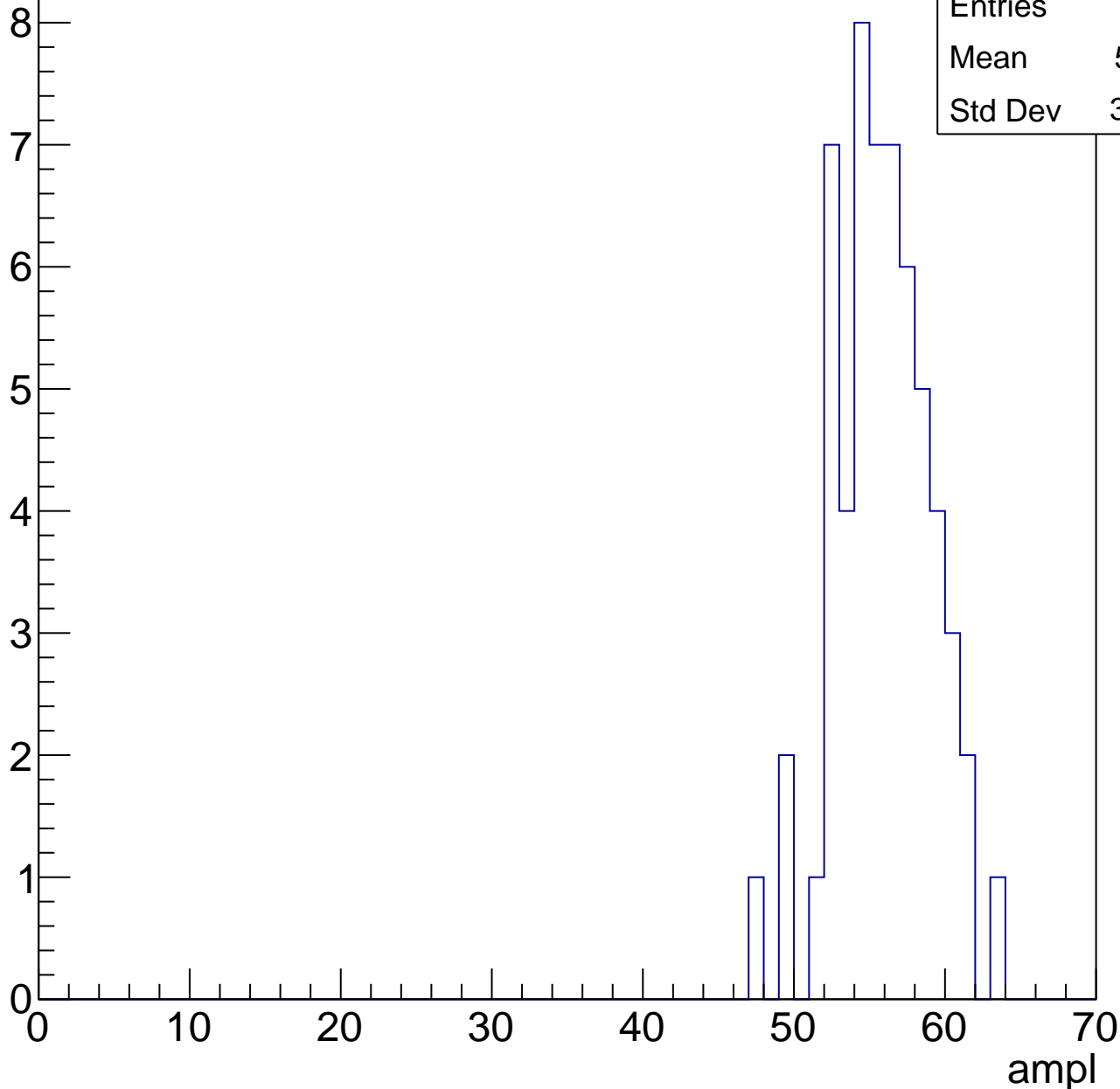


B1L101S, U23-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.41
Std Dev	3.162

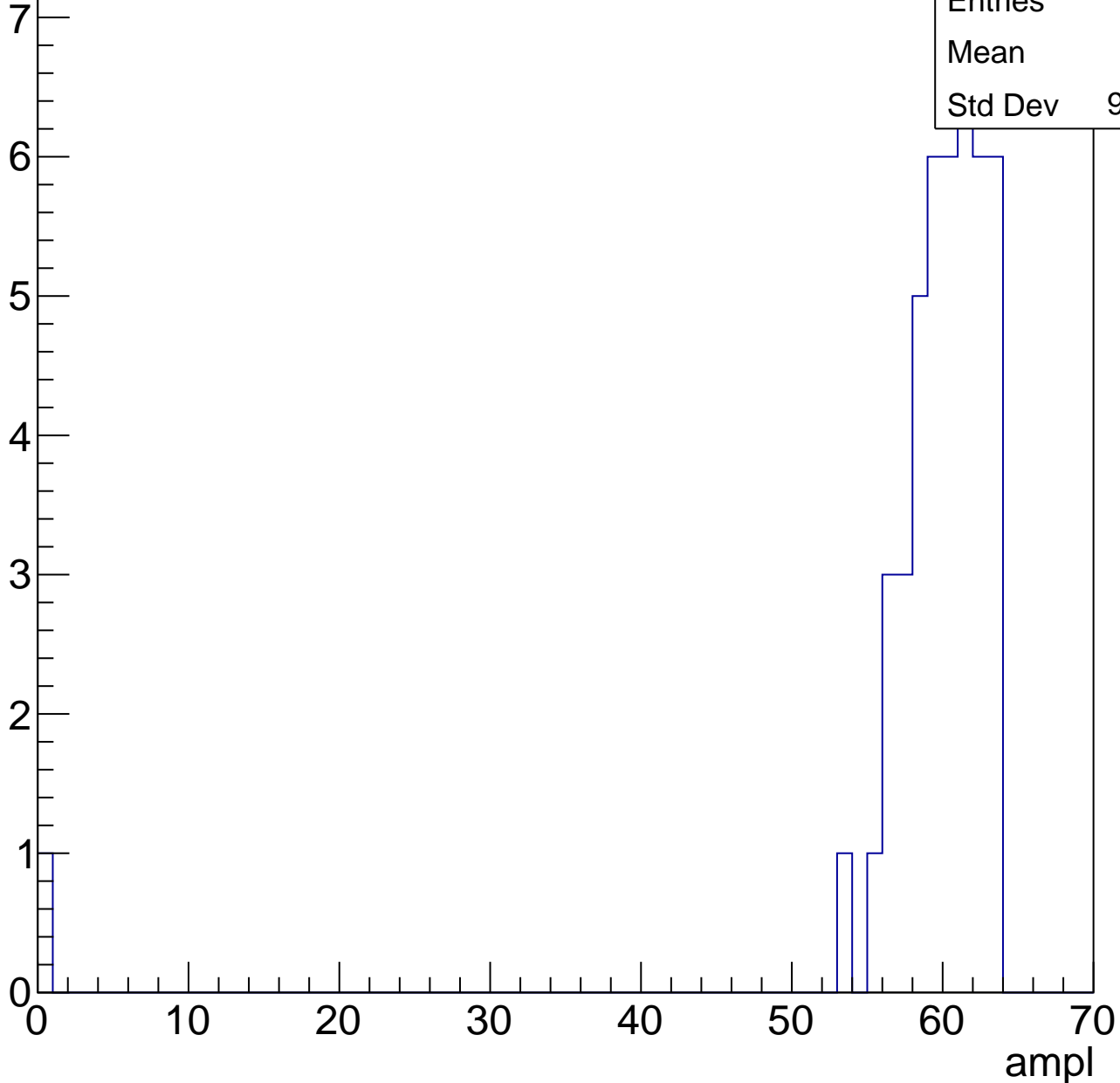


B1L101S, U23-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

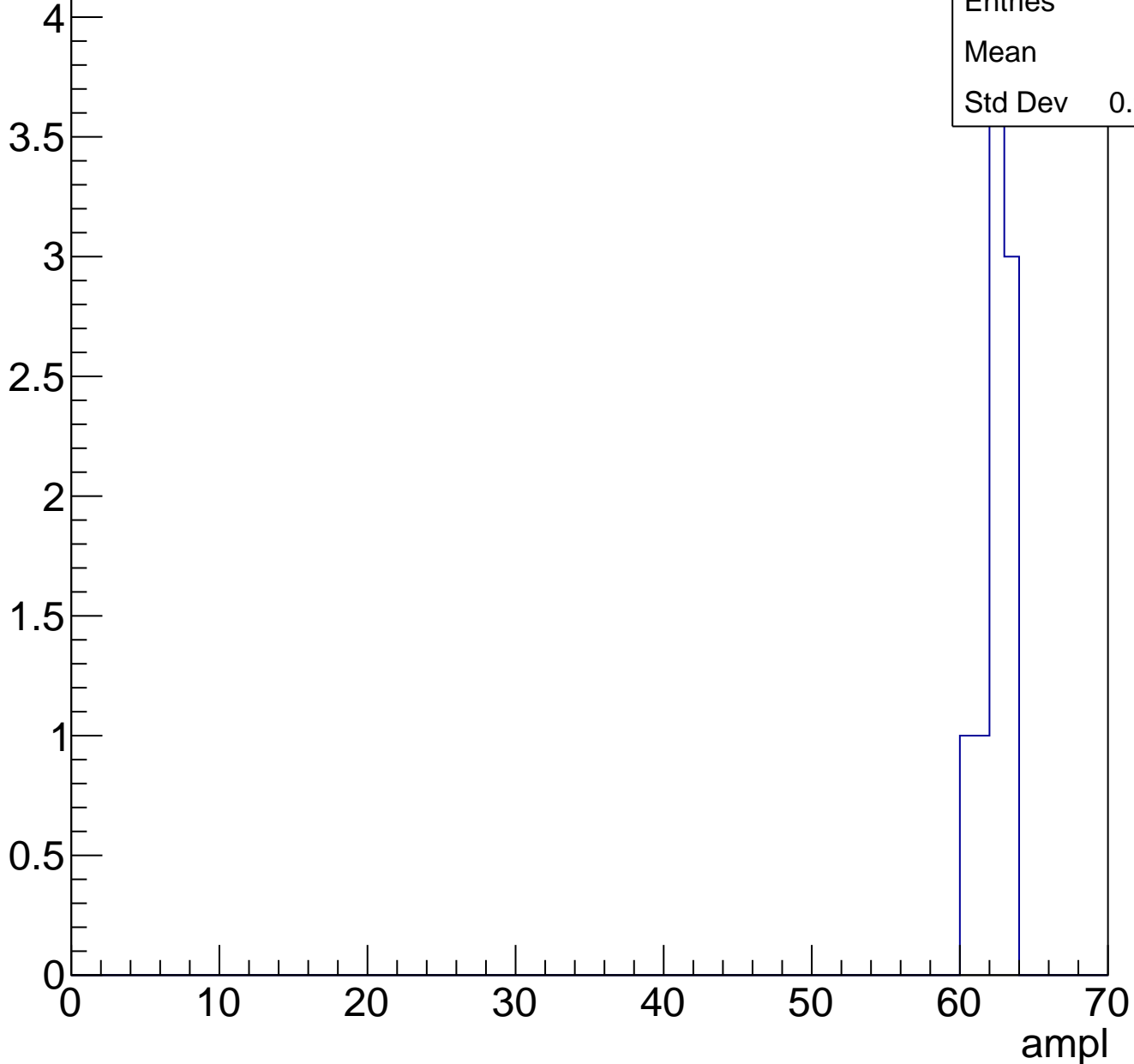
Entries	45
Mean	58.4
Std Dev	9.122



B1L101S, U23-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch69, adc0

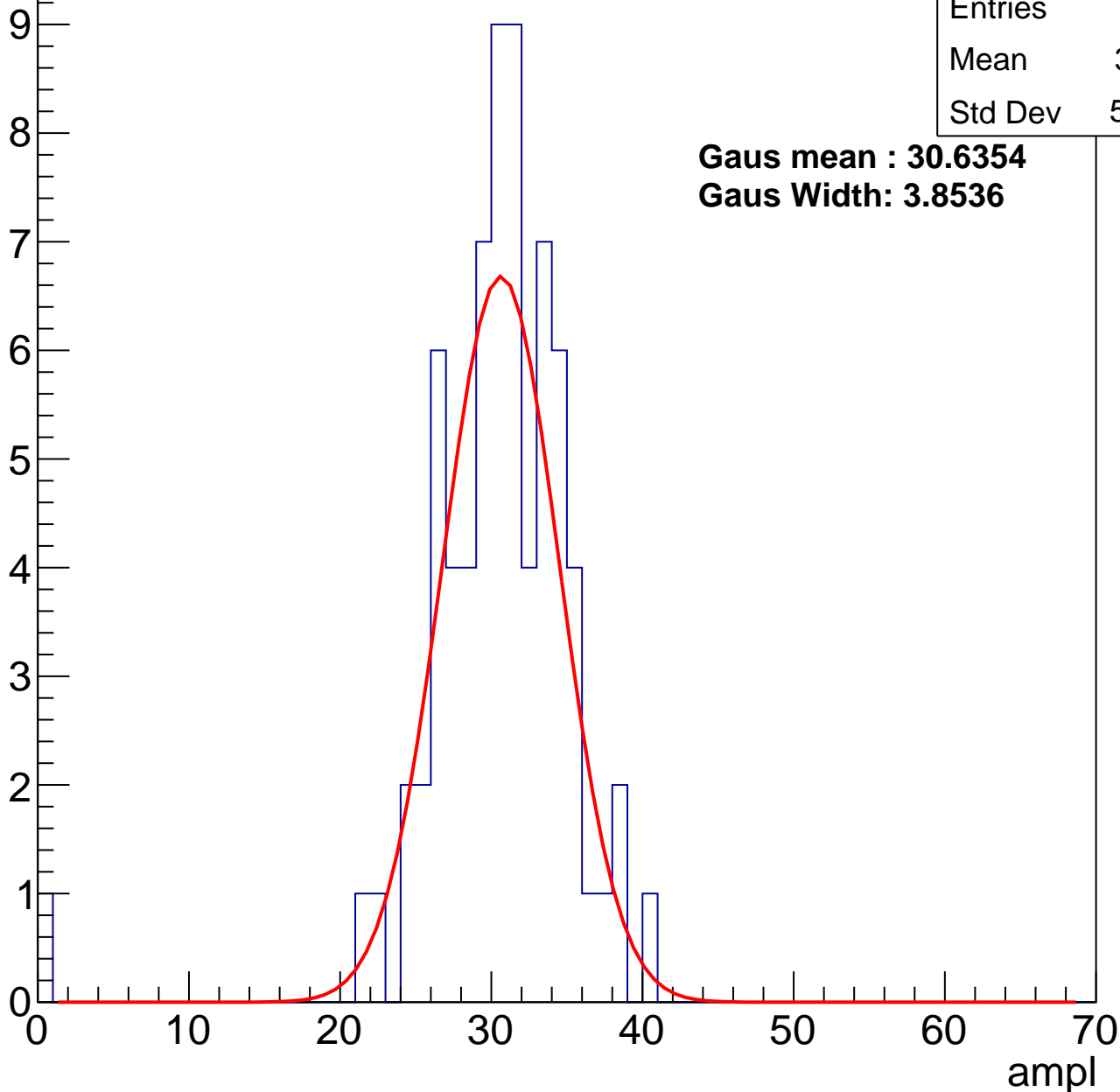
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.01
Std Dev	5.163

Gaus mean : 30.6354

Gaus Width: 3.8536



B1L101S, U23-ch69, adc1

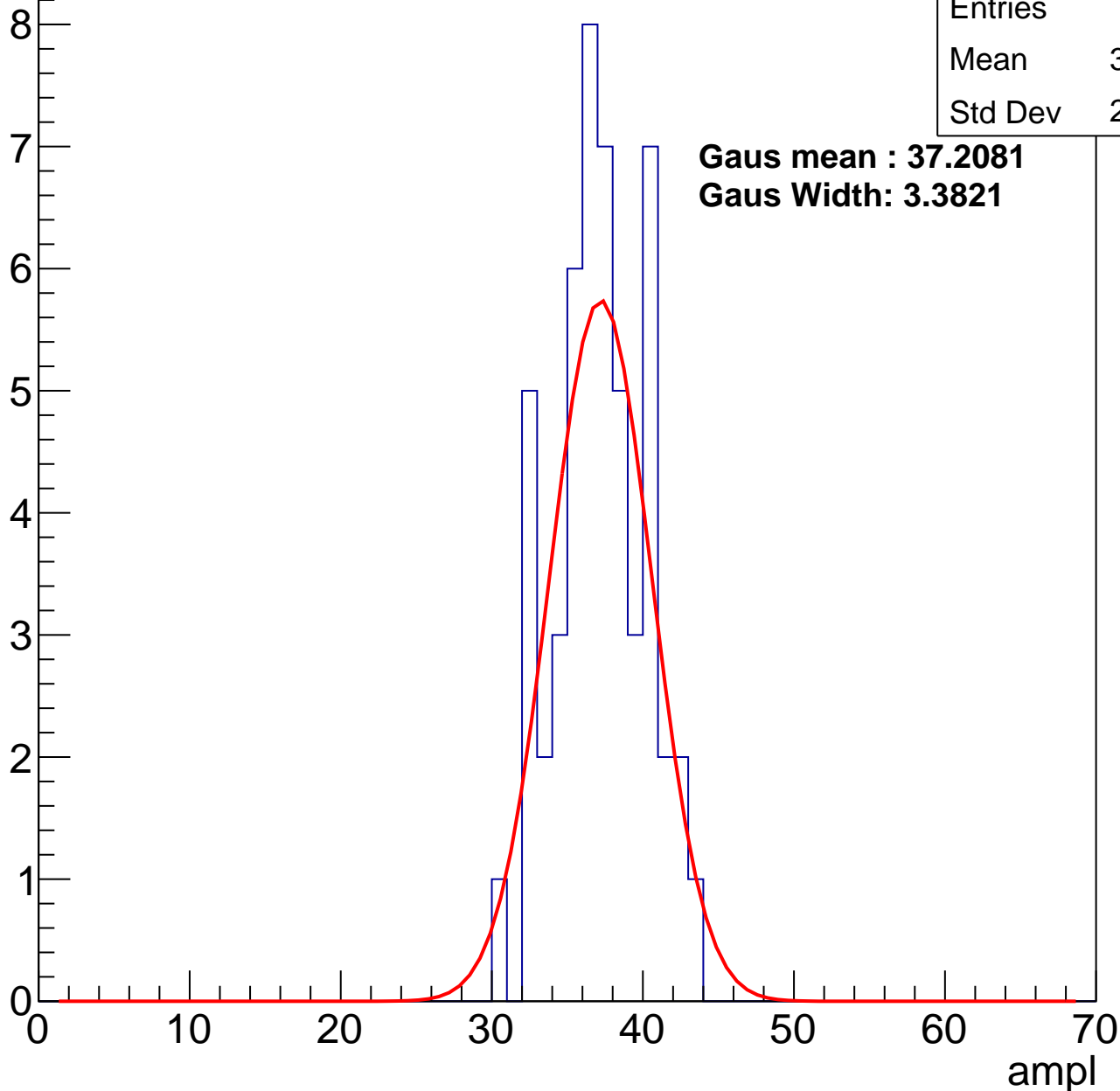
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	36.75
Std Dev	2.973

Gaus mean : 37.2081

Gaus Width: 3.3821



B1L101S, U23-ch69, adc2

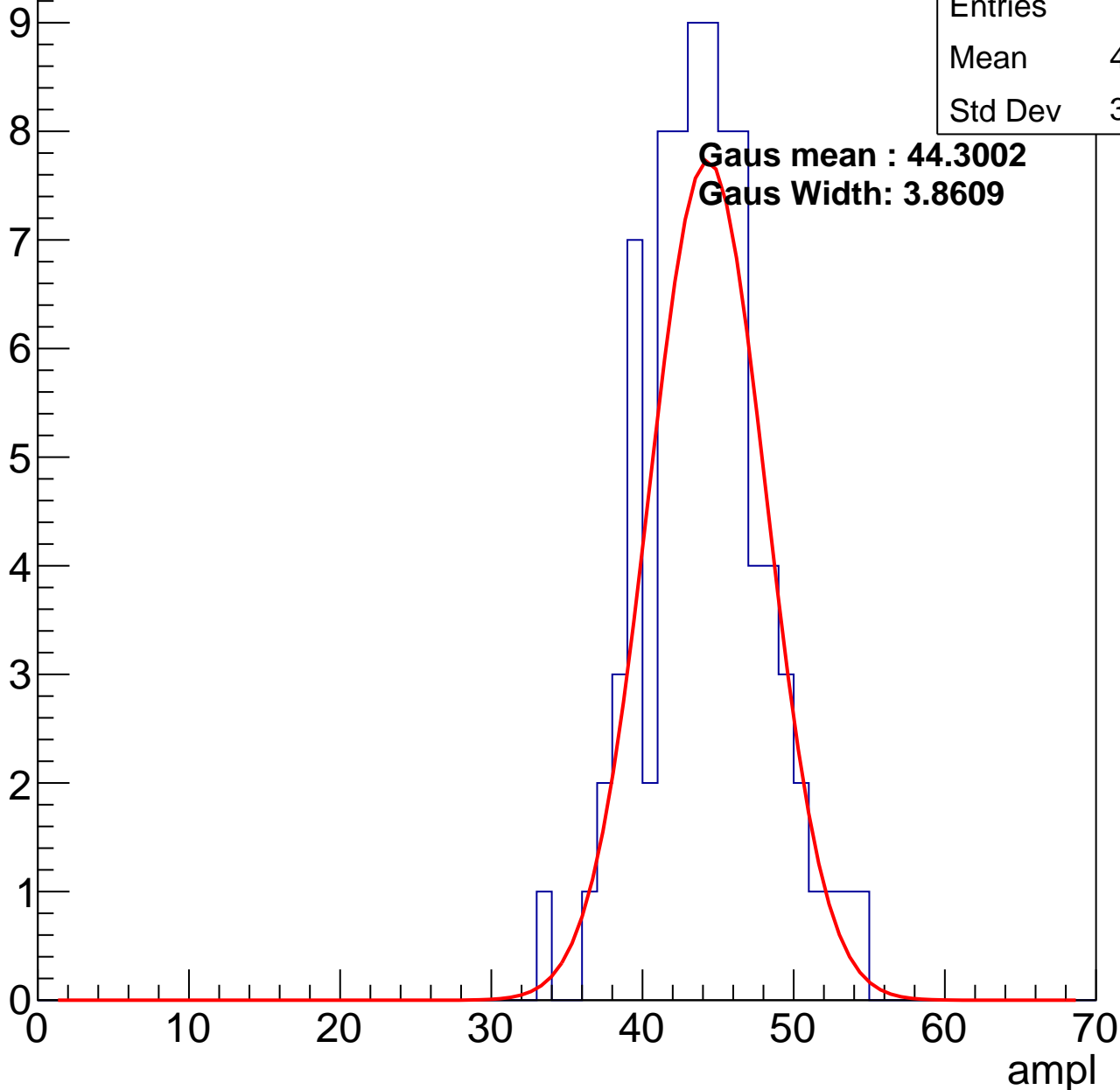
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	43.64
Std Dev	3.929

Gaus mean : 44.3002

Gaus Width: 3.8609

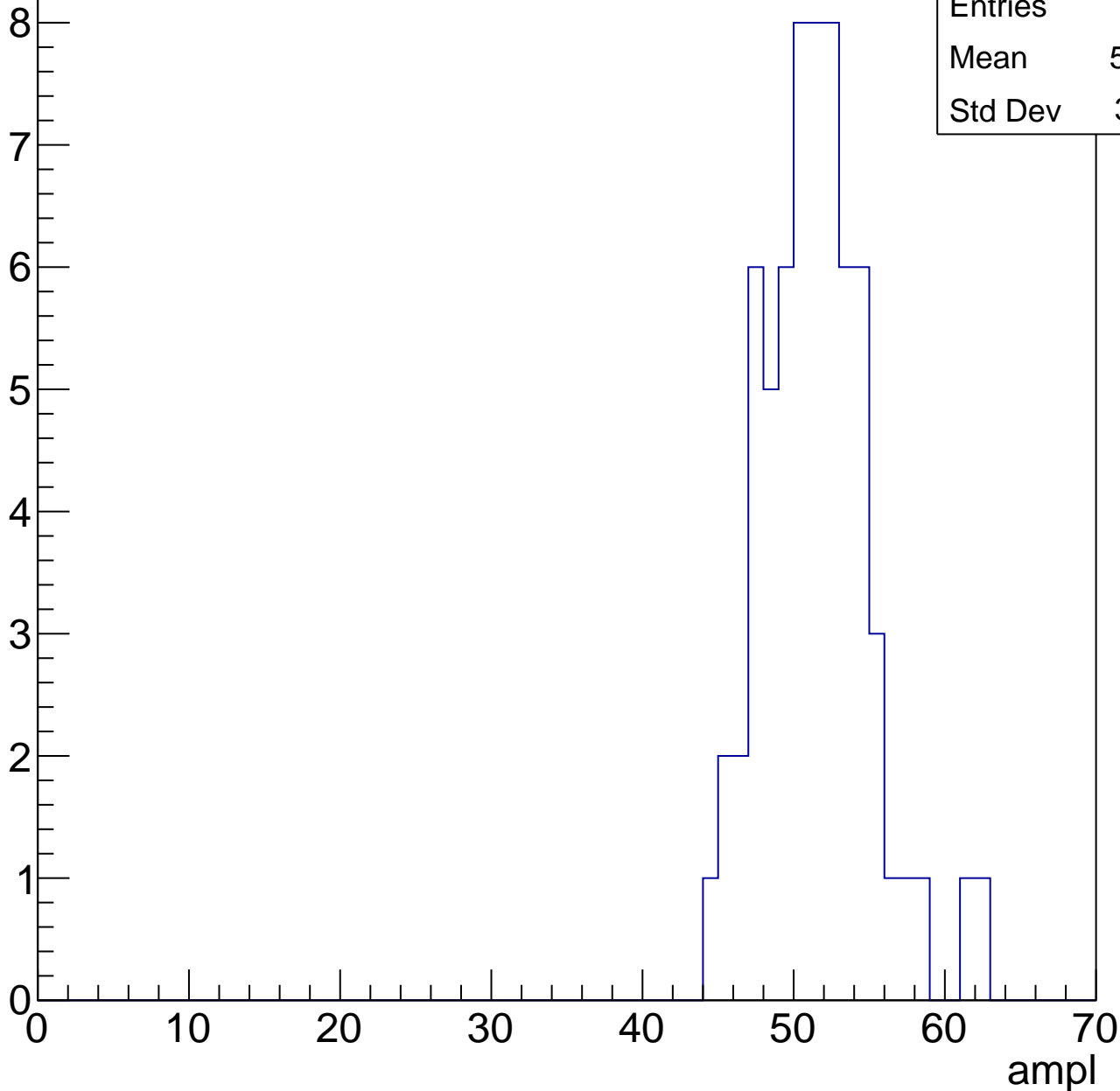


B1L101S, U23-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	51.02
Std Dev	3.501

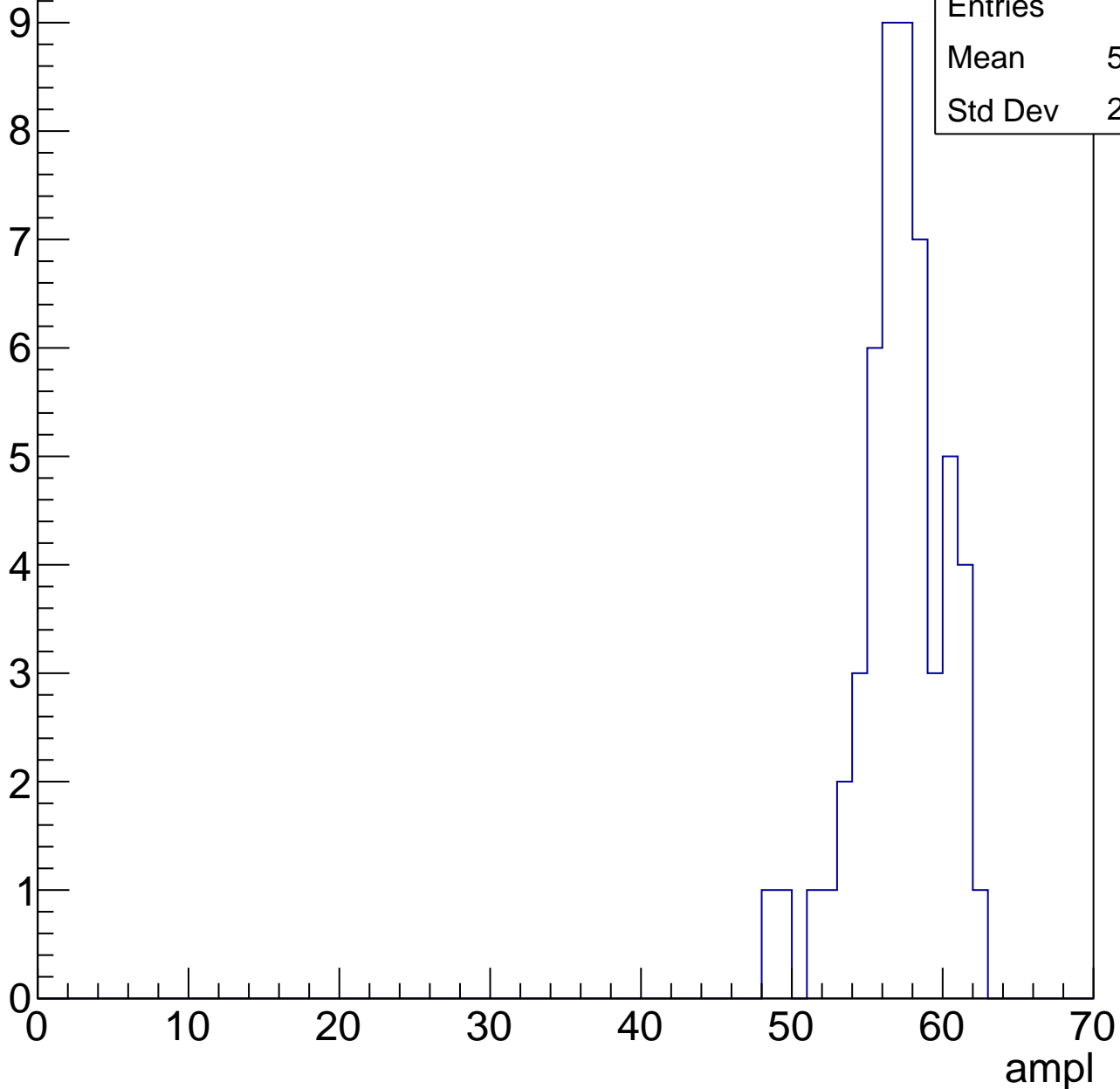


B1L101S, U23-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.68
Std Dev	2.899

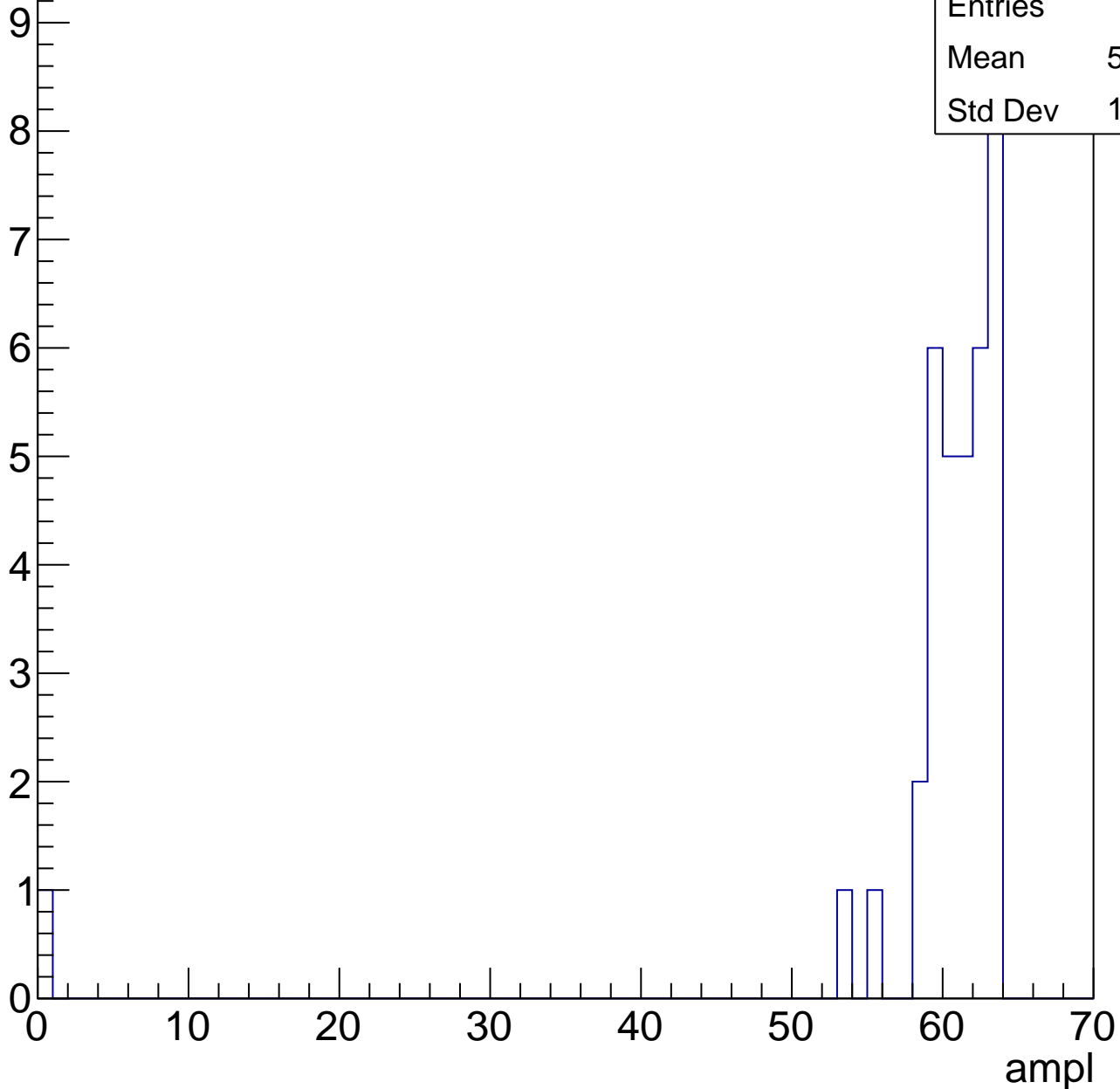


B1L101S, U23-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.94
Std Dev	10.22



B1L101S, U23-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch70, adc0

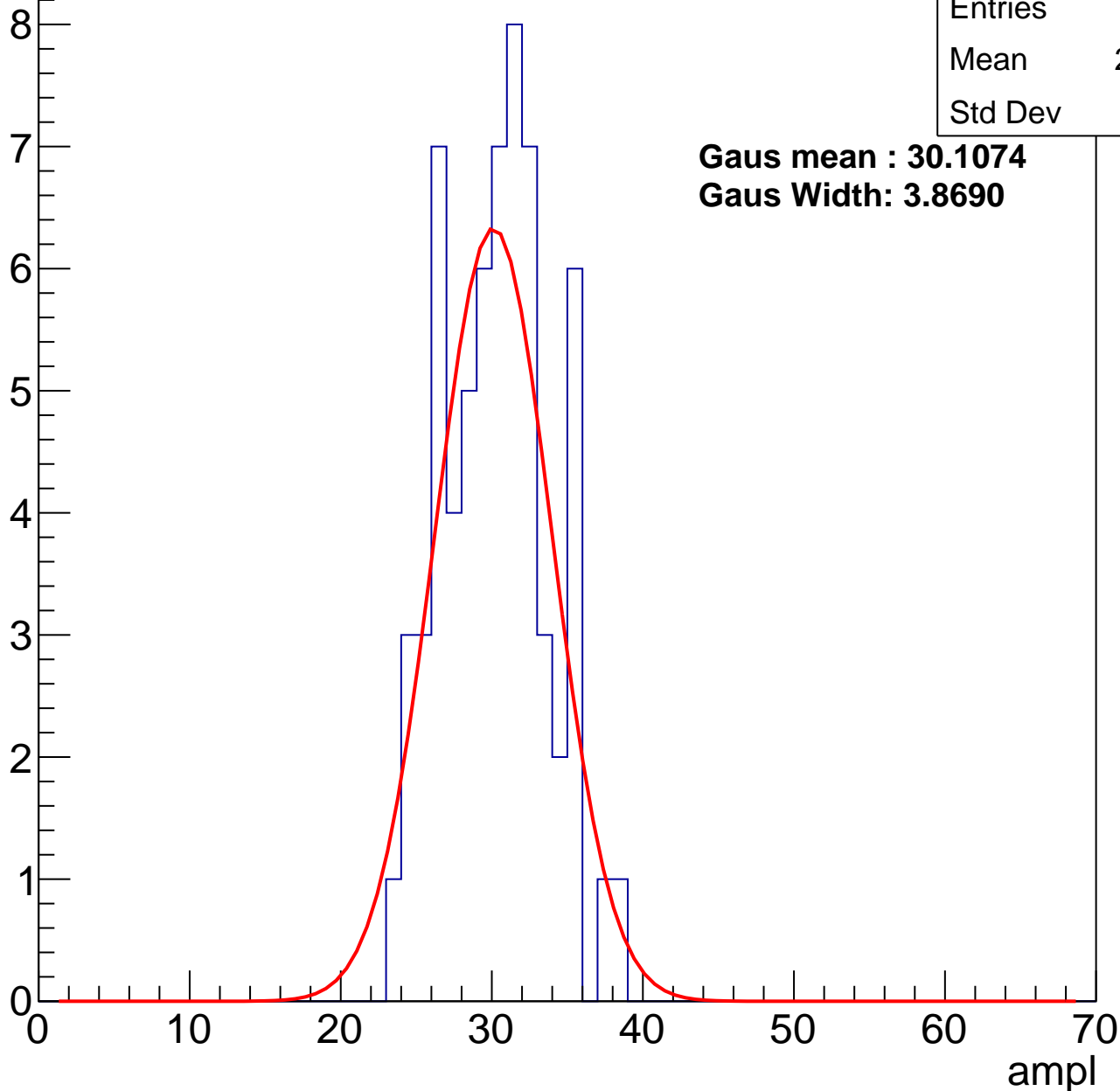
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	29.81
Std Dev	3.45

Gaus mean : 30.1074

Gaus Width: 3.8690



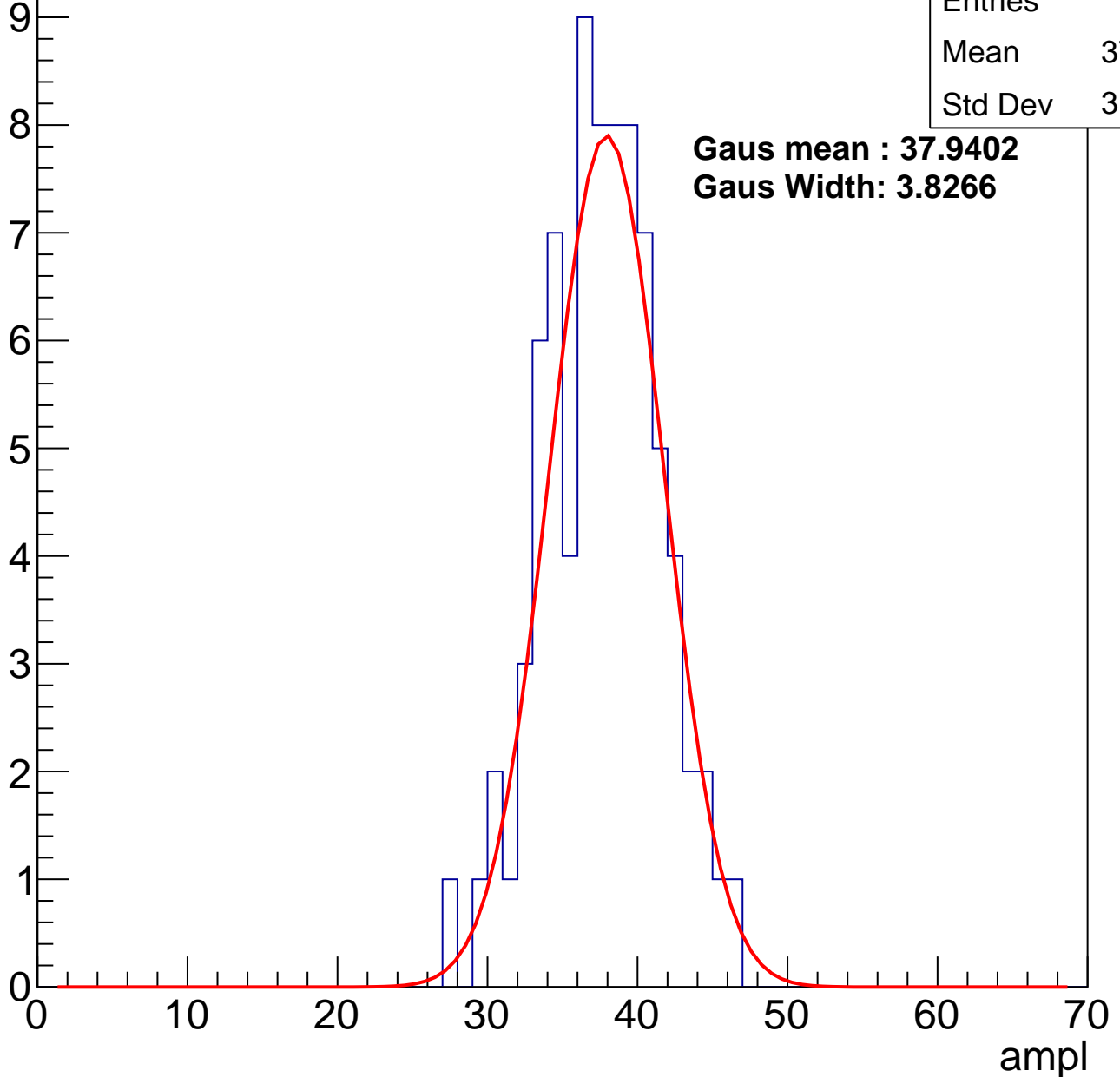
B1L101S, U23-ch70, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	37.16
Std Dev	3.796

Gaus mean : 37.9402
Gaus Width: 3.8266



B1L101S, U23-ch70, adc2

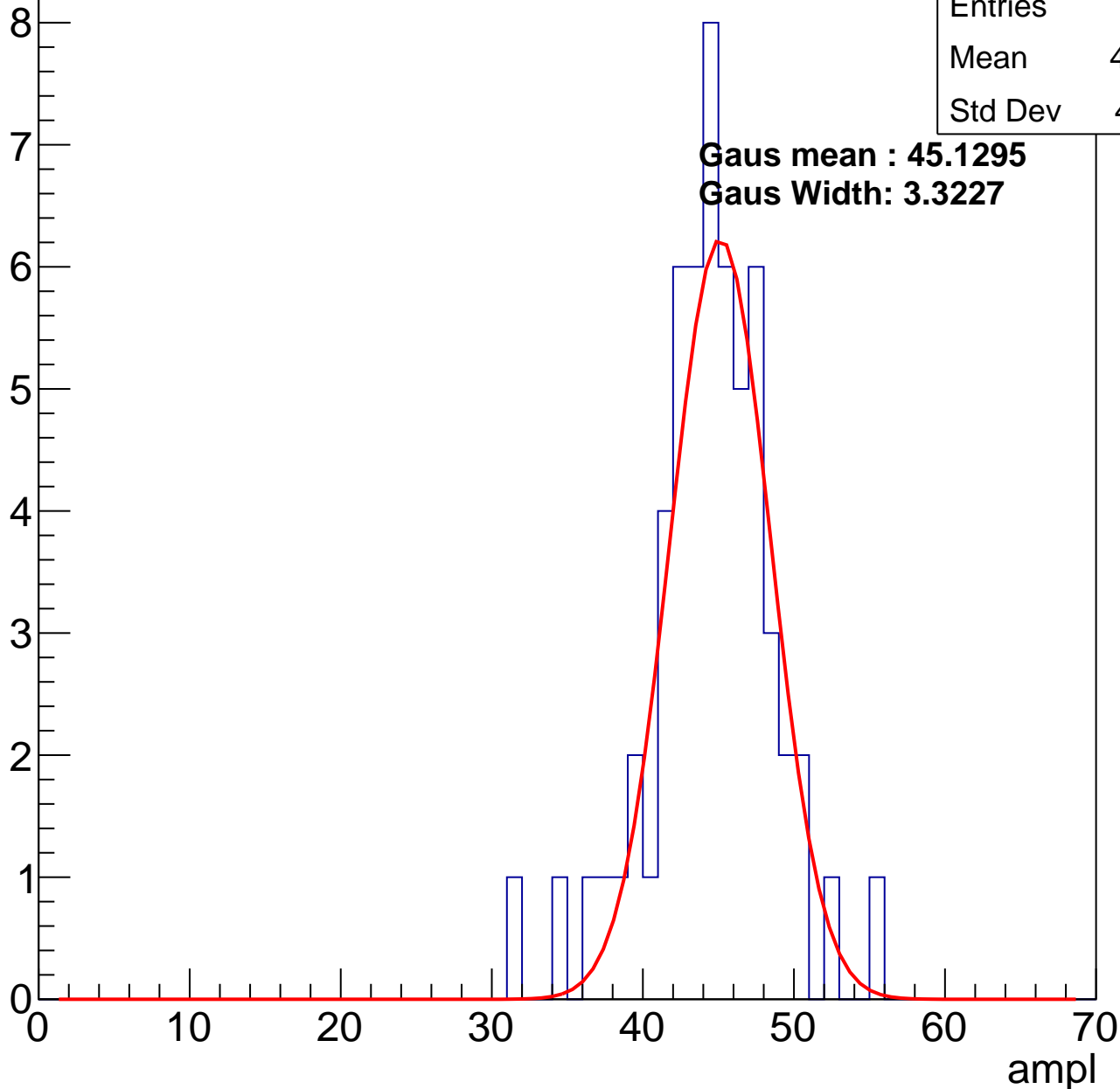
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.98
Std Dev	4.121

Gaus mean : 45.1295

Gaus Width: 3.3227

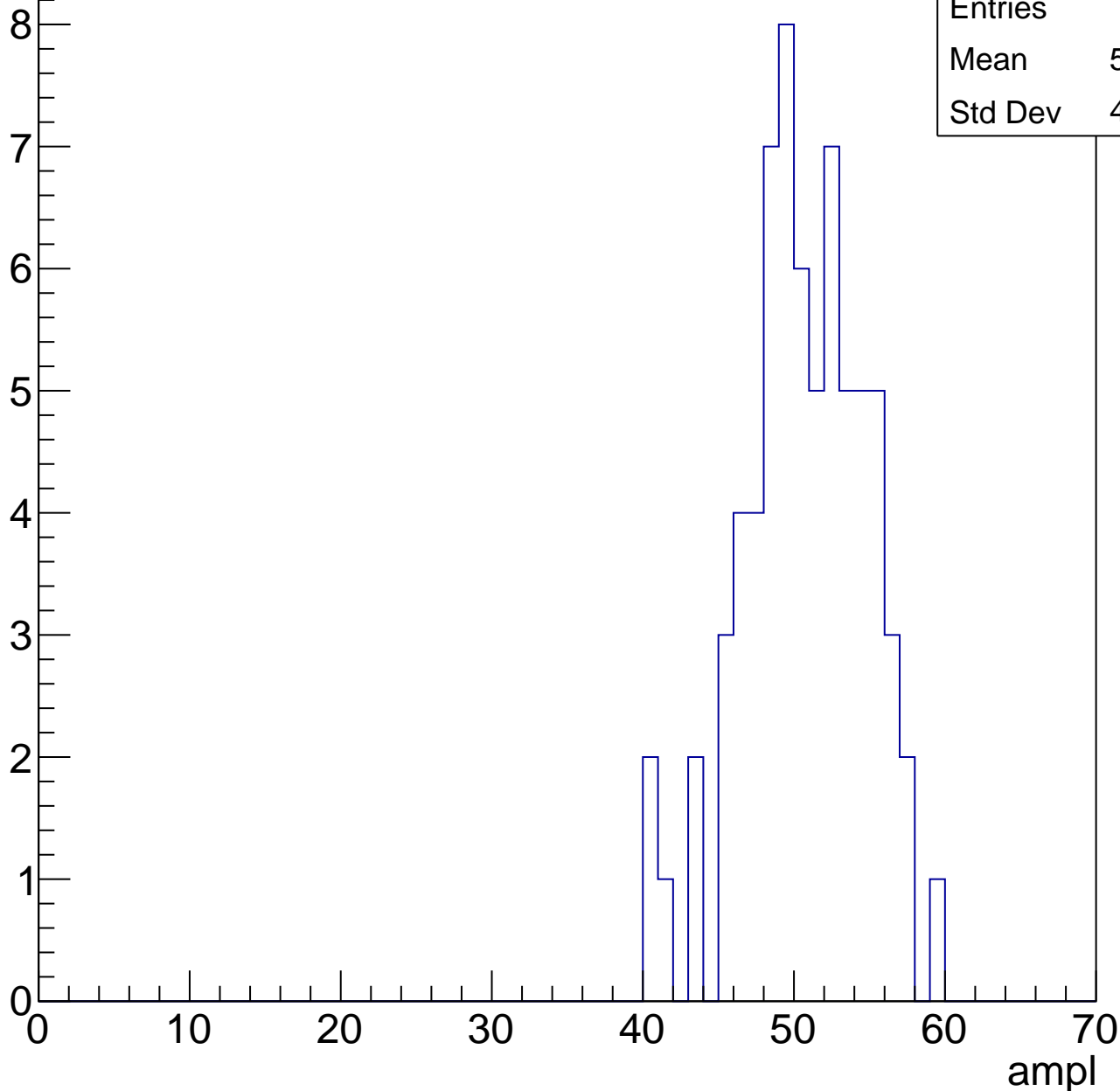


B1L101S, U23-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.17
Std Dev	4.067

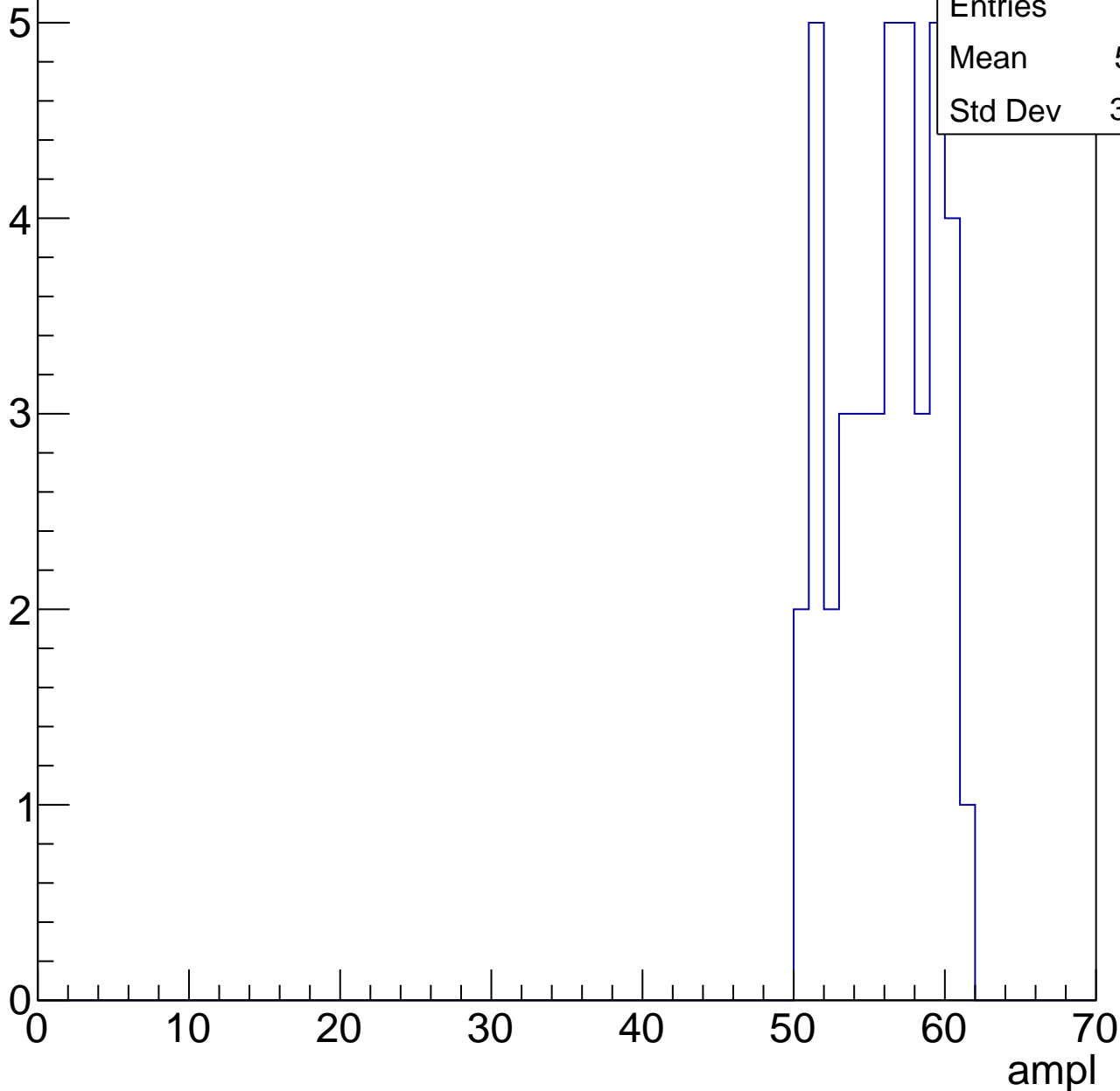


B1L101S, U23-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	55.61
Std Dev	3.184

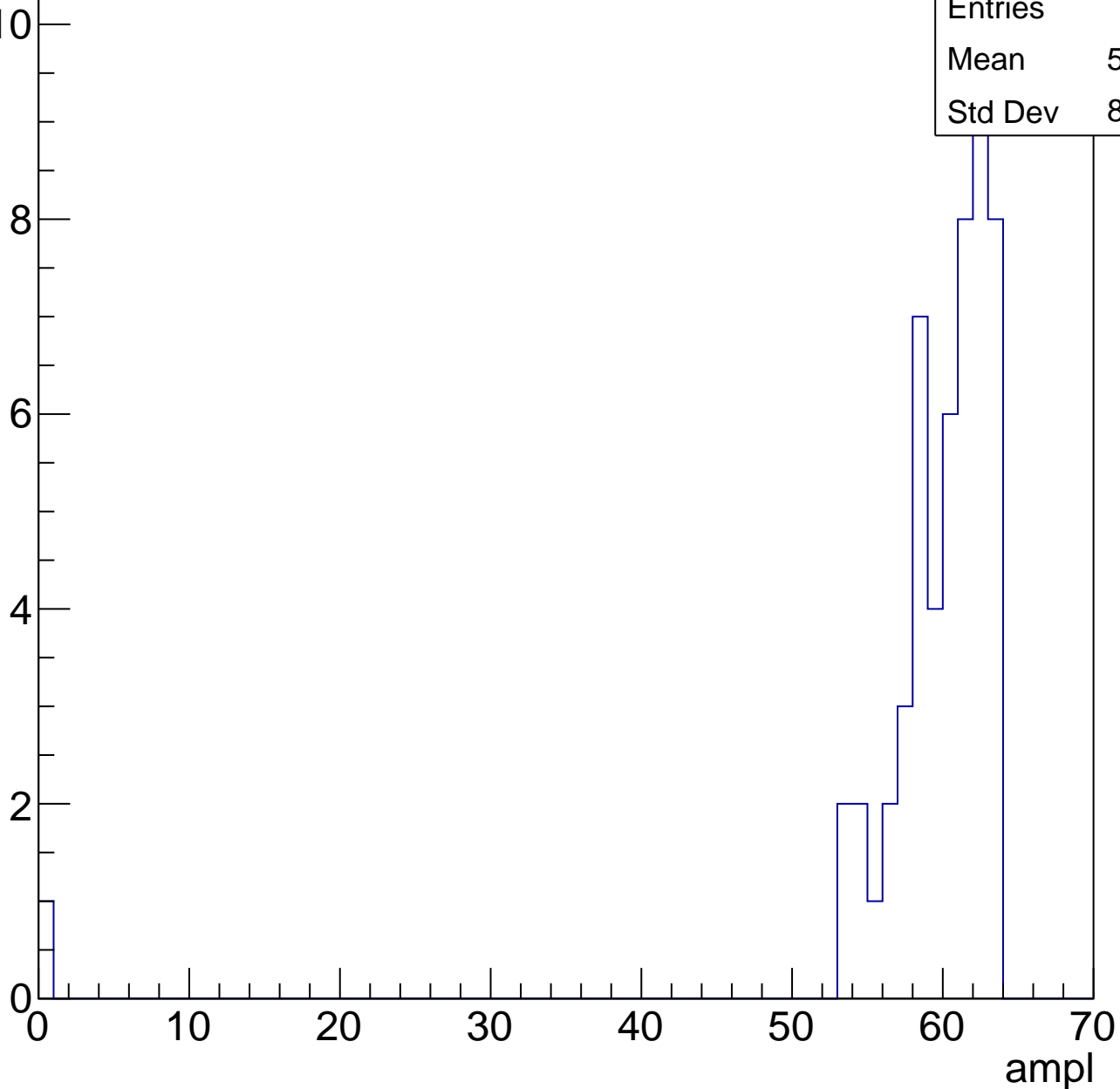


B1L101S, U23-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	58.63
Std Dev	8.503



B1L101S, U23-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch71, adc0

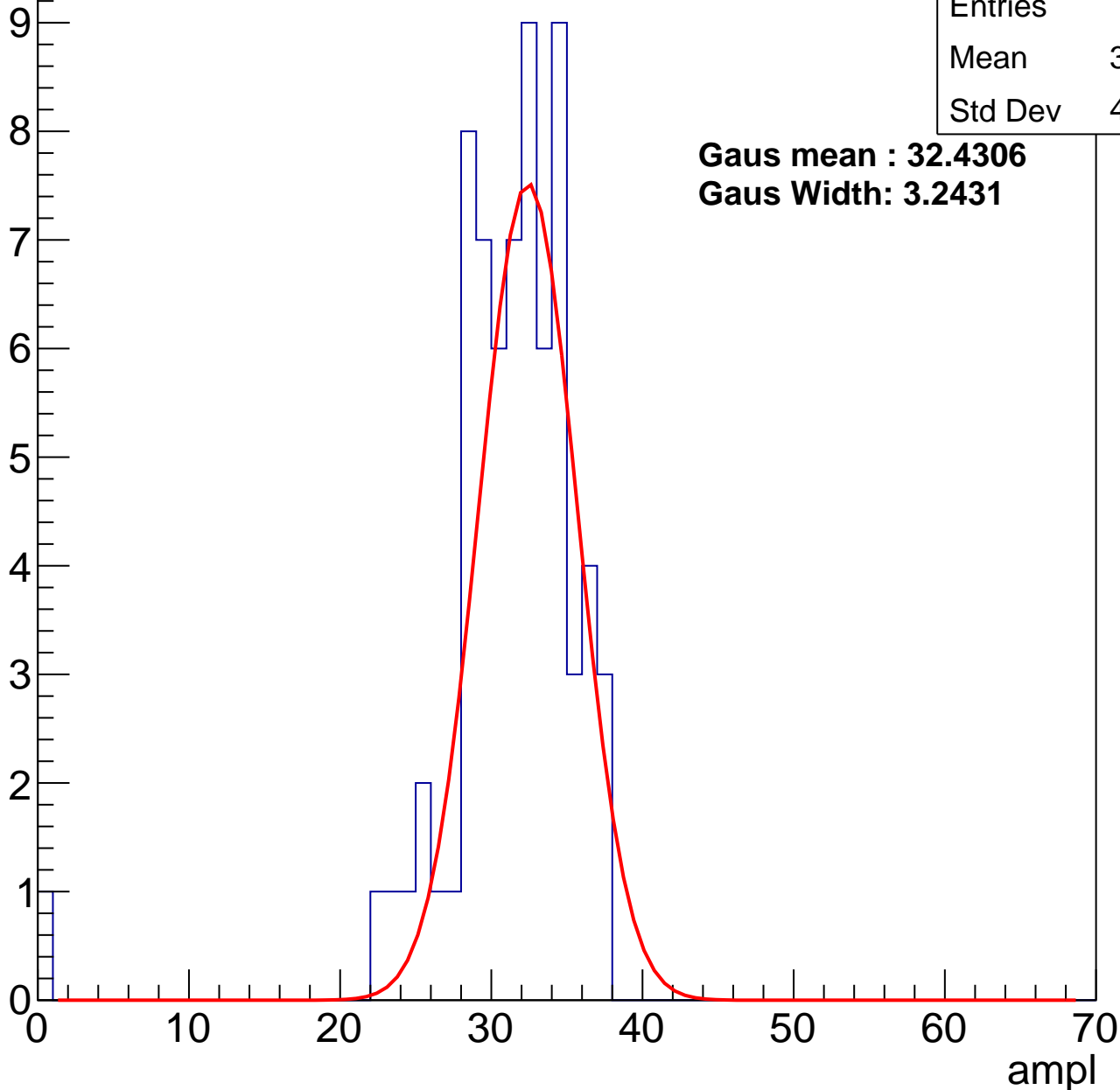
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.69
Std Dev	4.982

Gaus mean : 32.4306

Gaus Width: 3.2431



B1L101S, U23-ch71, adc1

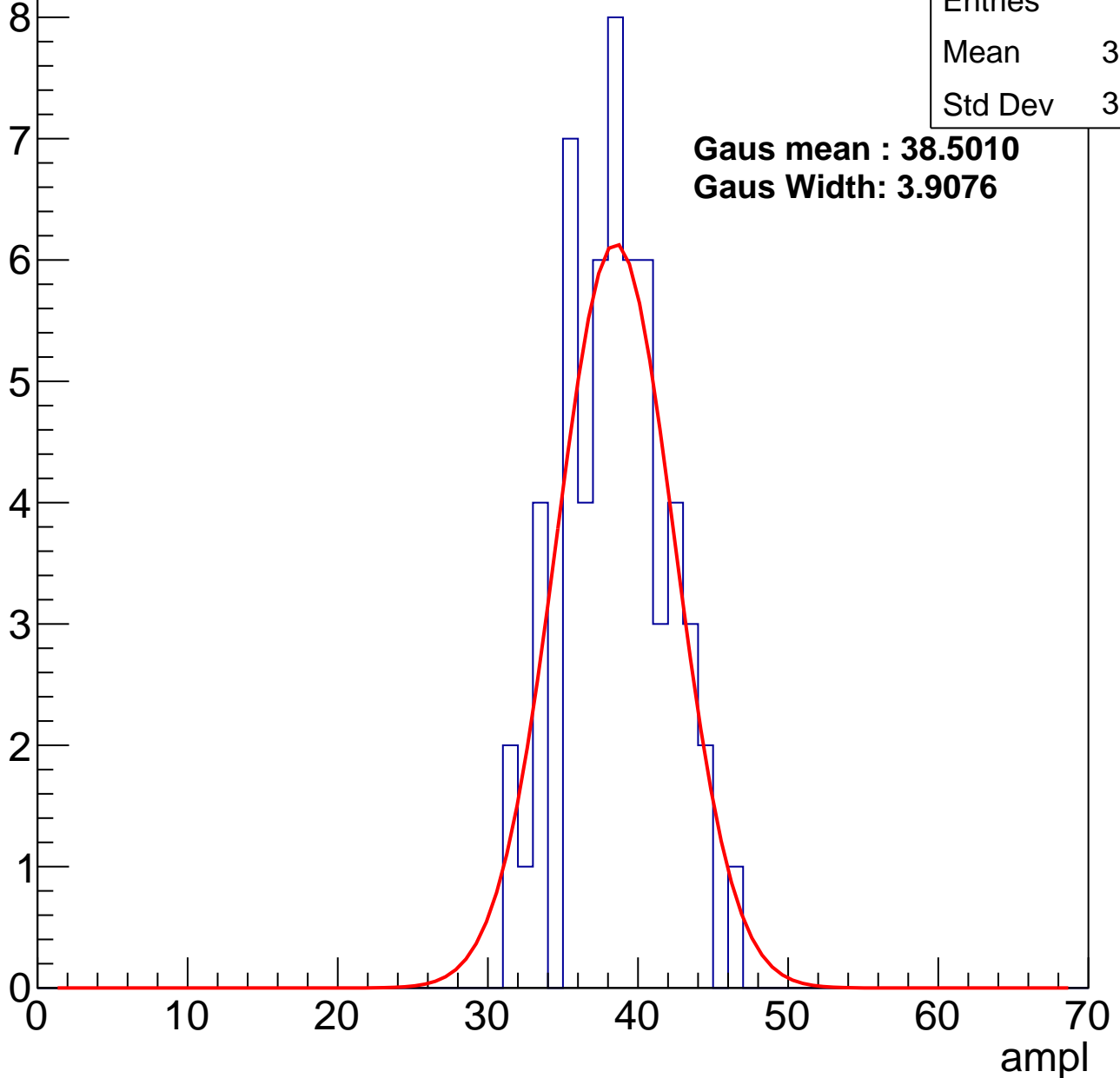
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	38.05
Std Dev	3.379

Gaus mean : 38.5010

Gaus Width: 3.9076



B1L101S, U23-ch71, adc2

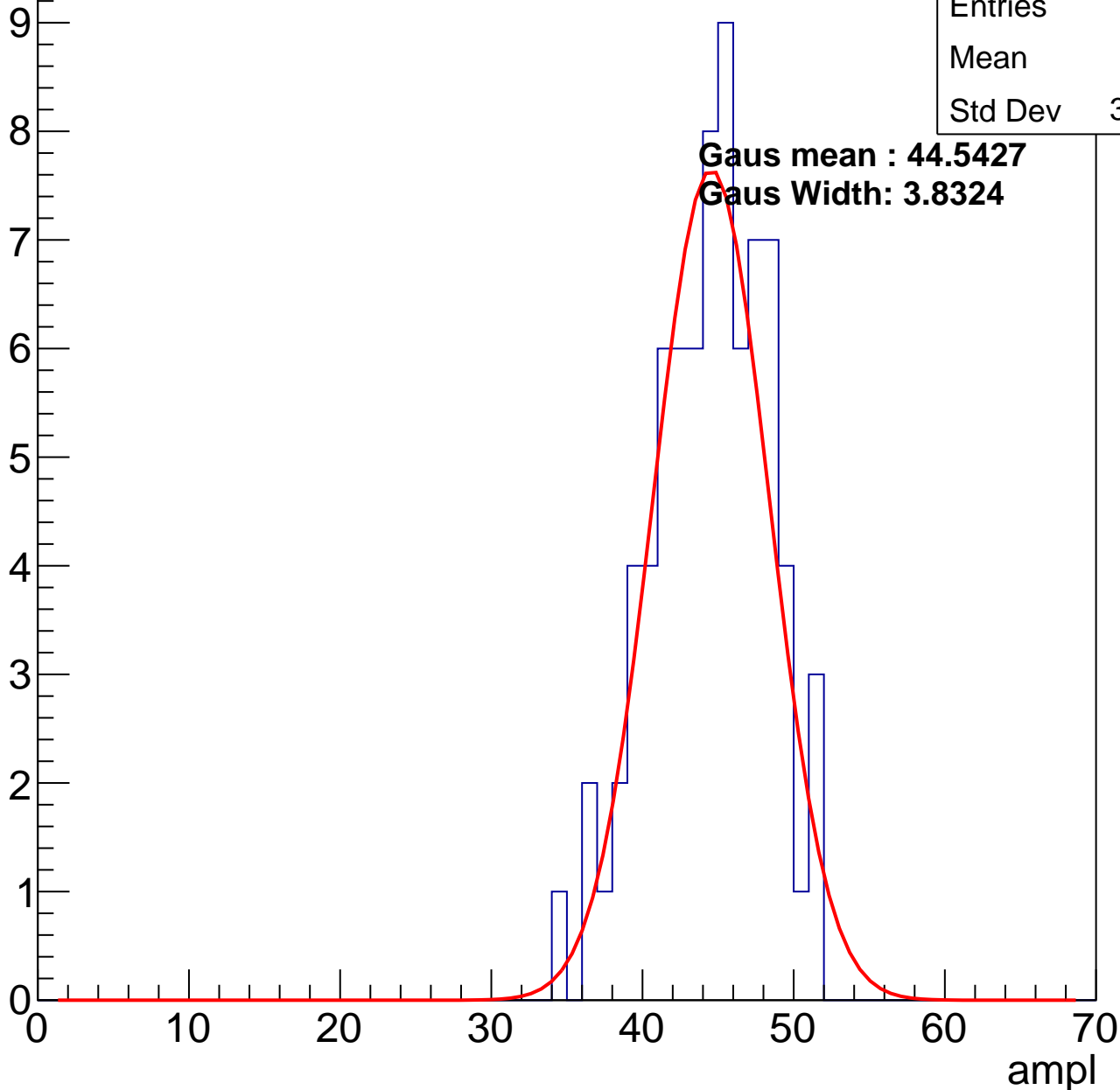
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44
Std Dev	3.759

Gaus mean : 44.5427

Gaus Width: 3.8324

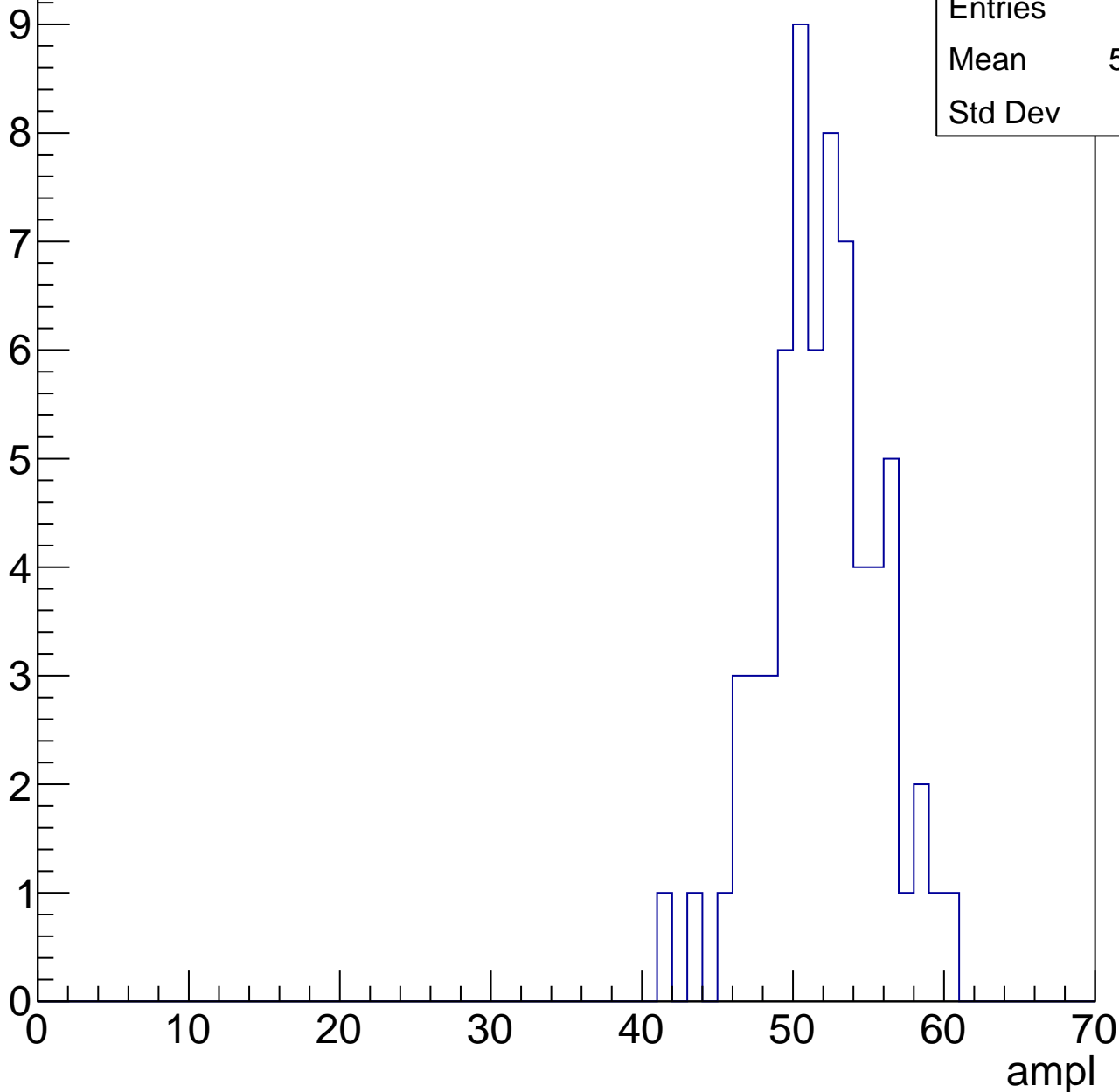


B1L101S, U23-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	51.47
Std Dev	3.71

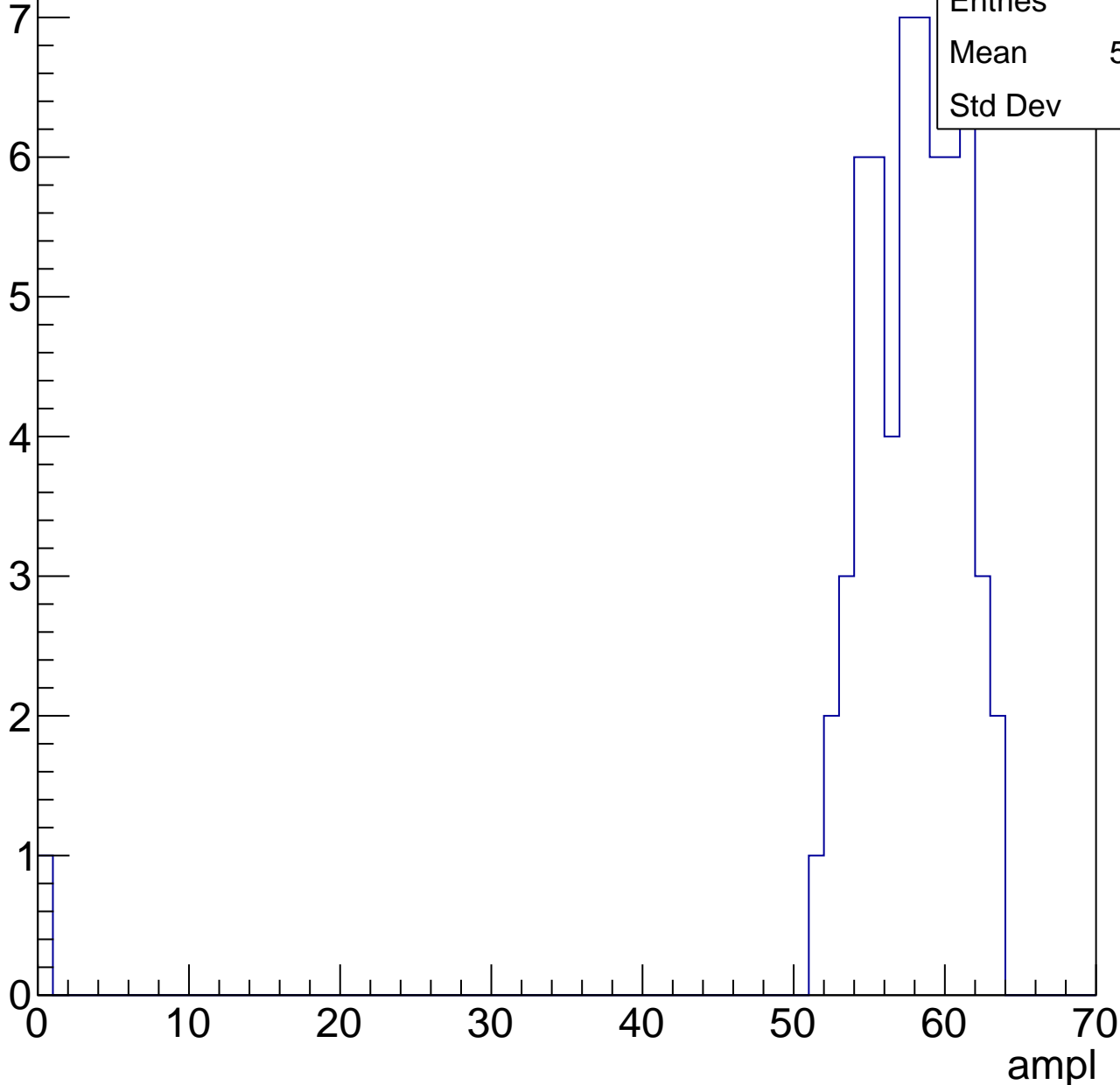


B1L101S, U23-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

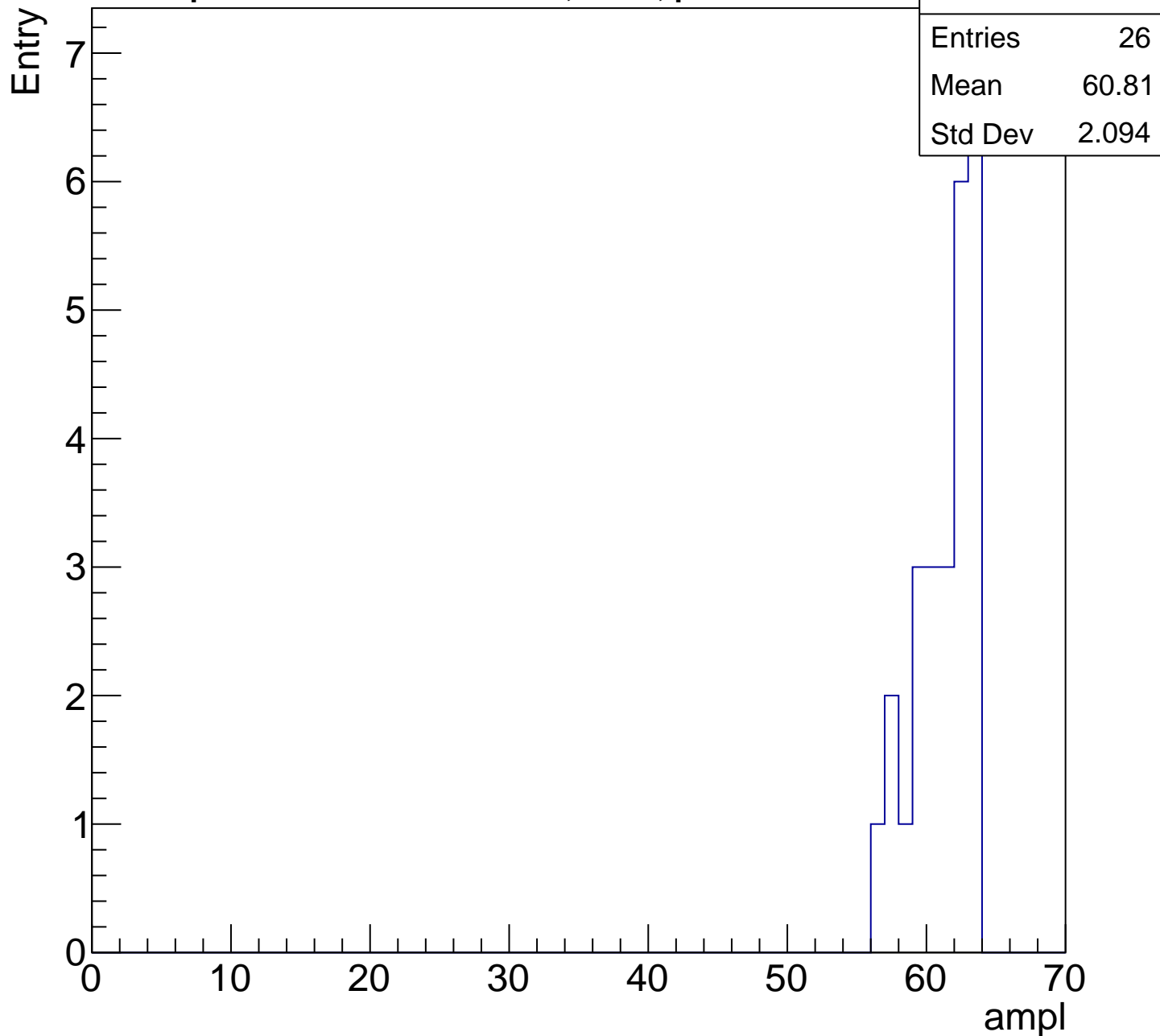
Entry

Entries	61
Mean	56.56
Std Dev	7.89



B1L101S, U23-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch72, adc0

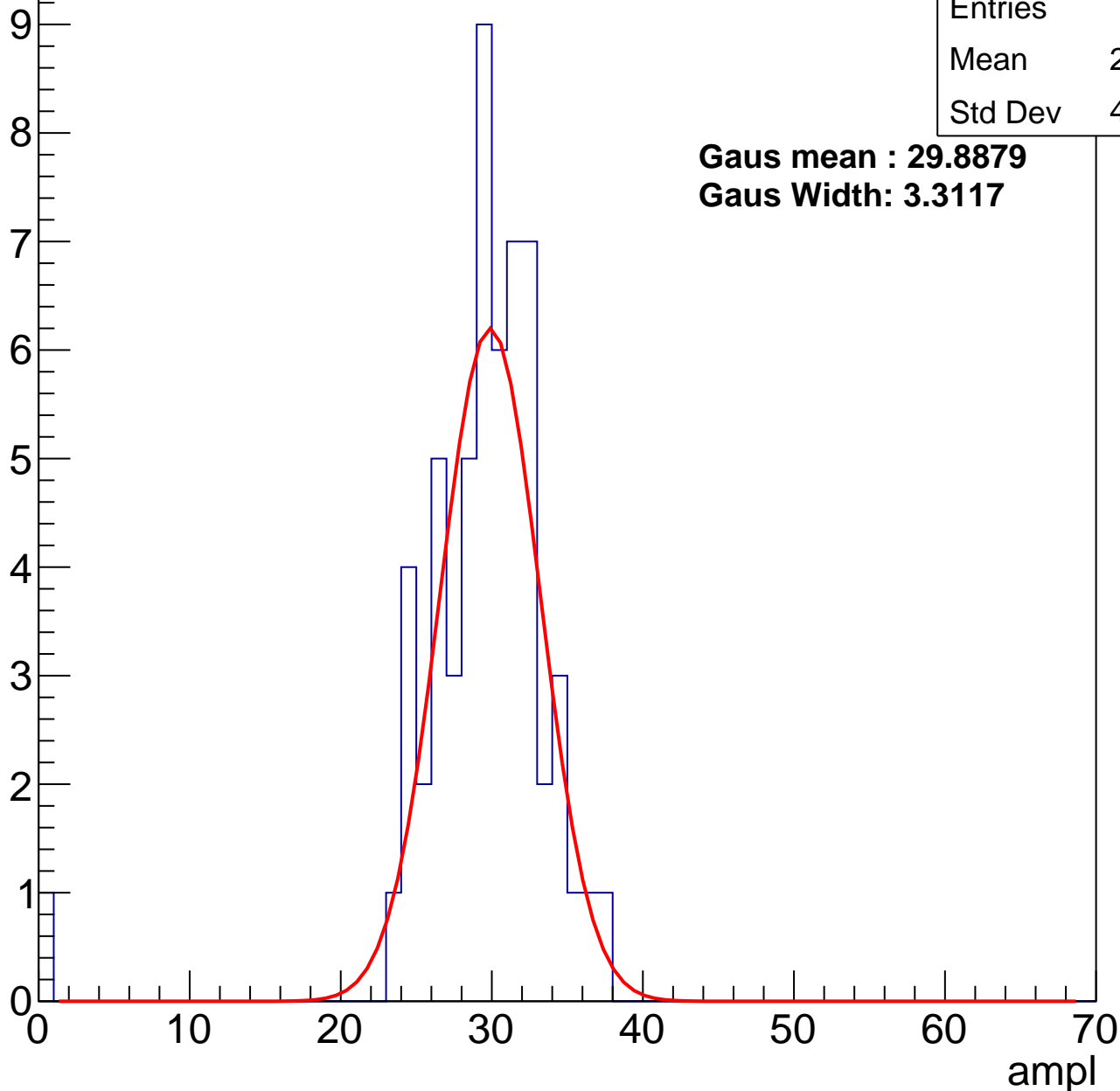
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	28.93
Std Dev	4.954

Gaus mean : 29.8879

Gaus Width: 3.3117



B1L101S, U23-ch72, adc1

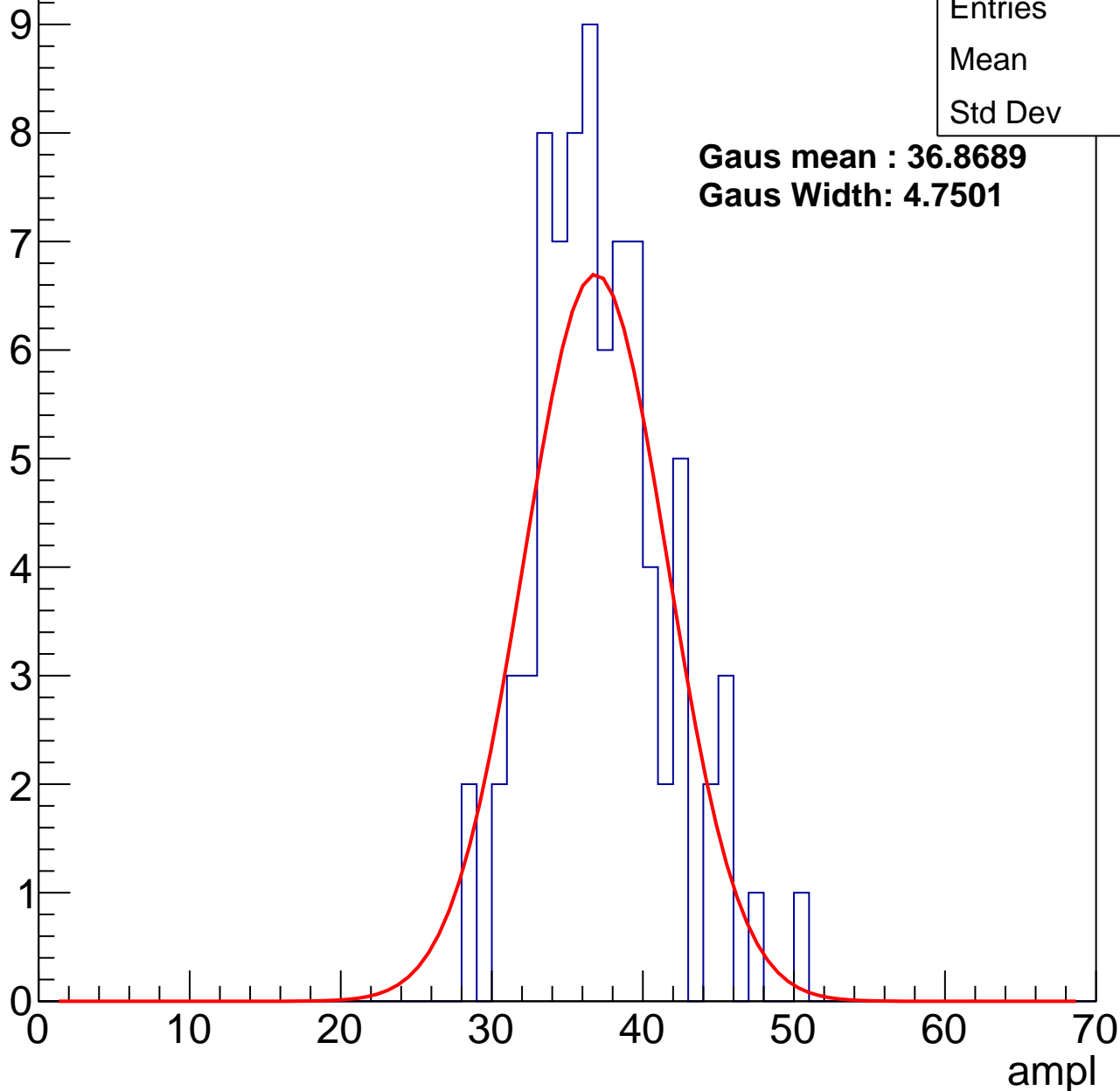
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.8
Std Dev	4.27

Gaus mean : 36.8689

Gaus Width: 4.7501



B1L101S, U23-ch72, adc2

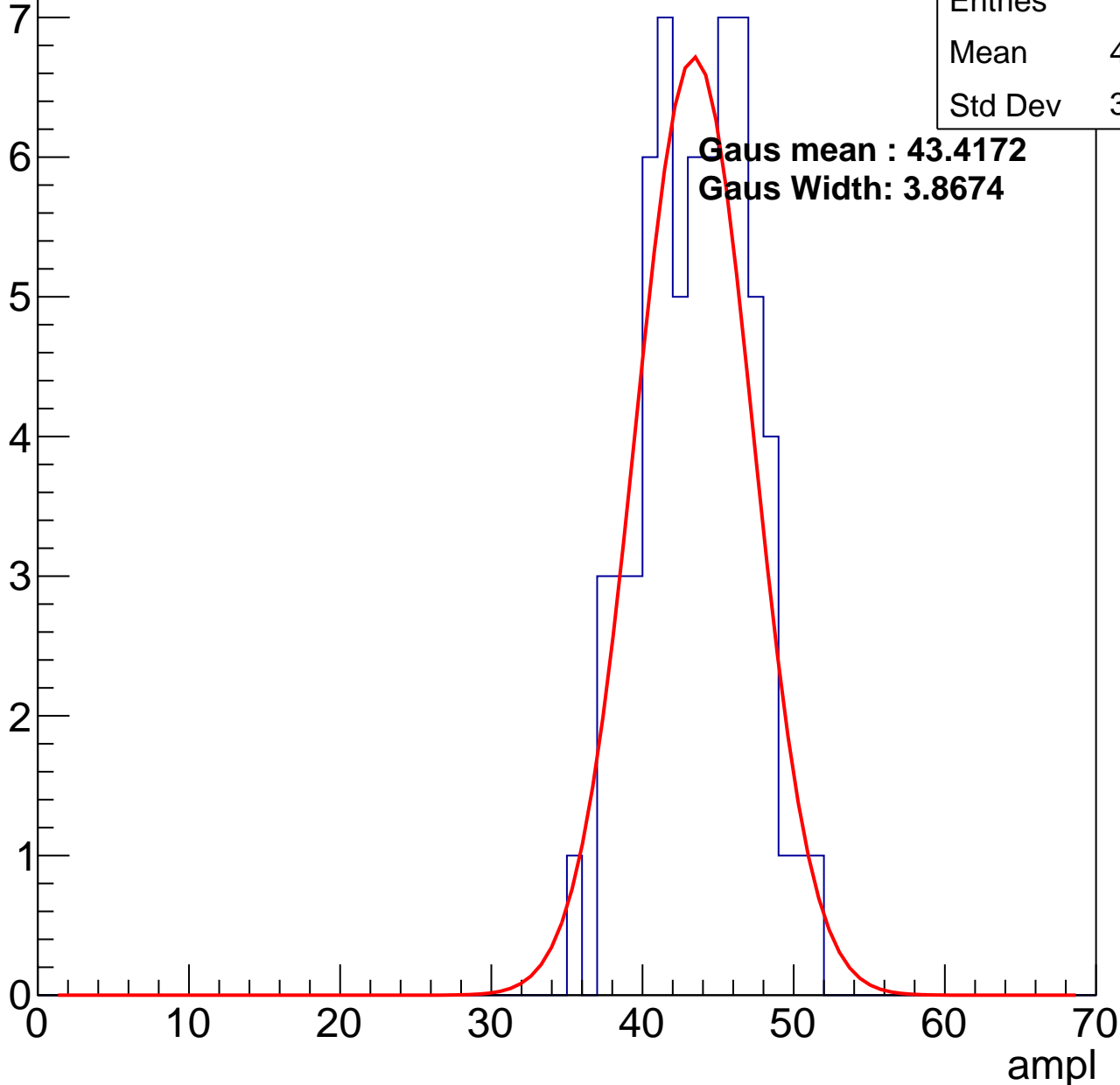
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.18
Std Dev	3.494

Gaus mean : 43.4172

Gaus Width: 3.8674

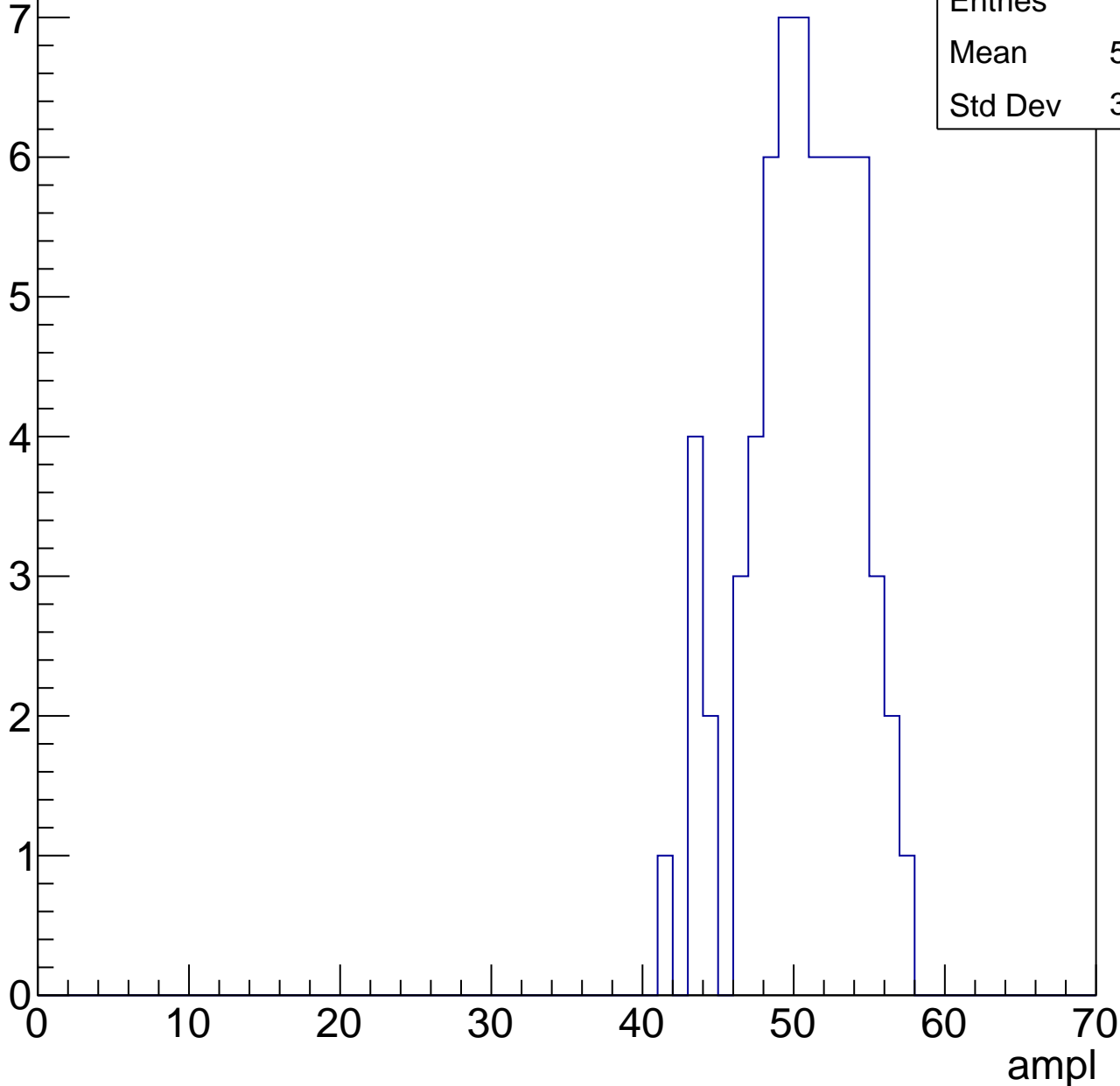


B1L101S, U23-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.03
Std Dev	3.623

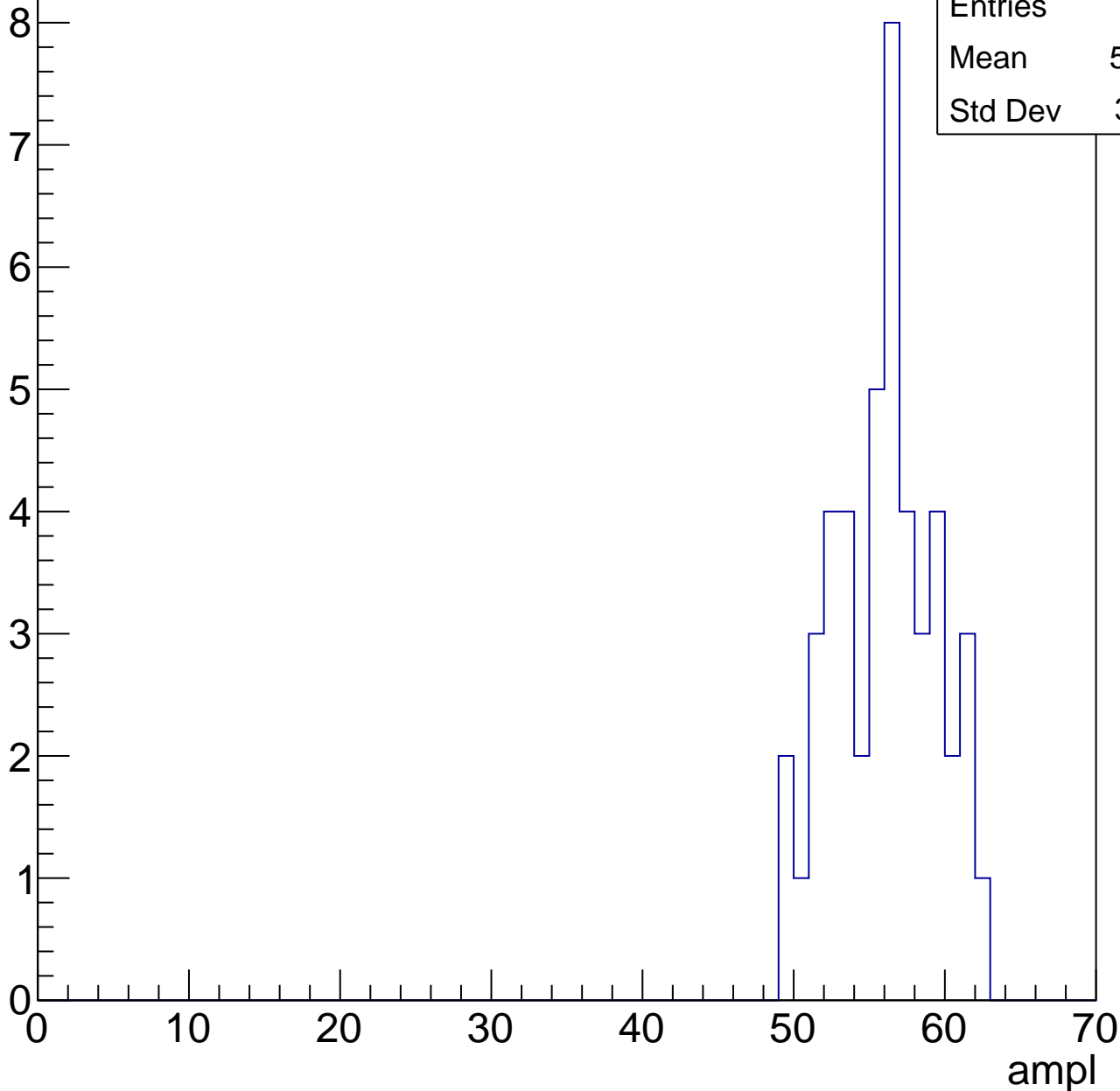


B1L101S, U23-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

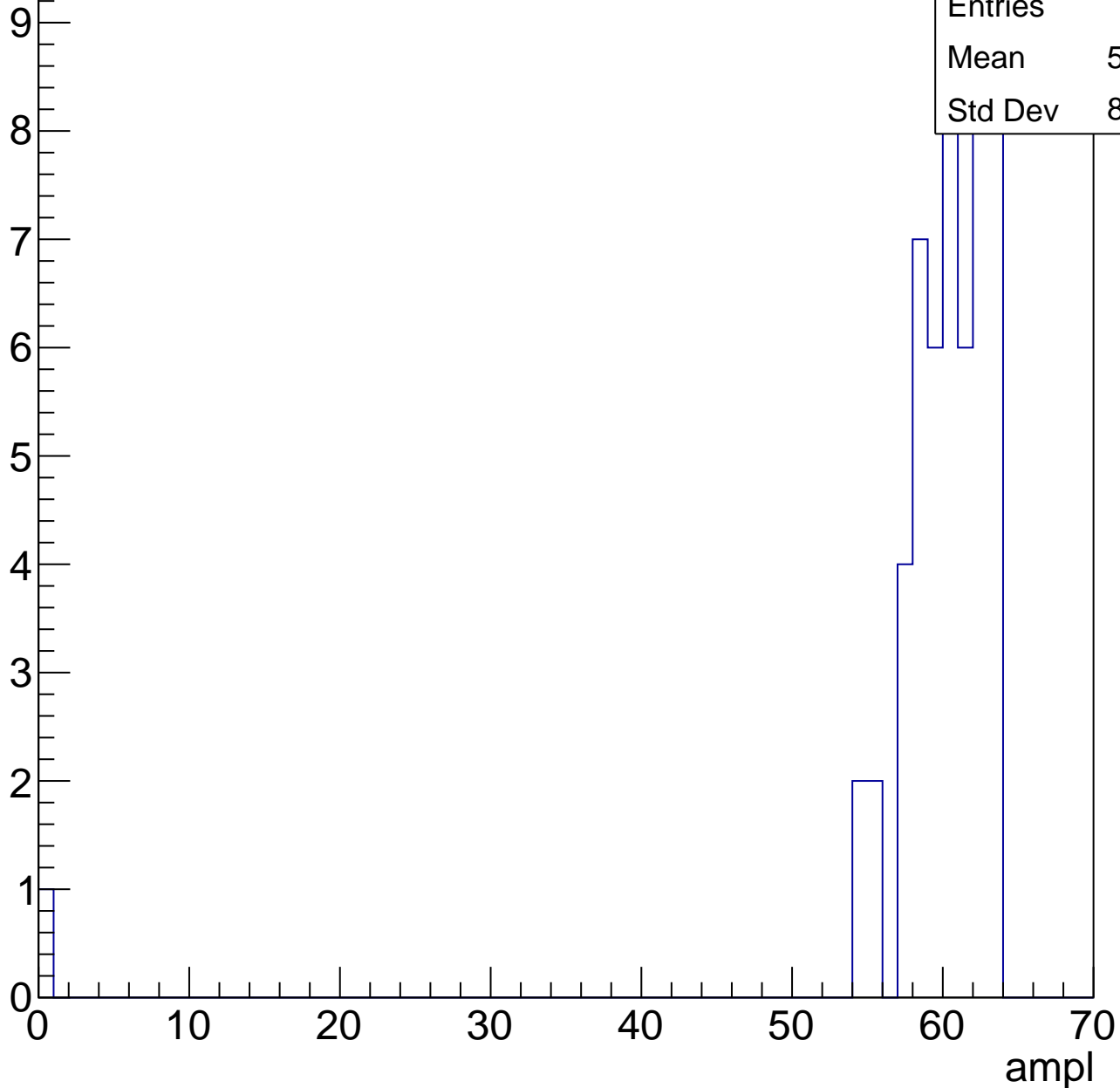
Entries	46
Mean	55.54
Std Dev	3.321



B1L101S, U23-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

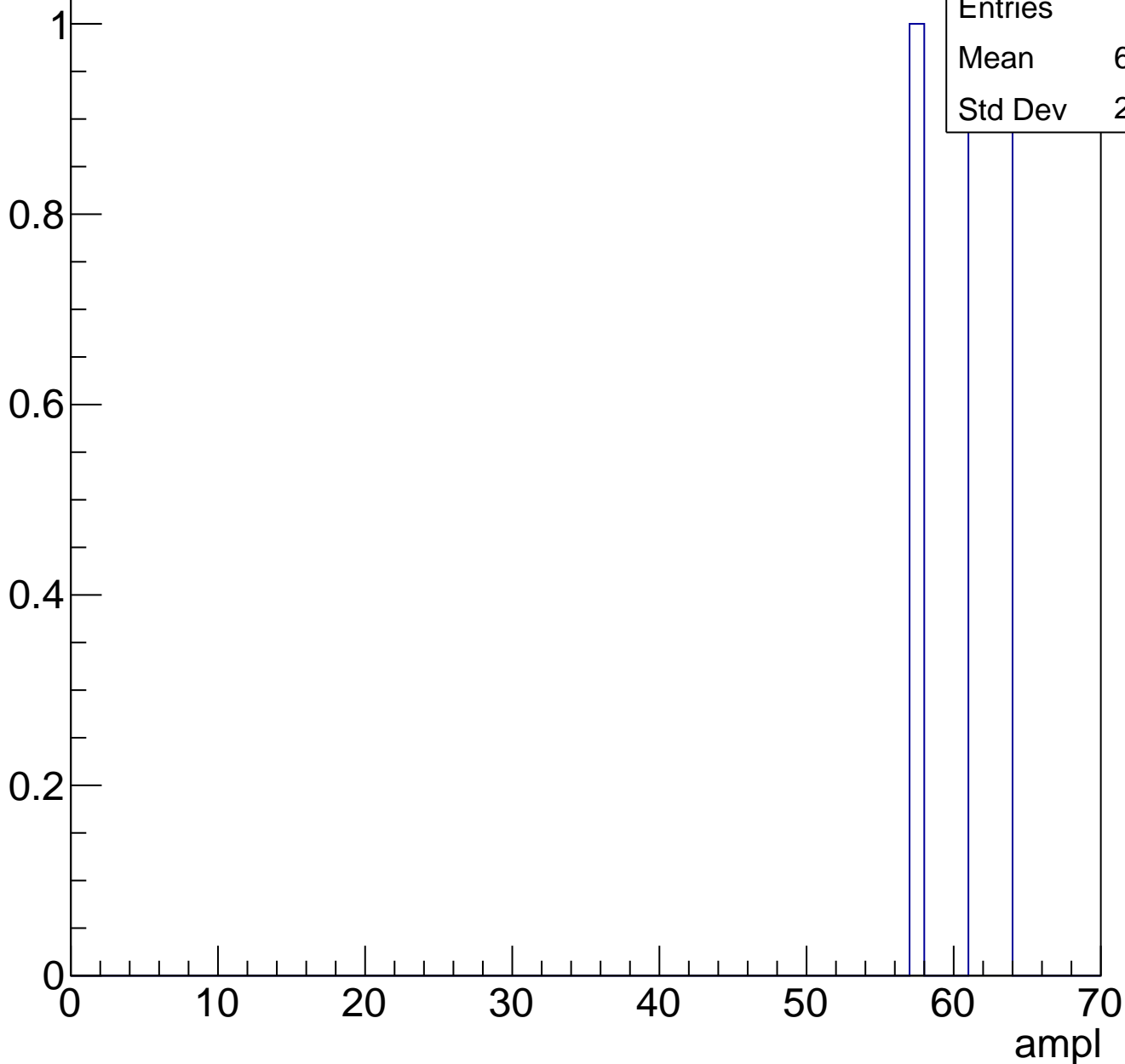
Entry



B1L101S, U23-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch73, adc0

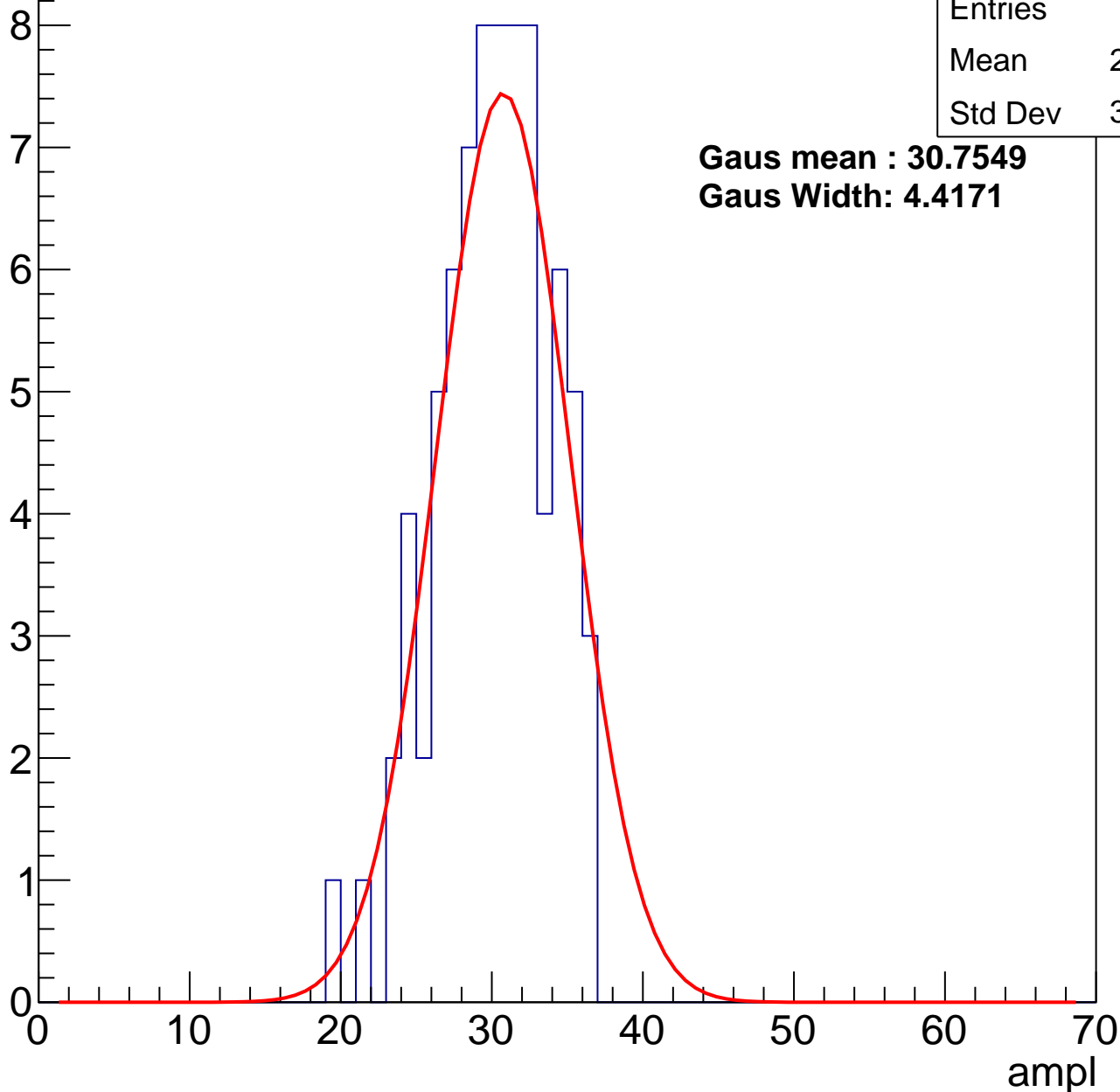
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.68
Std Dev	3.688

Gaus mean : 30.7549

Gaus Width: 4.4171



B1L101S, U23-ch73, adc1

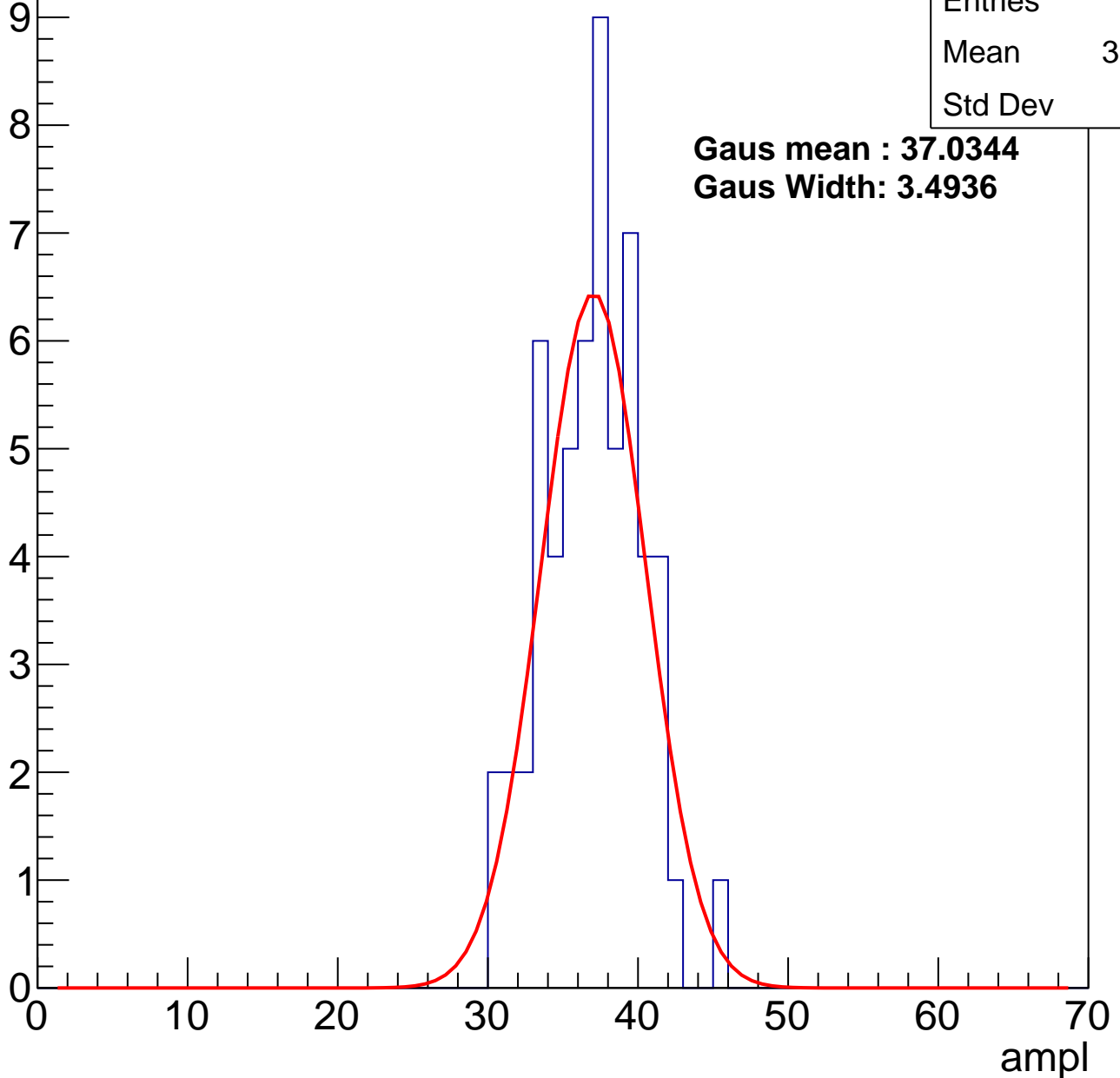
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.52
Std Dev	3.18

Gaus mean : 37.0344

Gaus Width: 3.4936



B1L101S, U23-ch73, adc2

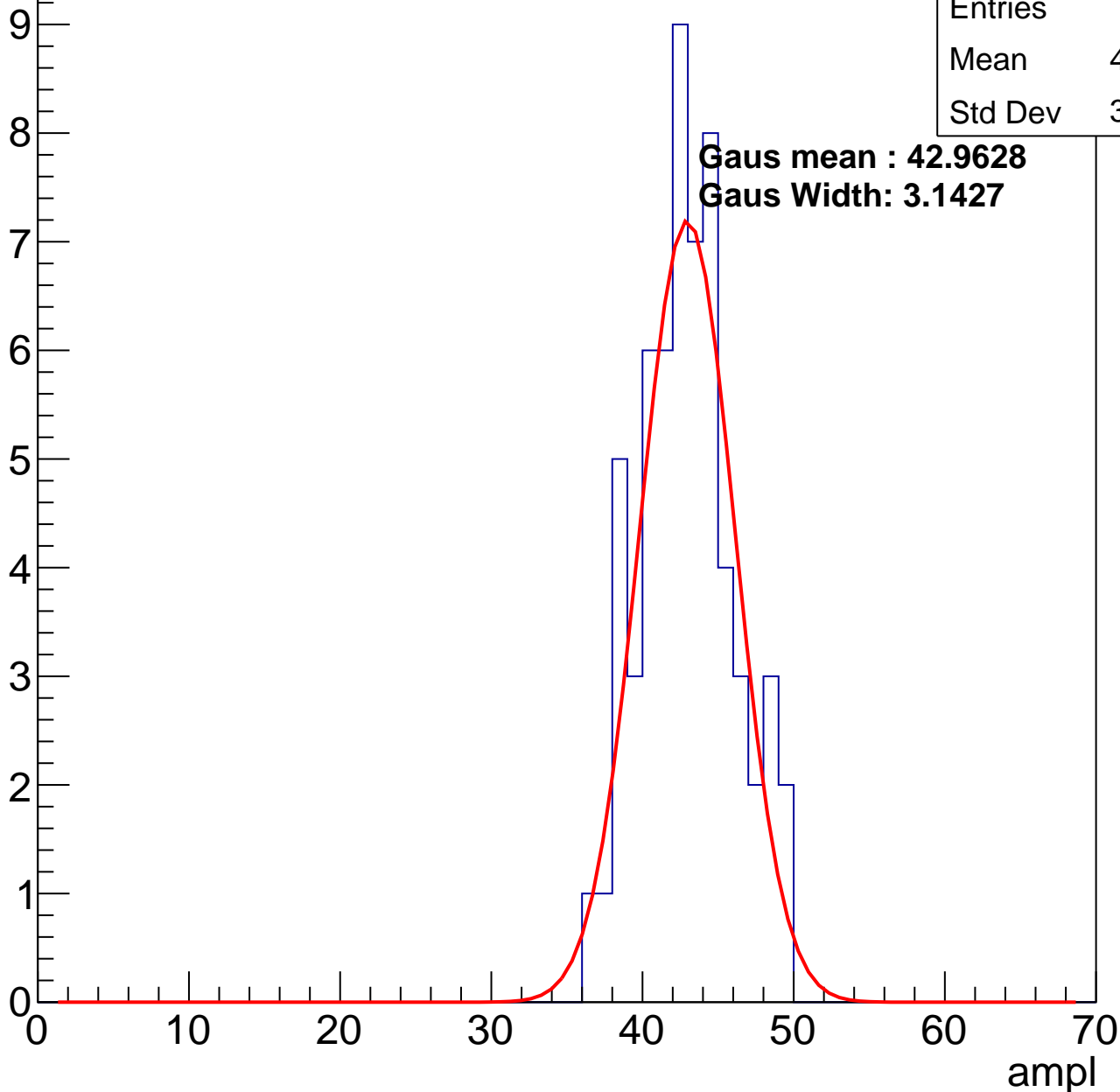
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.52
Std Dev	3.058

Gaus mean : 42.9628

Gaus Width: 3.1427



B1L101S, U23-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	49.29
Std Dev	3.224

Entry

10

8

6

4

2

0

0

10

20

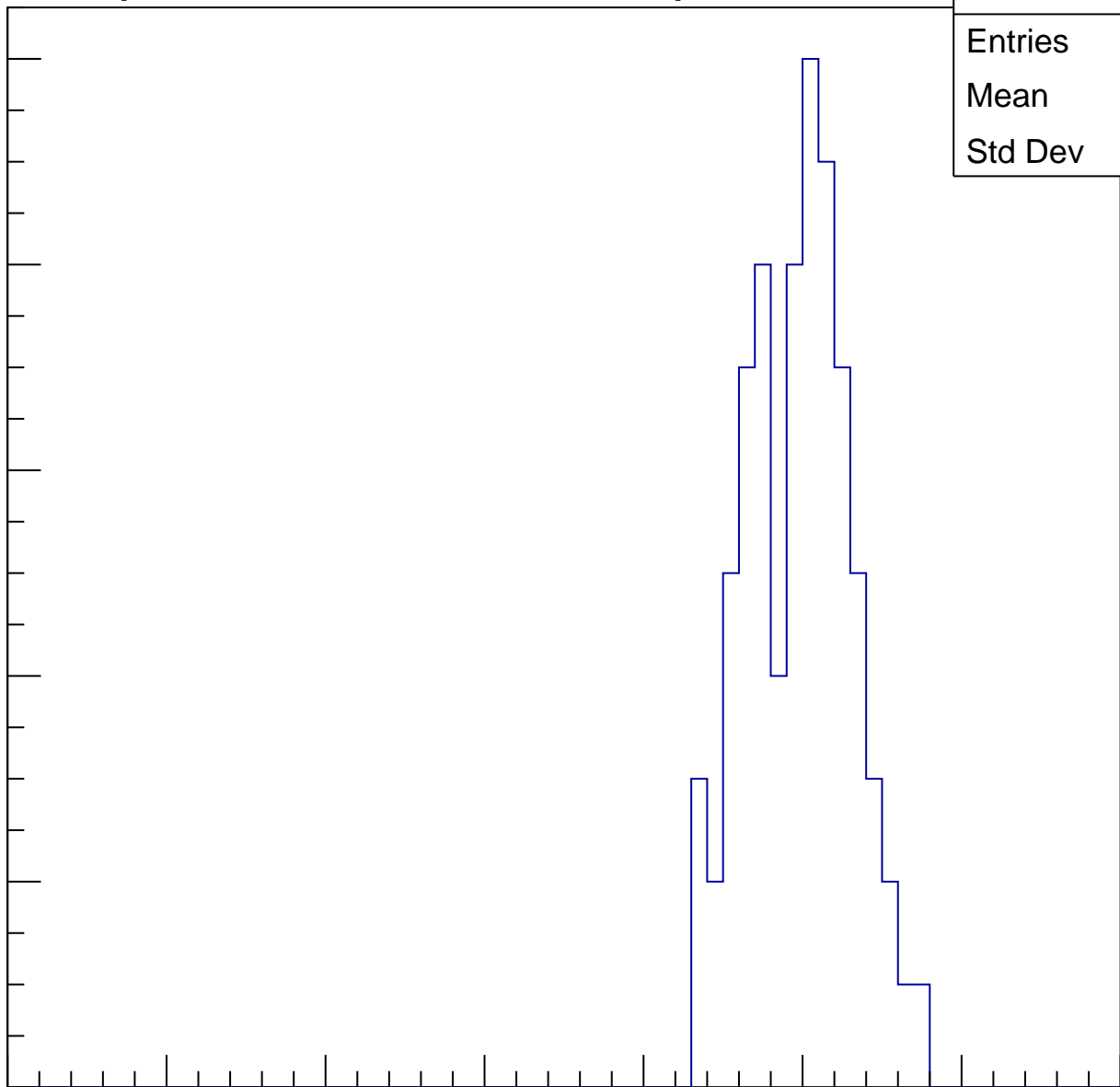
30

40

50

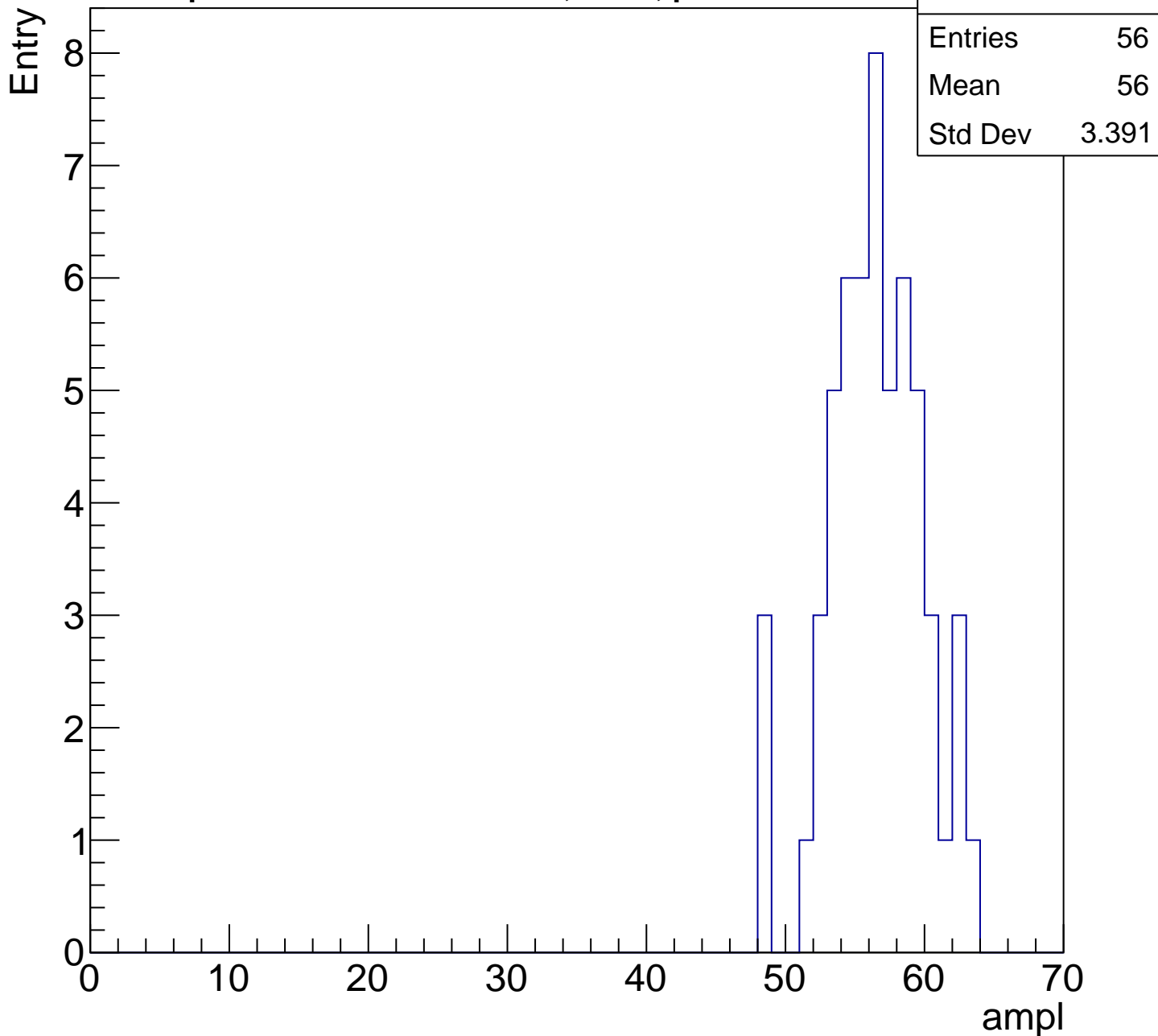
60

ampl



B1L101S, U23-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U23-ch73, adc5

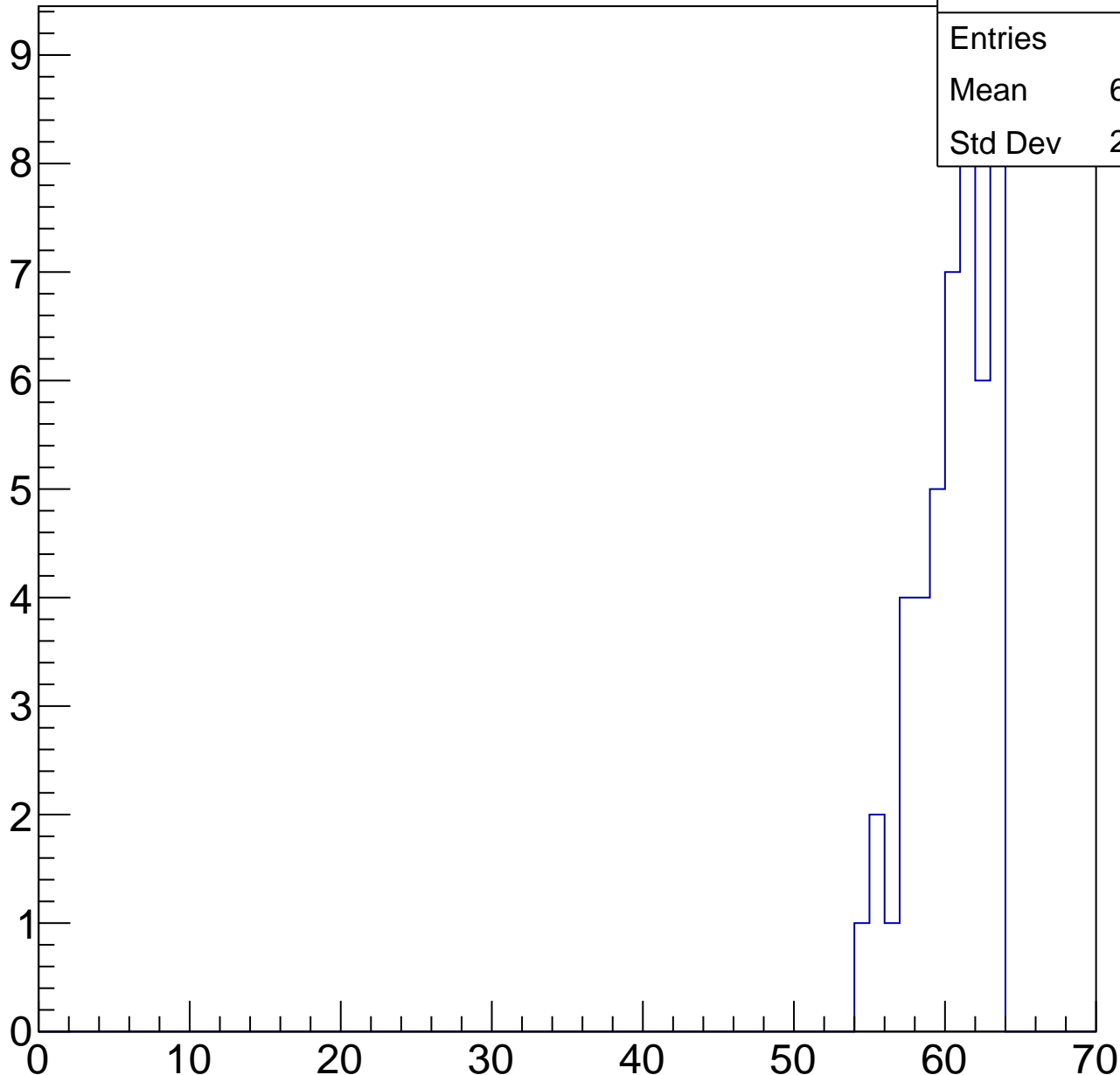
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	60.06
Std Dev	2.384

ampl



B1L101S, U23-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch74, adc0

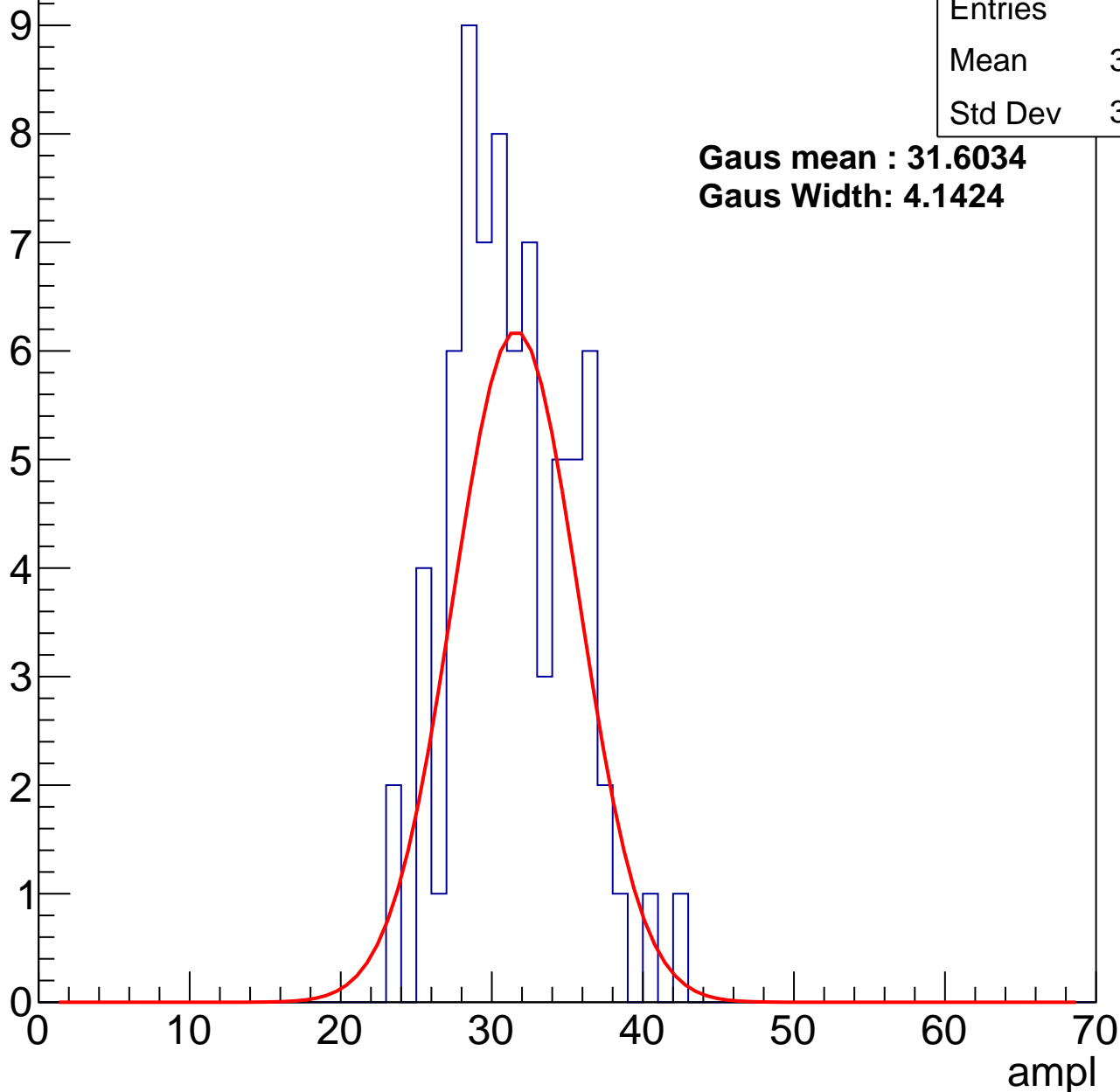
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.99
Std Dev	3.903

Gaus mean : 31.6034

Gaus Width: 4.1424



B1L101S, U23-ch74, adc1

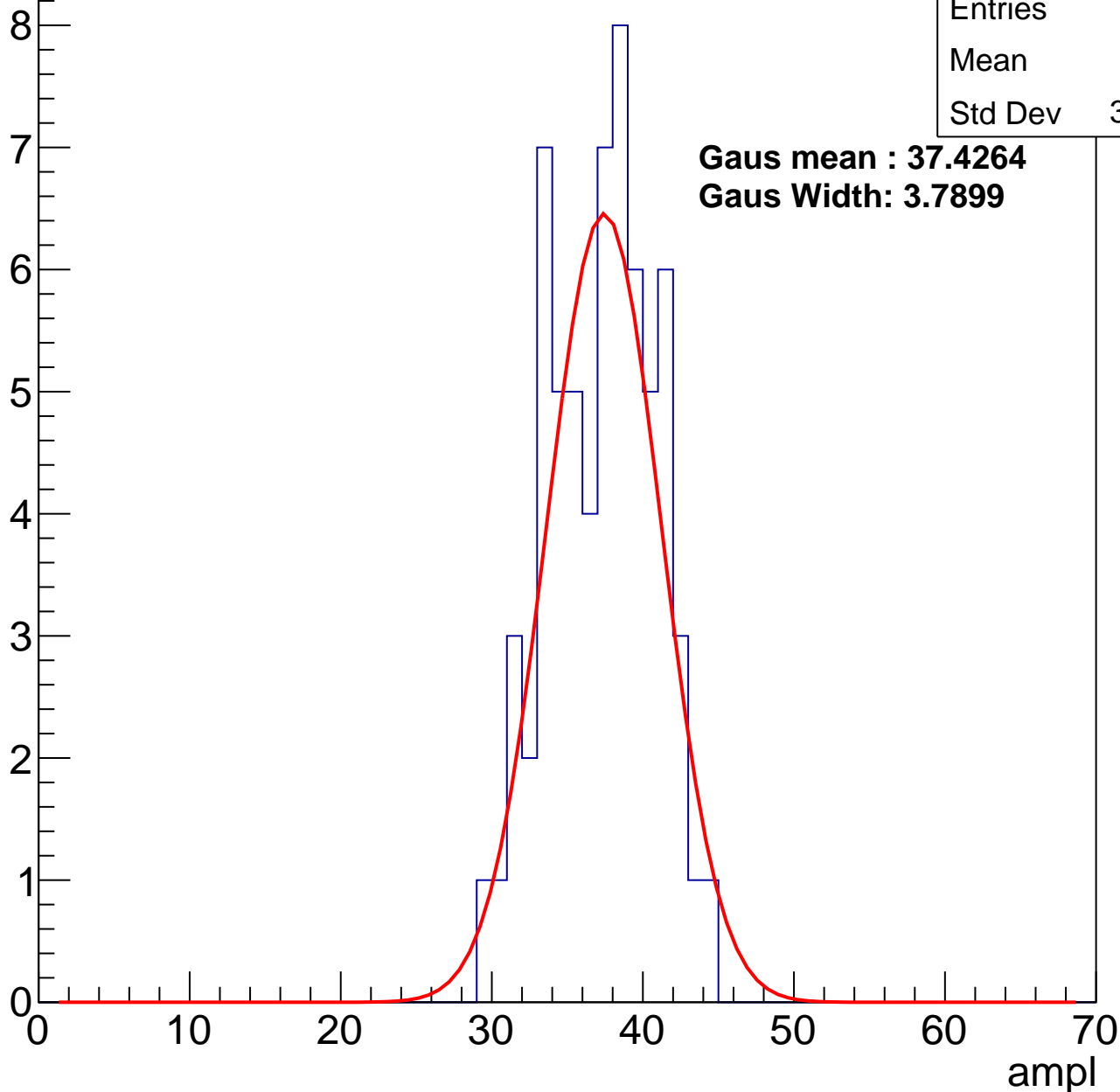
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.8
Std Dev	3.469

Gaus mean : 37.4264

Gaus Width: 3.7899



B1L101S, U23-ch74, adc2

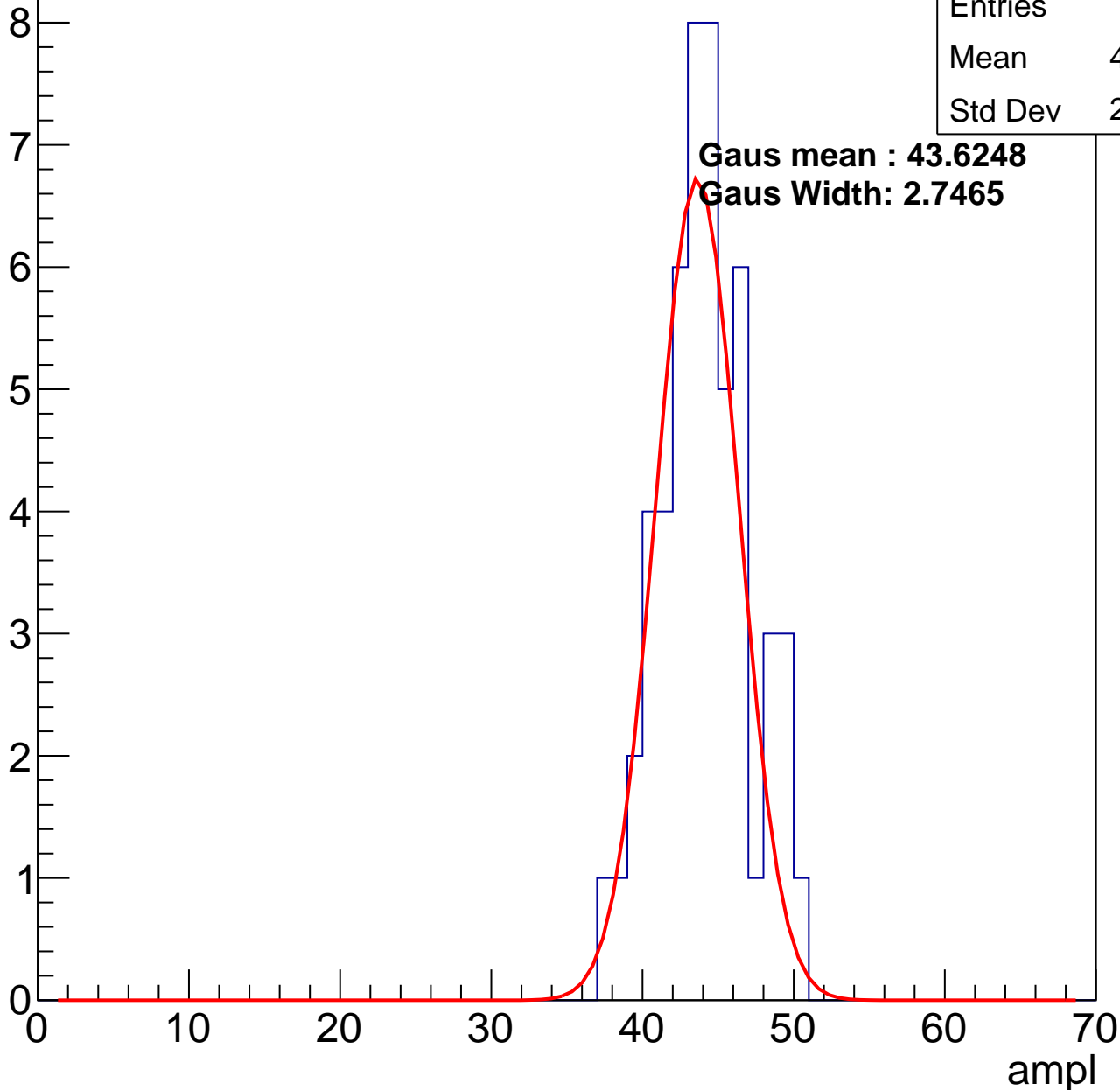
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	43.66
Std Dev	2.939

Gaus mean : 43.6248

Gaus Width: 2.7465

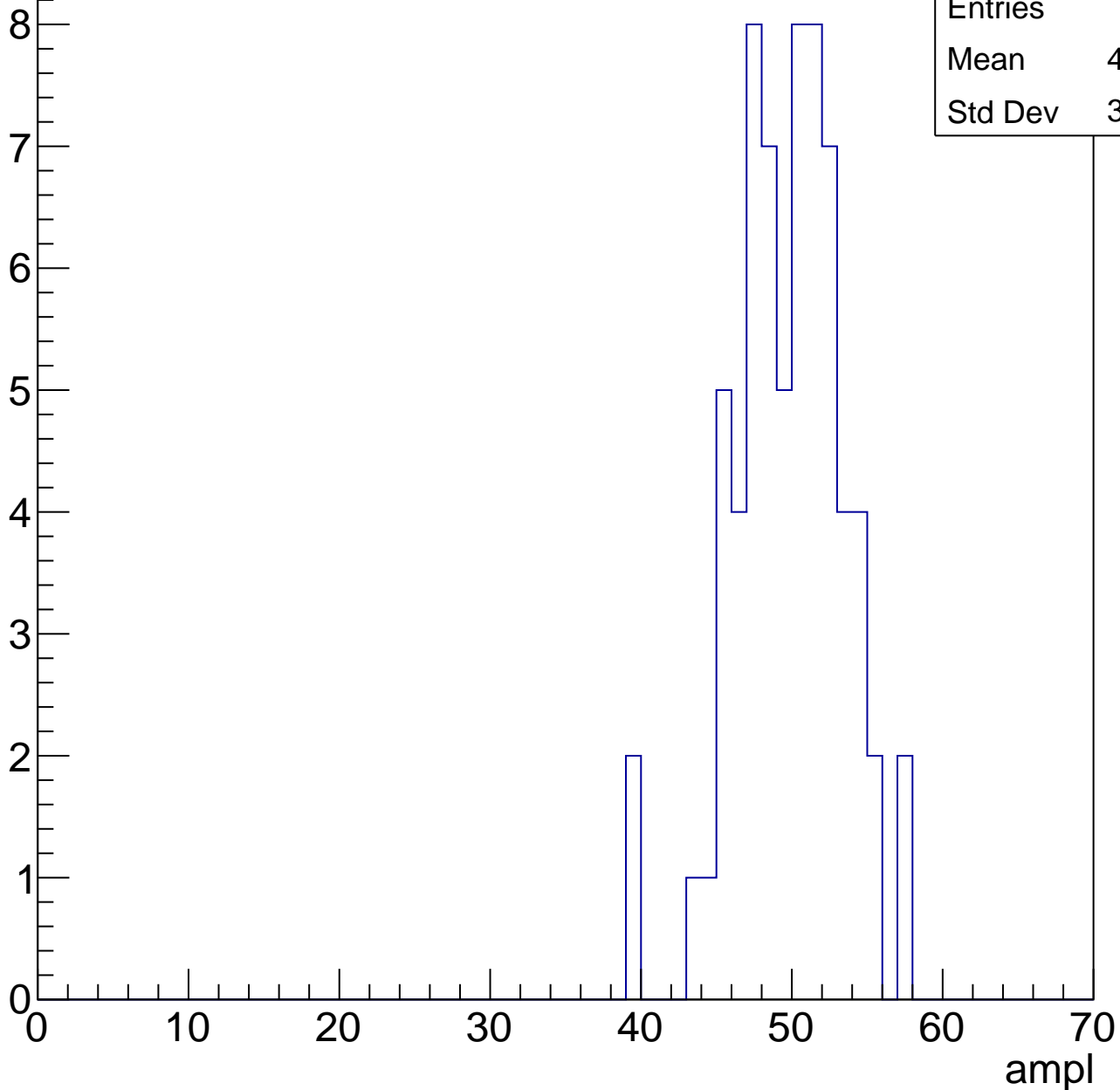


B1L101S, U23-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.34
Std Dev	3.588

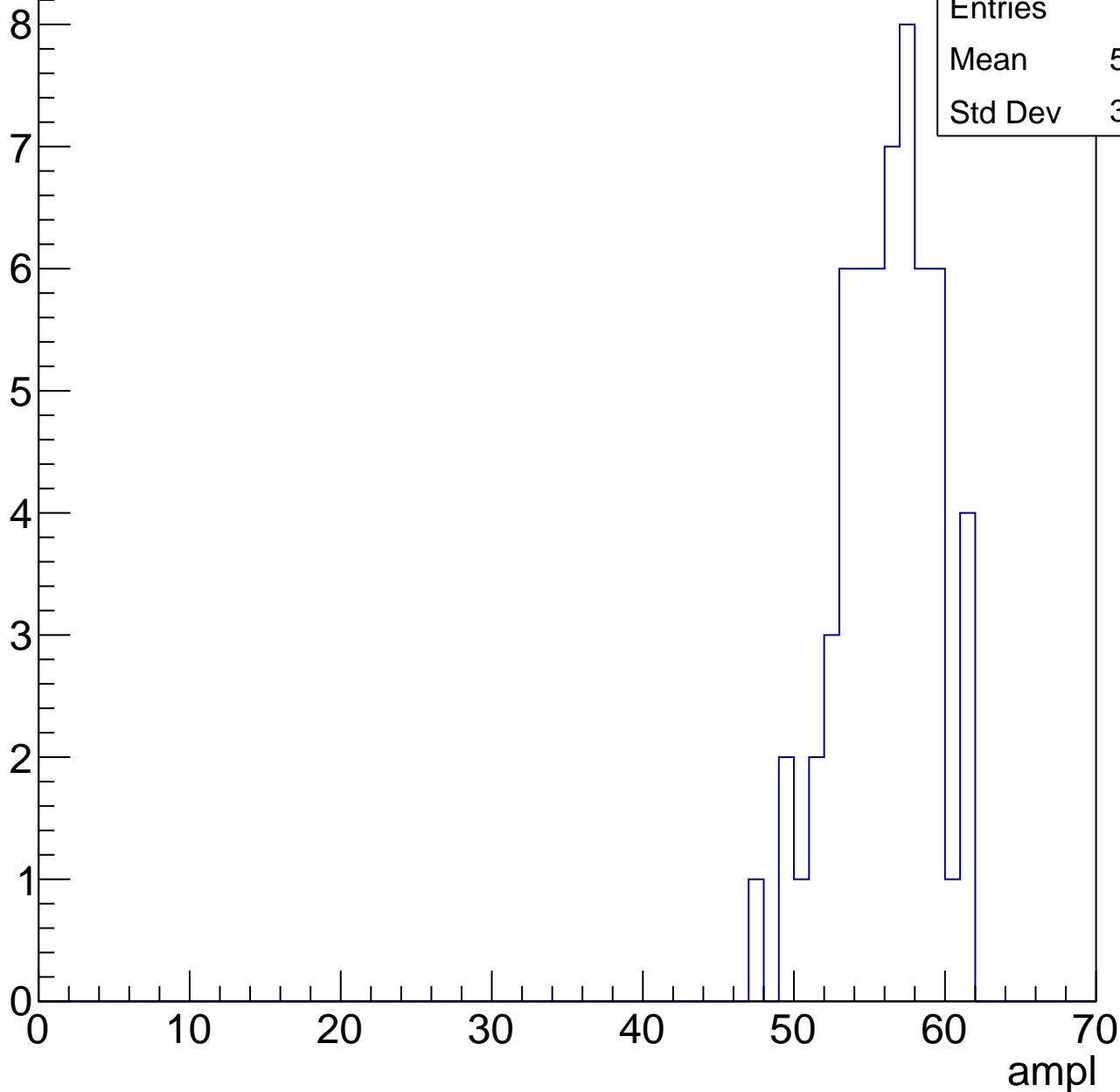


B1L101S, U23-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.58
Std Dev	3.158

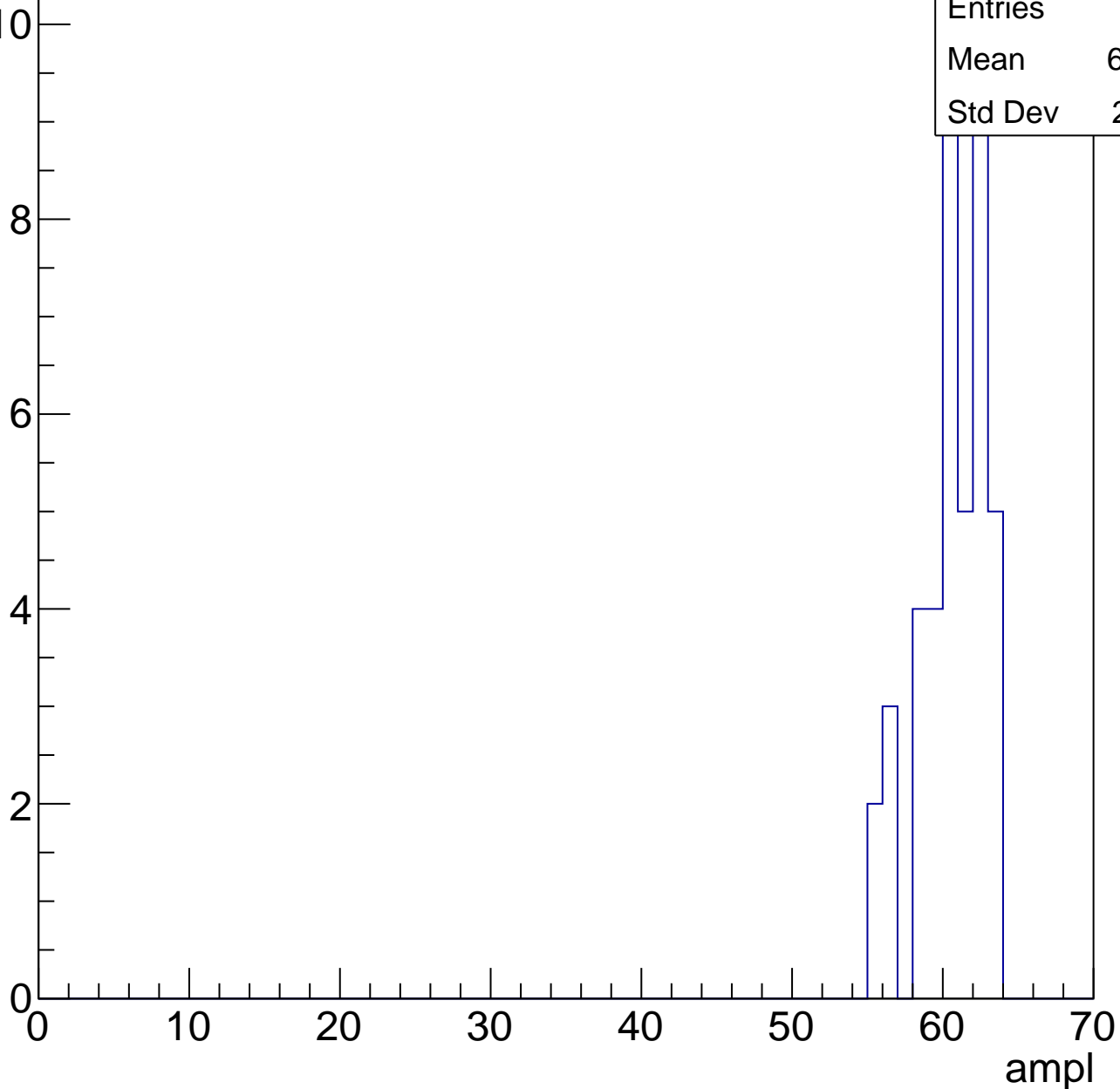


B1L101S, U23-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	60.14
Std Dev	2.221

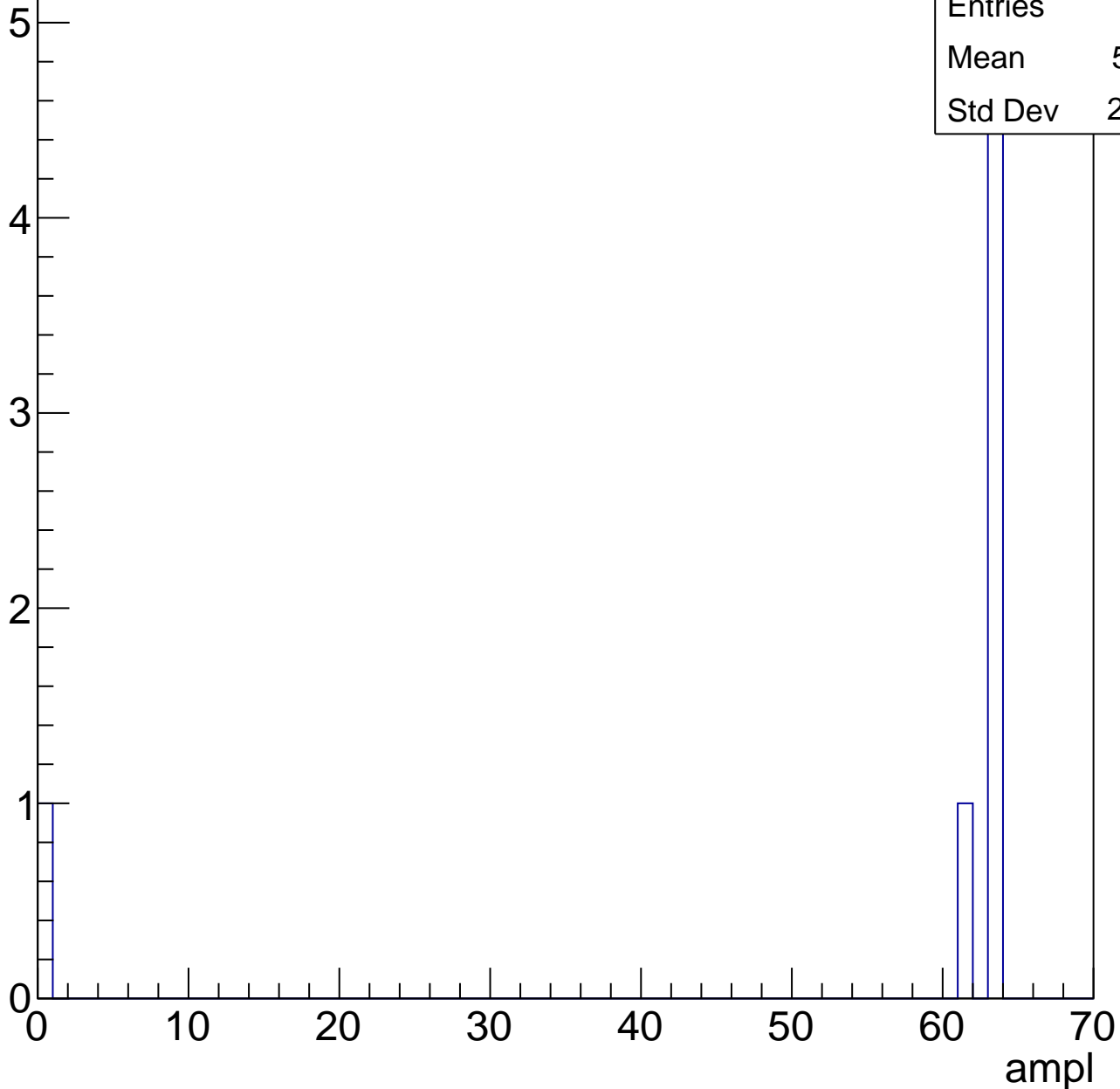


B1L101S, U23-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	7
Mean	53.71
Std Dev	21.94



B1L101S, U23-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch75, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	32.37
Std Dev	3.546

Gaus mean : 33.0294

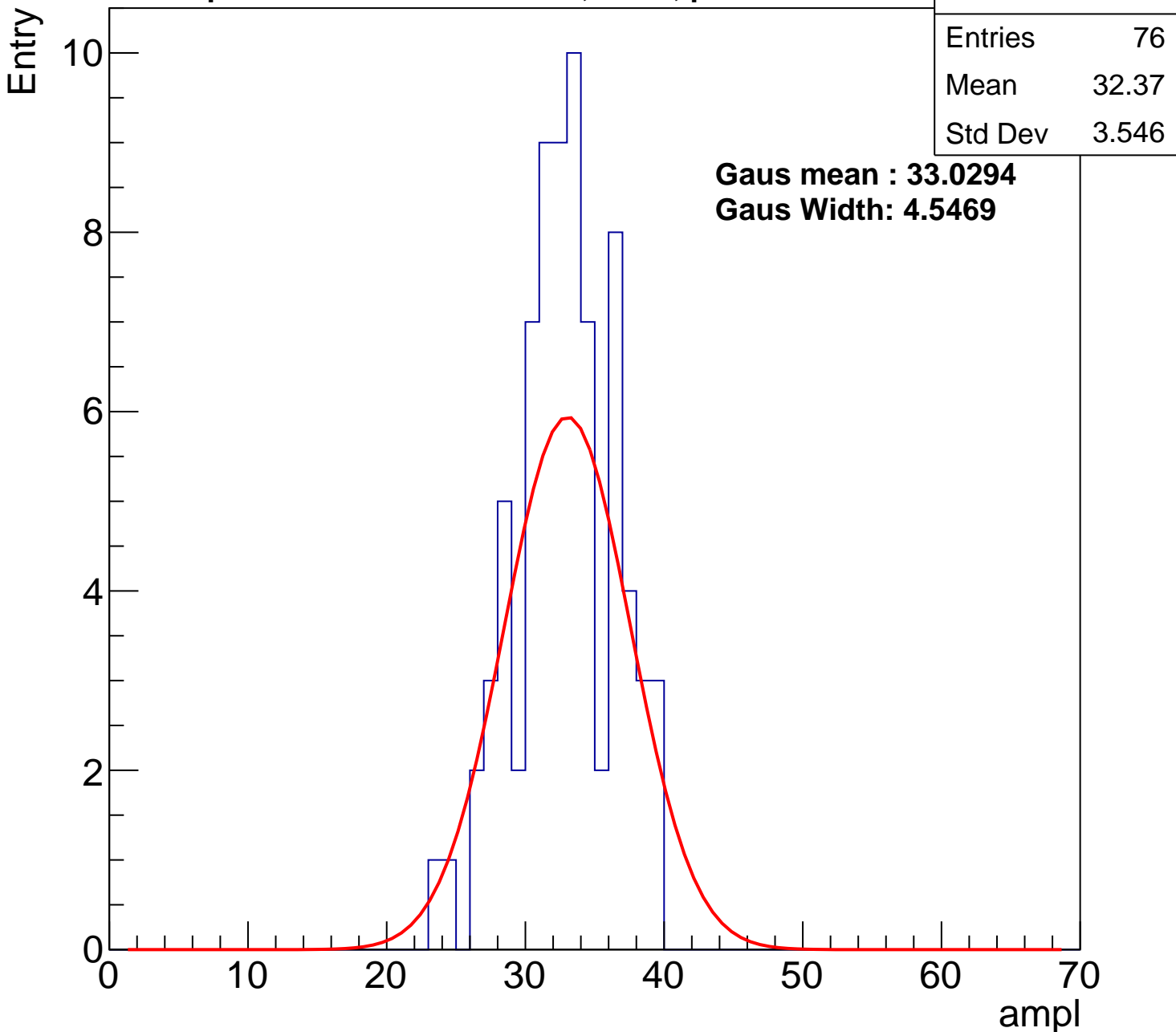
Gaus Width: 4.5469

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch75, adc1

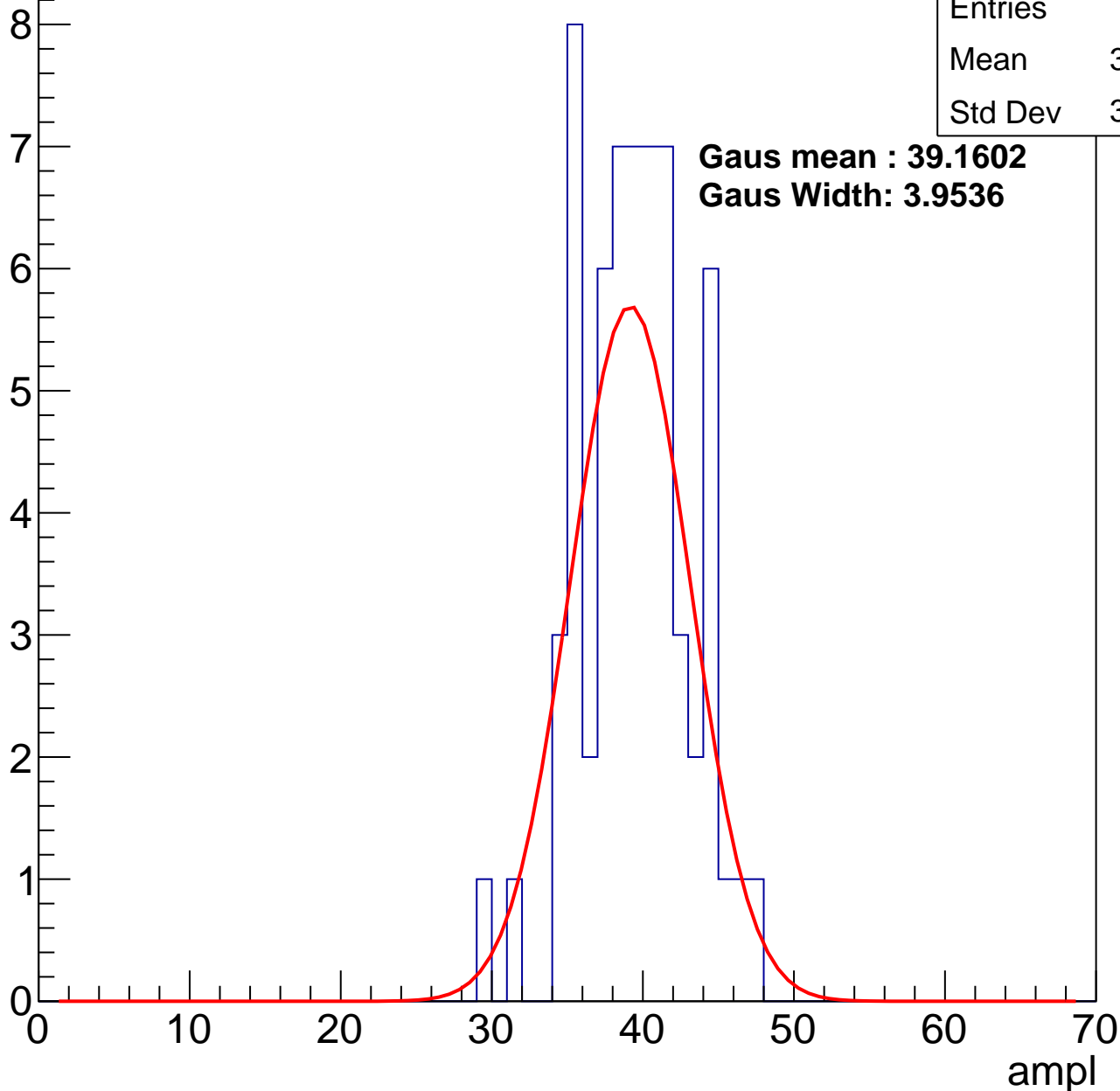
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.98
Std Dev	3.583

Gaus mean : 39.1602

Gaus Width: 3.9536



B1L101S, U23-ch75, adc2

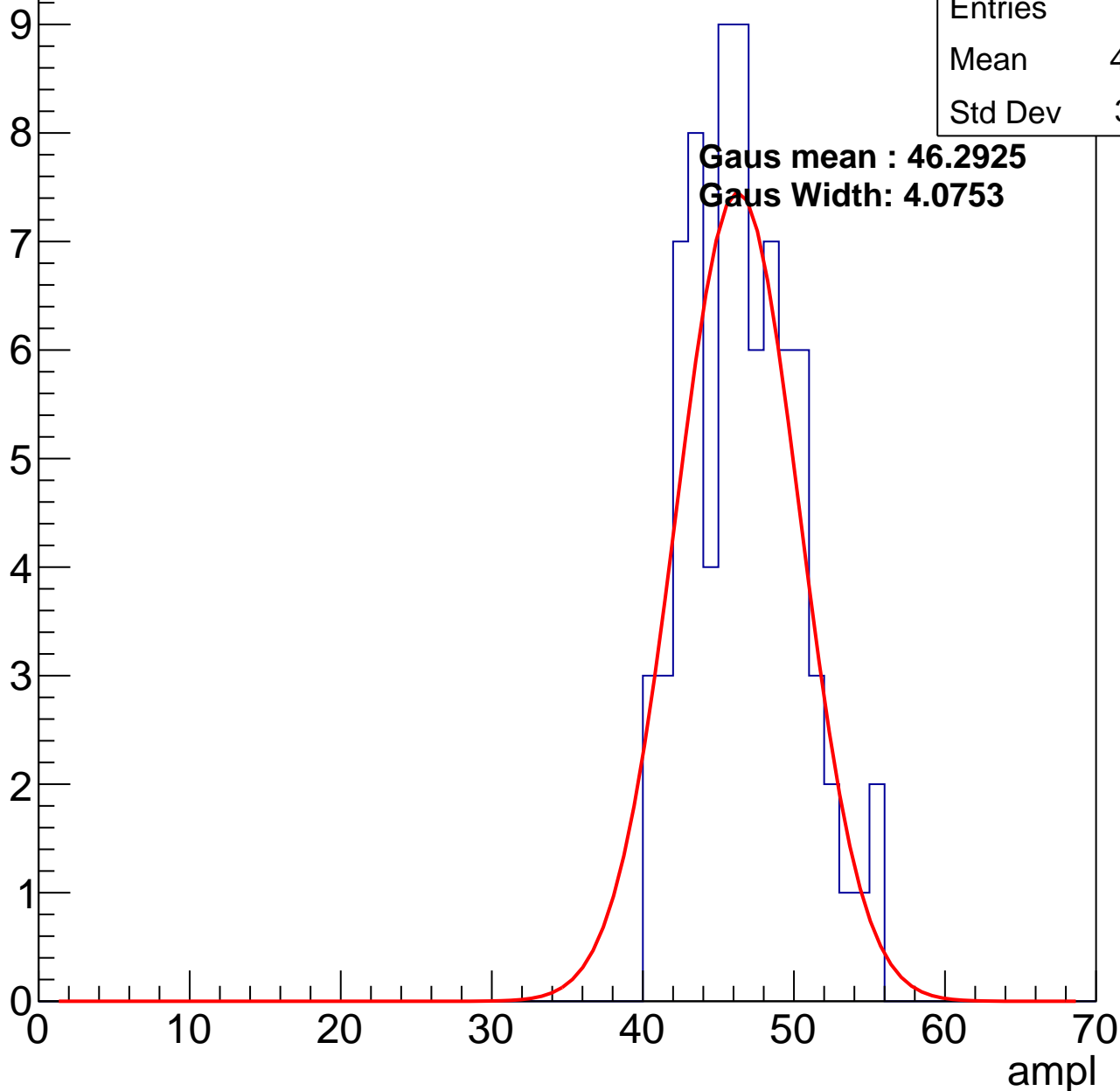
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	46.26
Std Dev	3.591

Gaus mean : 46.2925

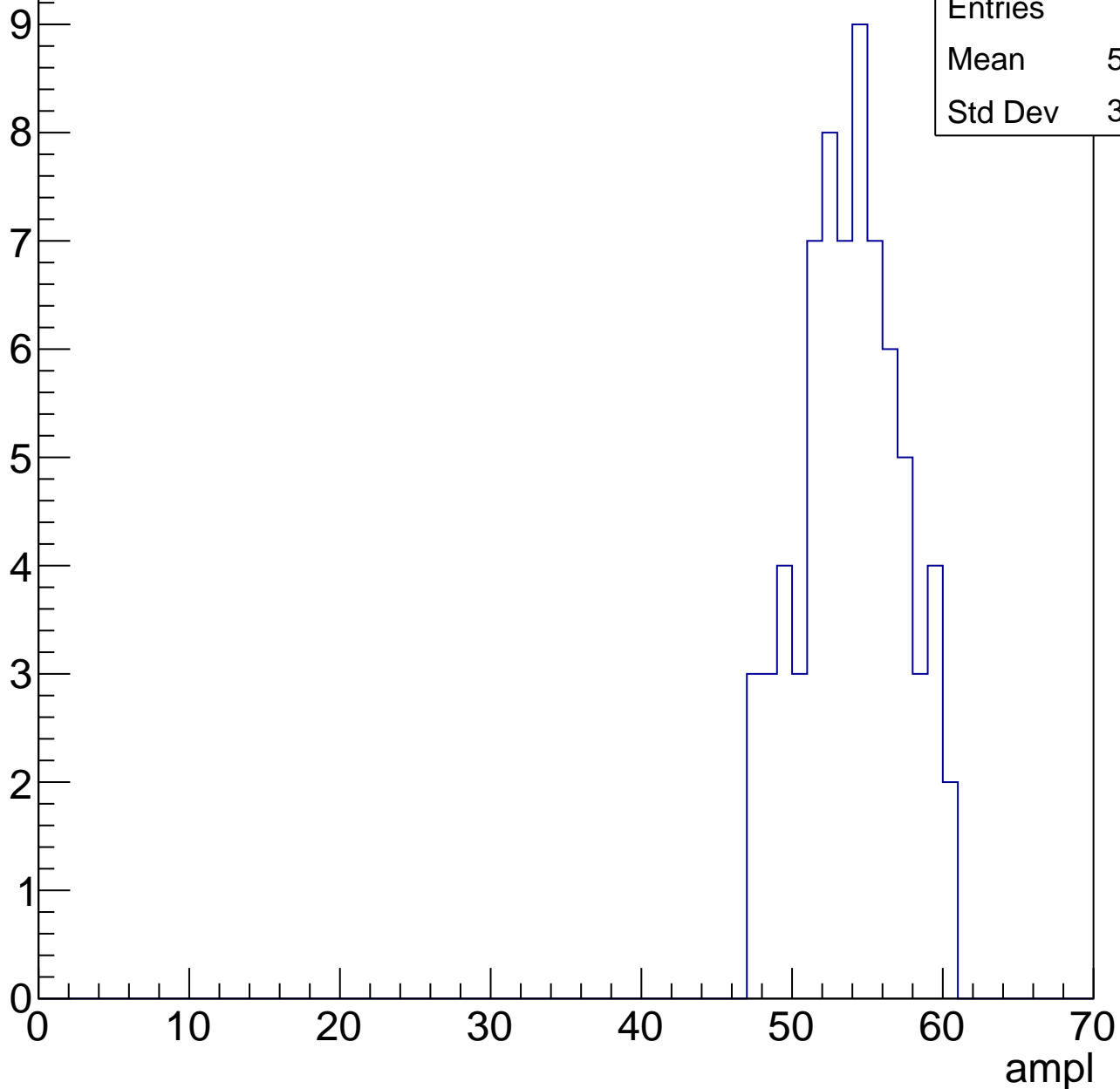
Gaus Width: 4.0753



B1L101S, U23-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



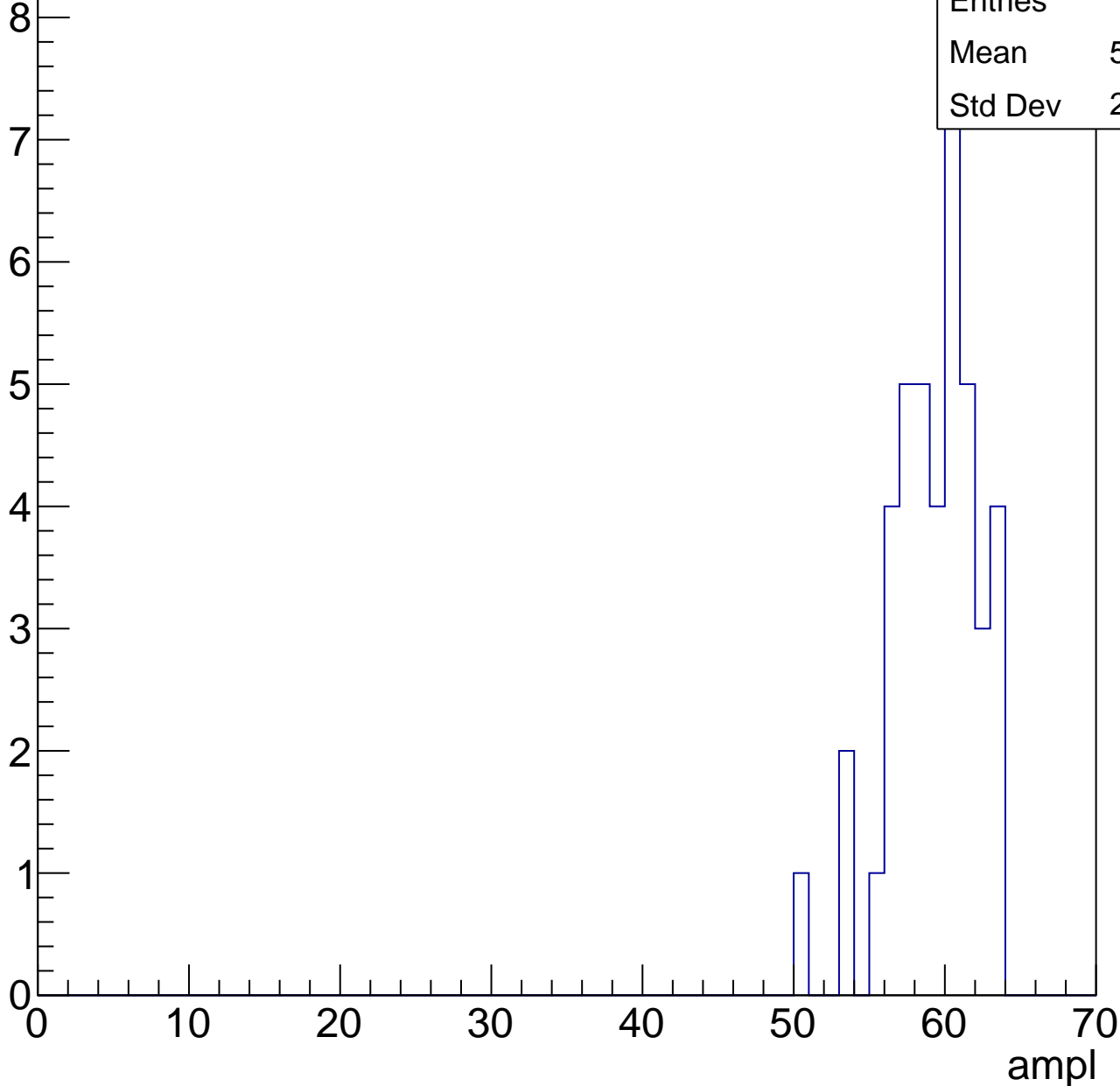
Entries	71
Mean	53.48
Std Dev	3.318

B1L101S, U23-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.79
Std Dev	2.875

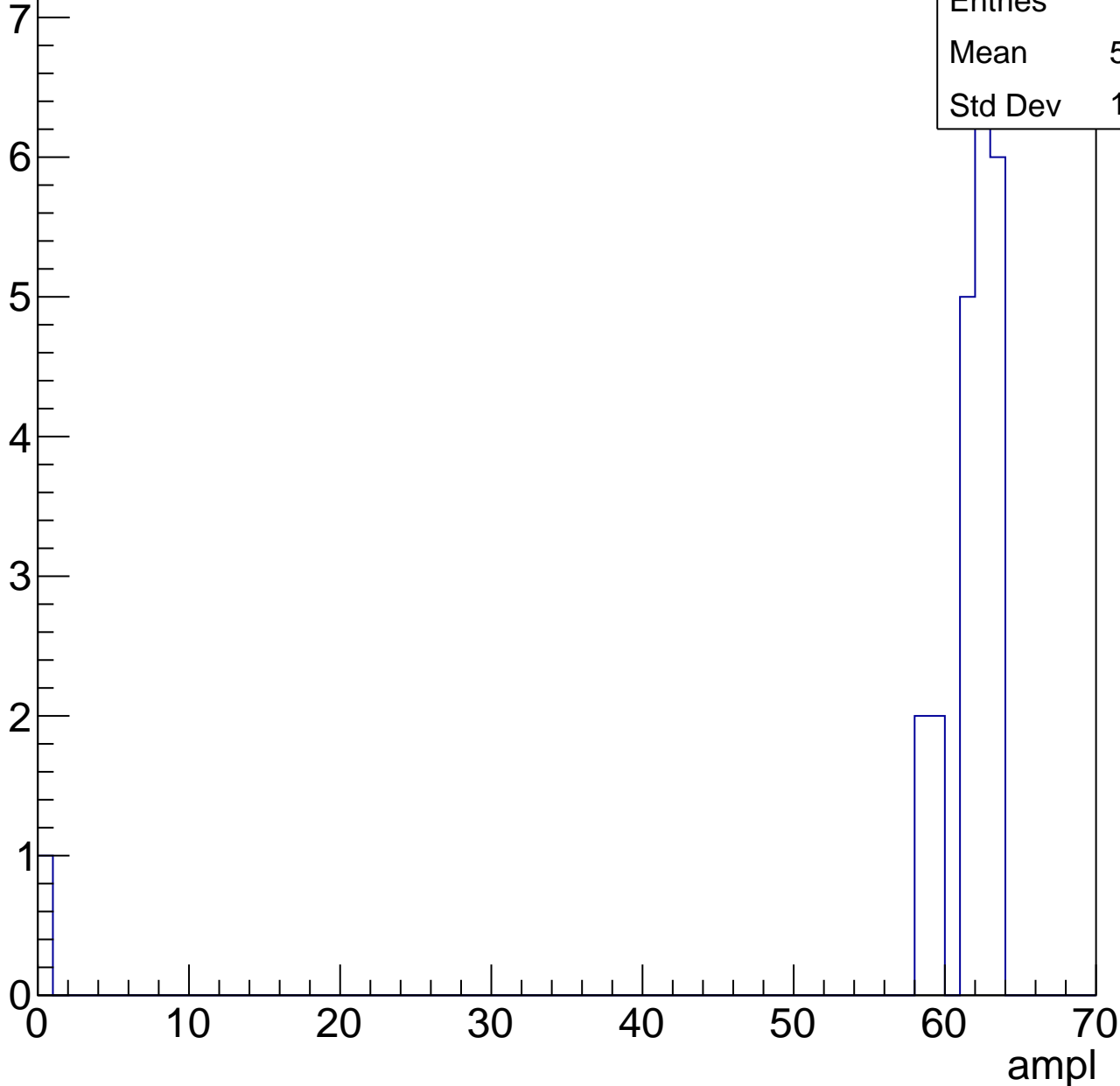


B1L101S, U23-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.74
Std Dev	12.62



B1L101S, U23-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch76, adc0

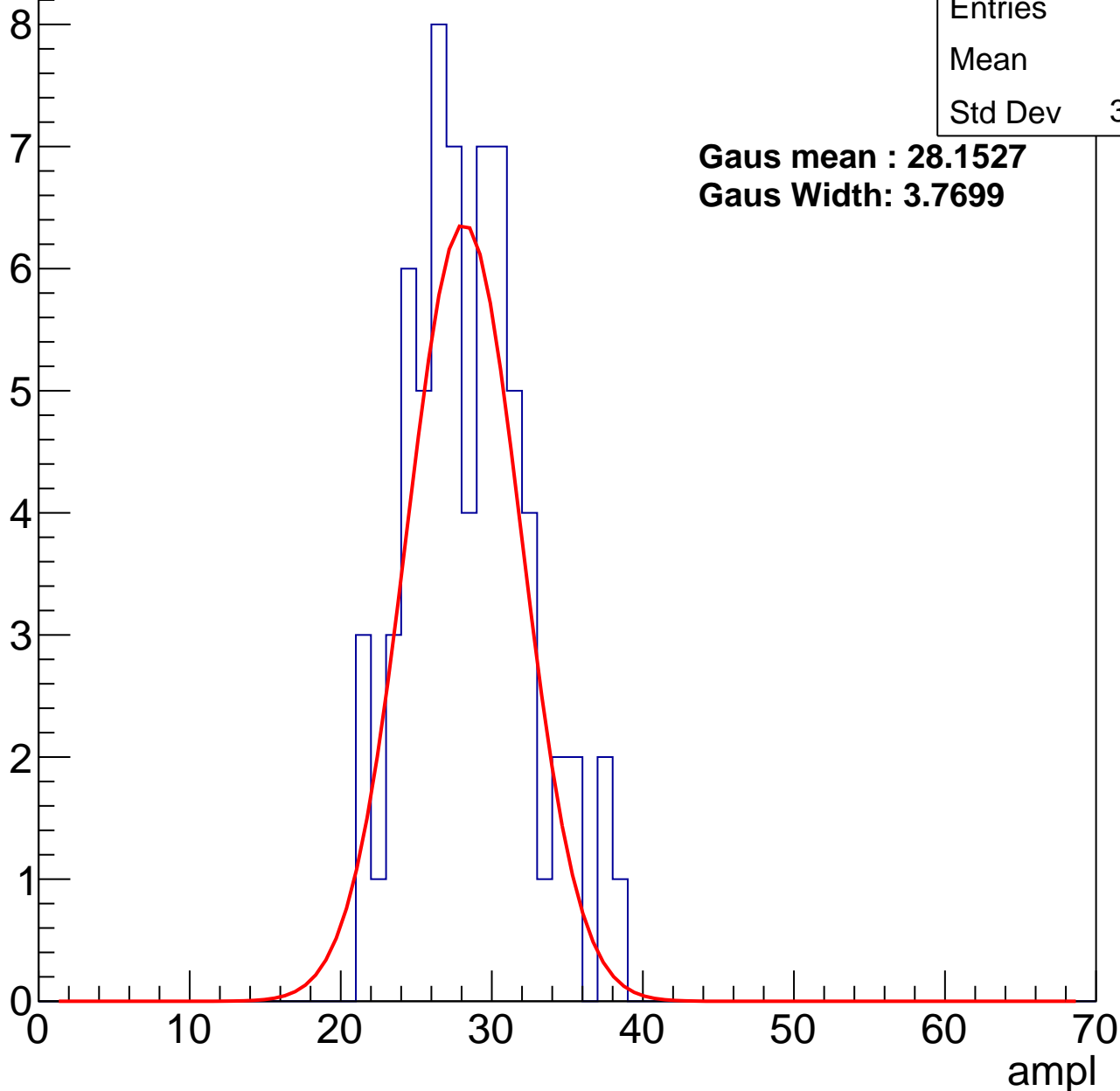
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.1
Std Dev	3.908

Gaus mean : 28.1527

Gaus Width: 3.7699



B1L101S, U23-ch76, adc1

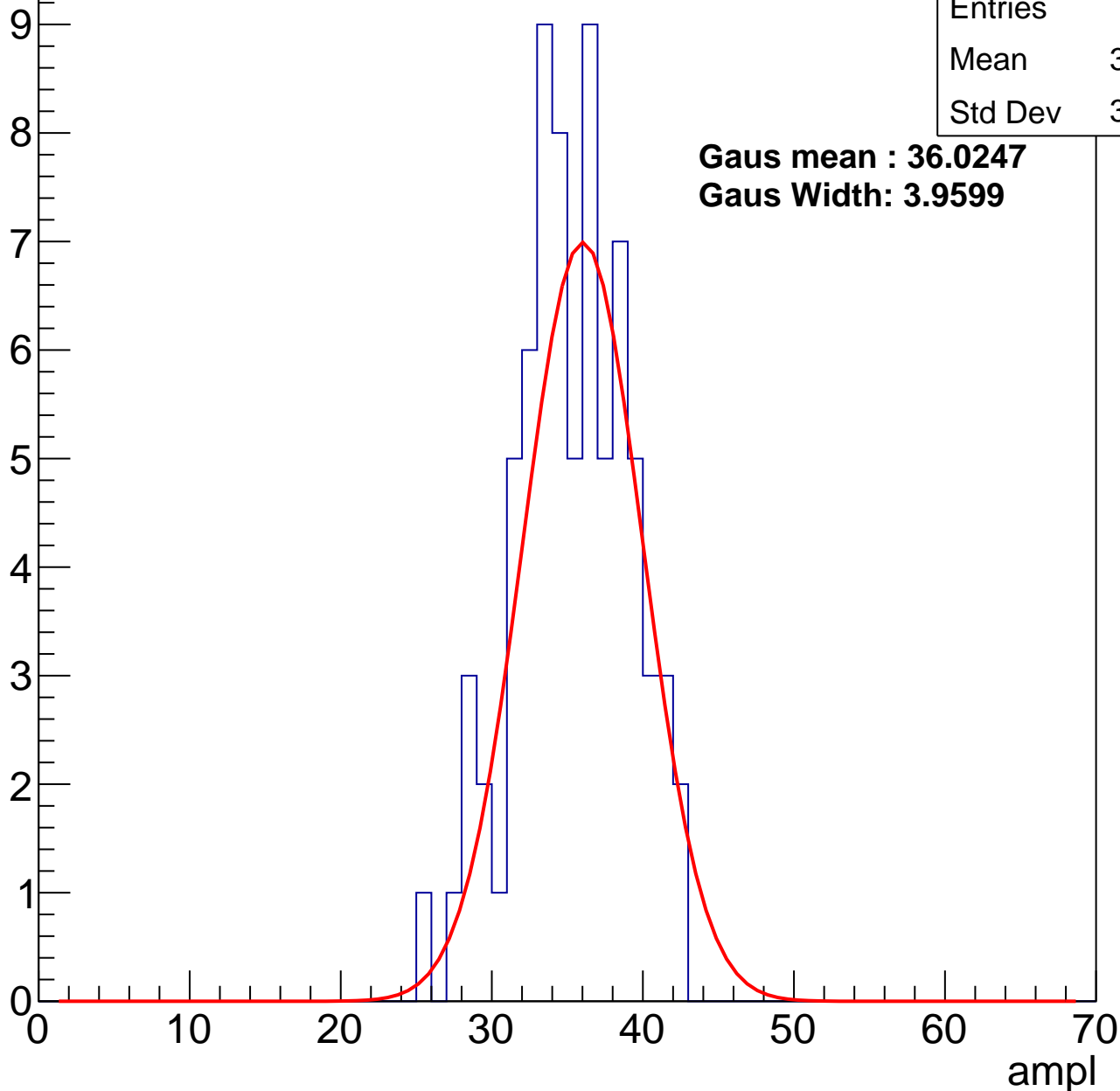
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	34.83
Std Dev	3.718

Gaus mean : 36.0247

Gaus Width: 3.9599



B1L101S, U23-ch76, adc2

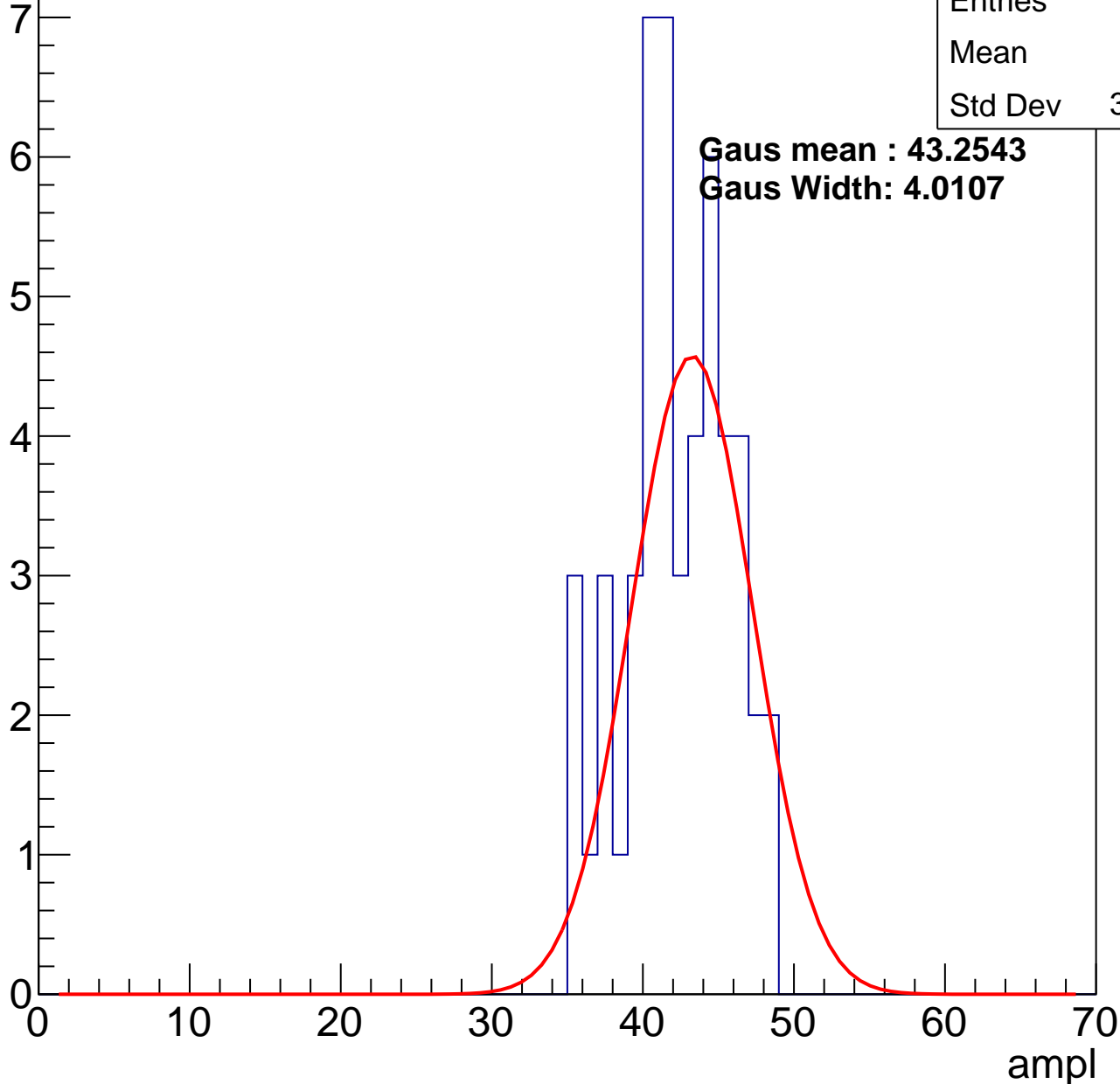
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	41.8
Std Dev	3.418

Gaus mean : 43.2543

Gaus Width: 4.0107

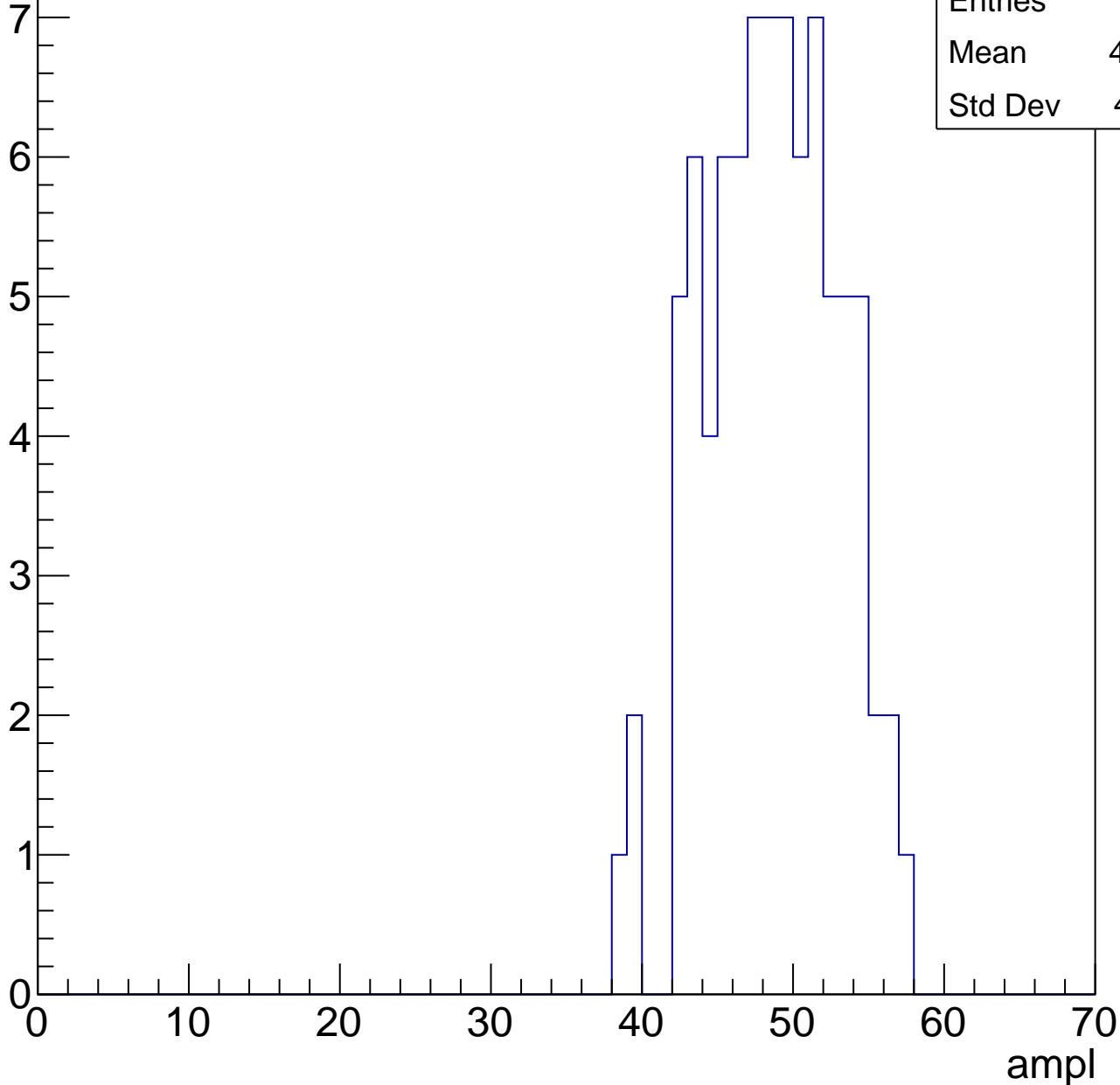


B1L101S, U23-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	48.15
Std Dev	4.261

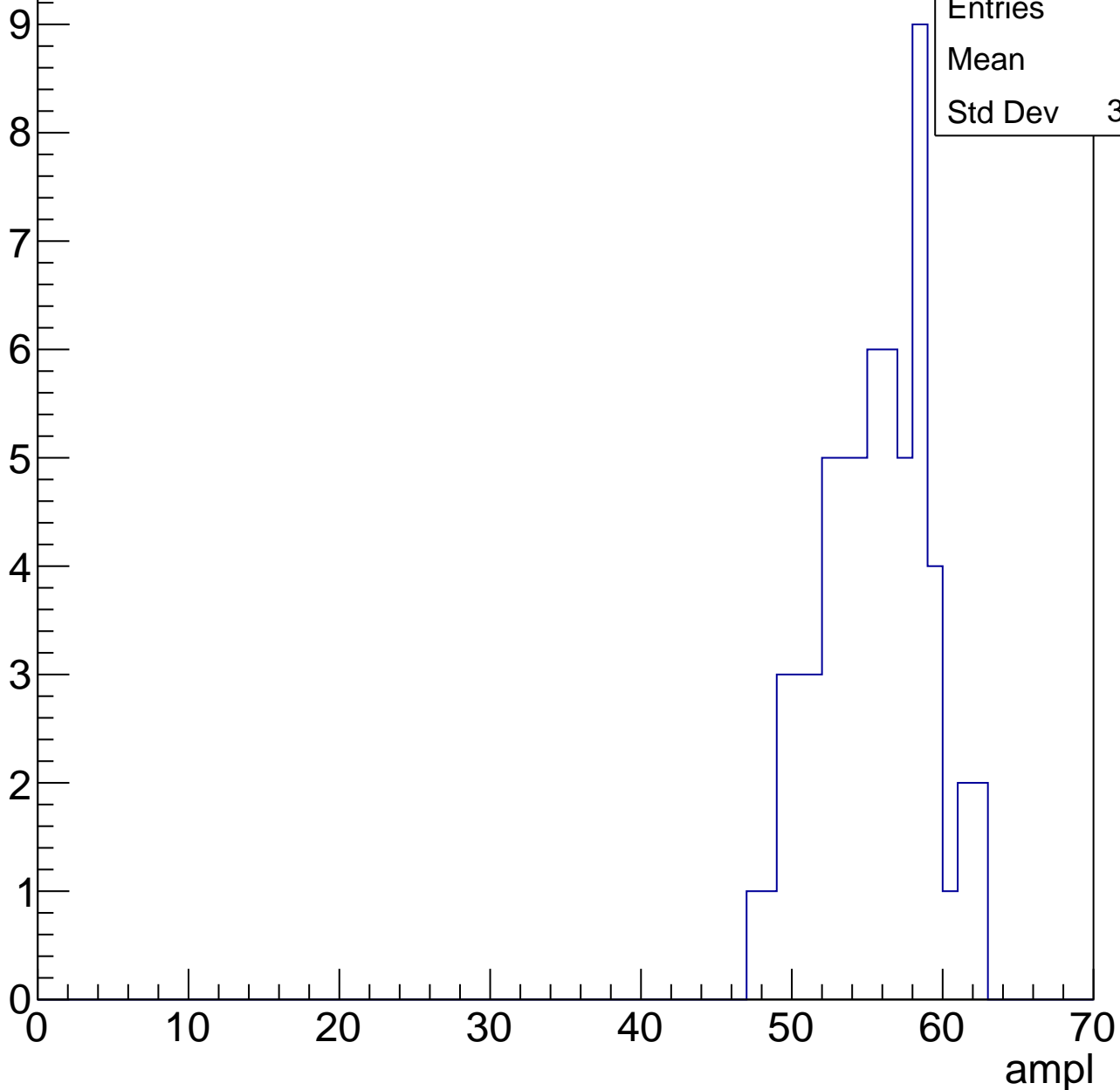


B1L101S, U23-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55
Std Dev	3.576



B1L101S, U23-ch76, adc5

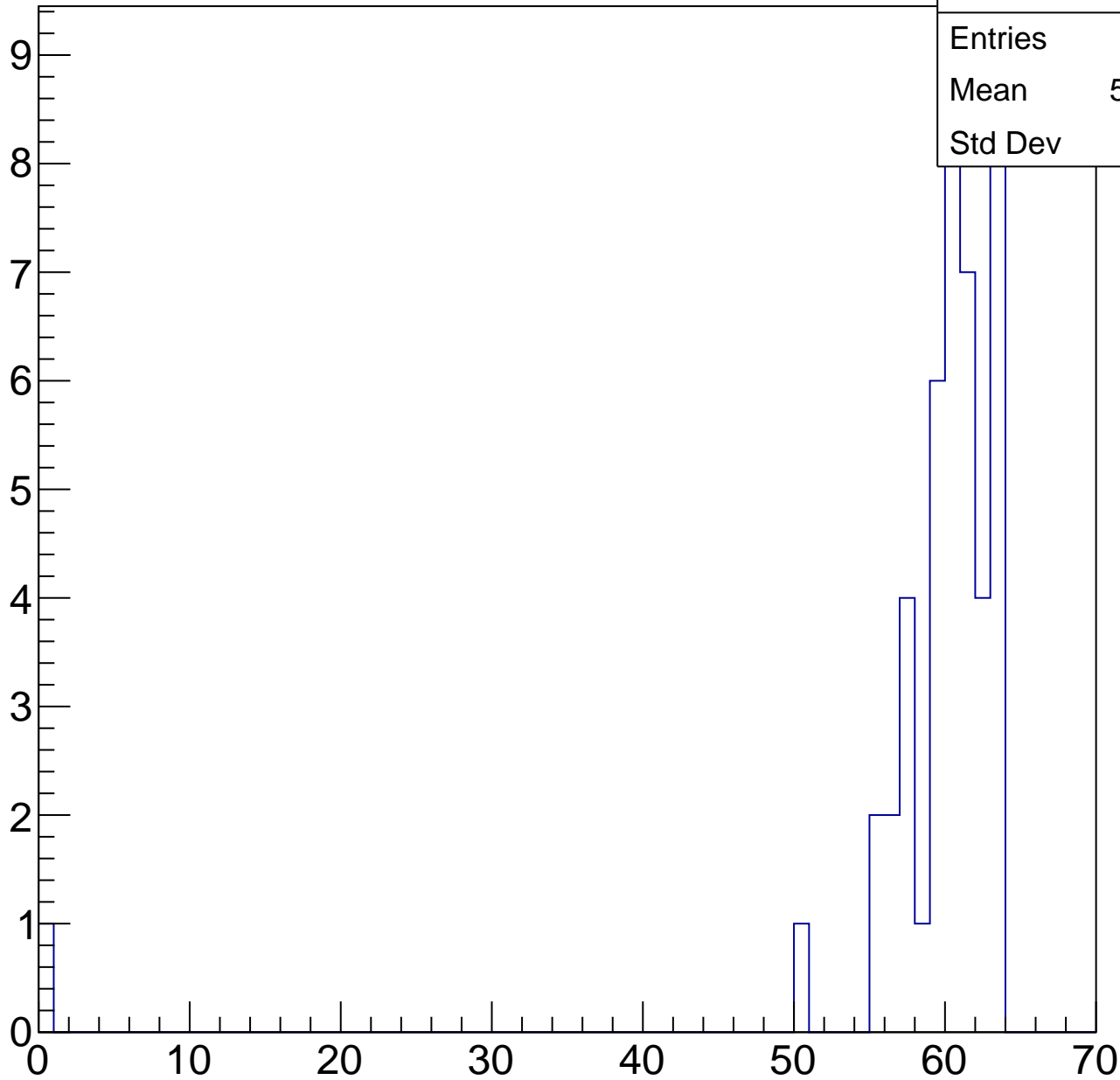
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.47
Std Dev	9.21

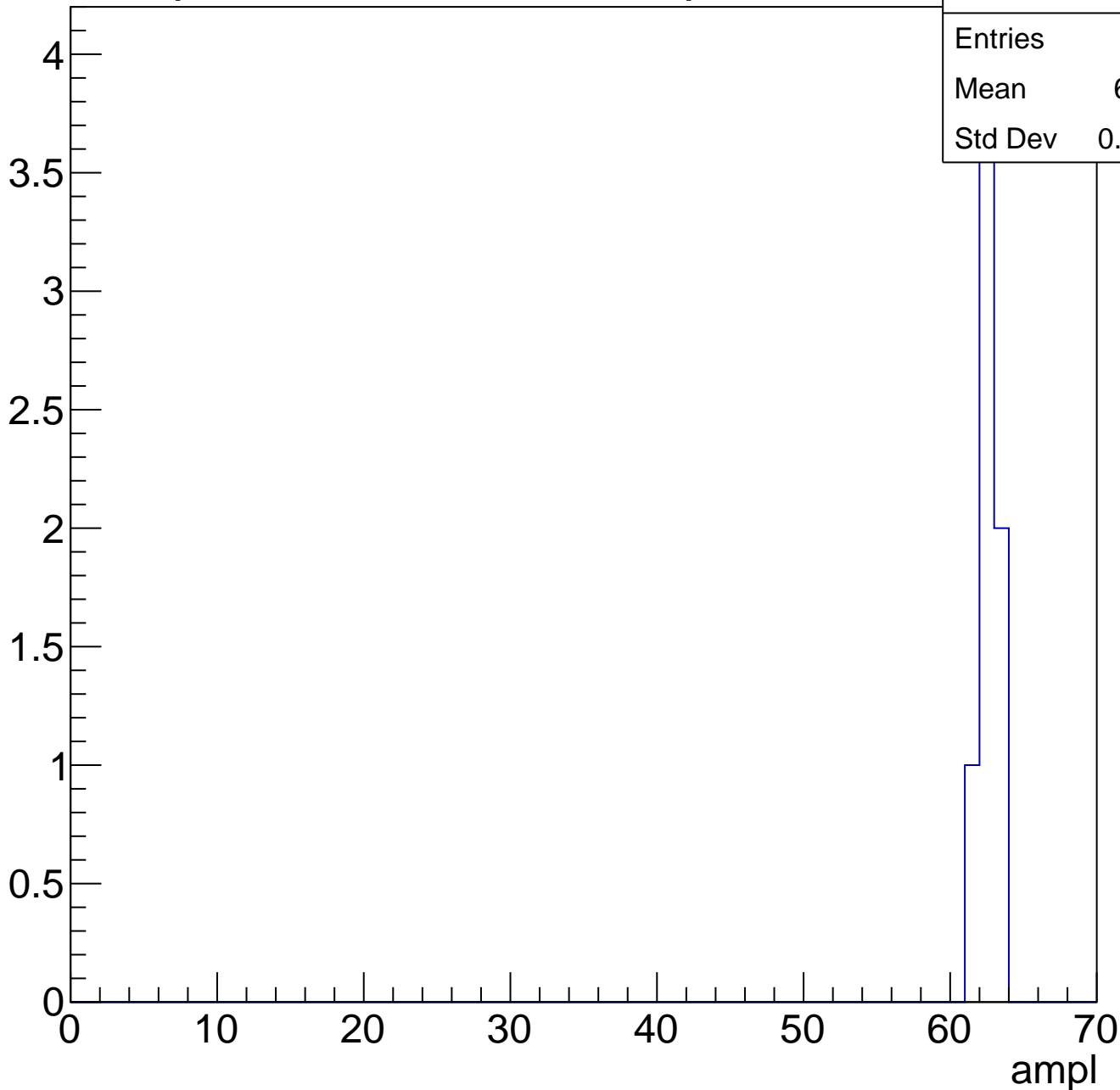
ampl



B1L101S, U23-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch77, adc0

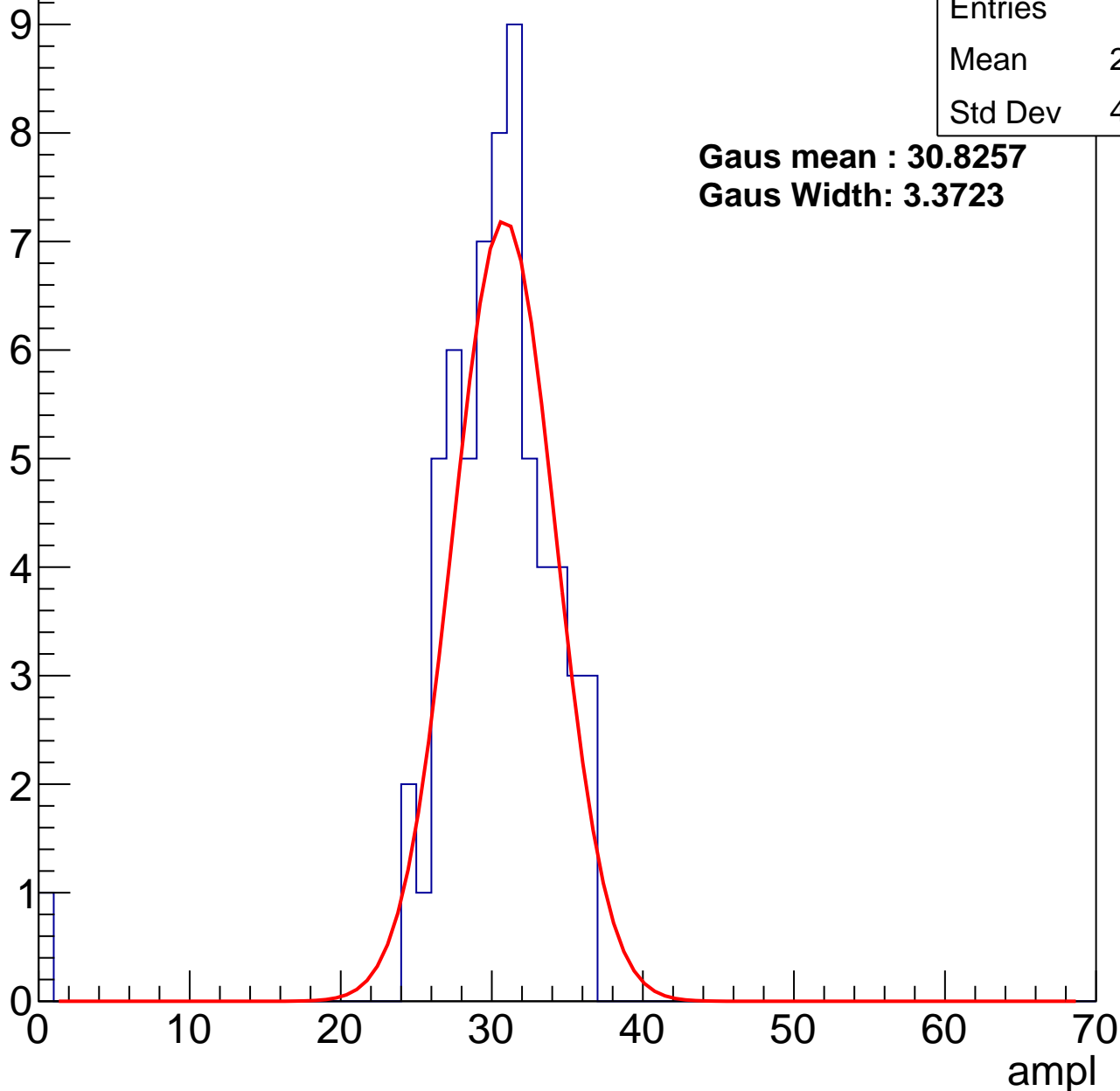
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.65
Std Dev	4.818

Gaus mean : 30.8257

Gaus Width: 3.3723



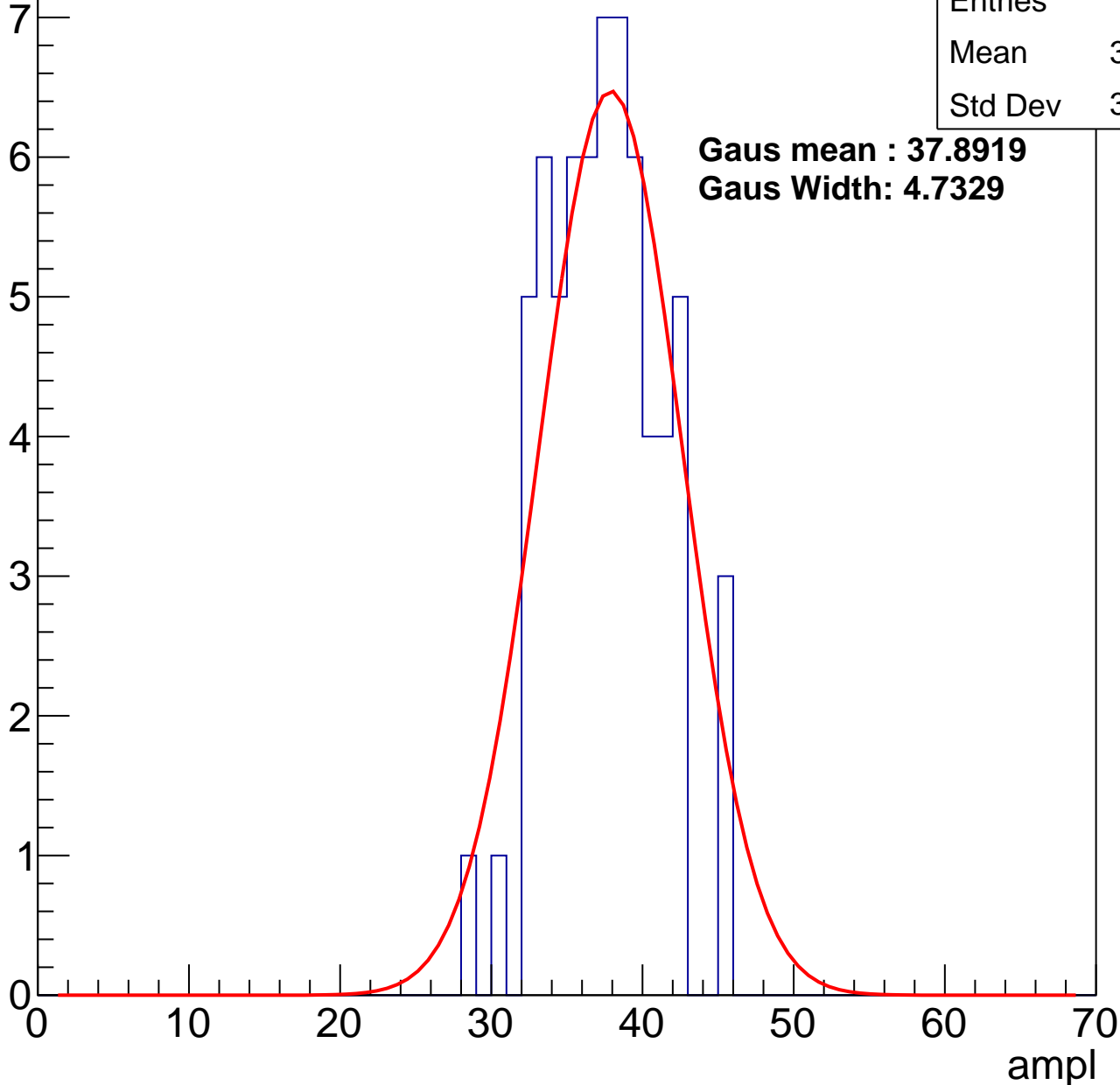
B1L101S, U23-ch77, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.97
Std Dev	3.639

Gaus mean : 37.8919
Gaus Width: 4.7329



B1L101S, U23-ch77, adc2

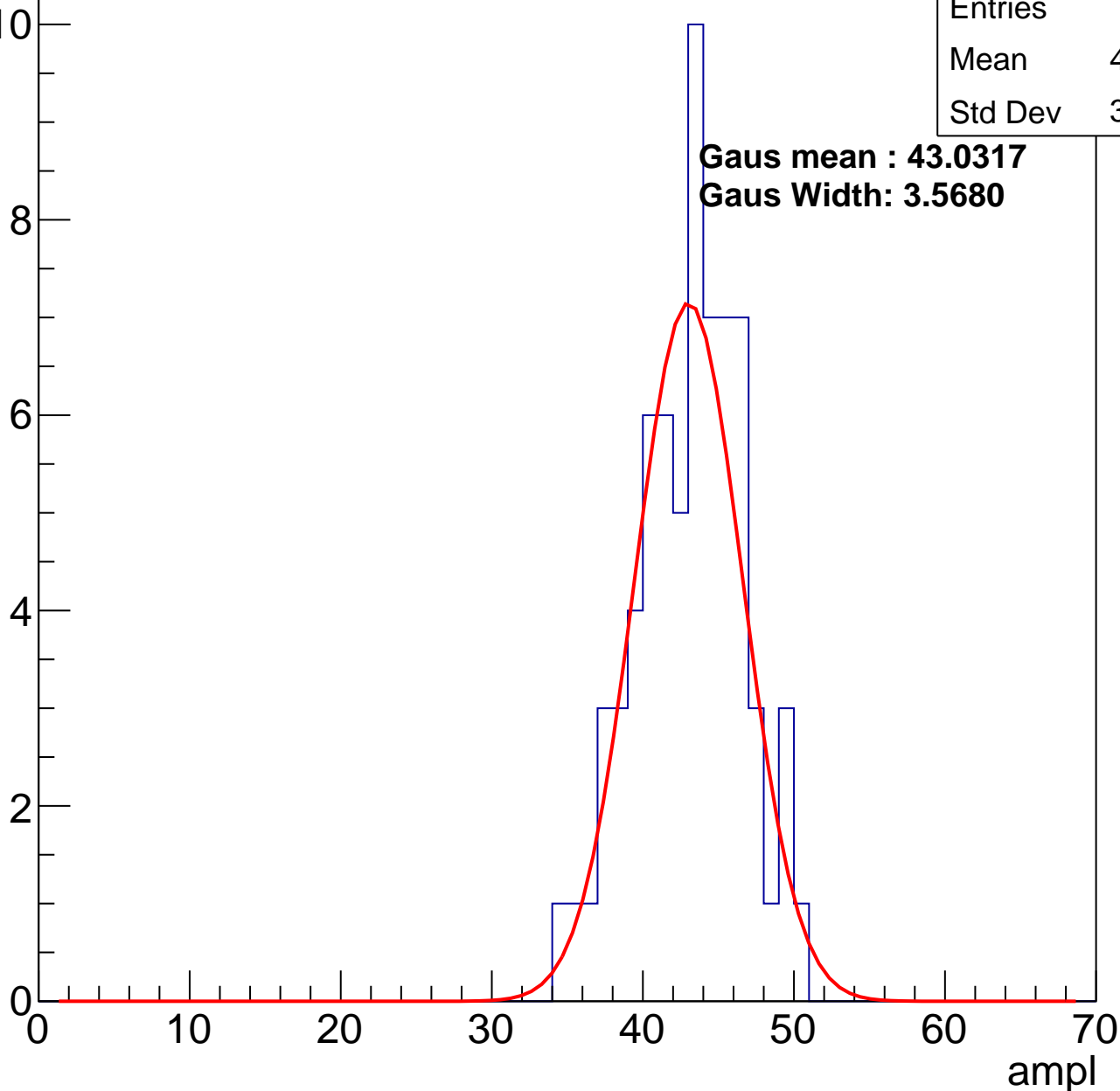
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.65
Std Dev	3.497

Gaus mean : 43.0317

Gaus Width: 3.5680

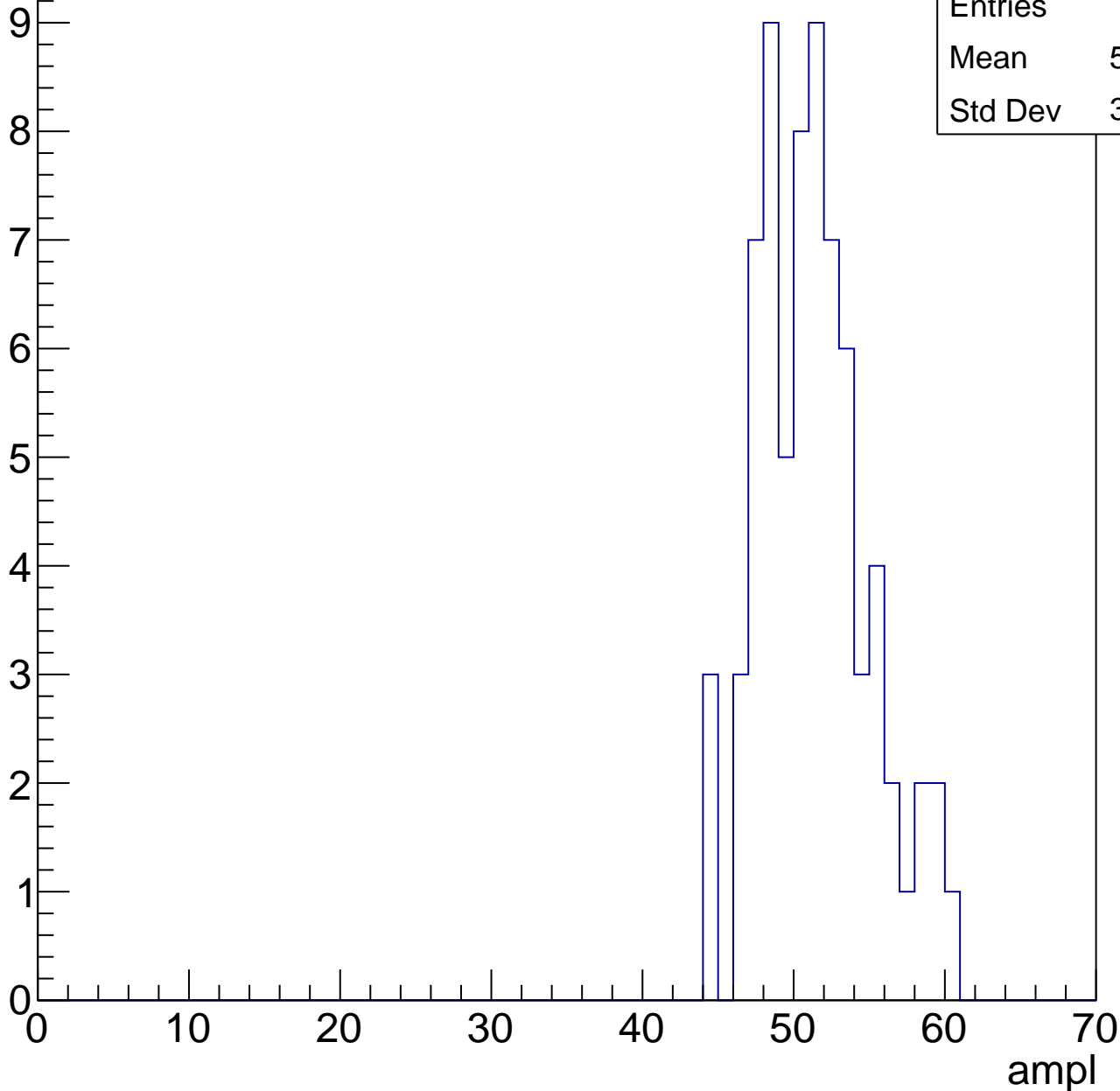


B1L101S, U23-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

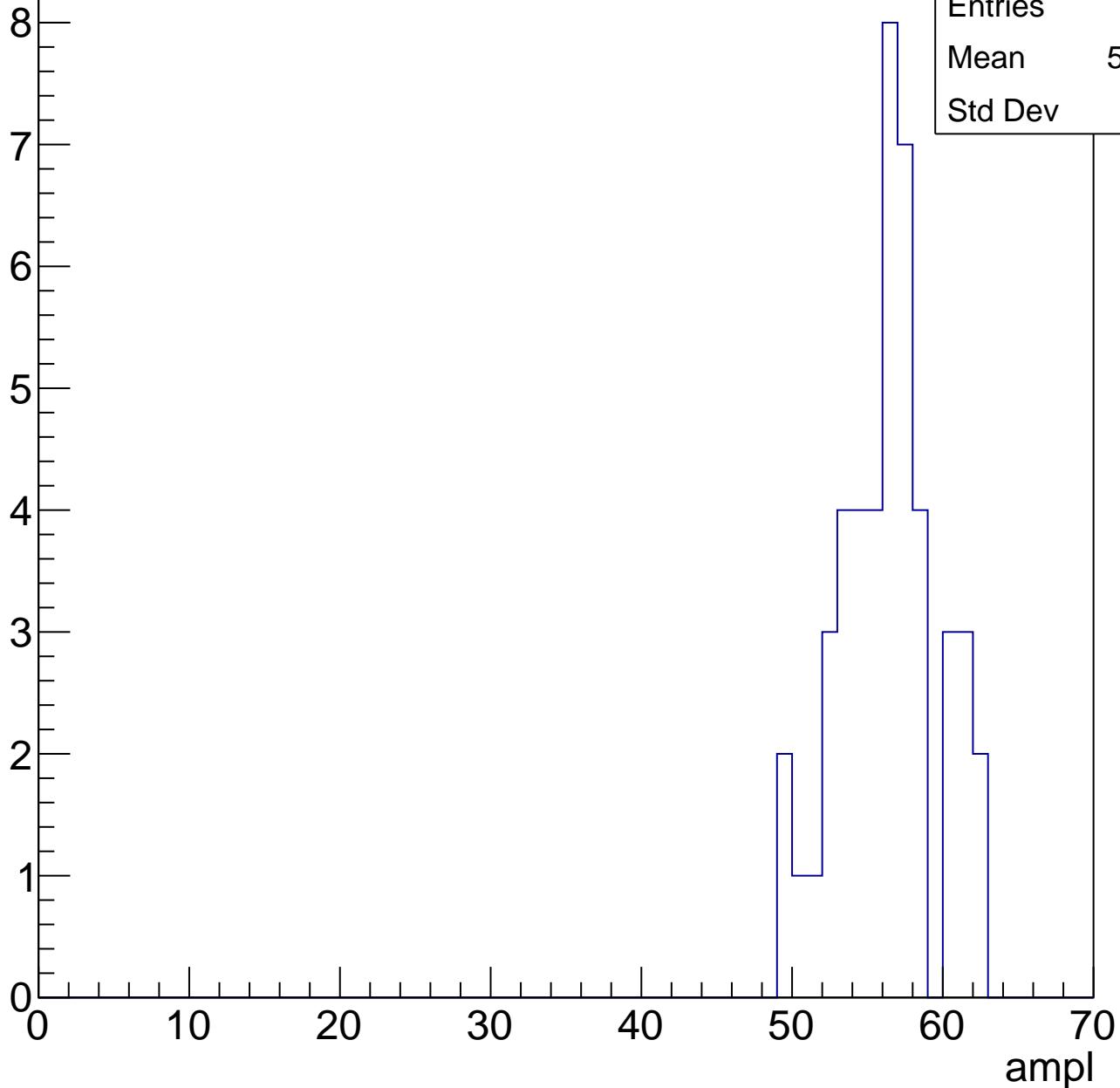
Entries	72
Mean	50.86
Std Dev	3.645



B1L101S, U23-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

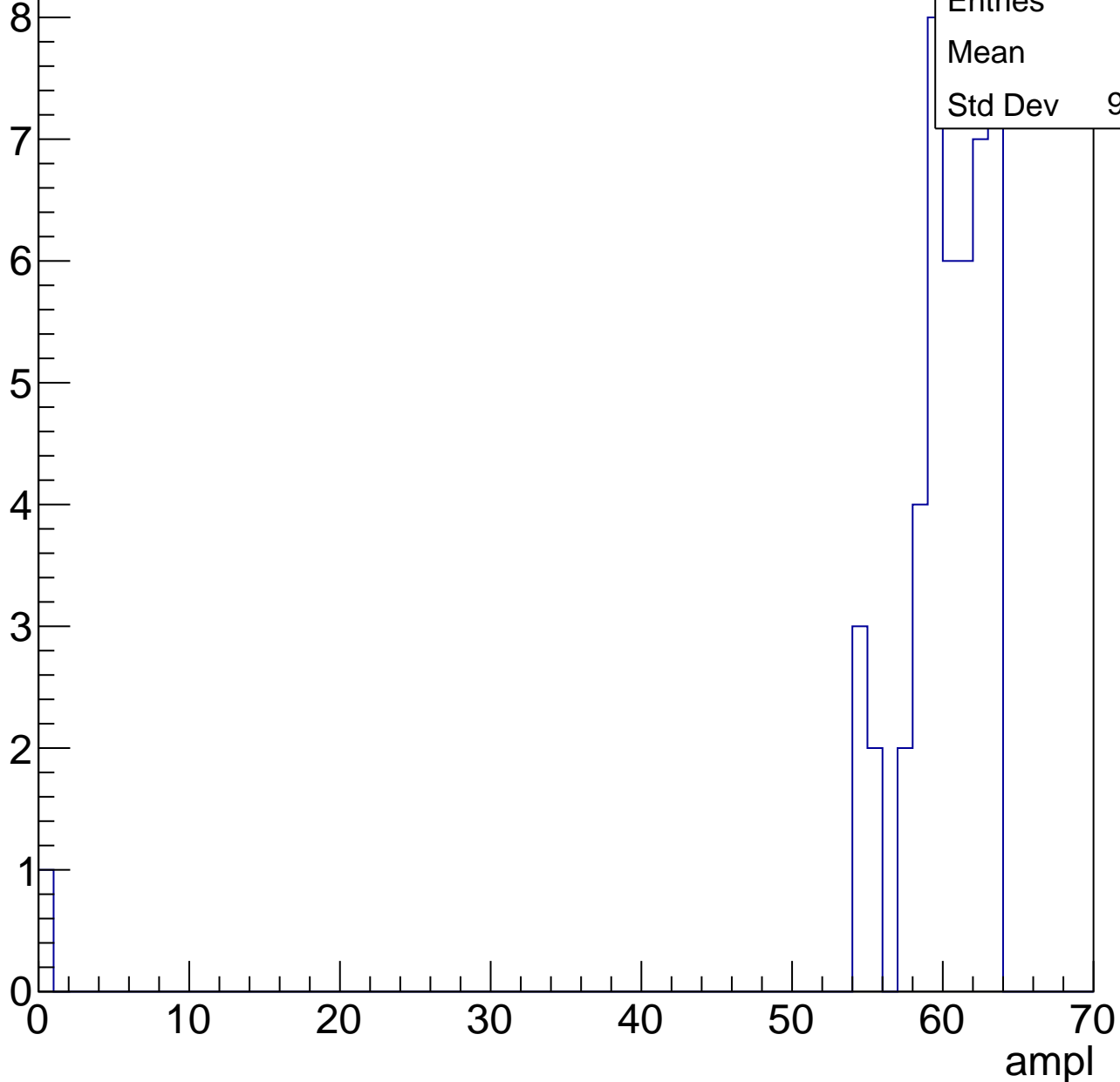


Entries	46
Mean	55.85
Std Dev	3.23

B1L101S, U23-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch78, adc0

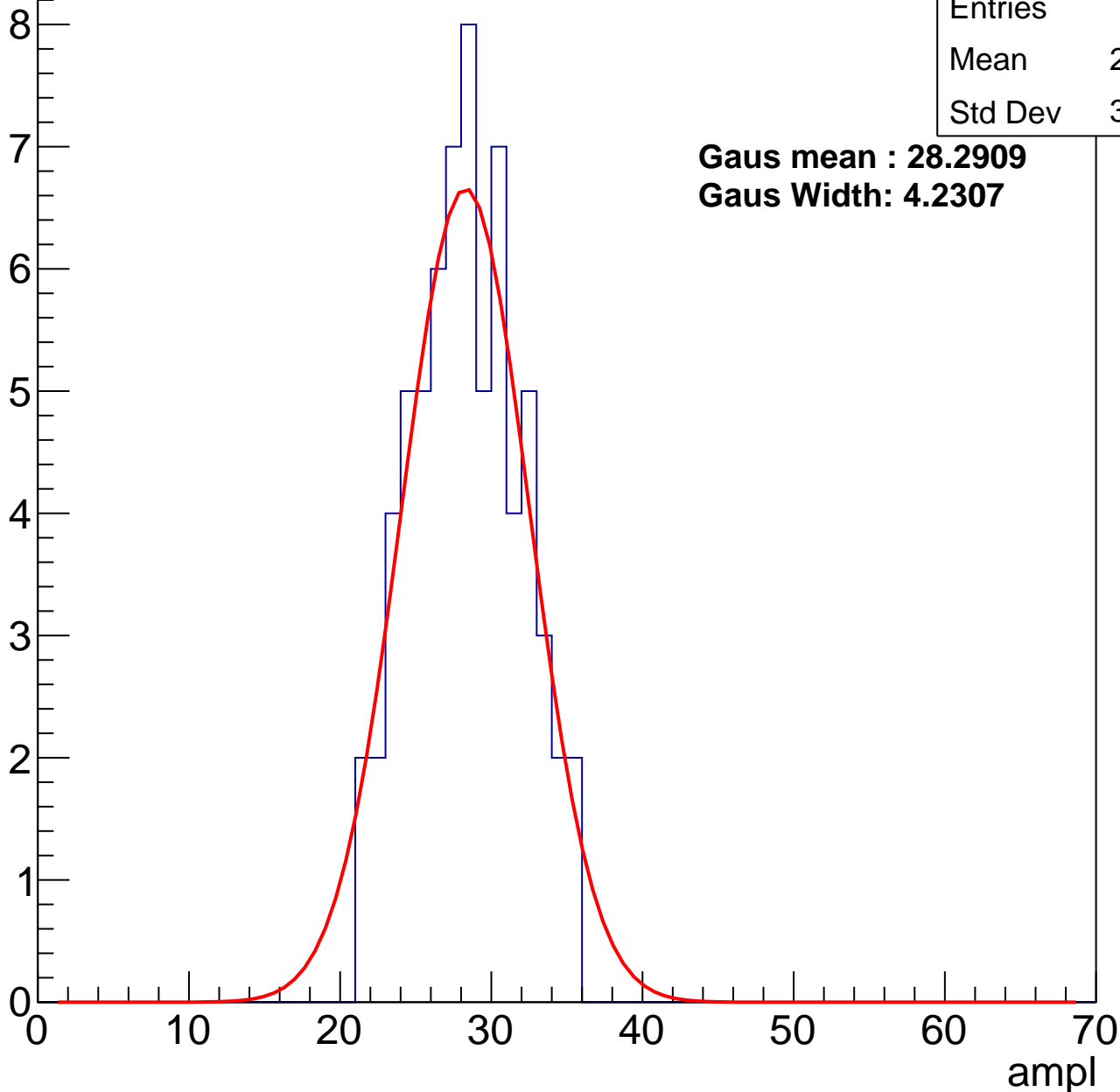
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	27.88
Std Dev	3.496

Gaus mean : 28.2909

Gaus Width: 4.2307



B1L101S, U23-ch78, adc1

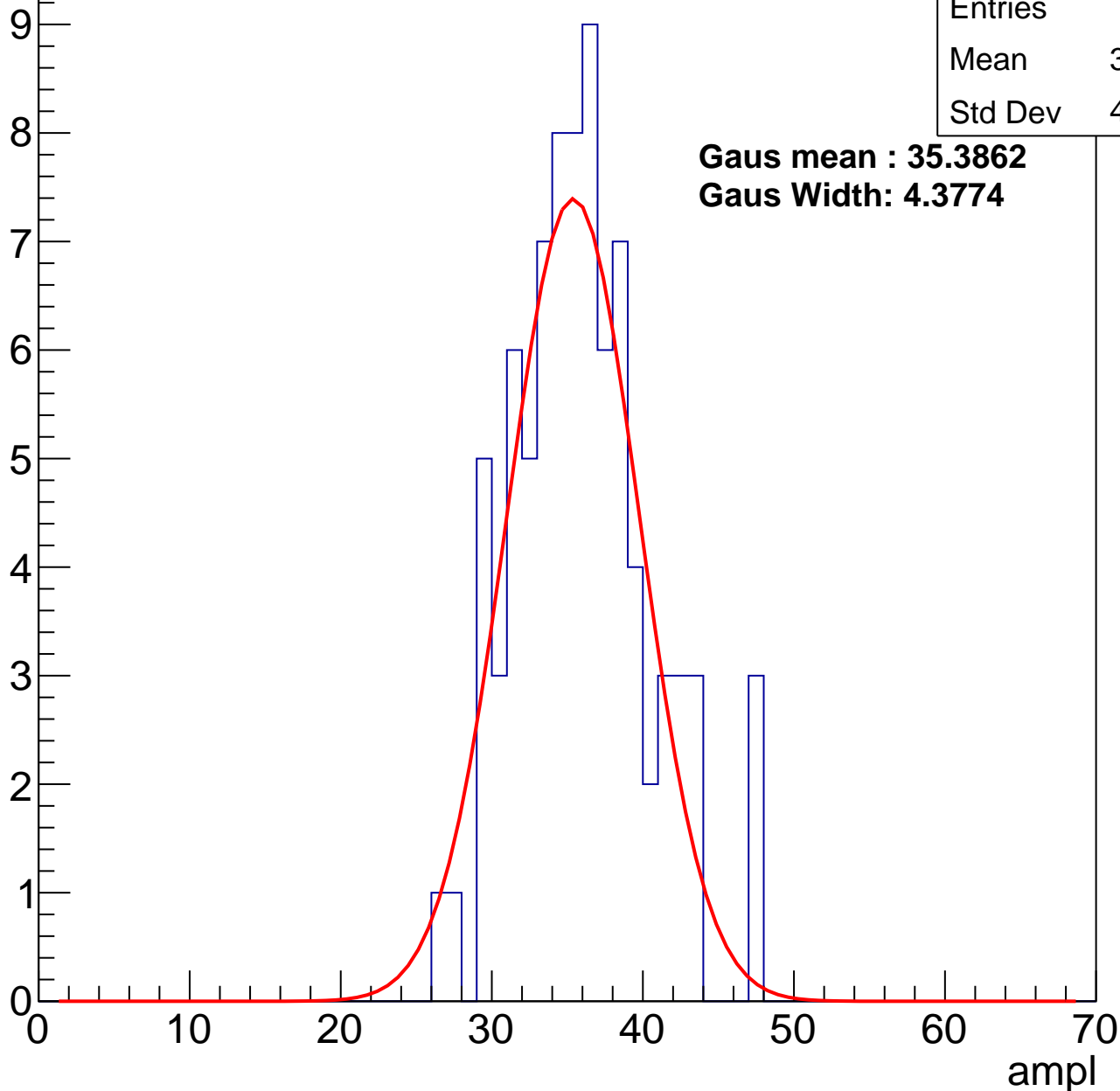
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	35.52
Std Dev	4.423

Gaus mean : 35.3862

Gaus Width: 4.3774



B1L101S, U23-ch78, adc2

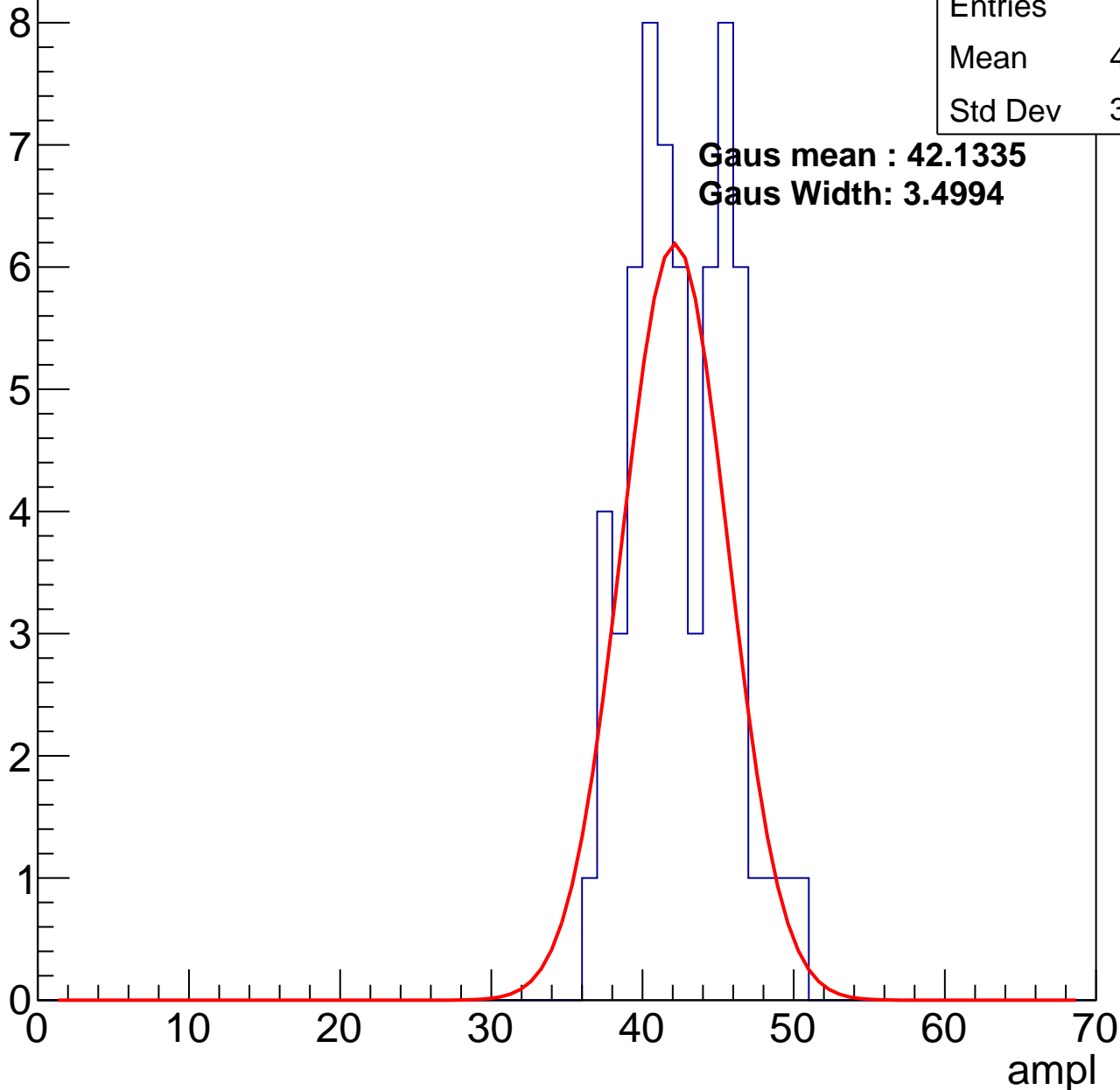
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.16
Std Dev	3.224

Gaus mean : 42.1335

Gaus Width: 3.4994

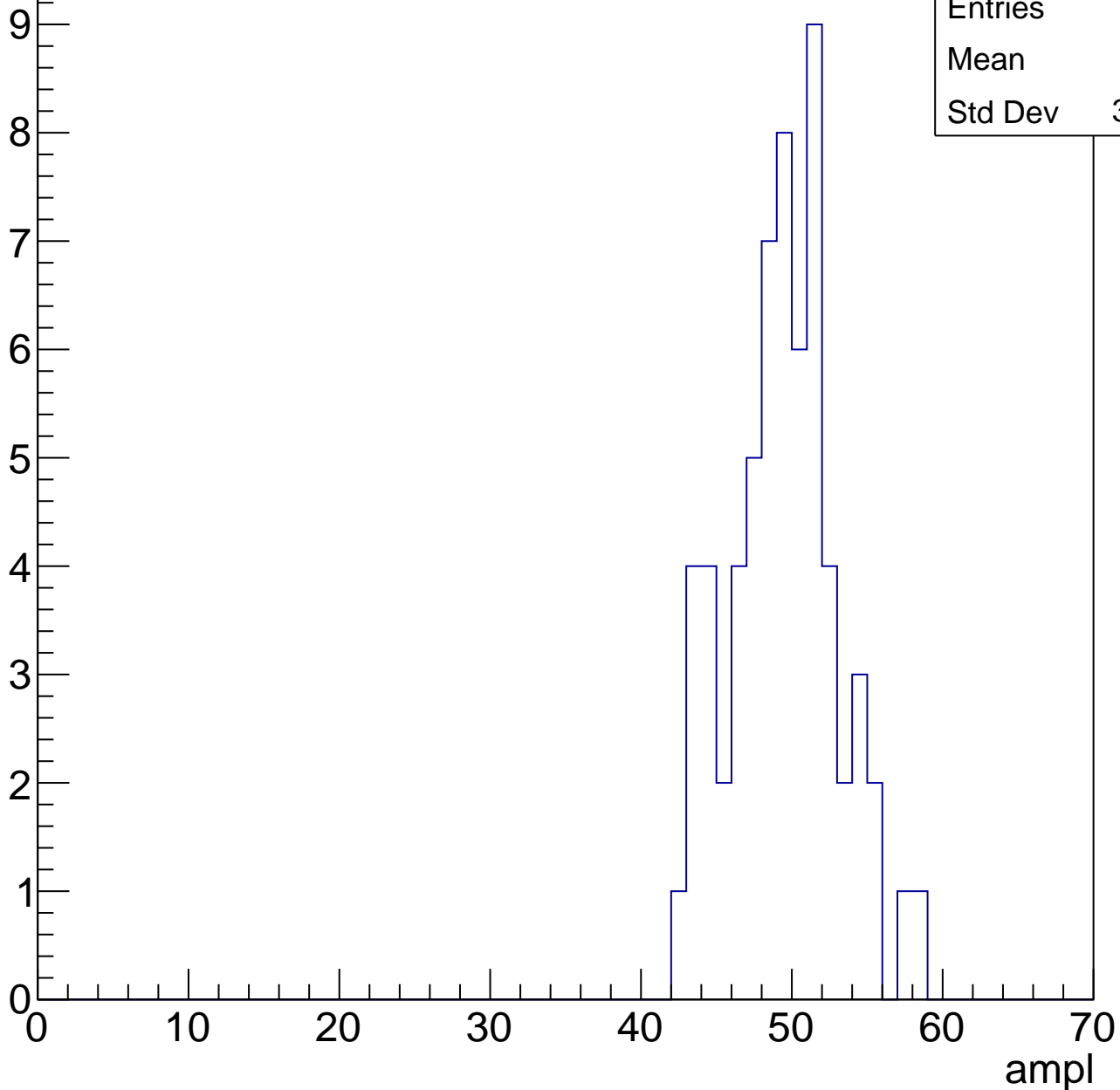


B1L101S, U23-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49
Std Dev	3.541

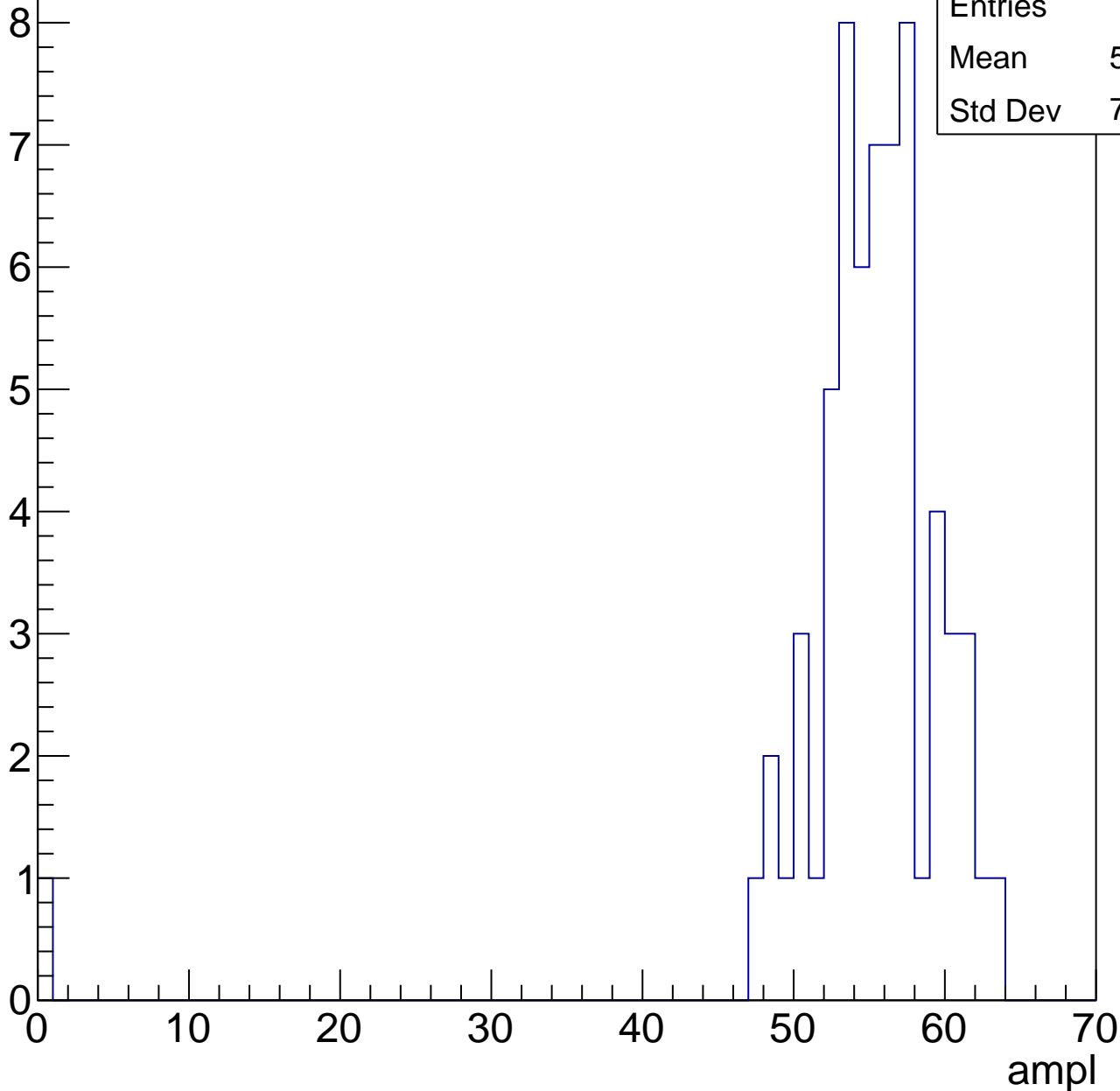


B1L101S, U23-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

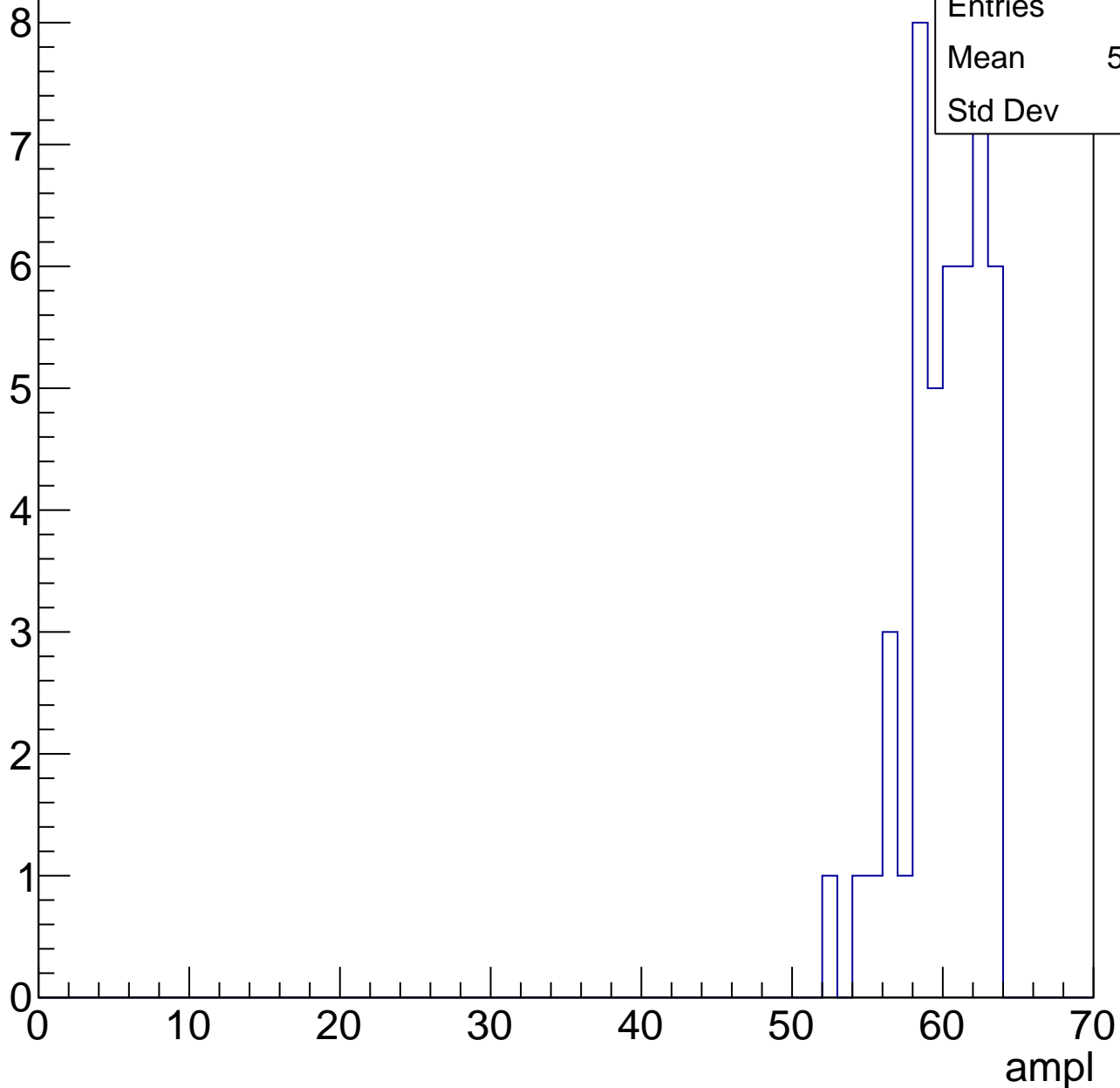
Entries	63
Mean	54.22
Std Dev	7.733



B1L101S, U23-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

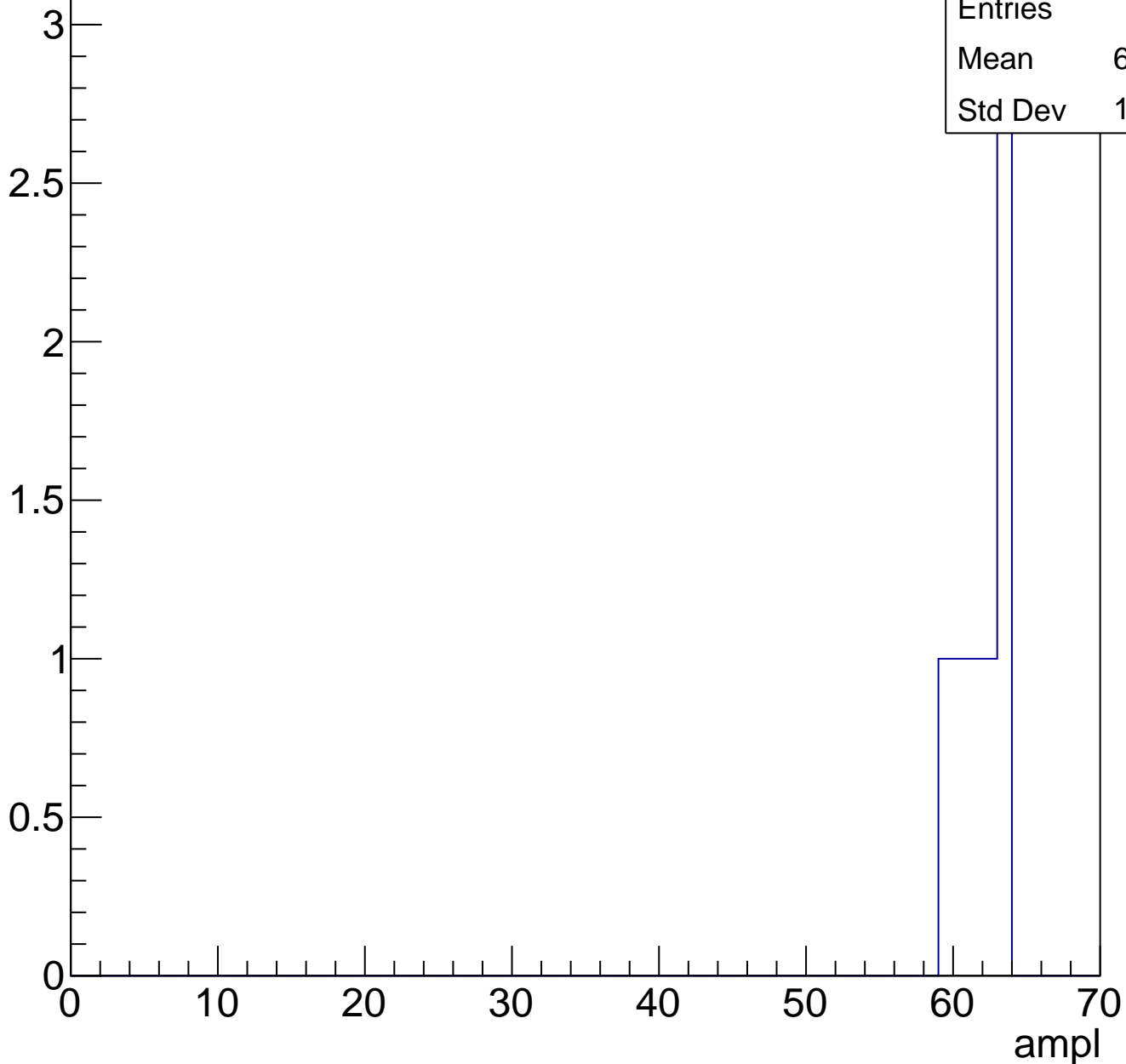
Entry



B1L101S, U23-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch79, adc0

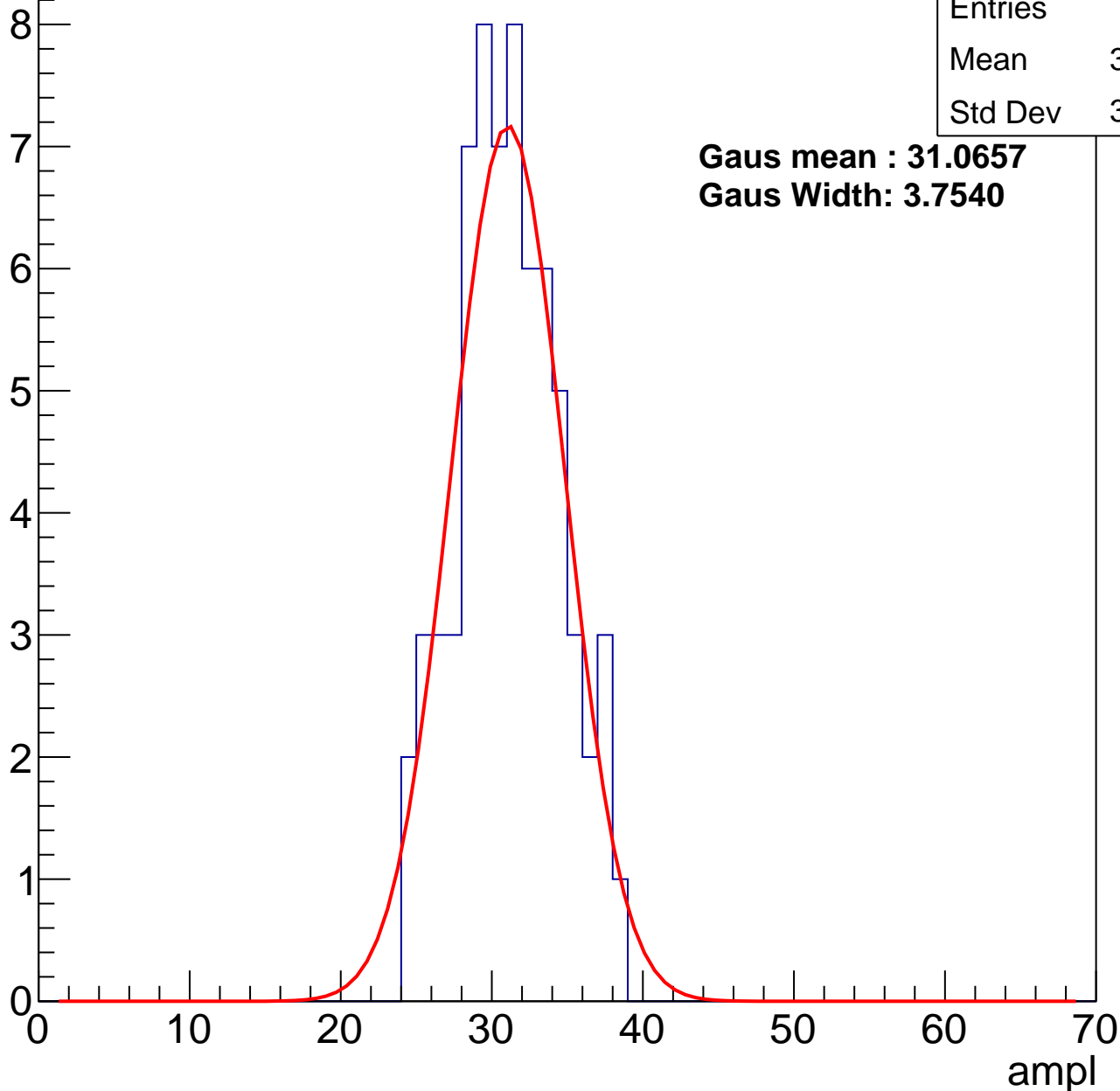
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.66
Std Dev	3.353

Gaus mean : 31.0657

Gaus Width: 3.7540



B1L101S, U23-ch79, adc1

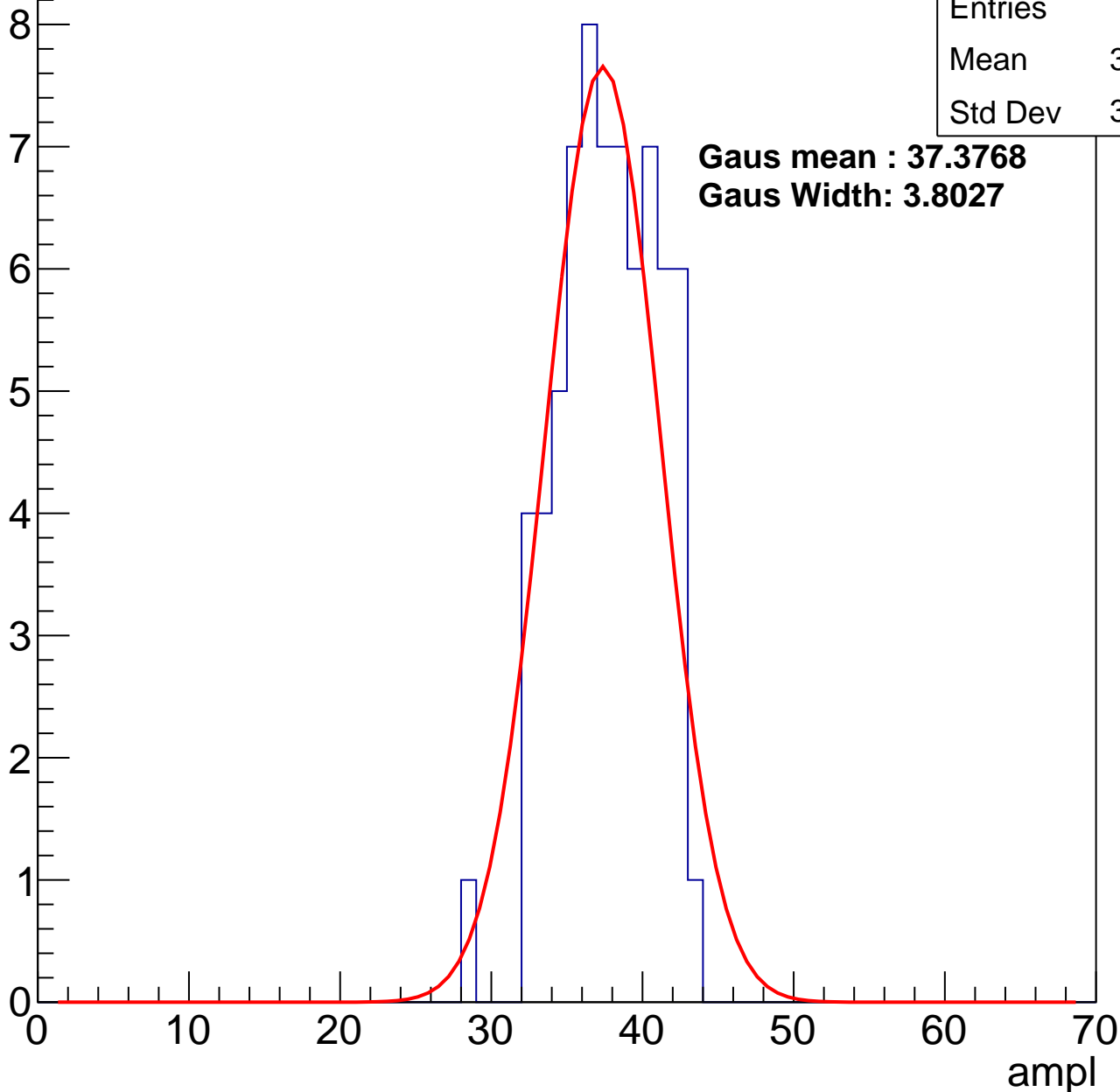
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.26
Std Dev	3.179

Gaus mean : 37.3768

Gaus Width: 3.8027



B1L101S, U23-ch79, adc2

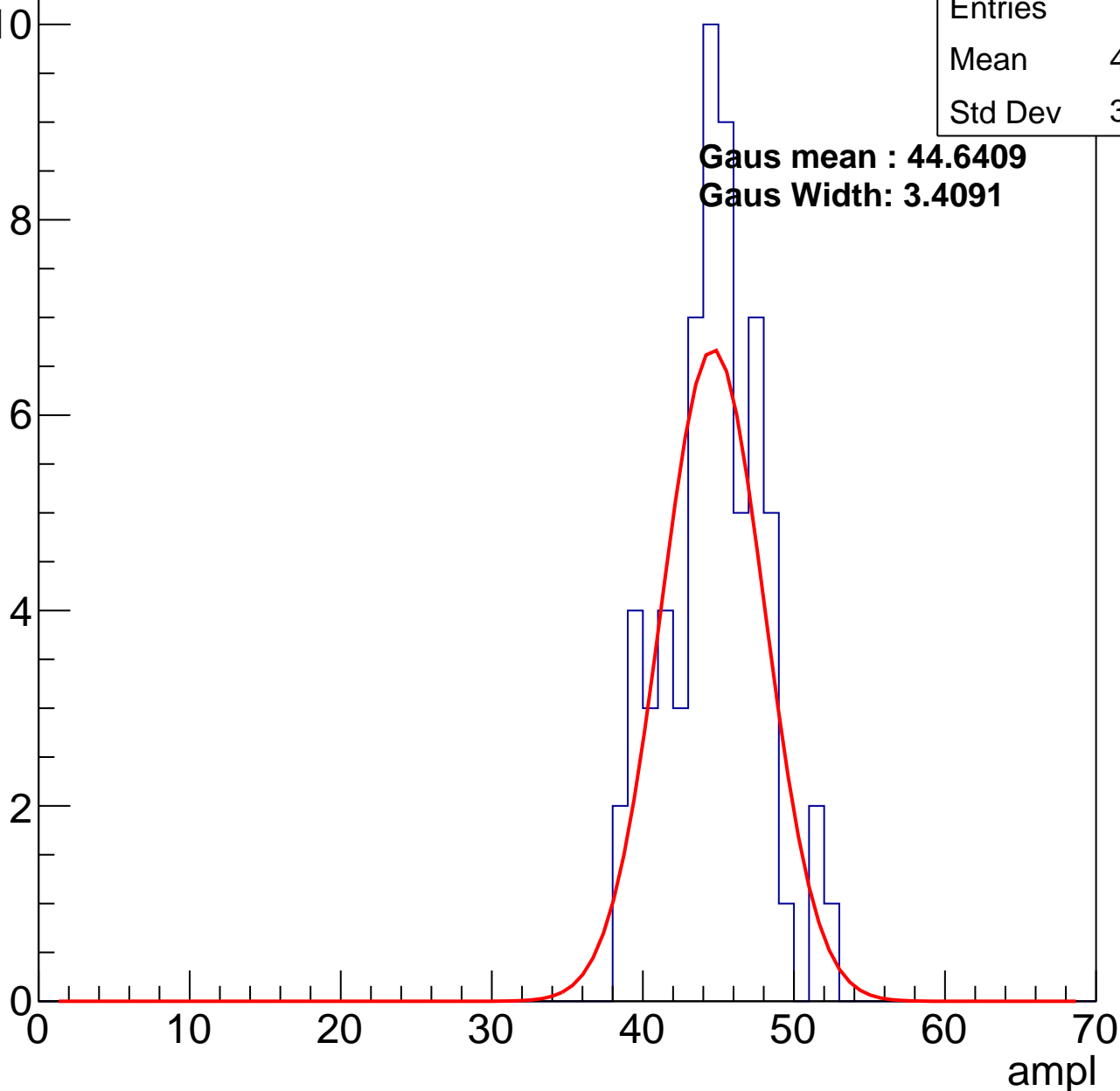
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.29
Std Dev	3.159

Gaus mean : 44.6409

Gaus Width: 3.4091

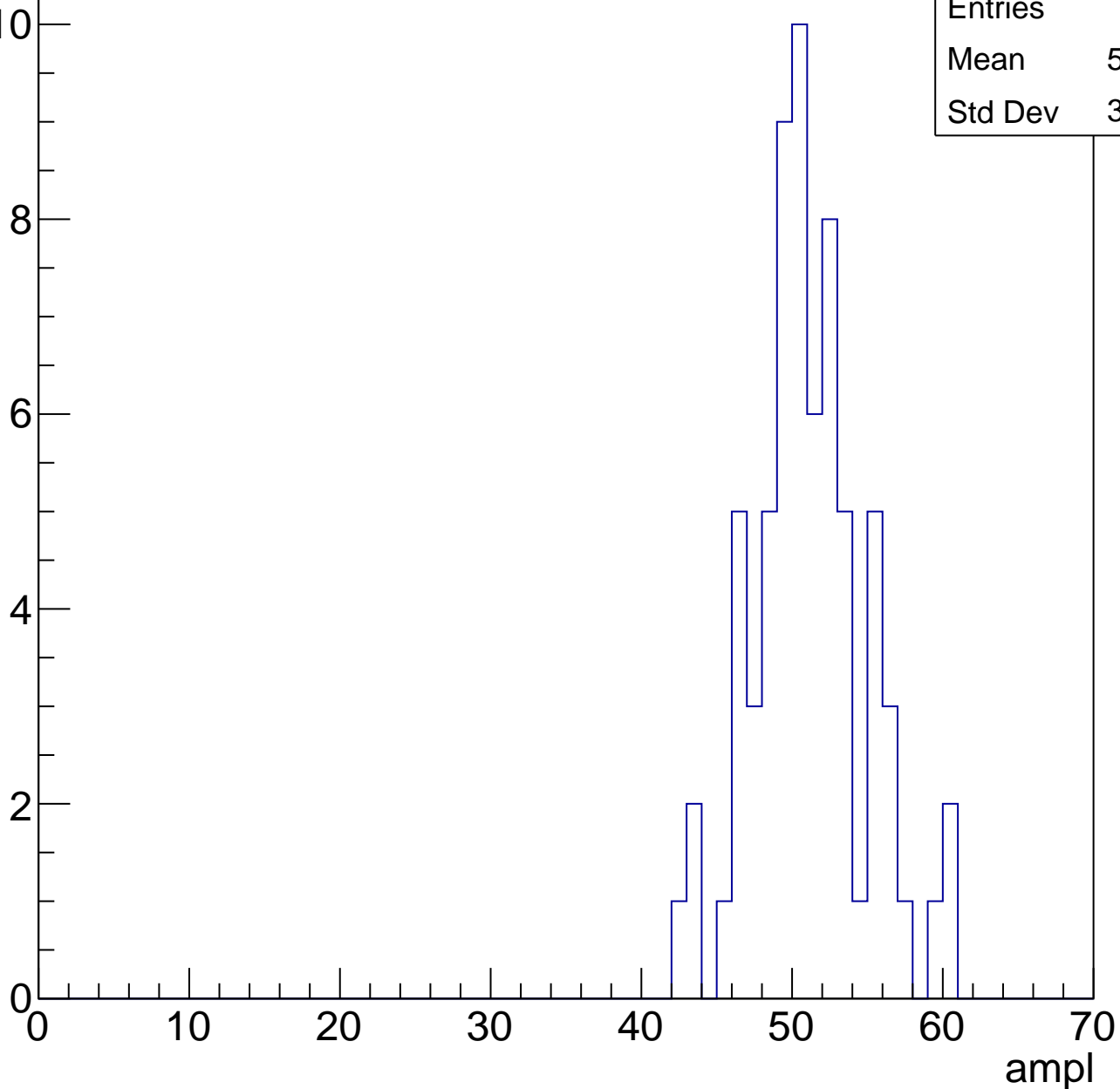


B1L101S, U23-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	50.66
Std Dev	3.748

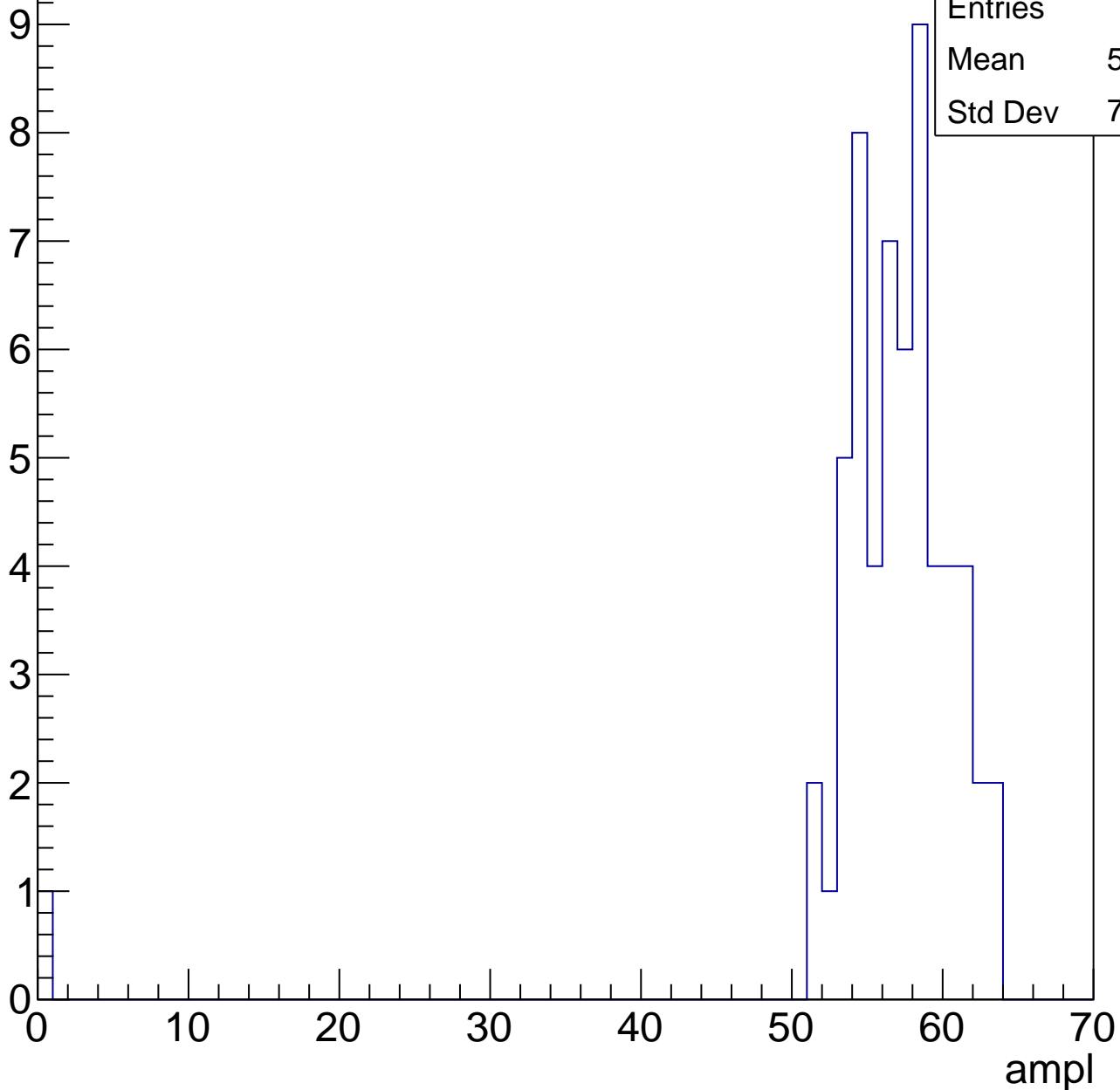


B1L101S, U23-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.88
Std Dev	7.913

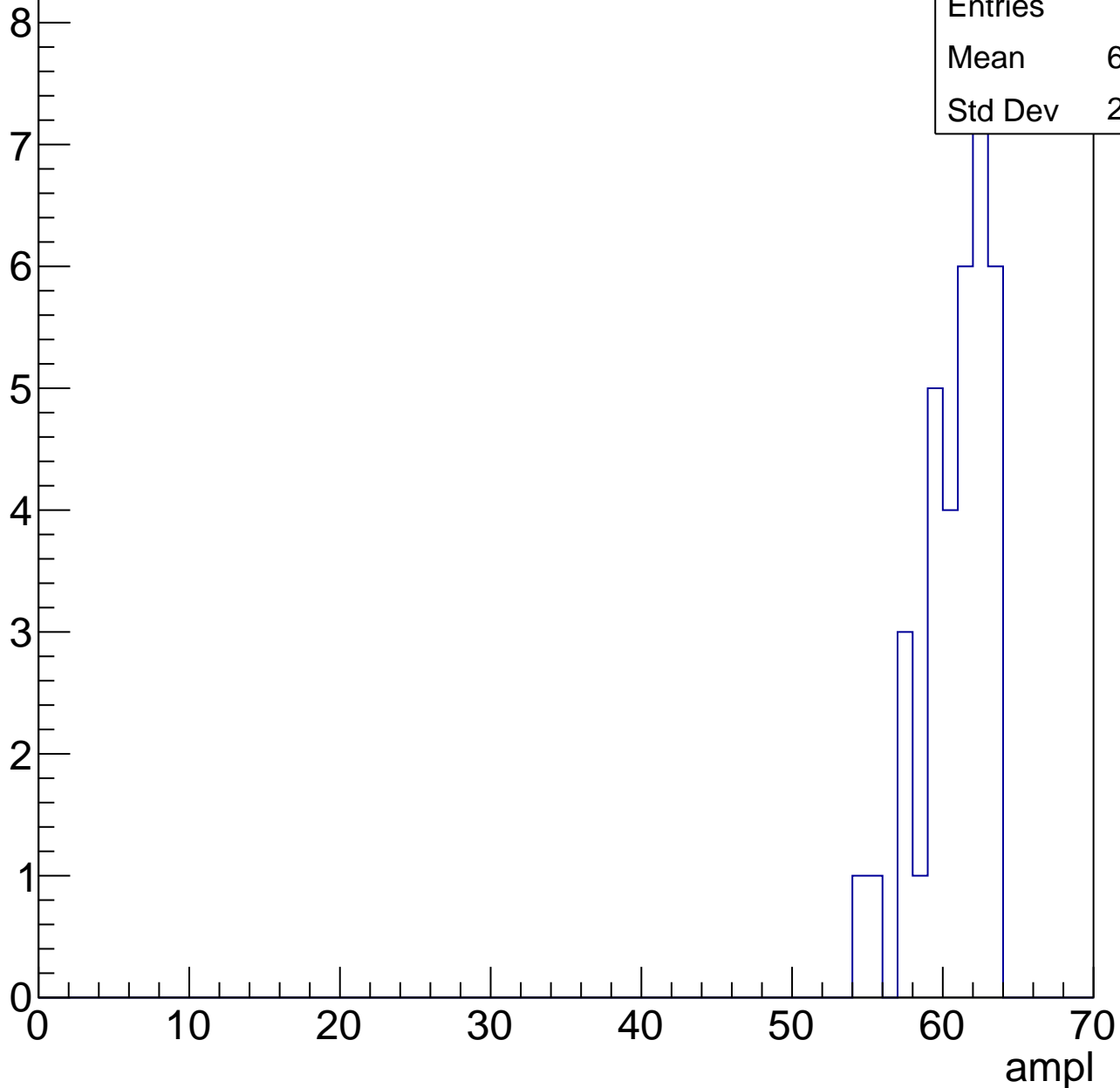


B1L101S, U23-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	60.37
Std Dev	2.294



B1L101S, U23-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L101S, U23-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch80, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	29.05
Std Dev	4.489

Gaus mean : 30.0351

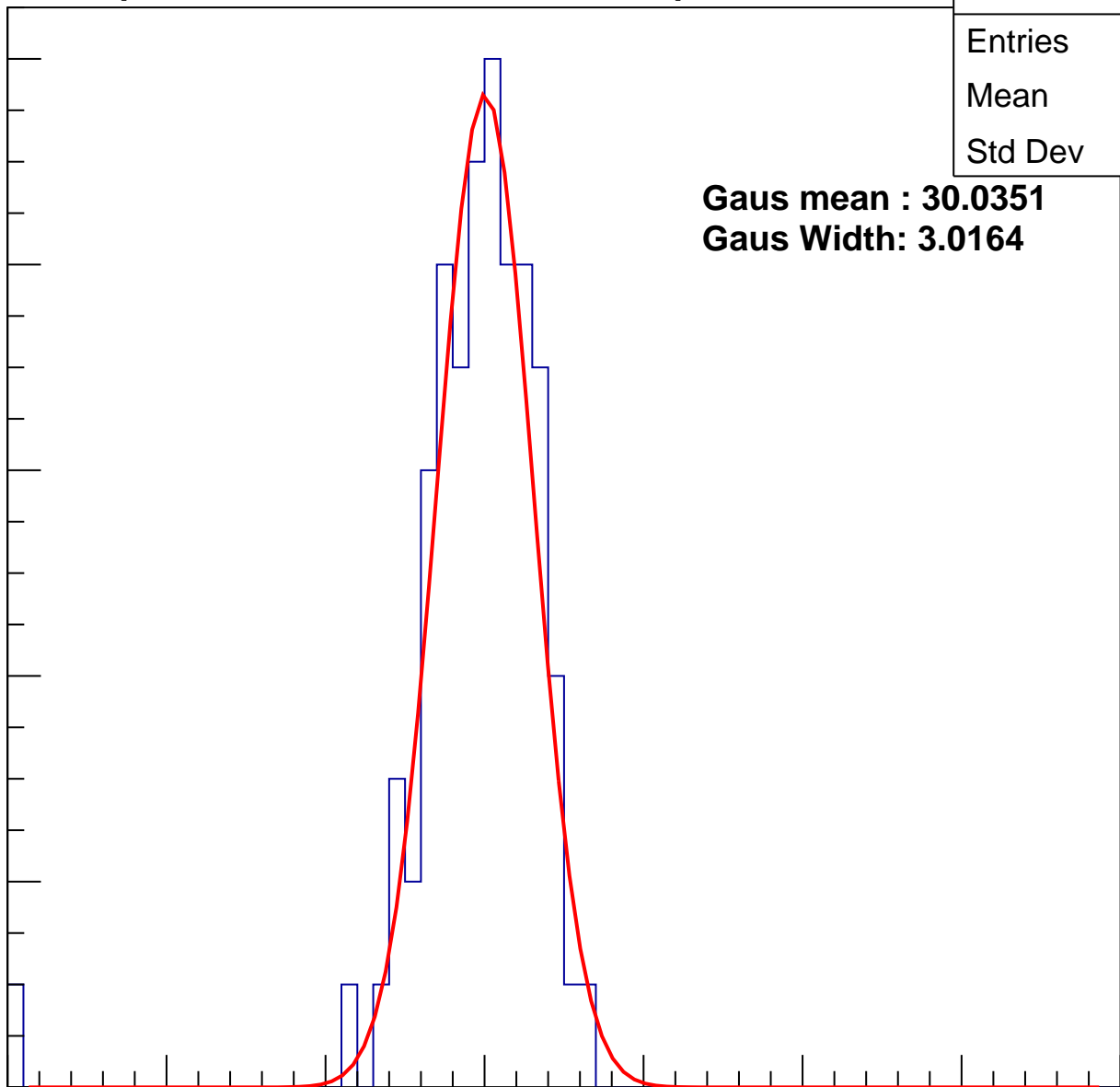
Gaus Width: 3.0164

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch80, adc1

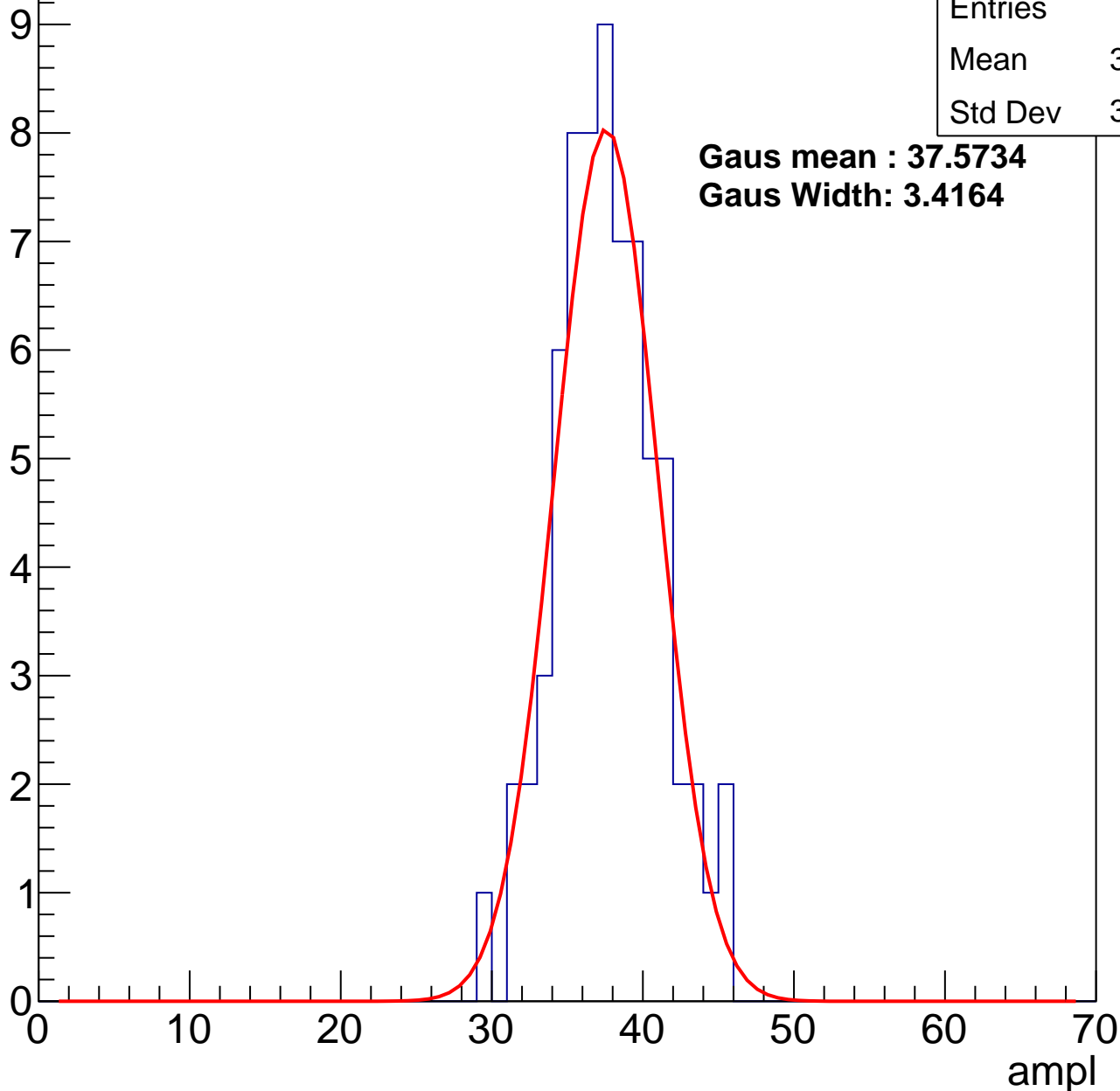
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.24
Std Dev	3.344

Gaus mean : 37.5734

Gaus Width: 3.4164



B1L101S, U23-ch80, adc2

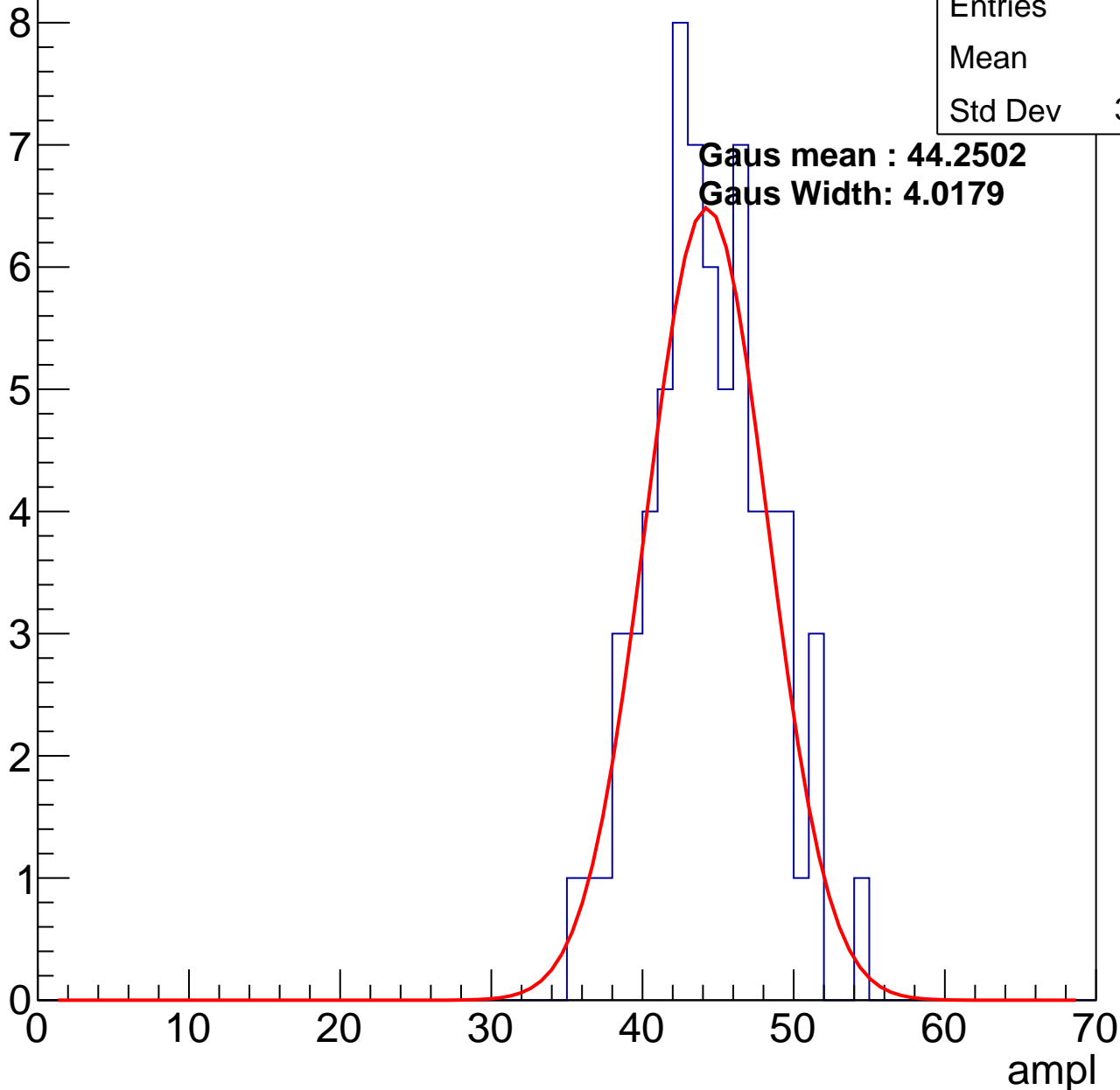
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.9
Std Dev	3.911

Gaus mean : 44.2502

Gaus Width: 4.0179

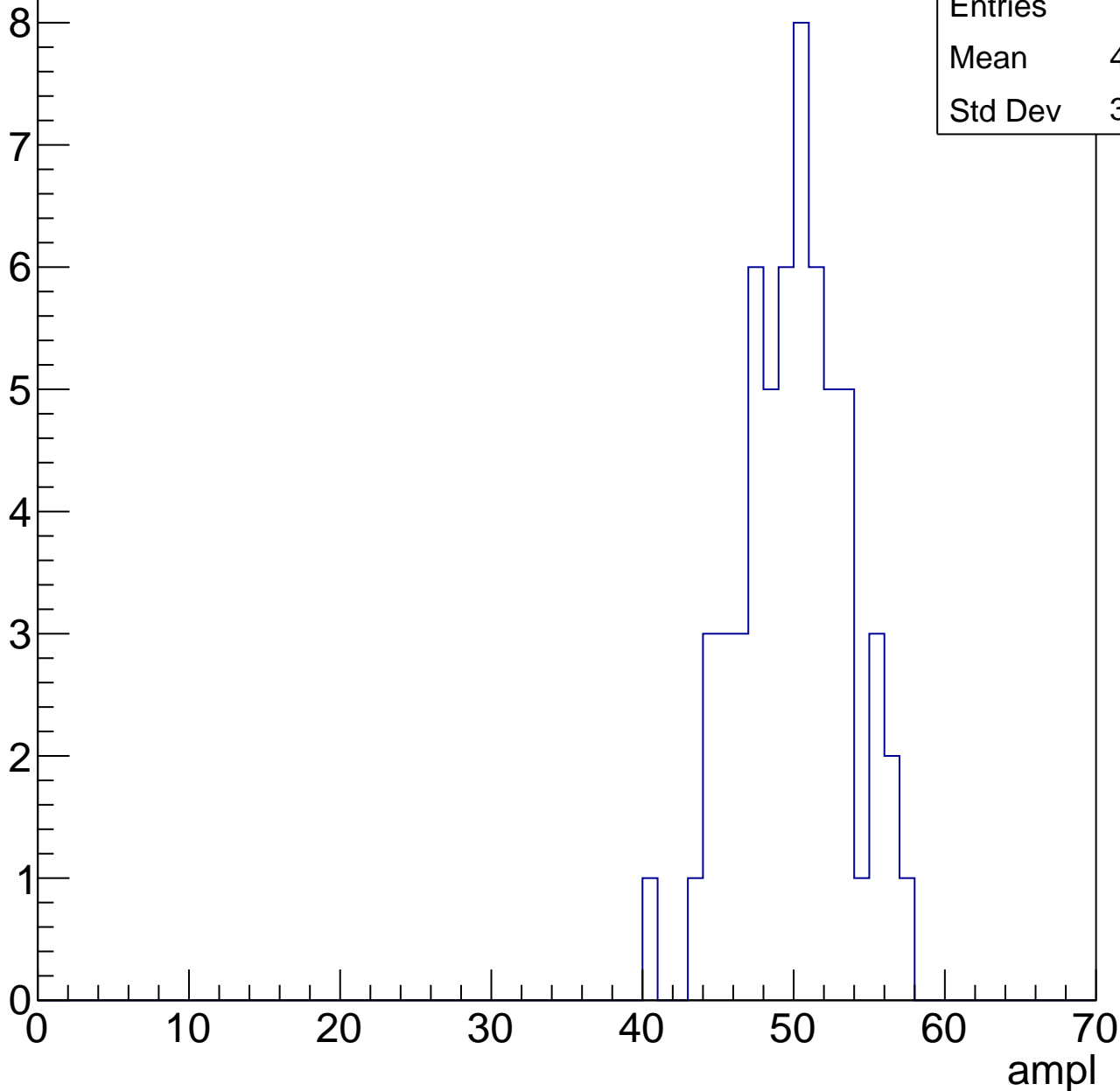


B1L101S, U23-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.54
Std Dev	3.519

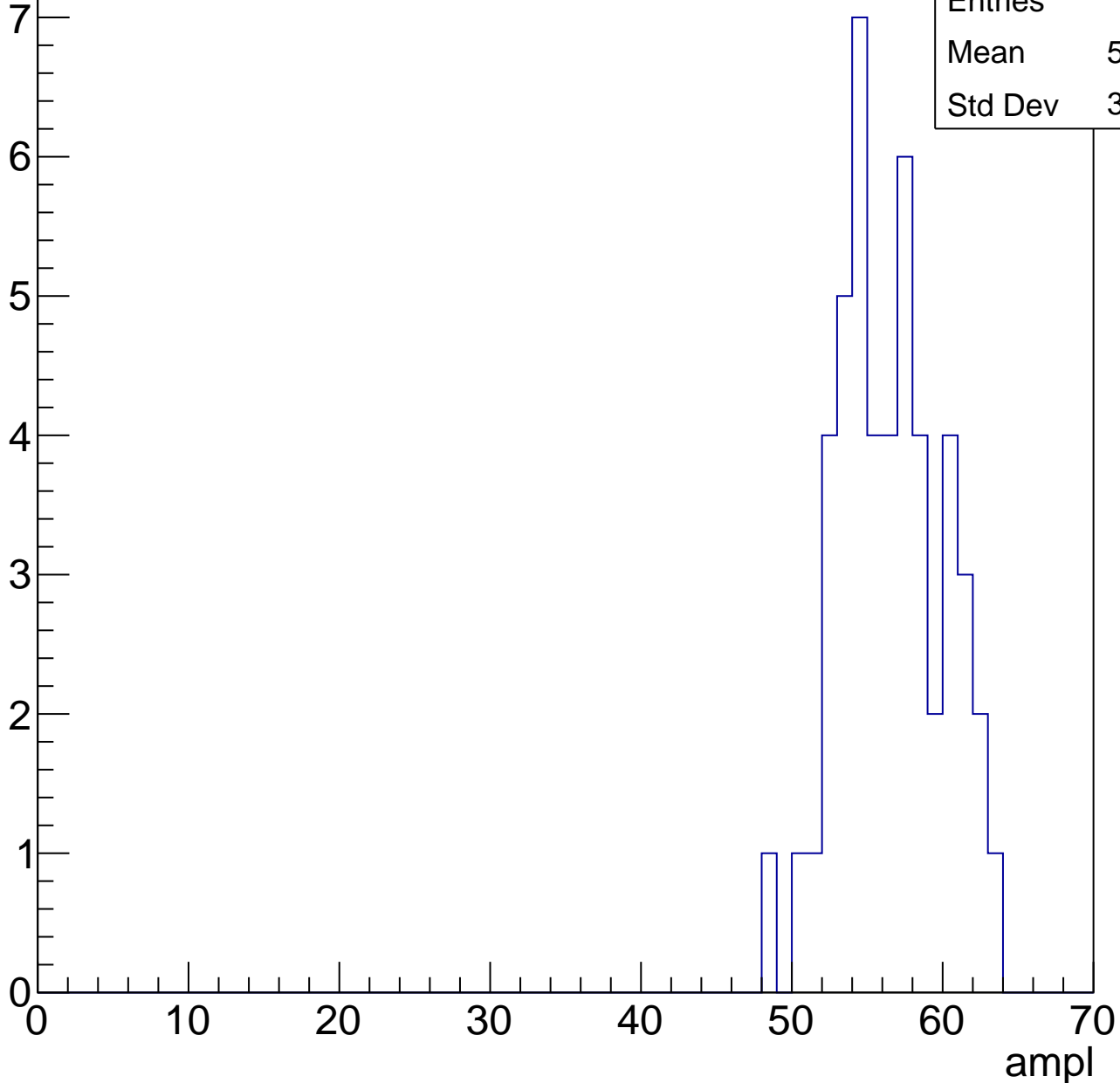


B1L101S, U23-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.04
Std Dev	3.398



B1L101S, U23-ch80, adc5

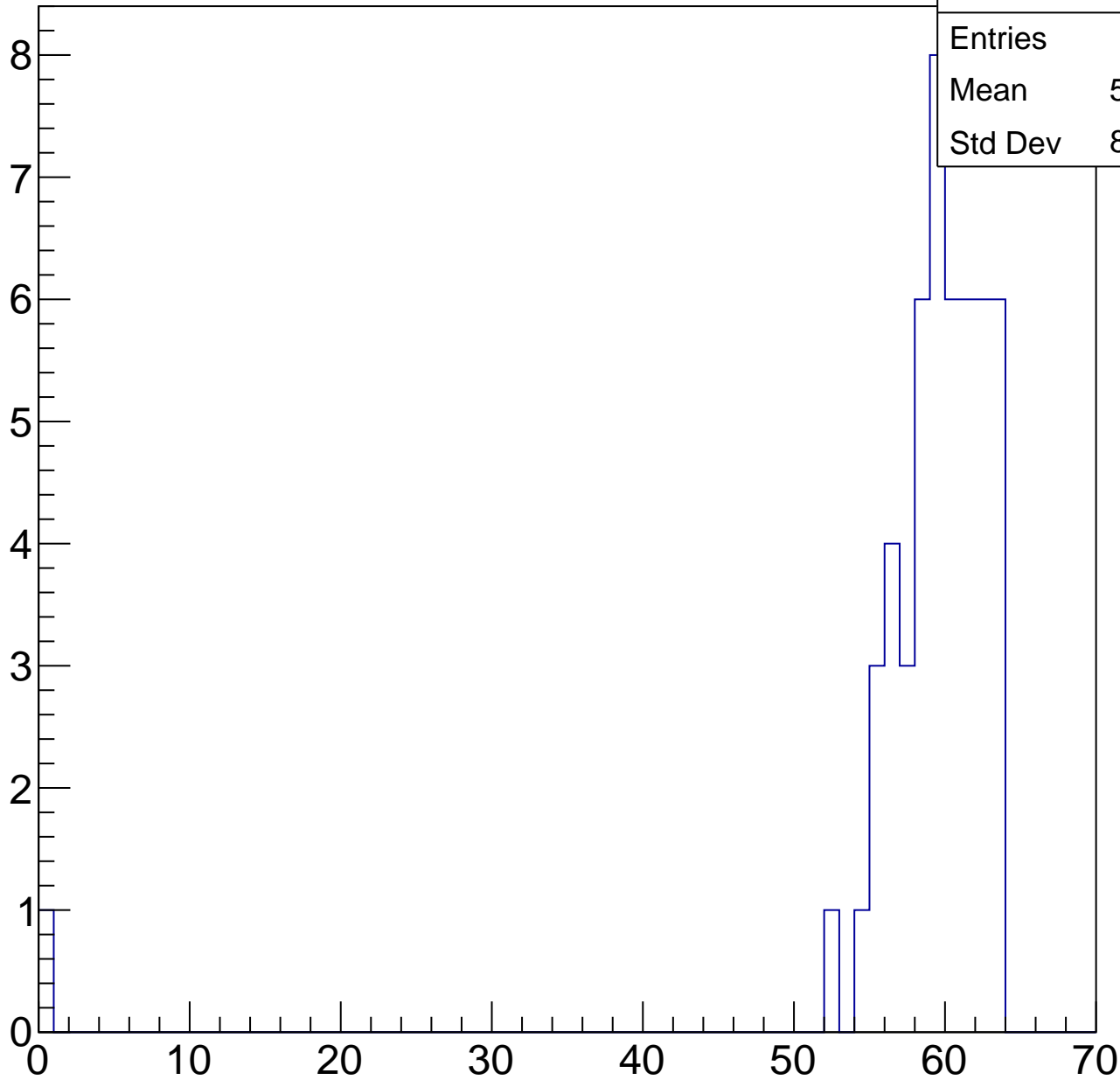
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.08
Std Dev	8.625

ampl



B1L101S, U23-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch81, adc0

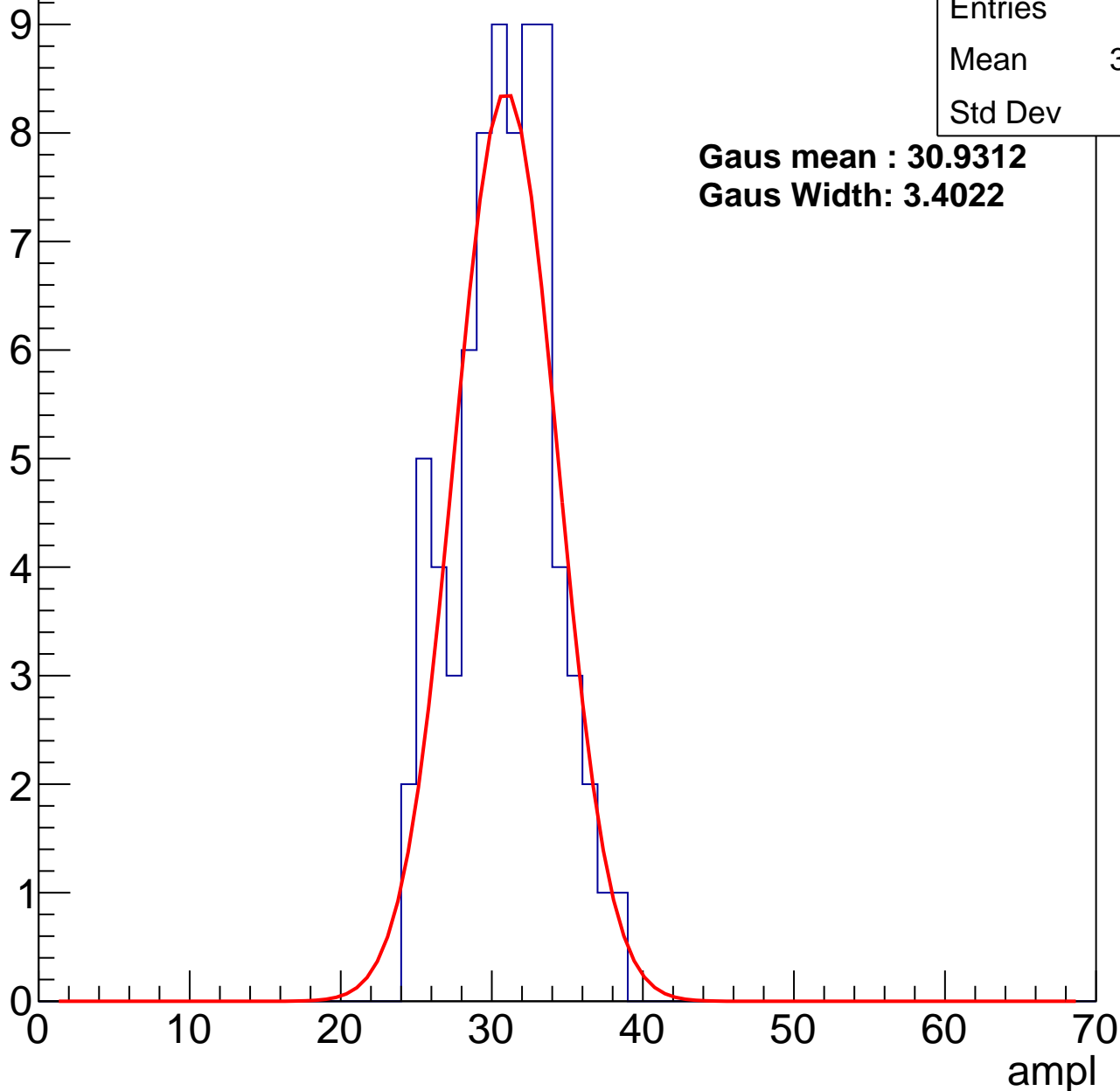
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.39
Std Dev	3.2

Gaus mean : 30.9312

Gaus Width: 3.4022



B1L101S, U23-ch81, adc1

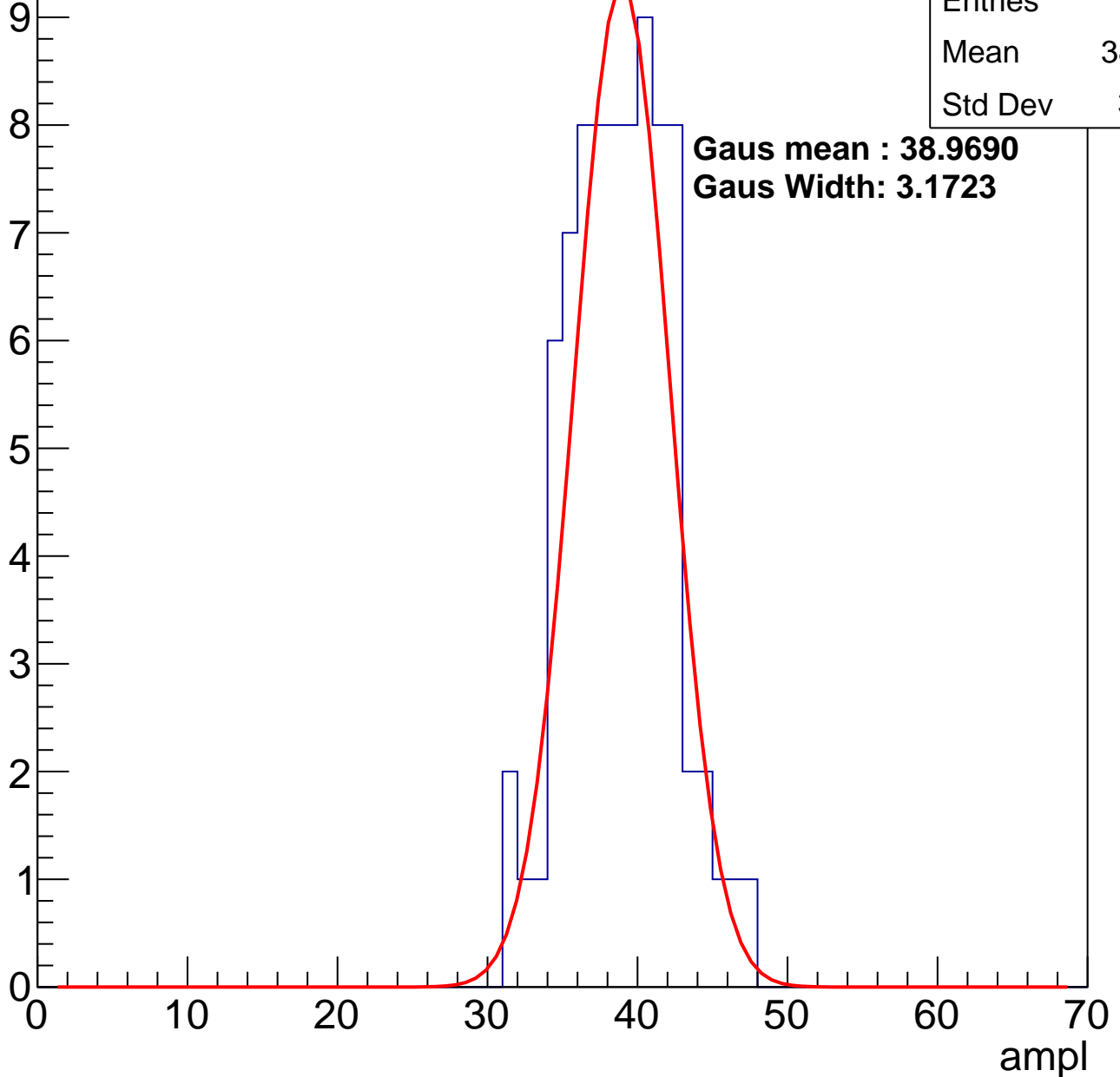
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	38.42
Std Dev	3.34

Gaus mean : 38.9690

Gaus Width: 3.1723



B1L101S, U23-ch81, adc2

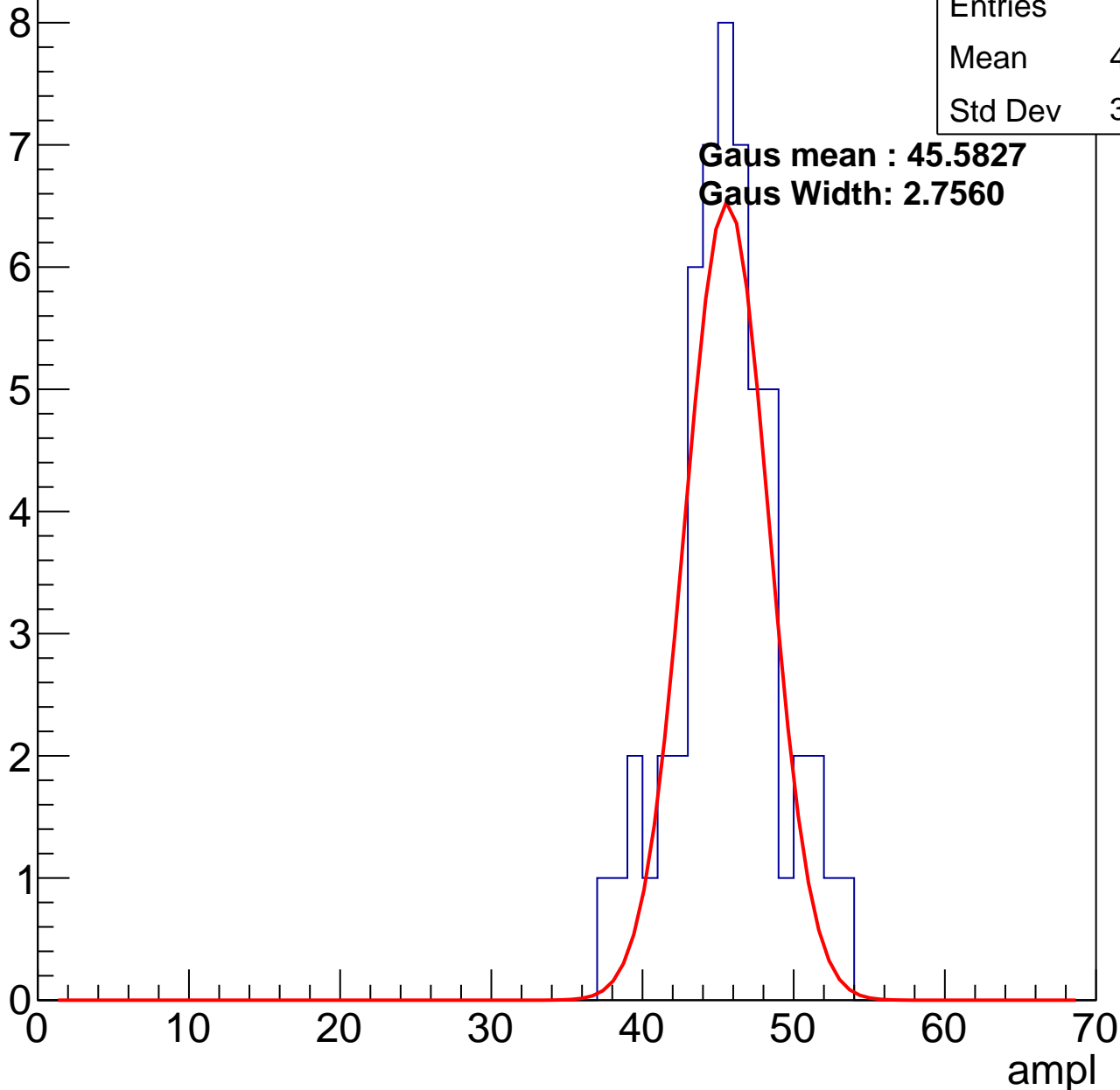
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	45.15
Std Dev	3.369

Gaus mean : 45.5827

Gaus Width: 2.7560

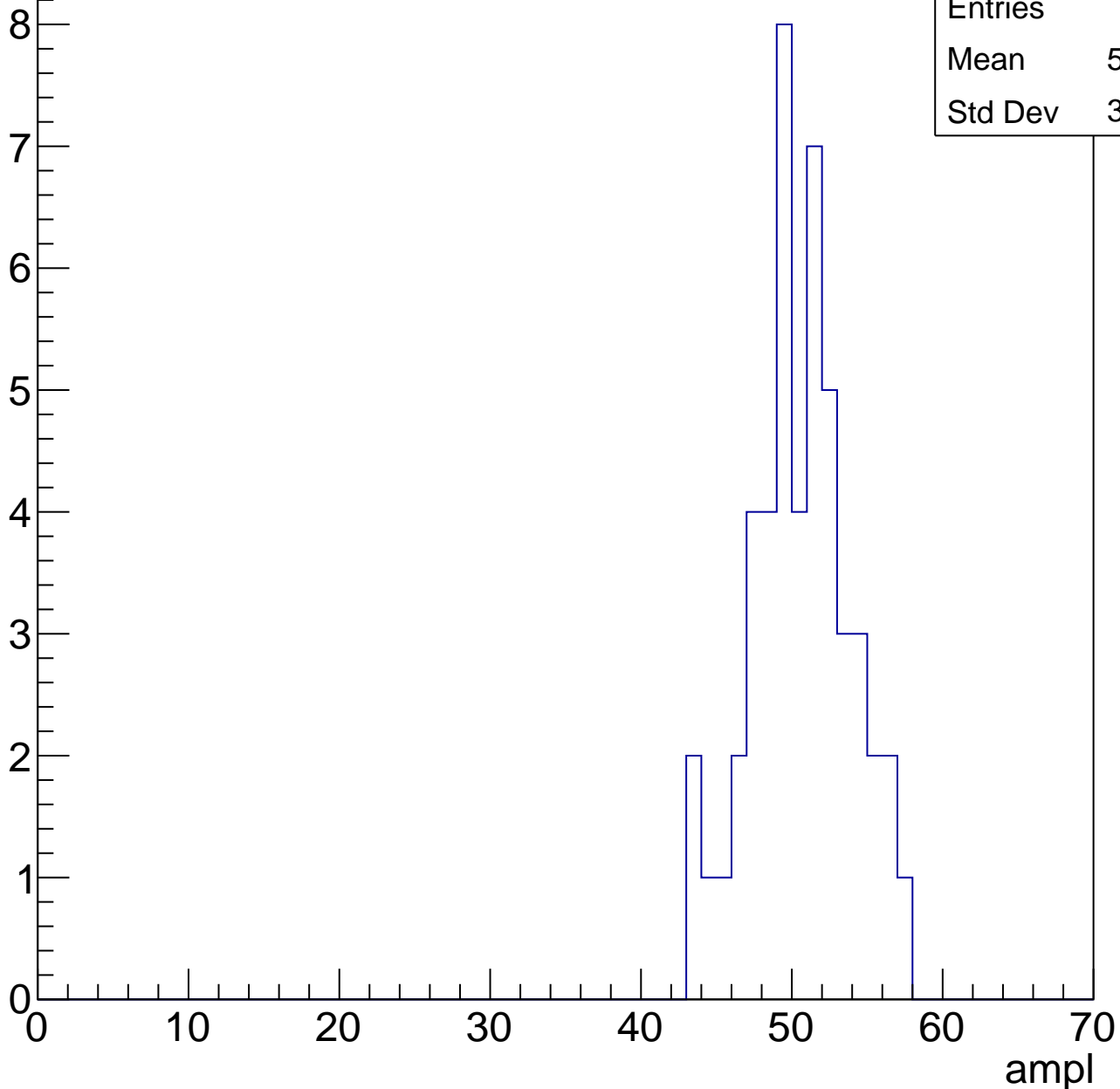


B1L101S, U23-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

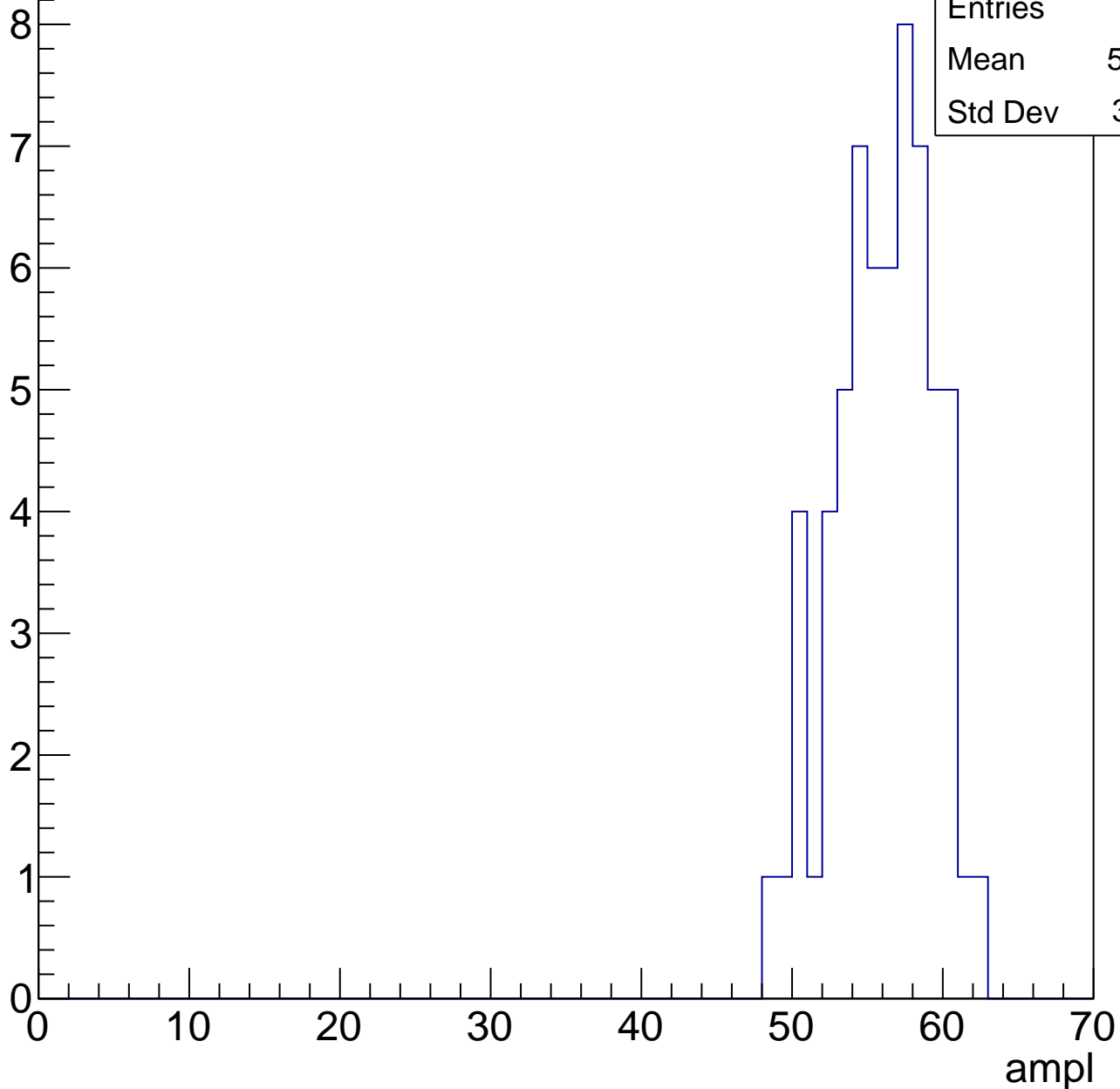
Entries	49
Mean	50.12
Std Dev	3.268



B1L101S, U23-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	62
Mean	55.56
Std Dev	3.201

B1L101S, U23-ch81, adc5

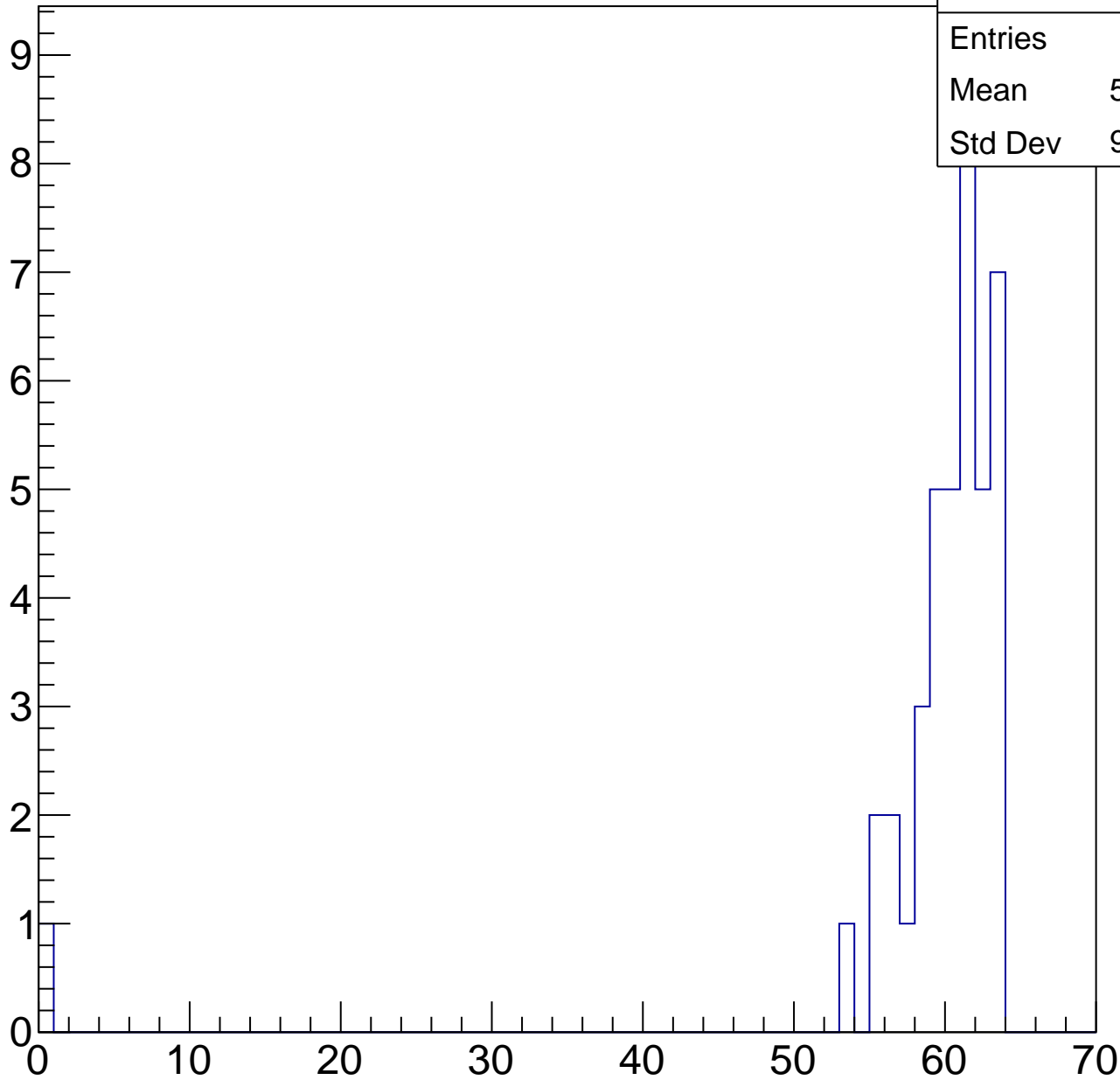
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.56
Std Dev	9.582

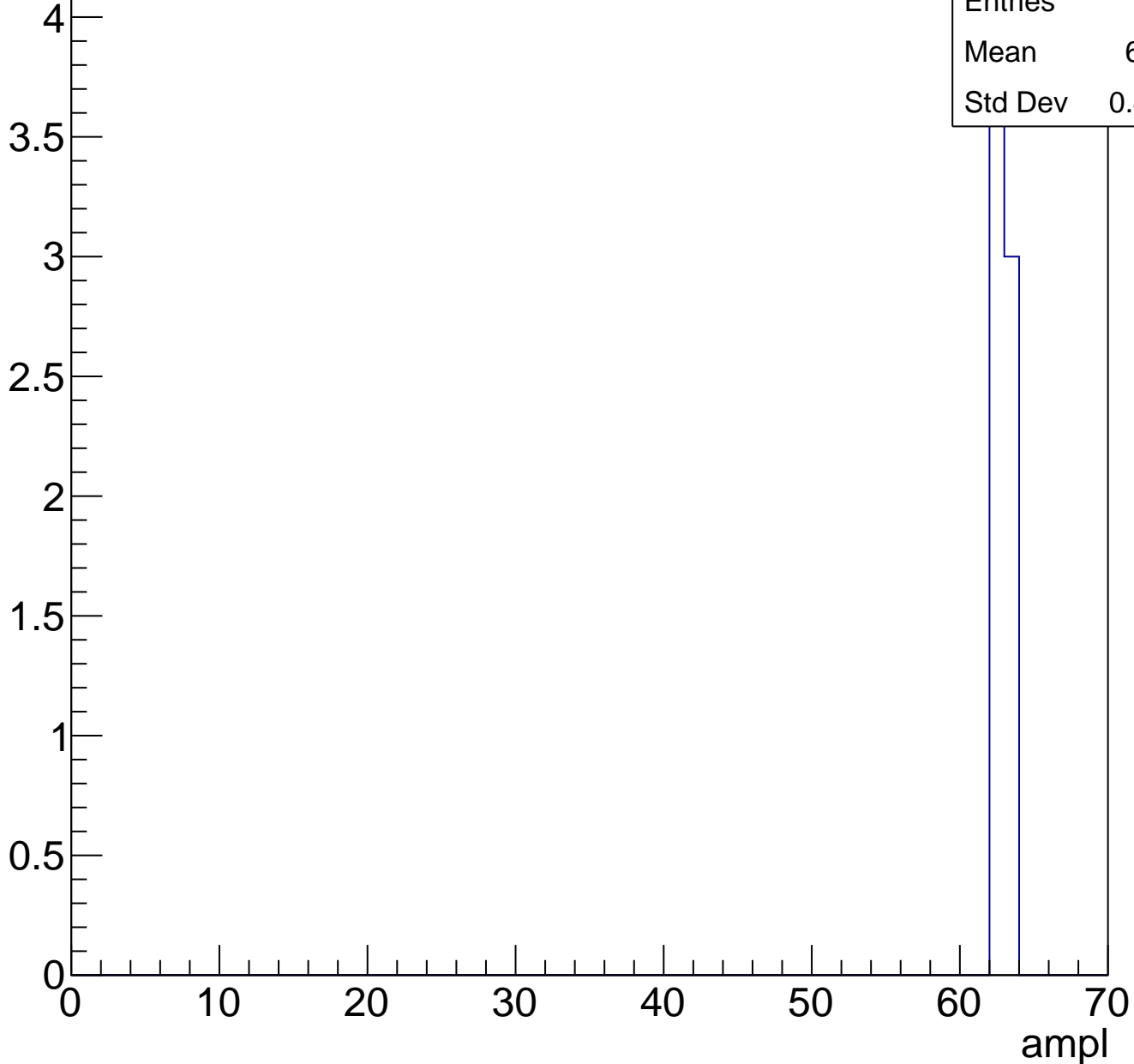
ampl



B1L101S, U23-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	7
Mean	62.43
Std Dev	0.4949

B1L101S, U23-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch82, adc0

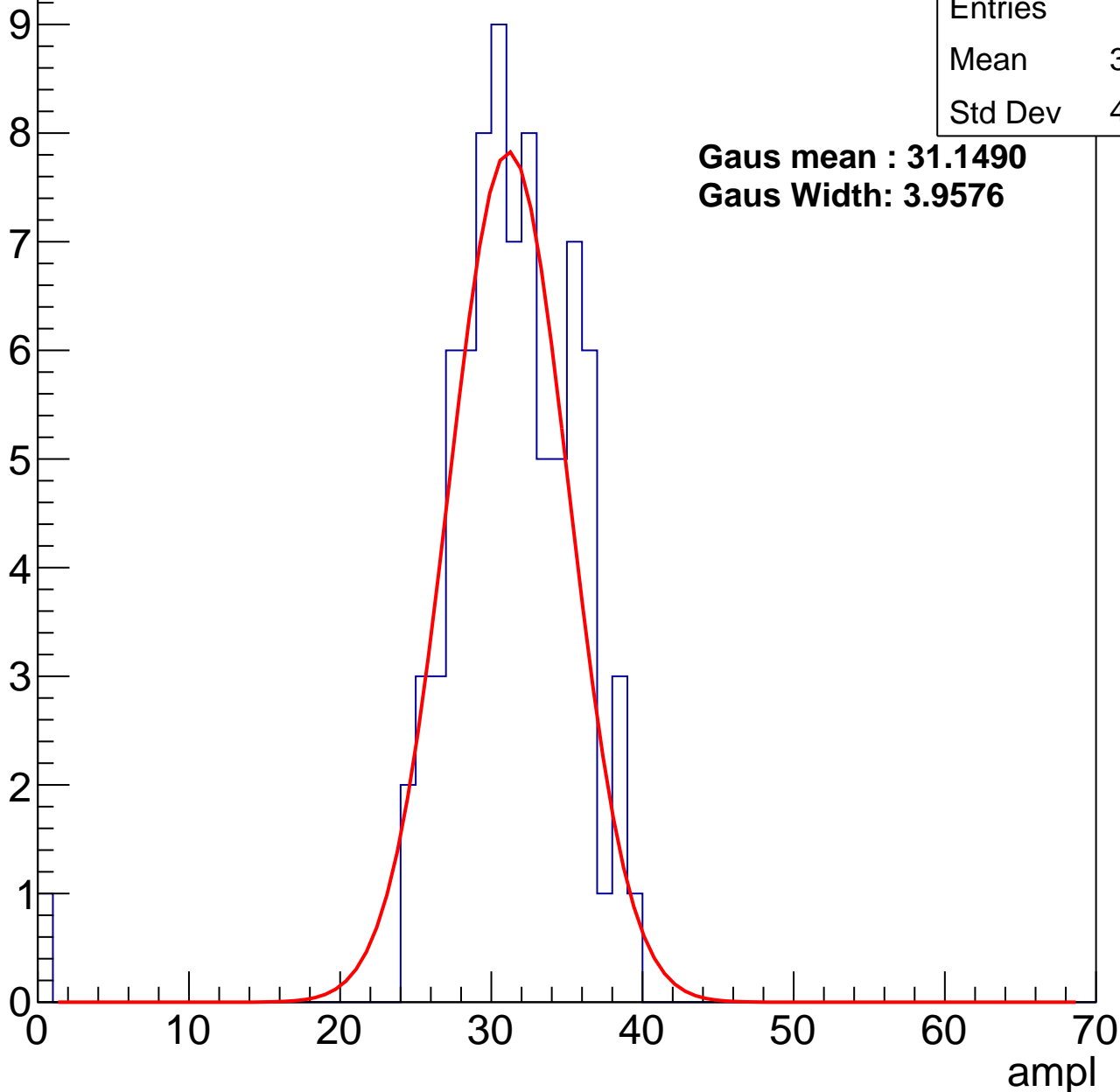
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	30.77
Std Dev	4.982

Gaus mean : 31.1490

Gaus Width: 3.9576



B1L101S, U23-ch82, adc1

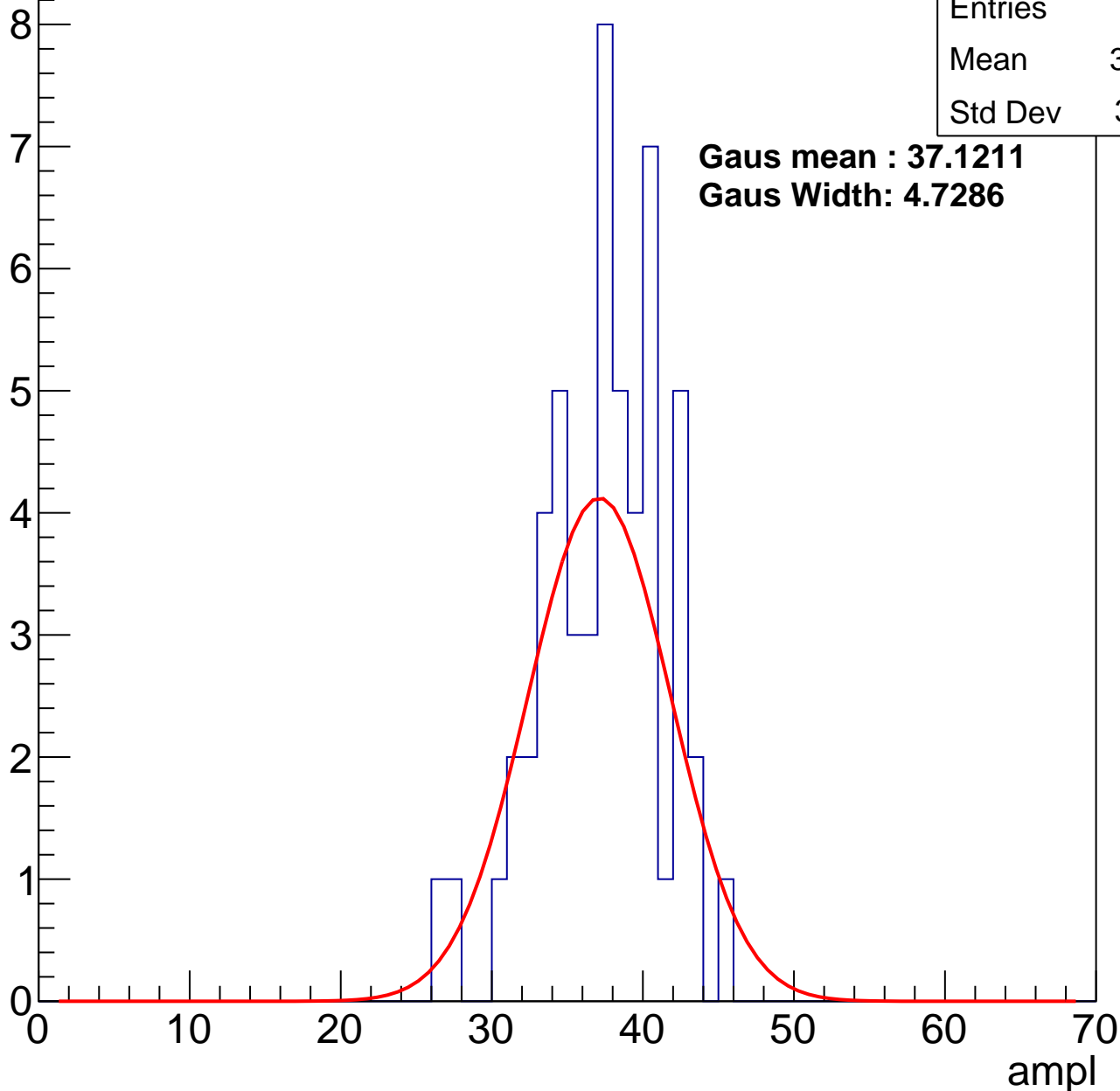
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.87
Std Dev	3.991

Gaus mean : 37.1211

Gaus Width: 4.7286



B1L101S, U23-ch82, adc2

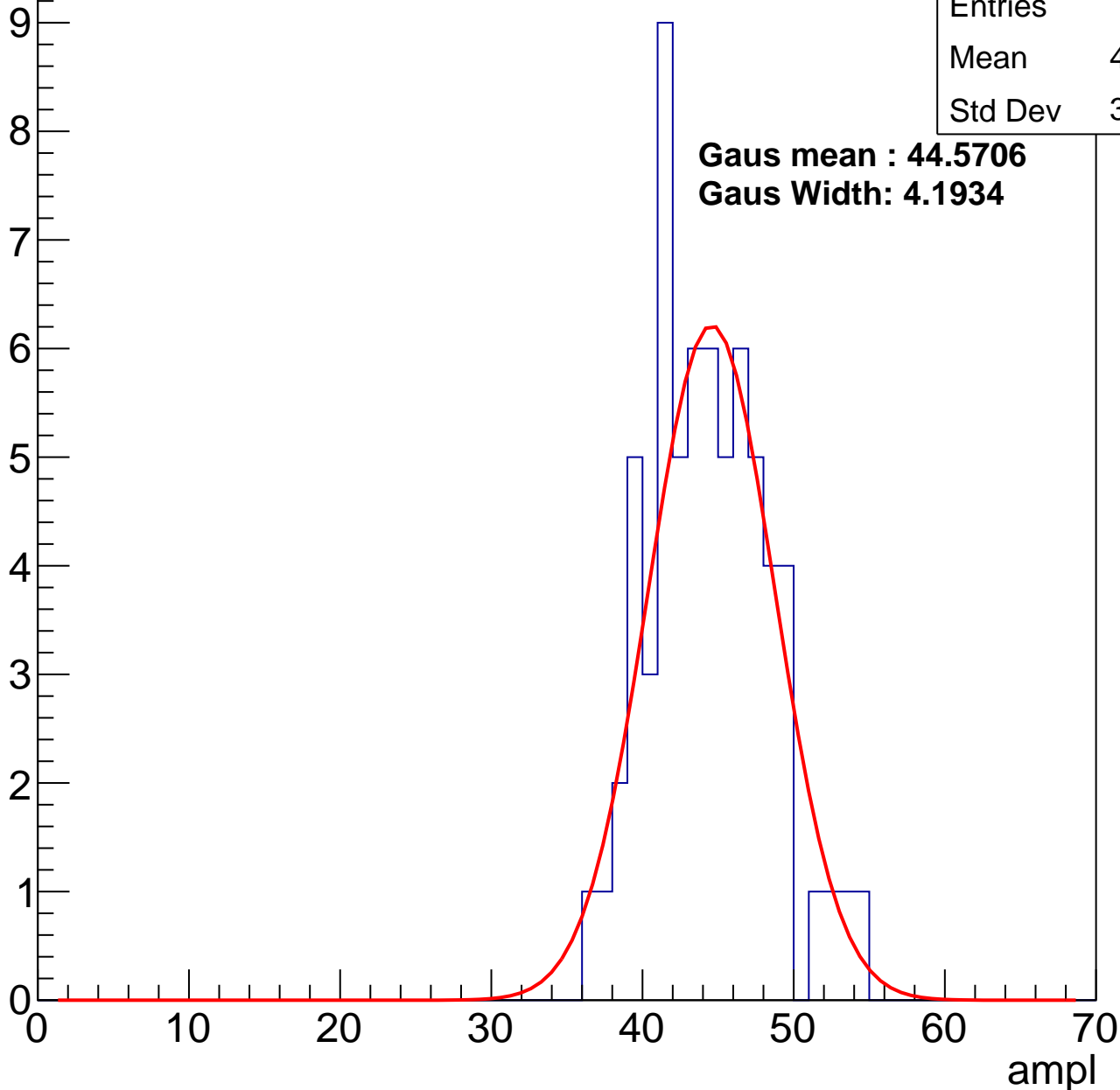
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.92
Std Dev	3.886

Gaus mean : 44.5706

Gaus Width: 4.1934

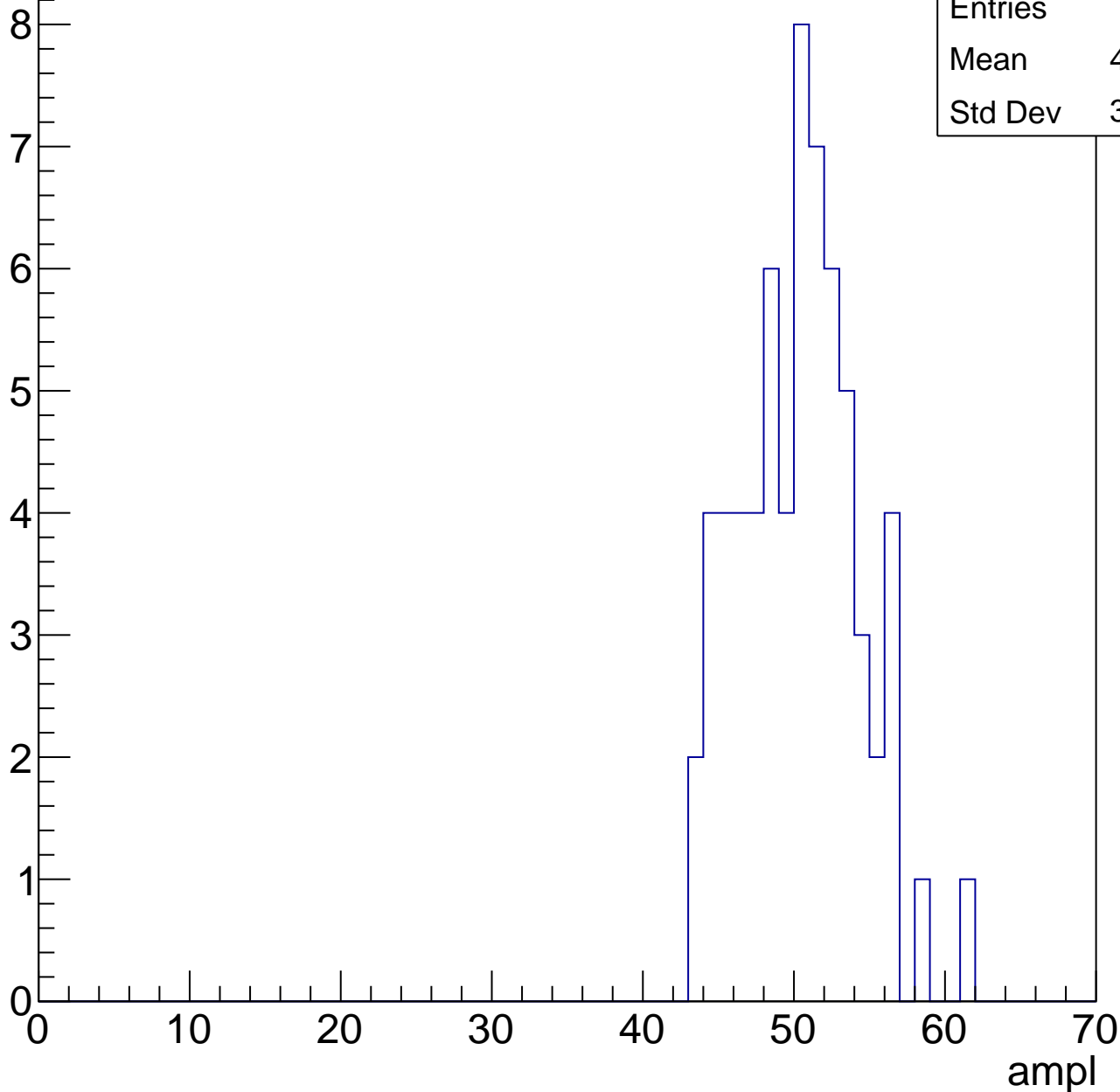


B1L101S, U23-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

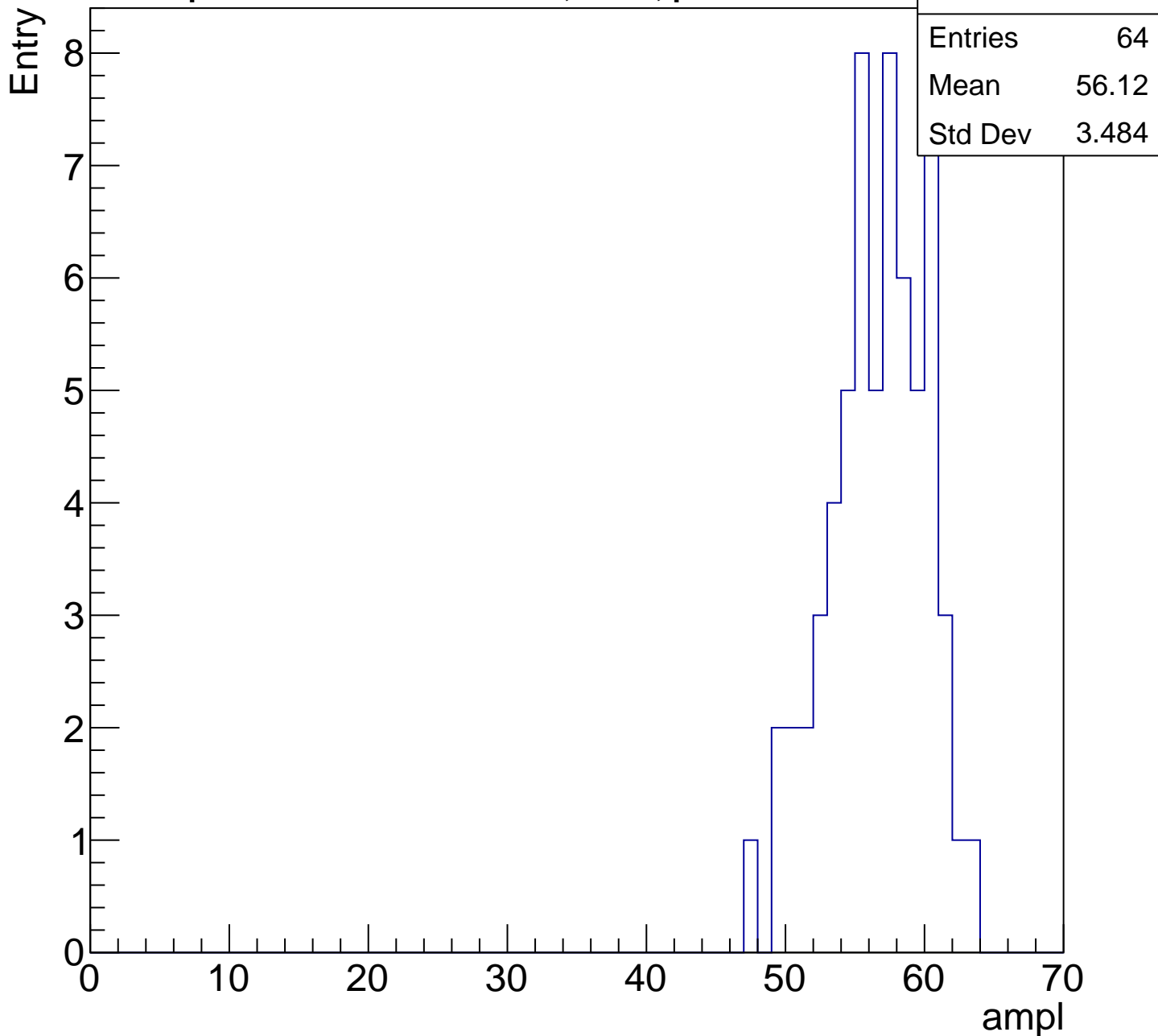
Entry

Entries	65
Mean	49.95
Std Dev	3.869



B1L101S, U23-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

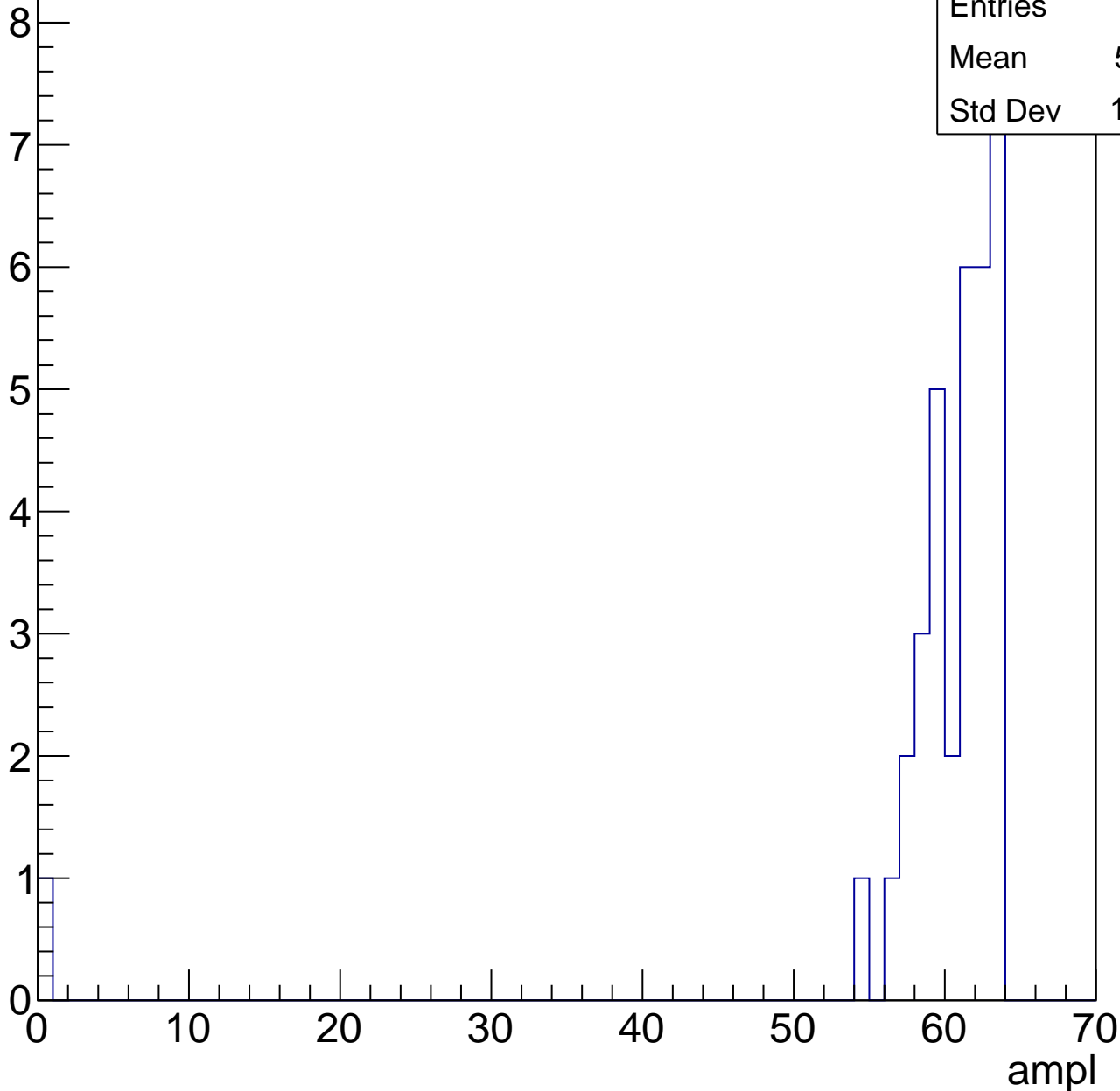


B1L101S, U23-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.71
Std Dev	10.32



B1L101S, U23-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch83, adc0

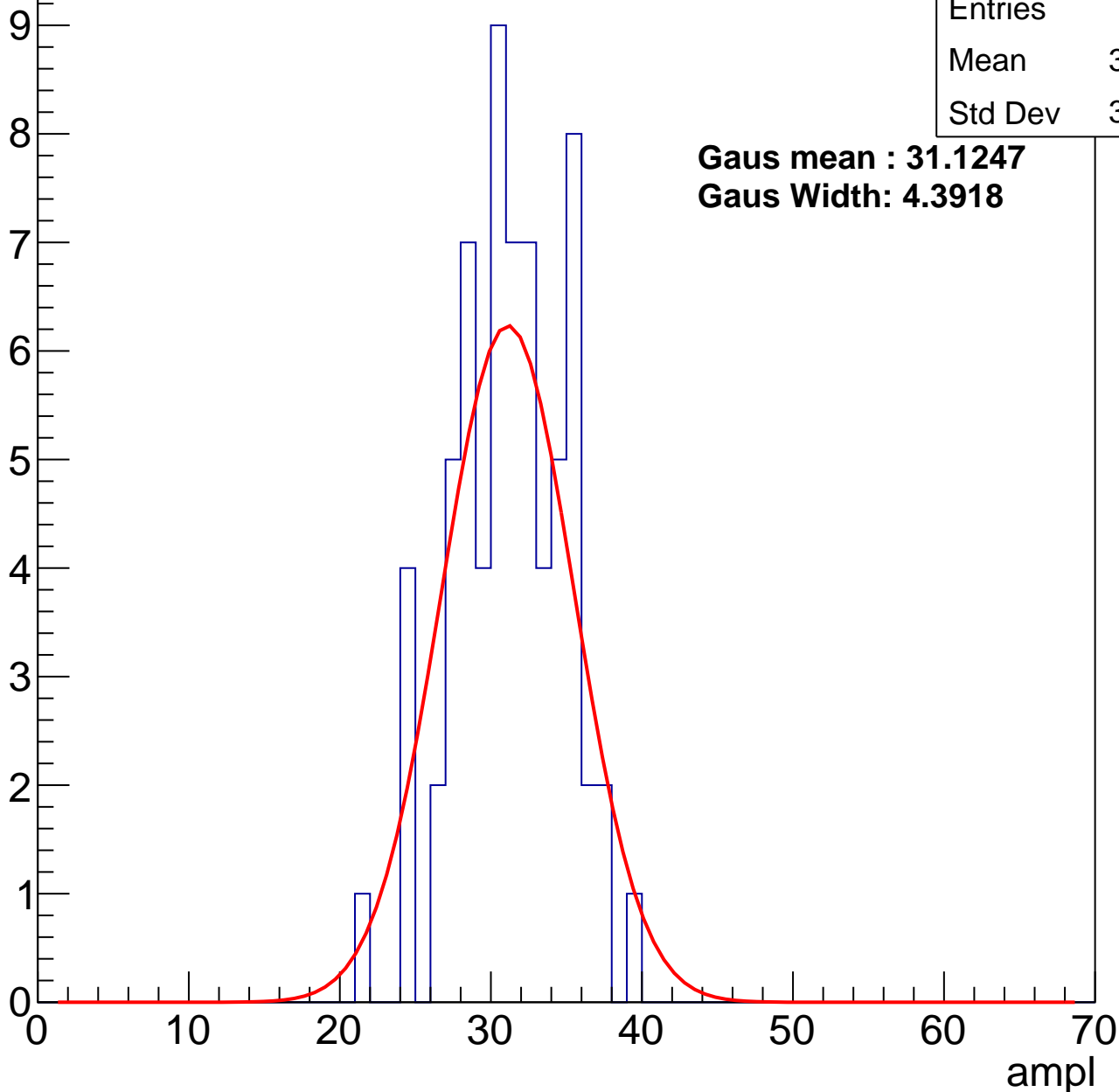
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.79
Std Dev	3.624

Gaus mean : 31.1247

Gaus Width: 4.3918



B1L101S, U23-ch83, adc1

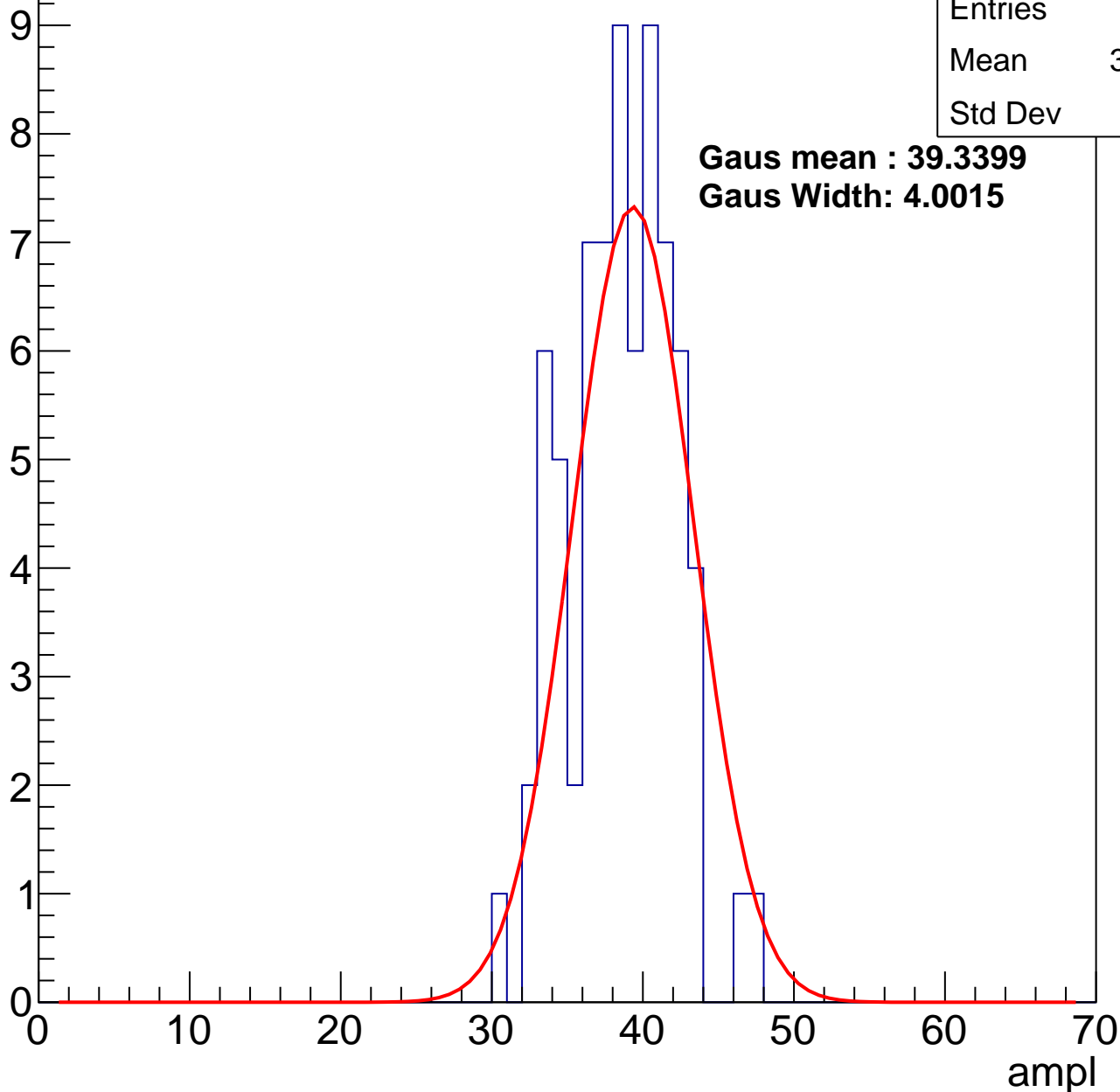
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	38.12
Std Dev	3.44

Gaus mean : 39.3399

Gaus Width: 4.0015



B1L101S, U23-ch83, adc2

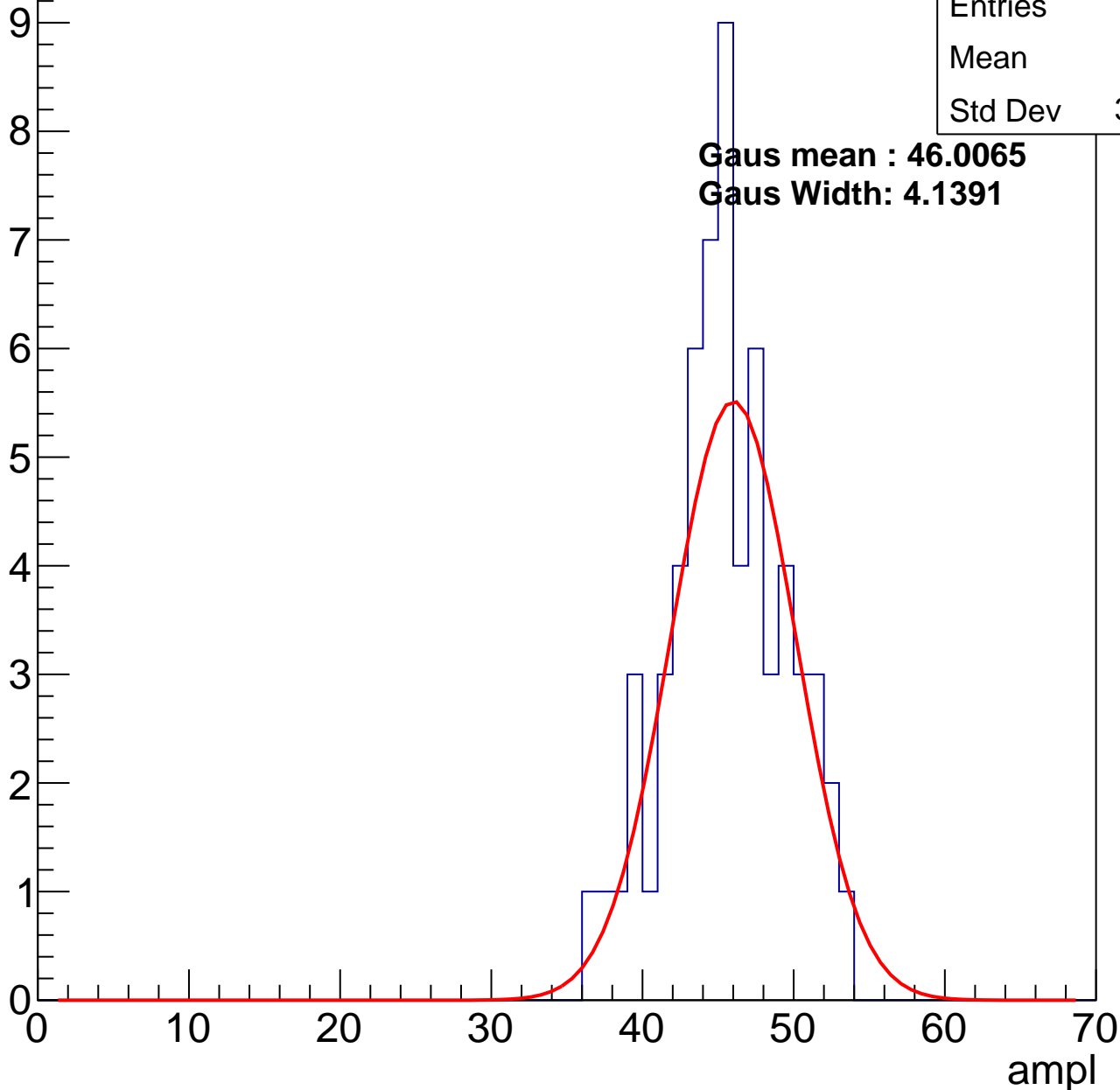
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	45.1
Std Dev	3.821

Gaus mean : 46.0065

Gaus Width: 4.1391

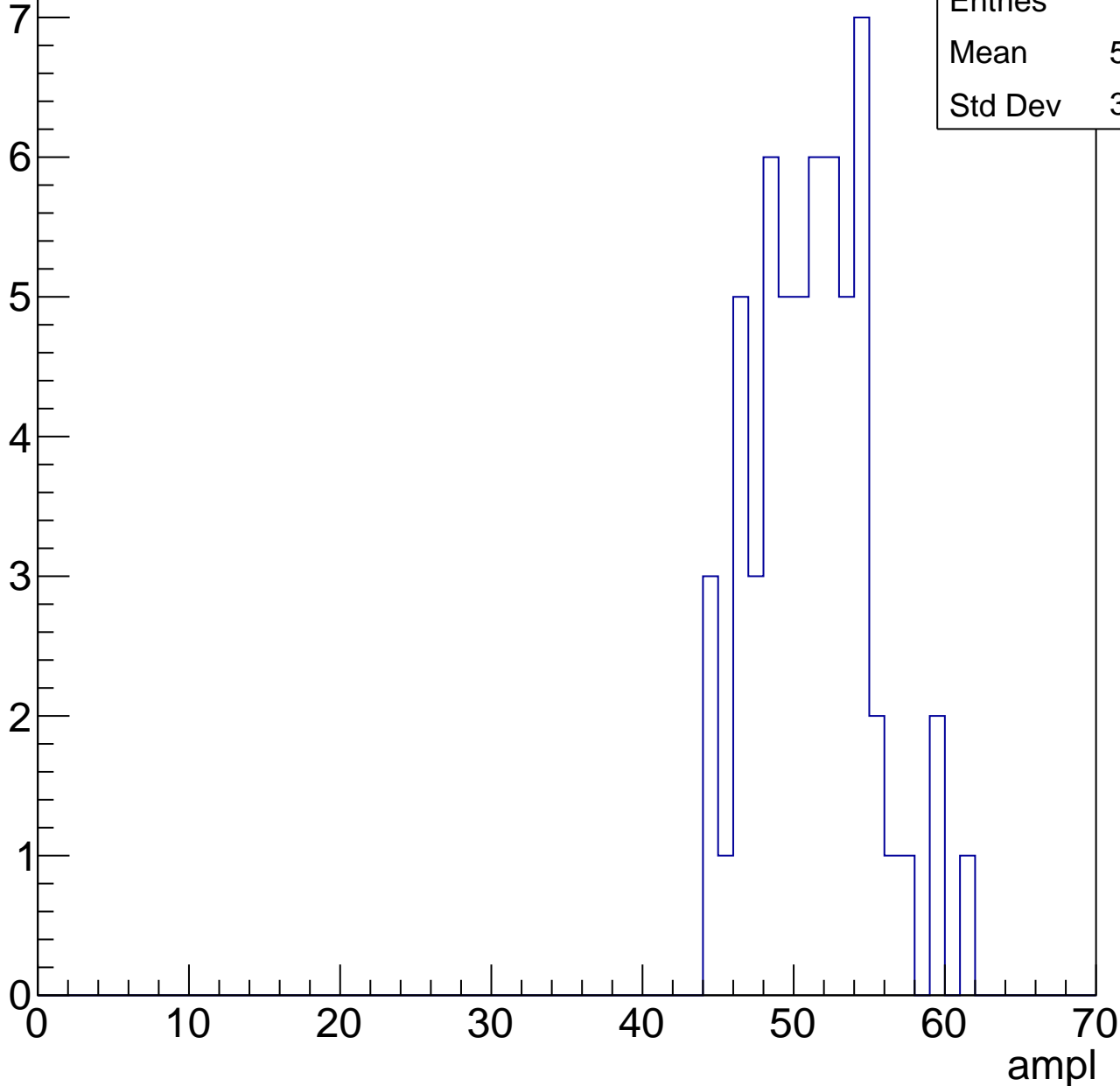


B1L101S, U23-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

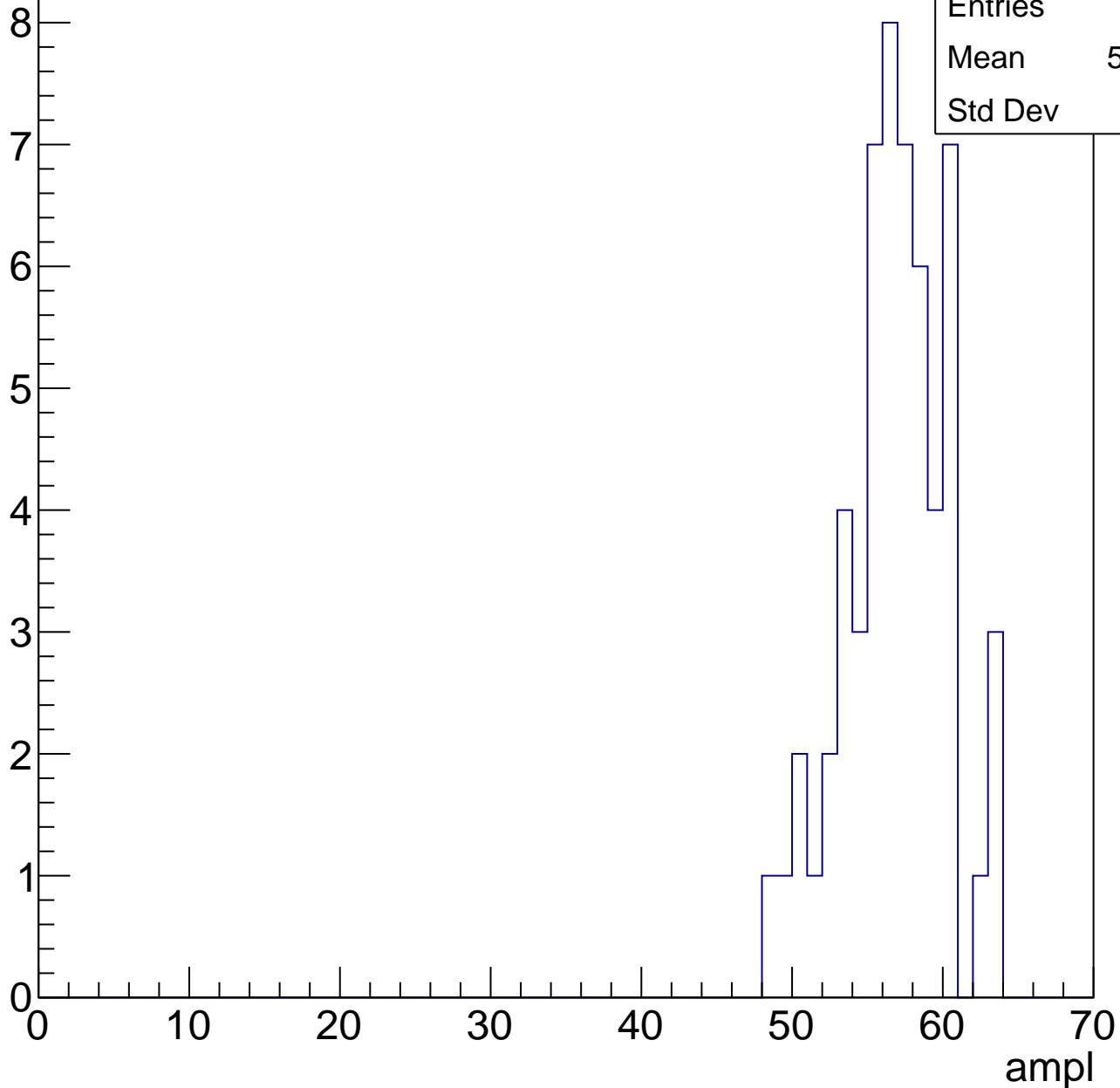
Entries	59
Mean	50.75
Std Dev	3.776



B1L101S, U23-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	57
Mean	56.37
Std Dev	3.38

B1L101S, U23-ch83, adc5

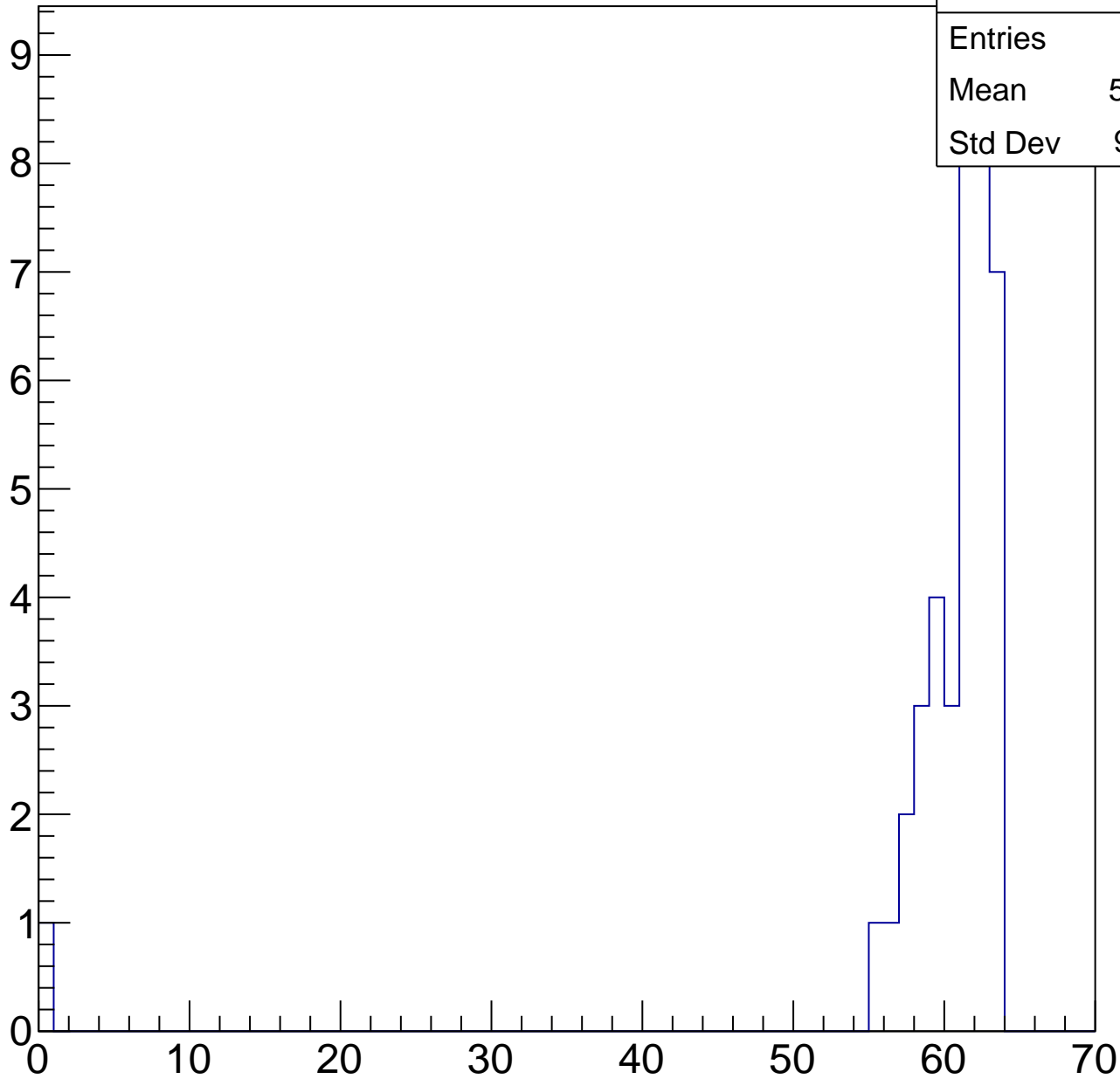
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.08
Std Dev	9.681

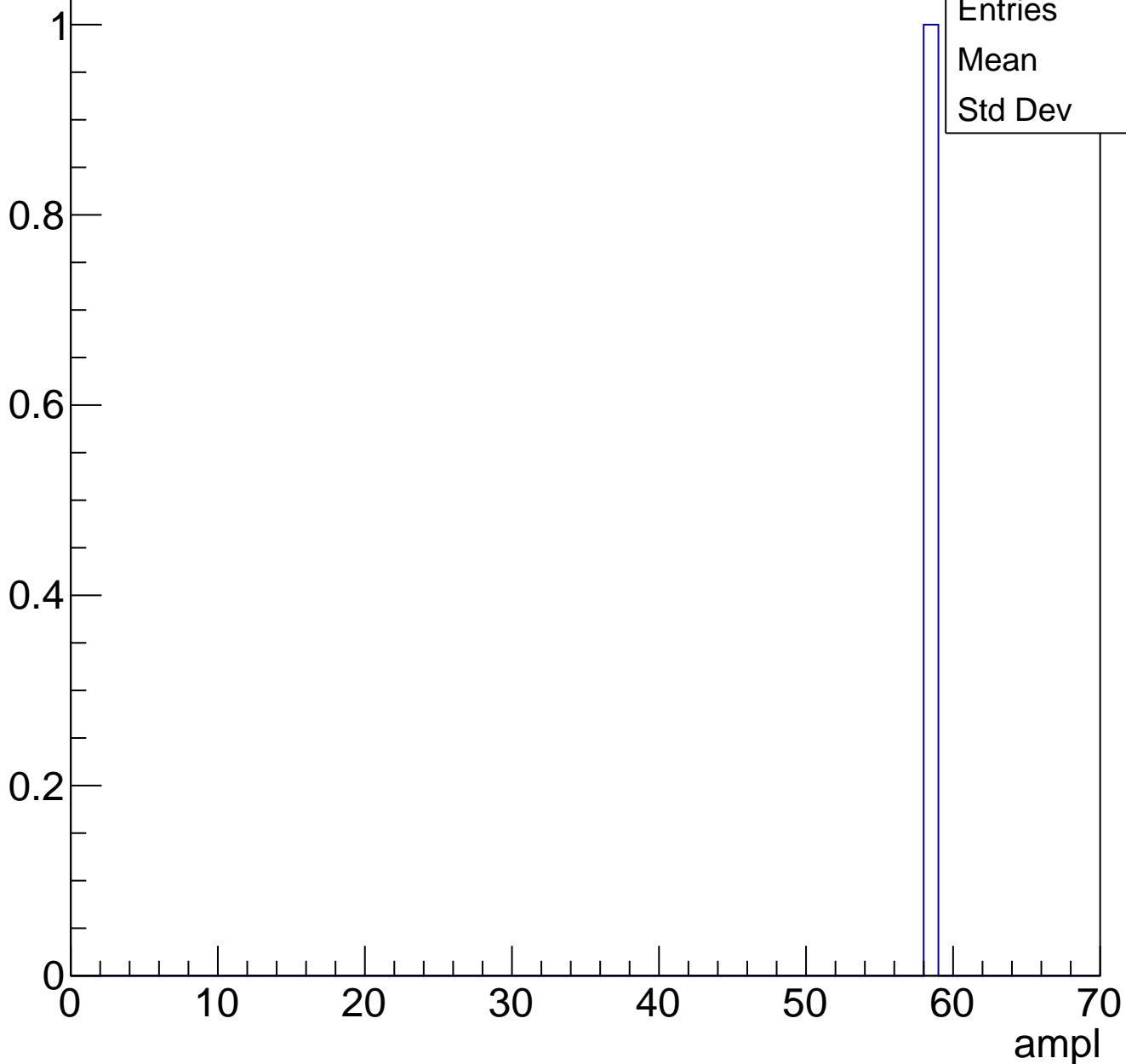
ampl



B1L101S, U23-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	58
Std Dev	0

B1L101S, U23-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch84, adc0

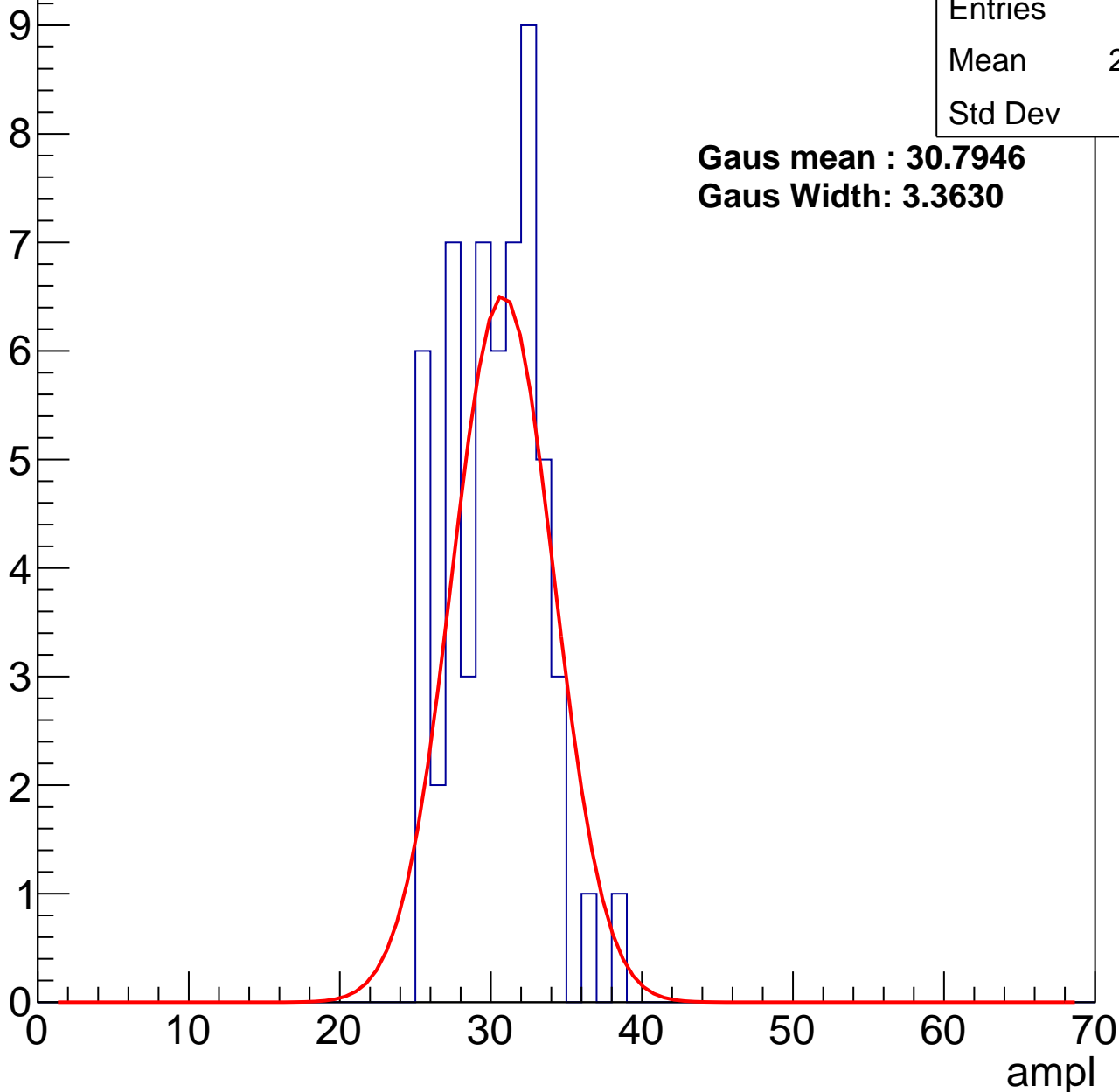
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	29.89
Std Dev	2.96

Gaus mean : 30.7946

Gaus Width: 3.3630



B1L101S, U23-ch84, adc1

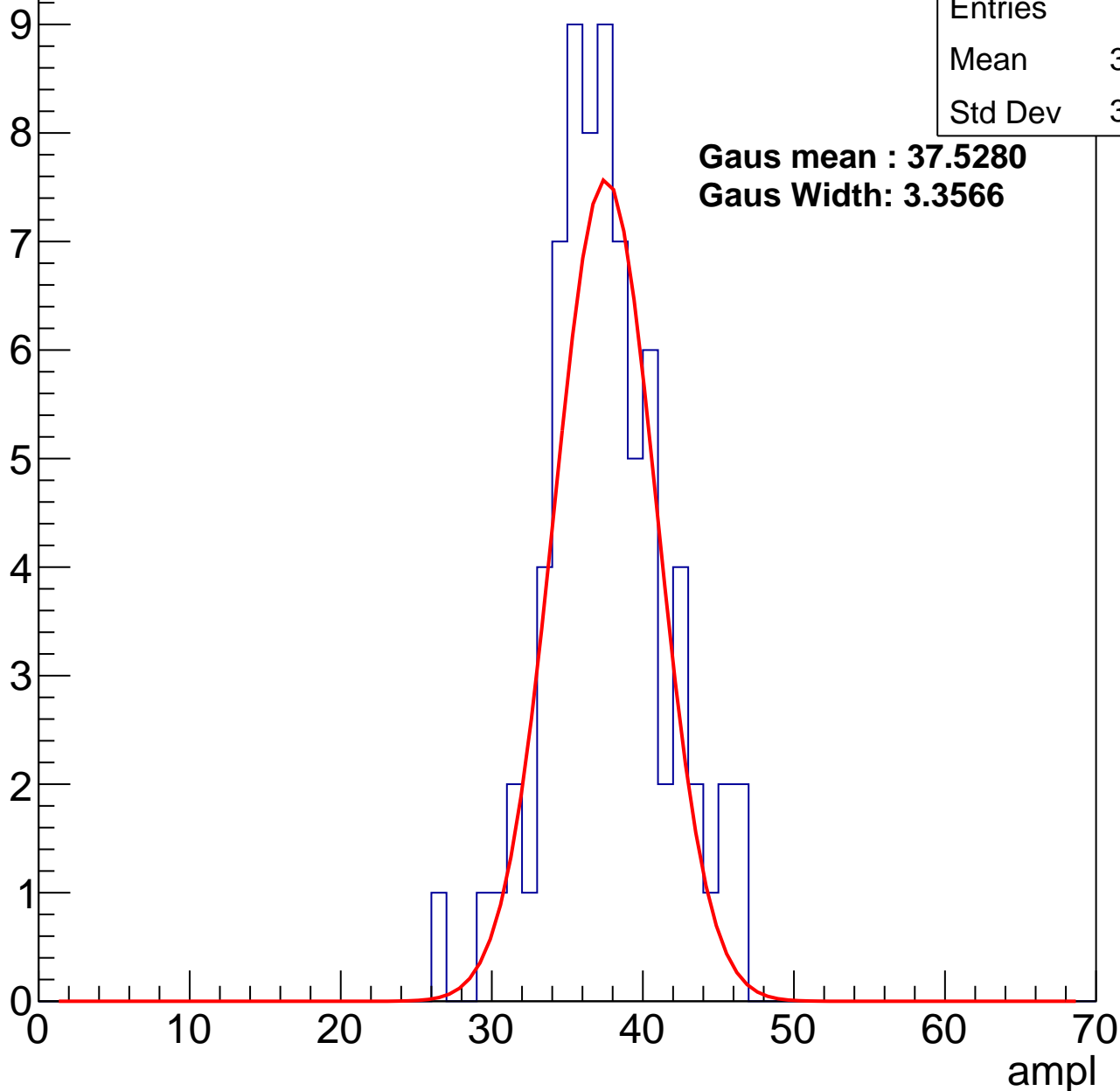
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.14
Std Dev	3.898

Gaus mean : 37.5280

Gaus Width: 3.3566



B1L101S, U23-ch84, adc2

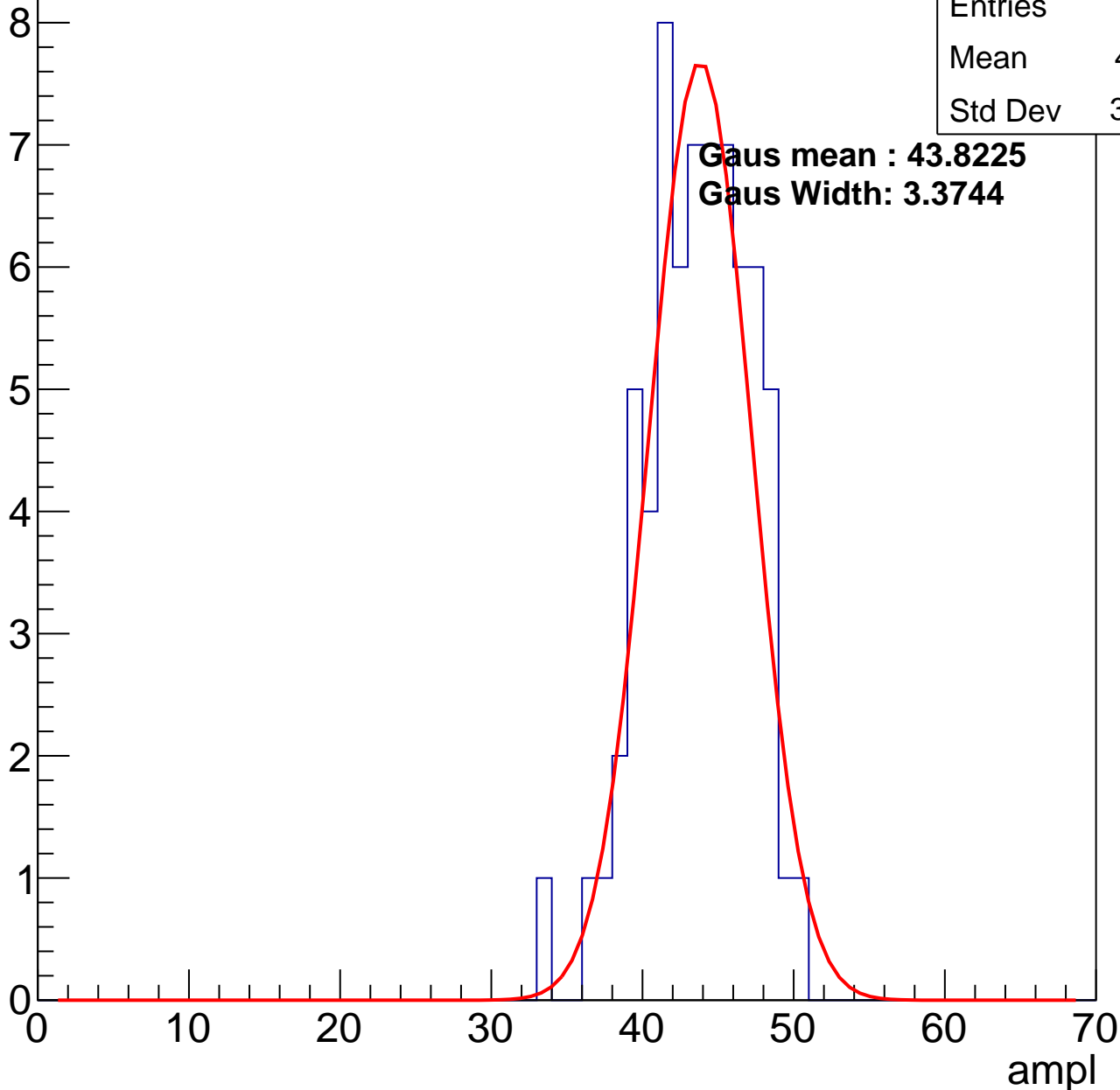
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.21
Std Dev	3.389

Gaus mean : 43.8225

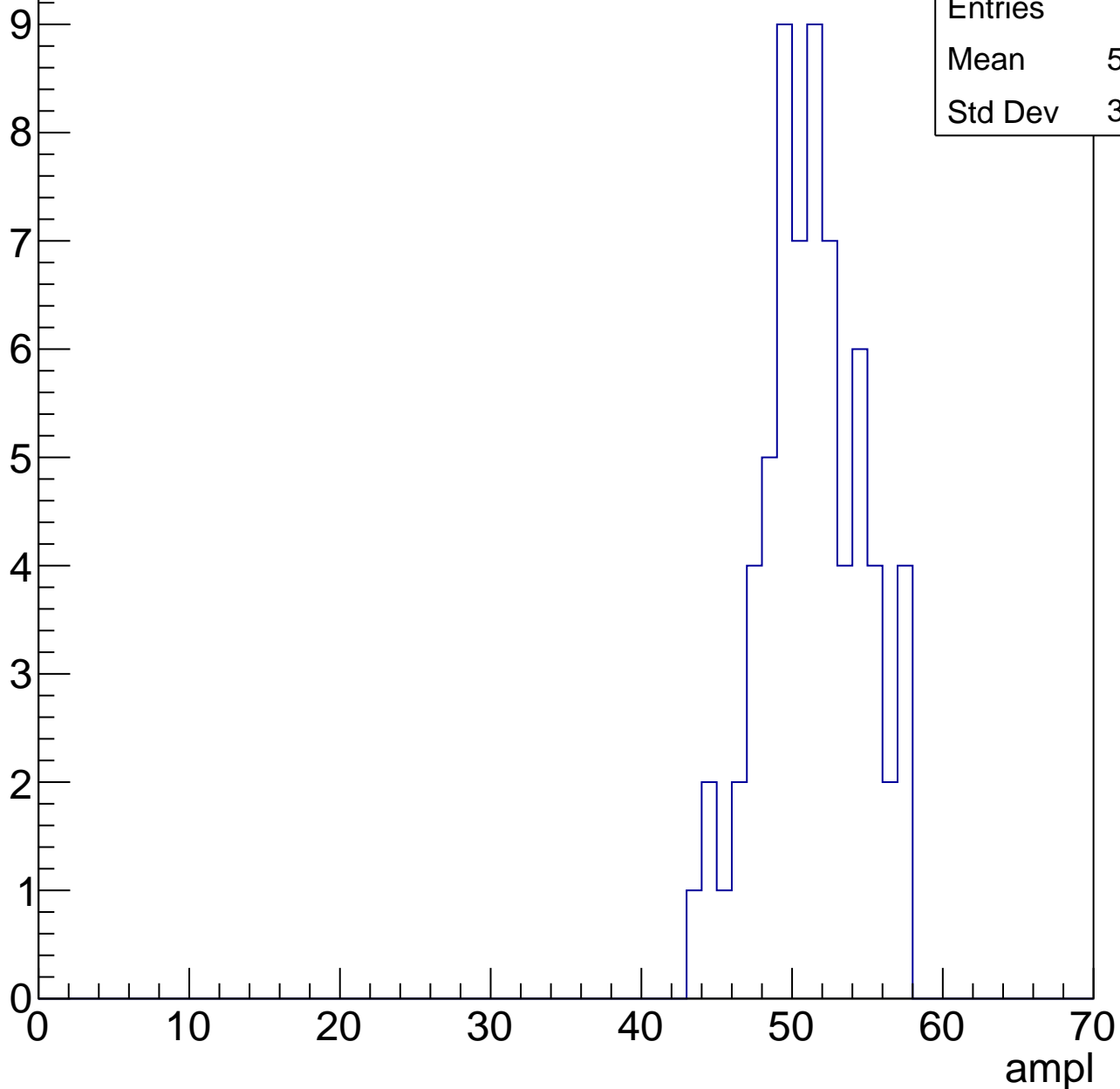
Gaus Width: 3.3744



B1L101S, U23-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



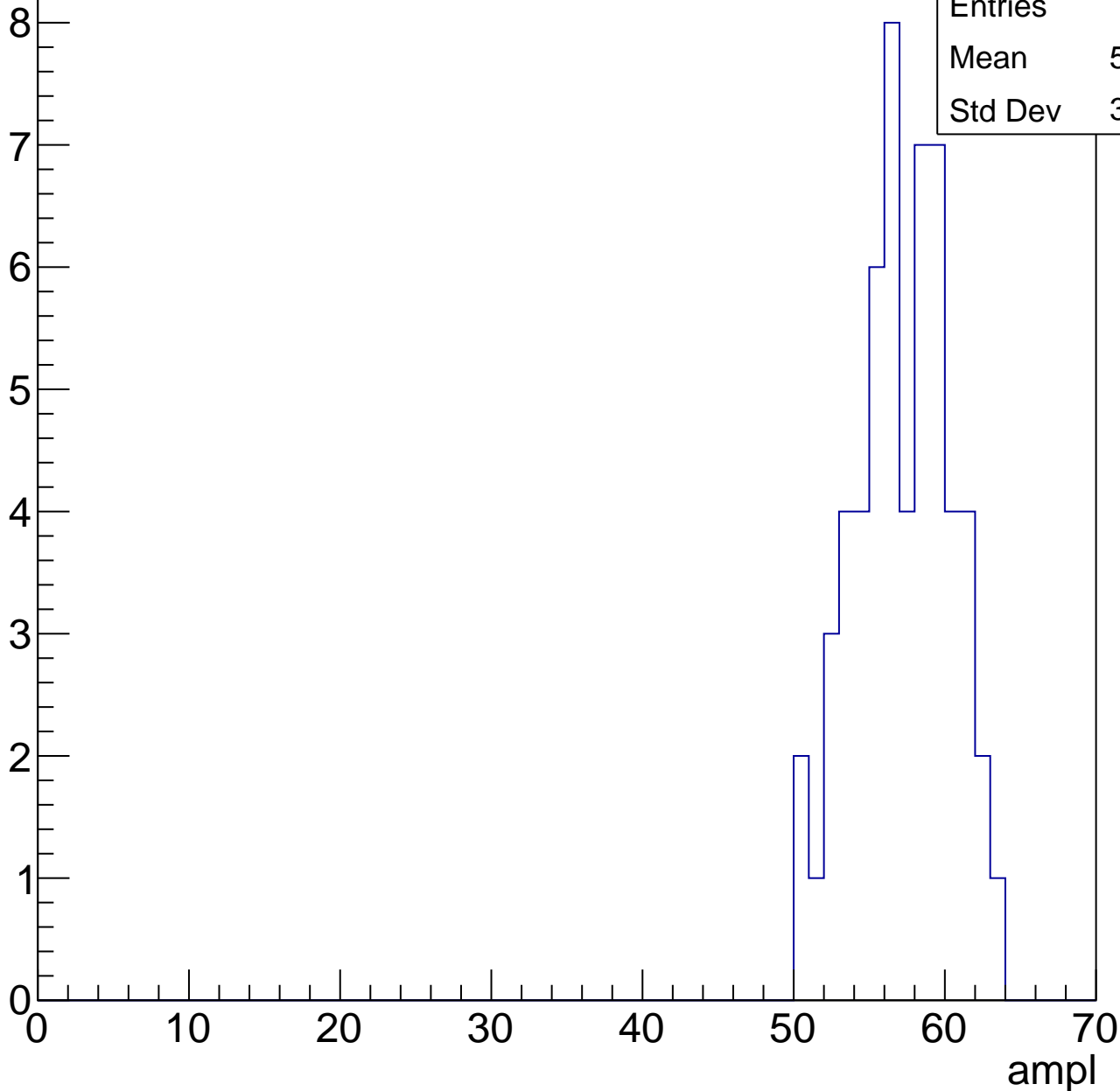
Entries	67
Mean	50.84
Std Dev	3.308

B1L101S, U23-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.68
Std Dev	3.124

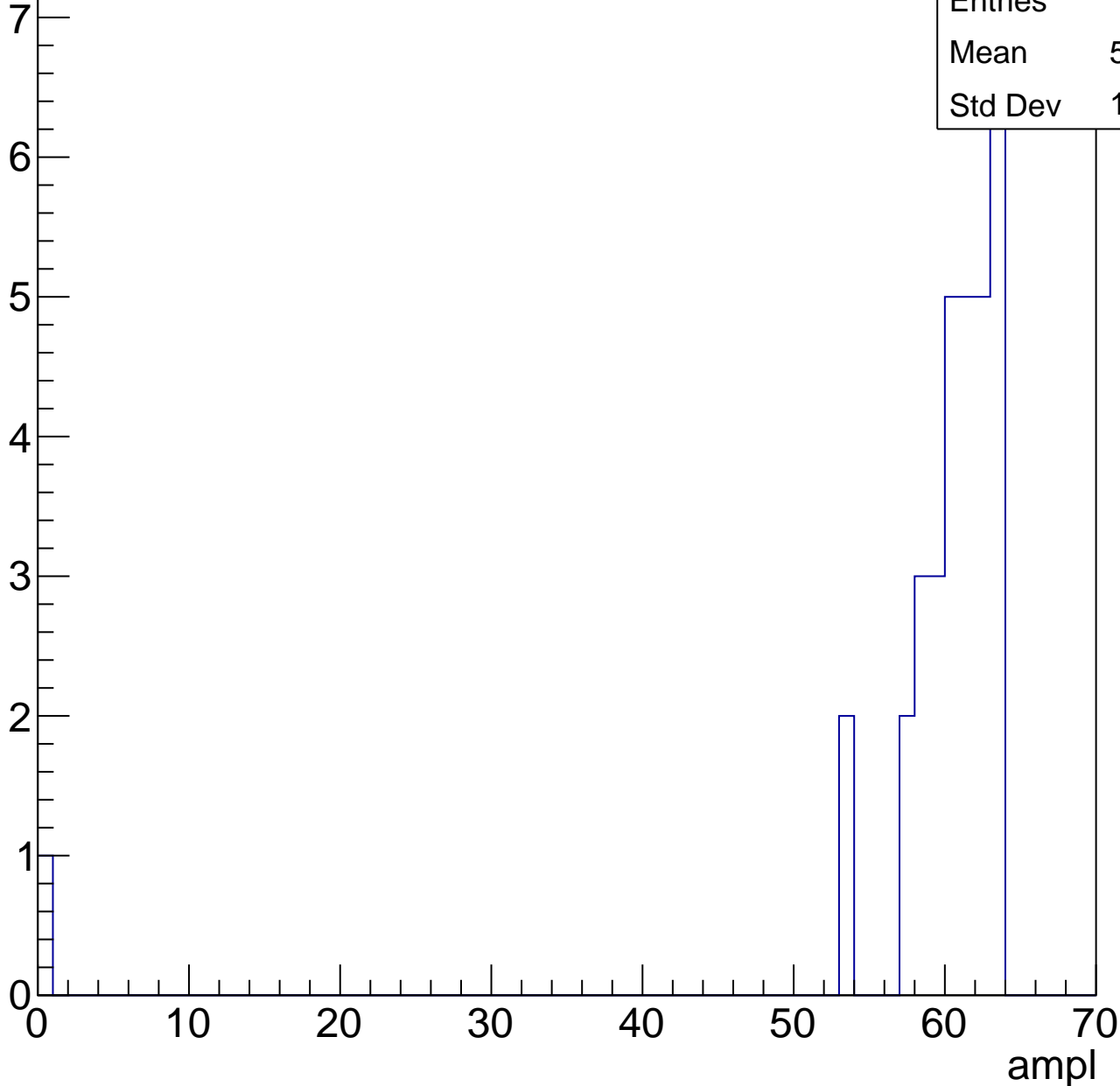


B1L101S, U23-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.39
Std Dev	10.64



B1L101S, U23-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

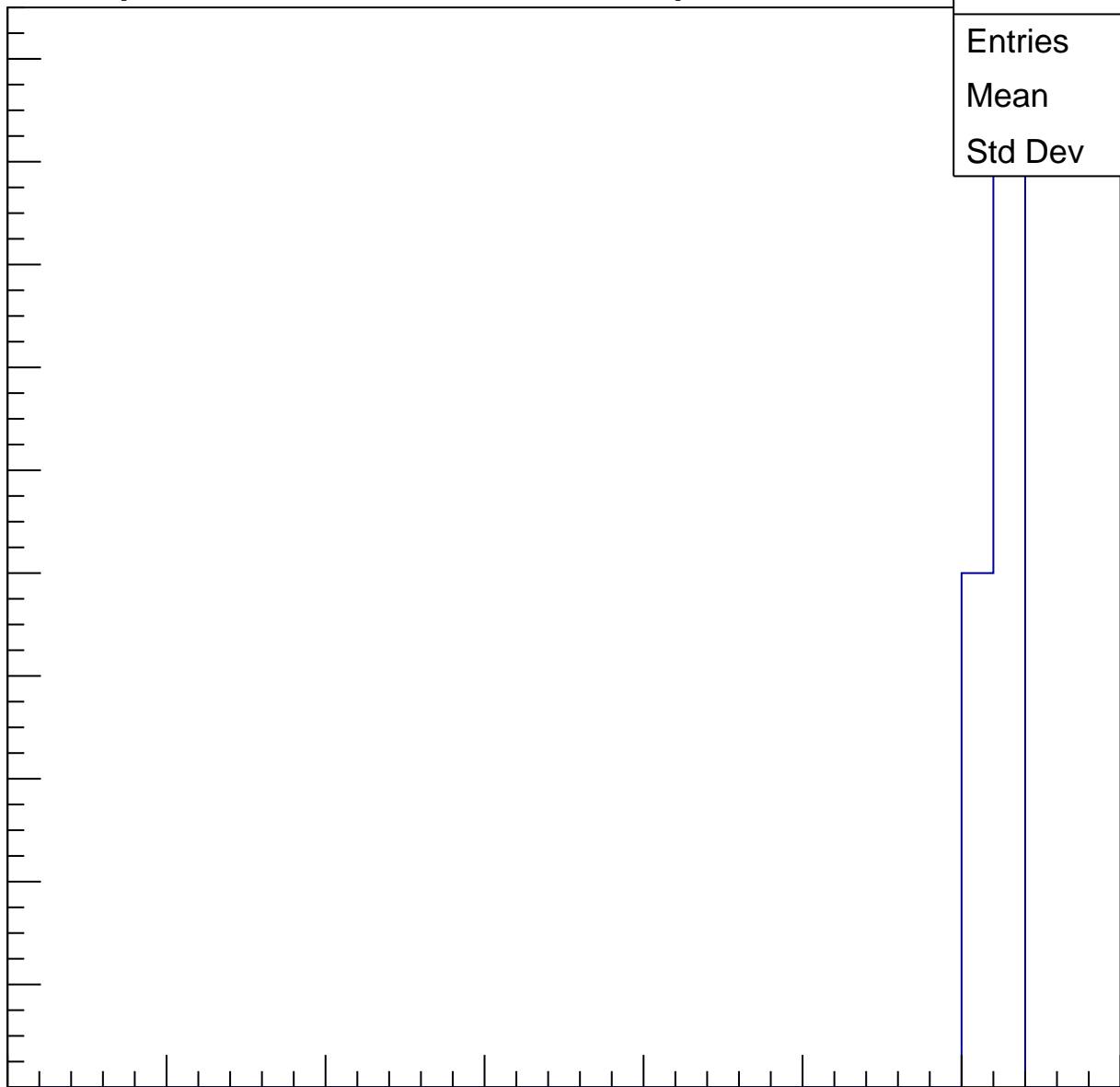
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch85, adc0

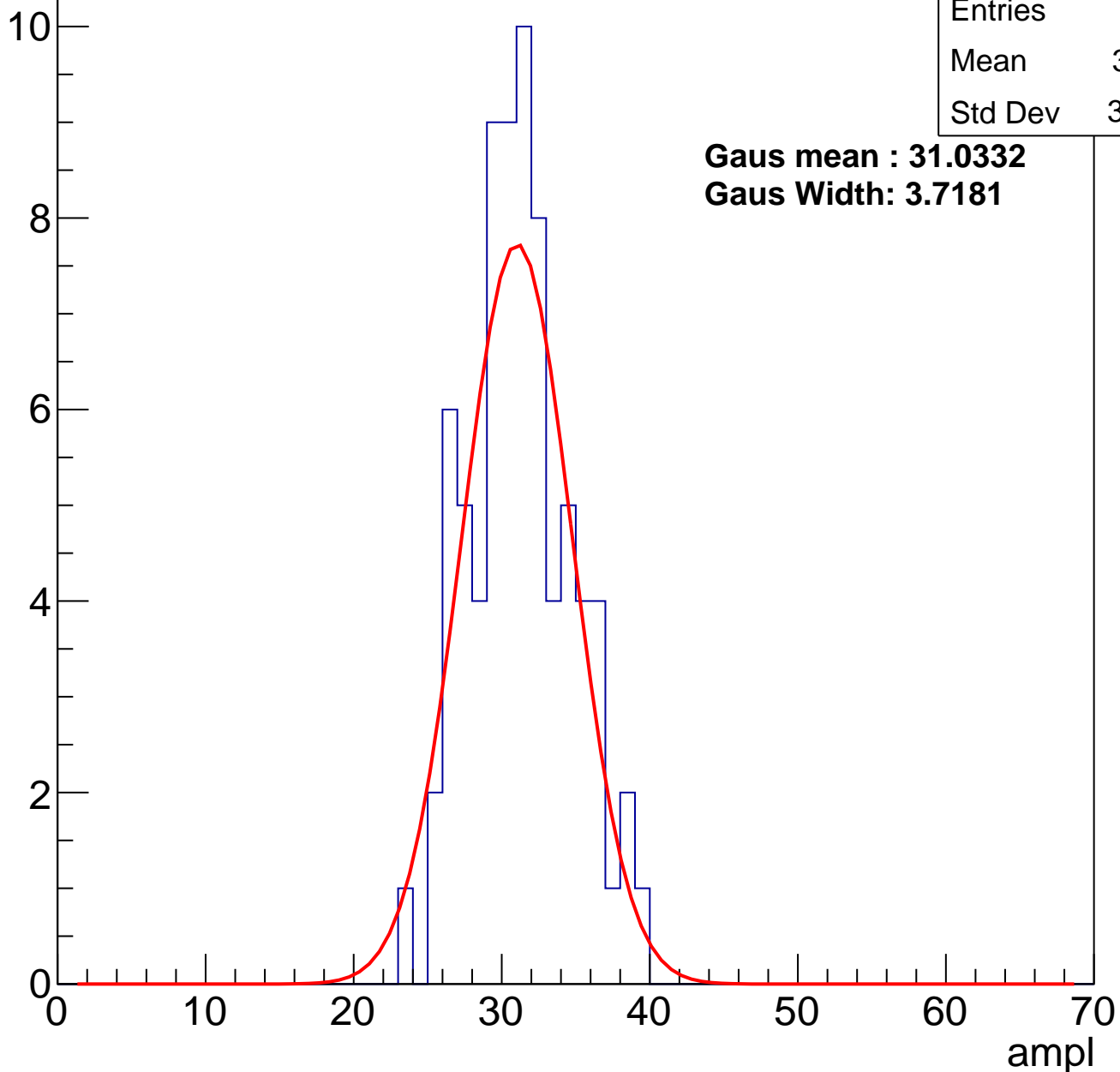
calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	30.81
Std Dev	3.416

Gaus mean : 31.0332

Gaus Width: 3.7181

Entry

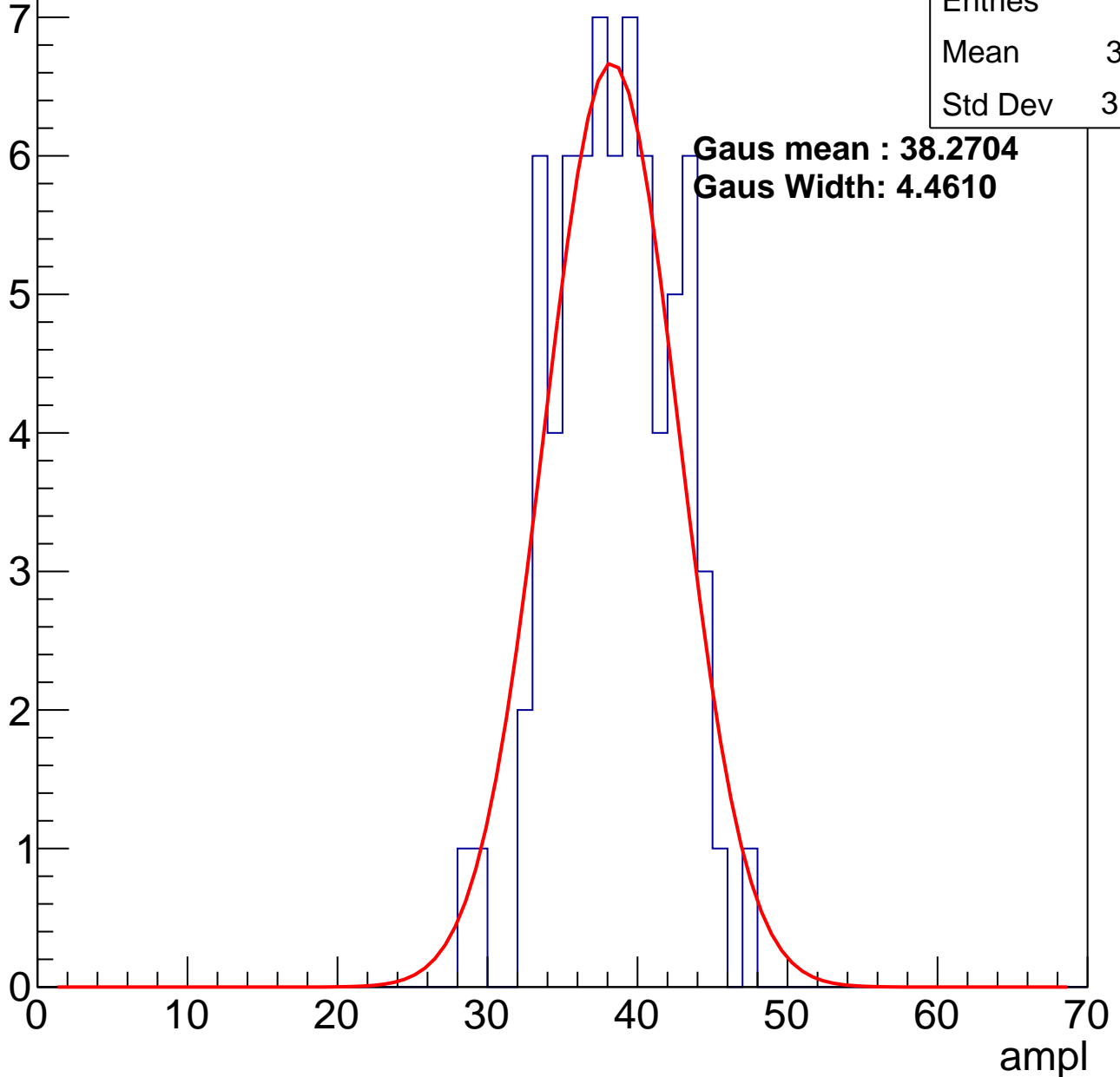


B1L101S, U23-ch85, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	38.01
Std Dev	3.885



B1L101S, U23-ch85, adc2

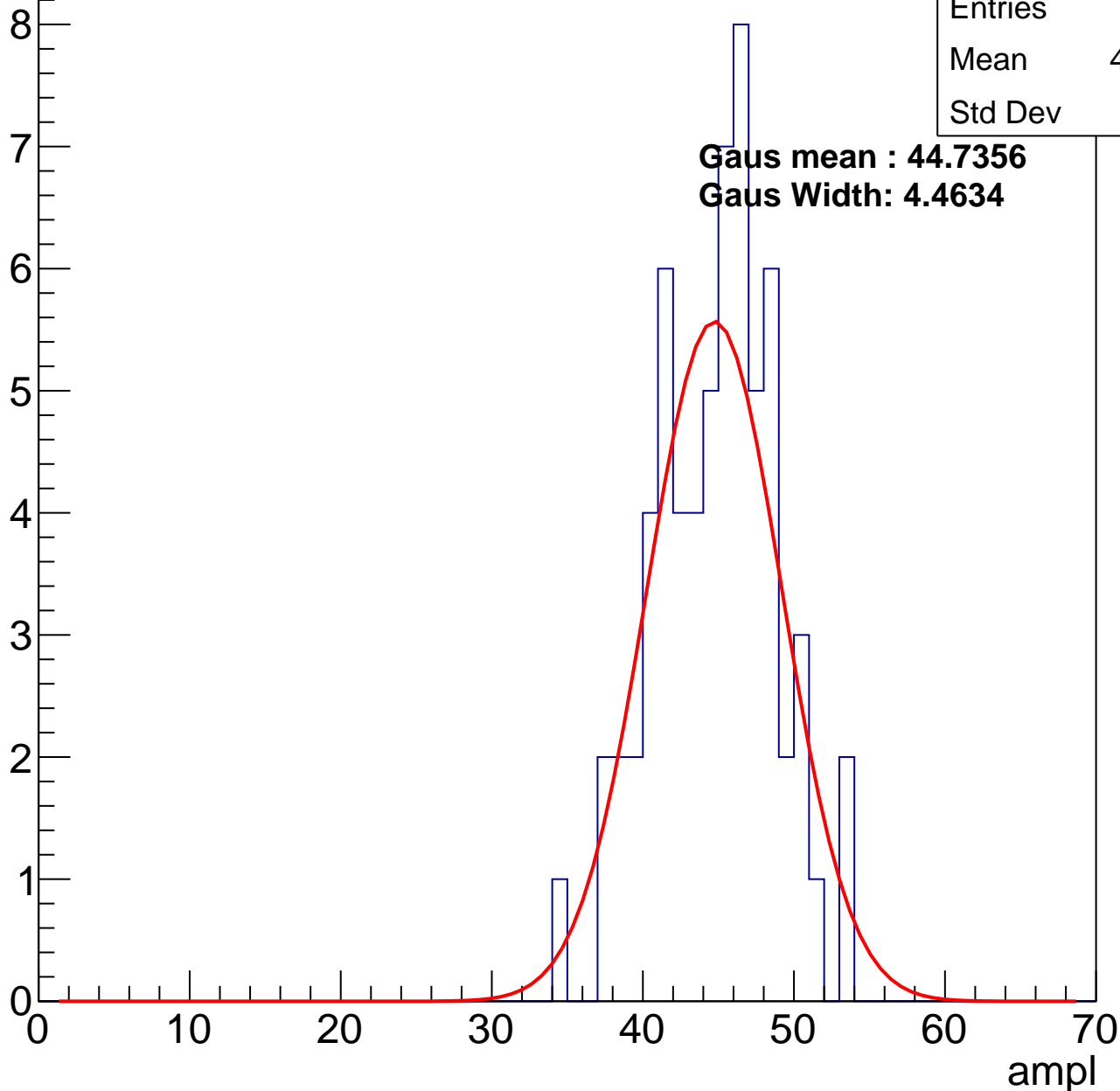
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.36
Std Dev	3.95

Gaus mean : 44.7356

Gaus Width: 4.4634

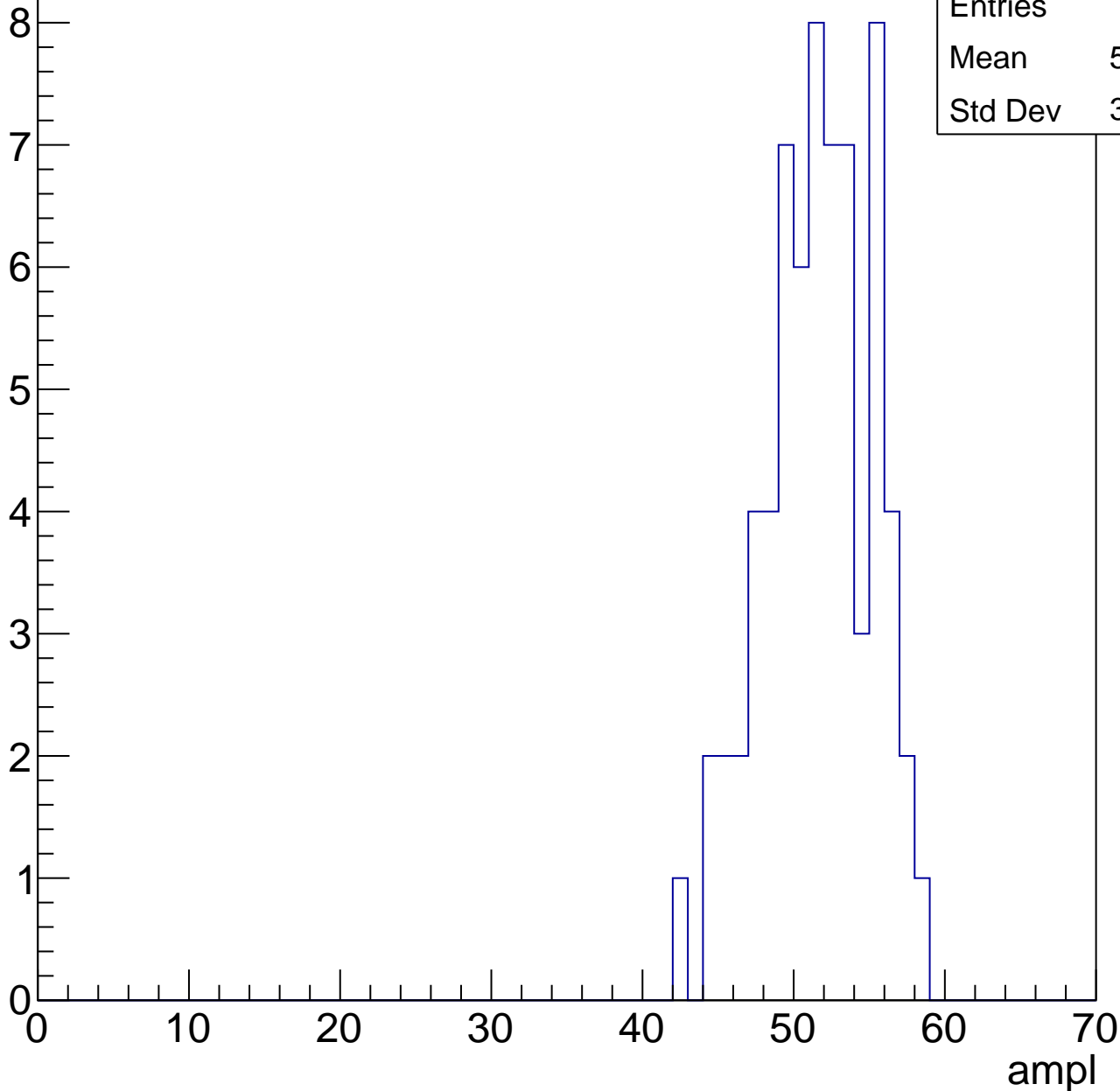


B1L101S, U23-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51.12
Std Dev	3.525

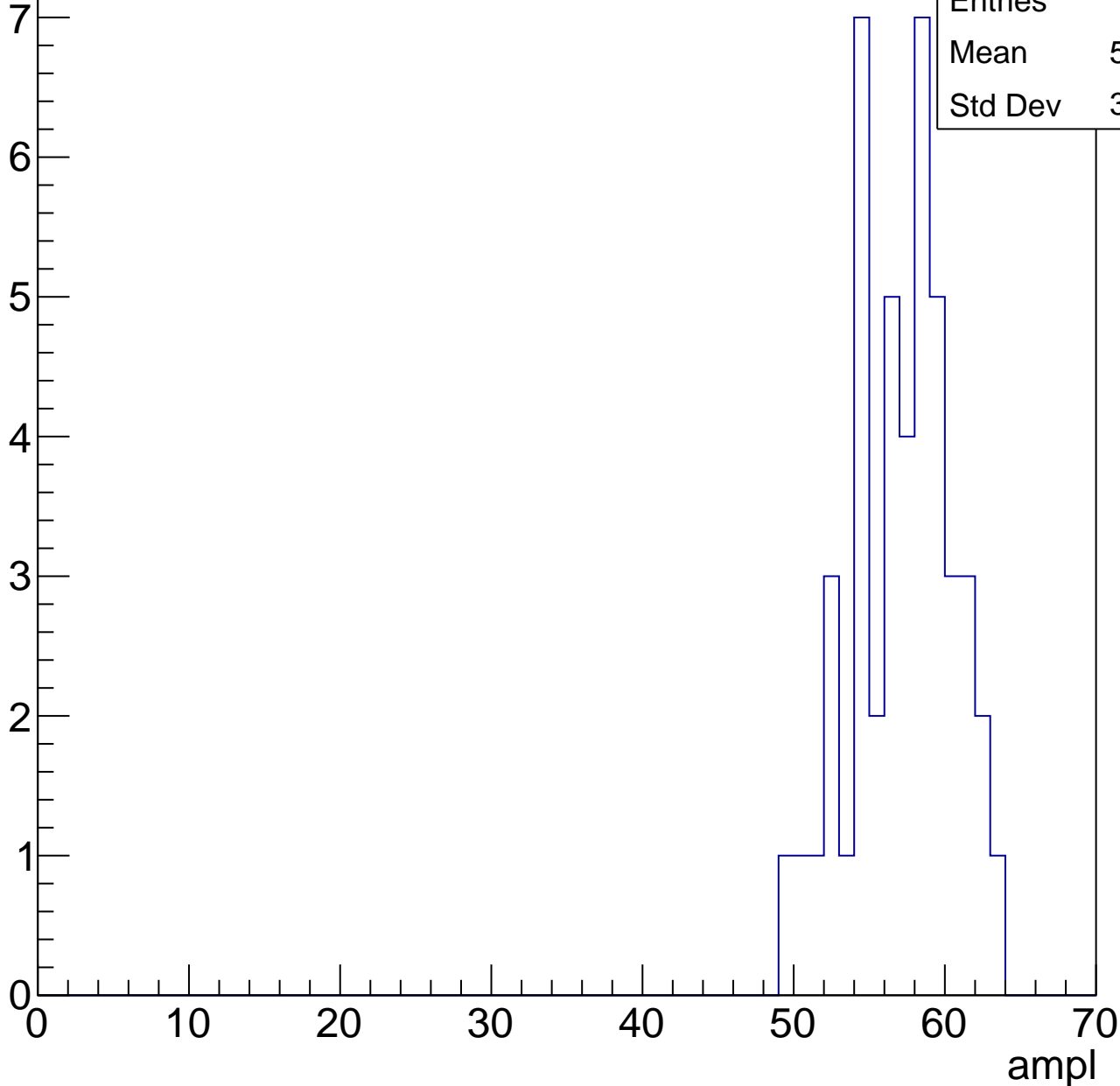


B1L101S, U23-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

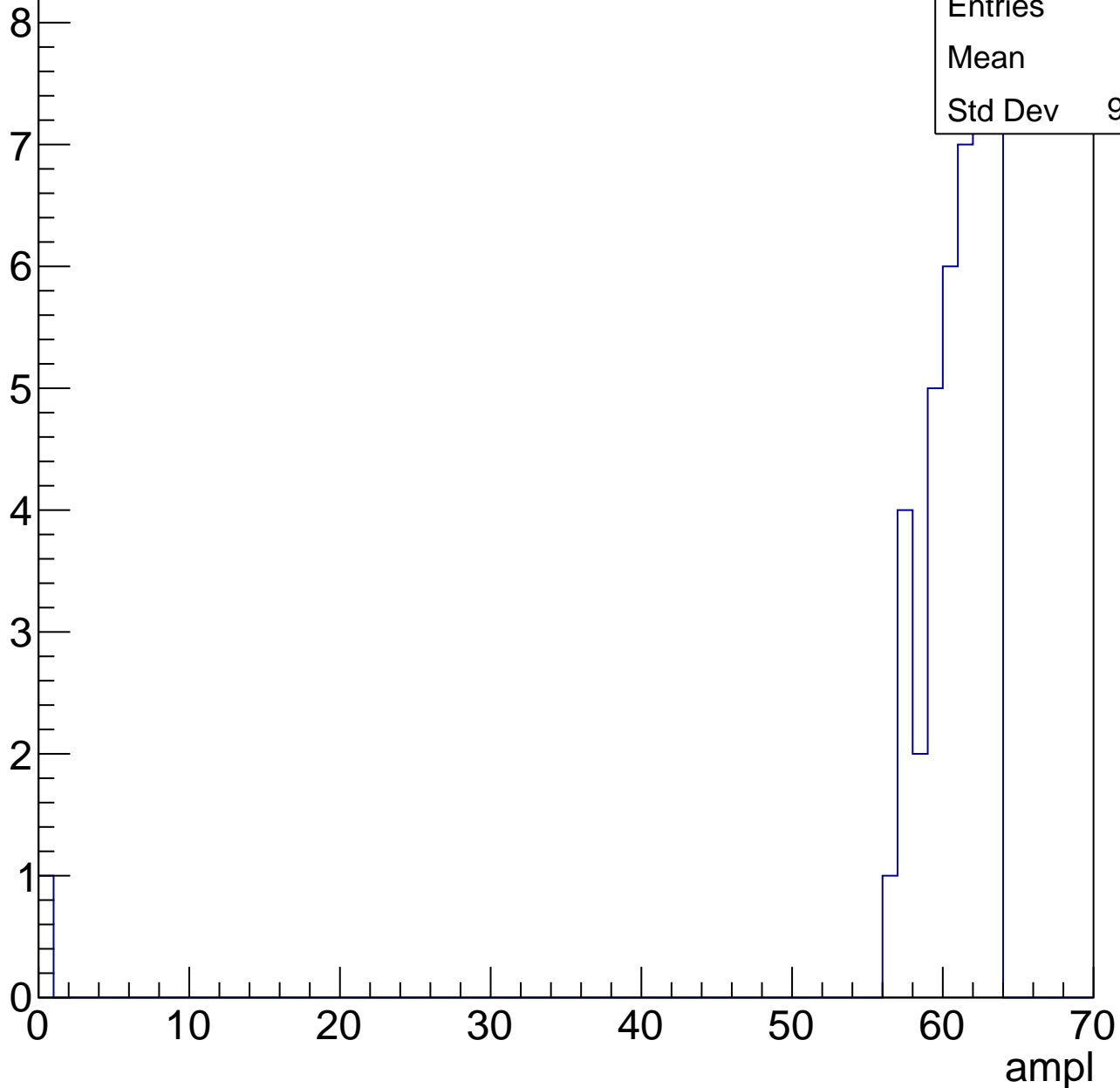
Entries	46
Mean	56.65
Std Dev	3.292



B1L101S, U23-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch86, adc0

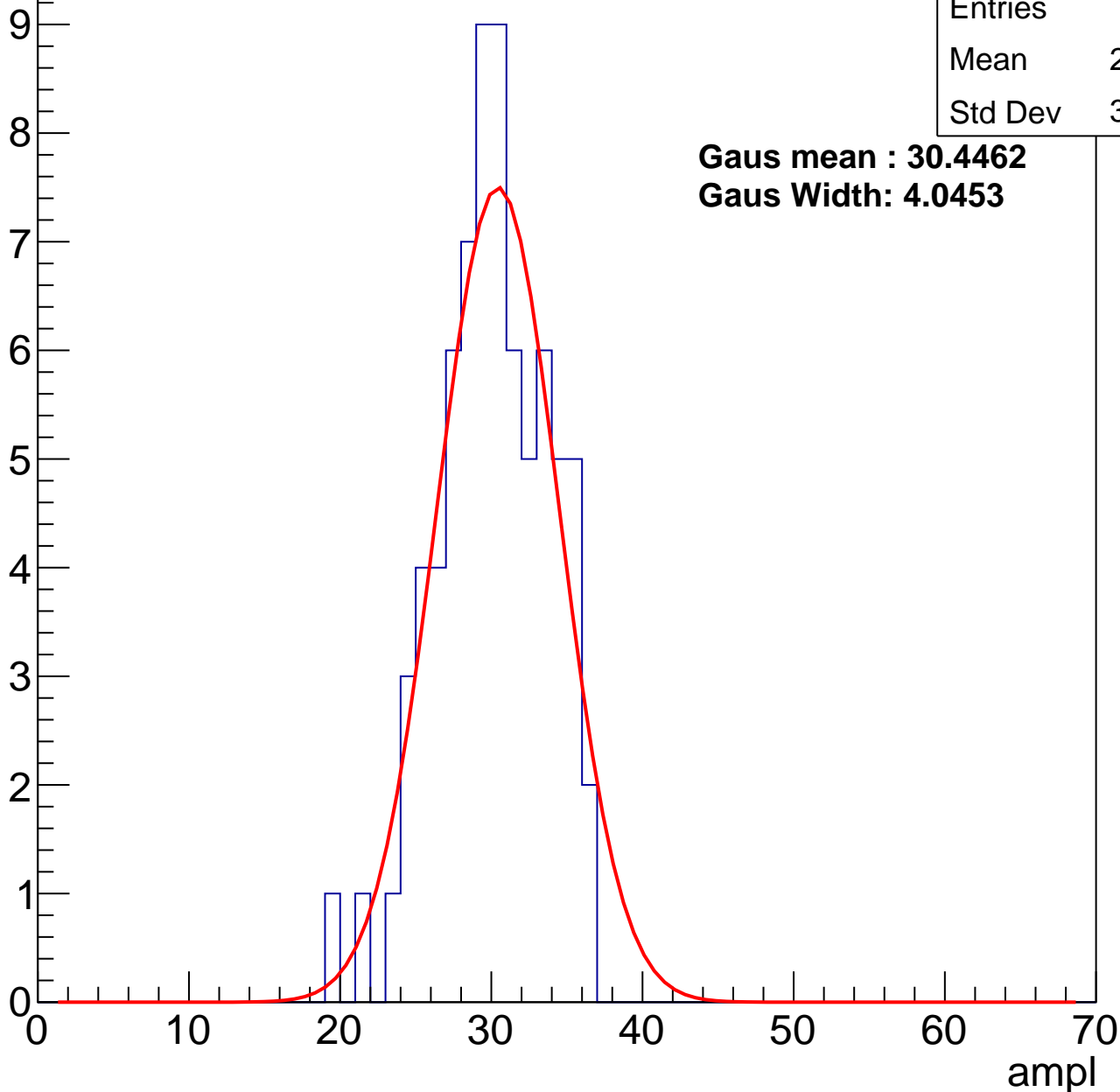
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.58
Std Dev	3.598

Gaus mean : 30.4462

Gaus Width: 4.0453



B1L101S, U23-ch86, adc1

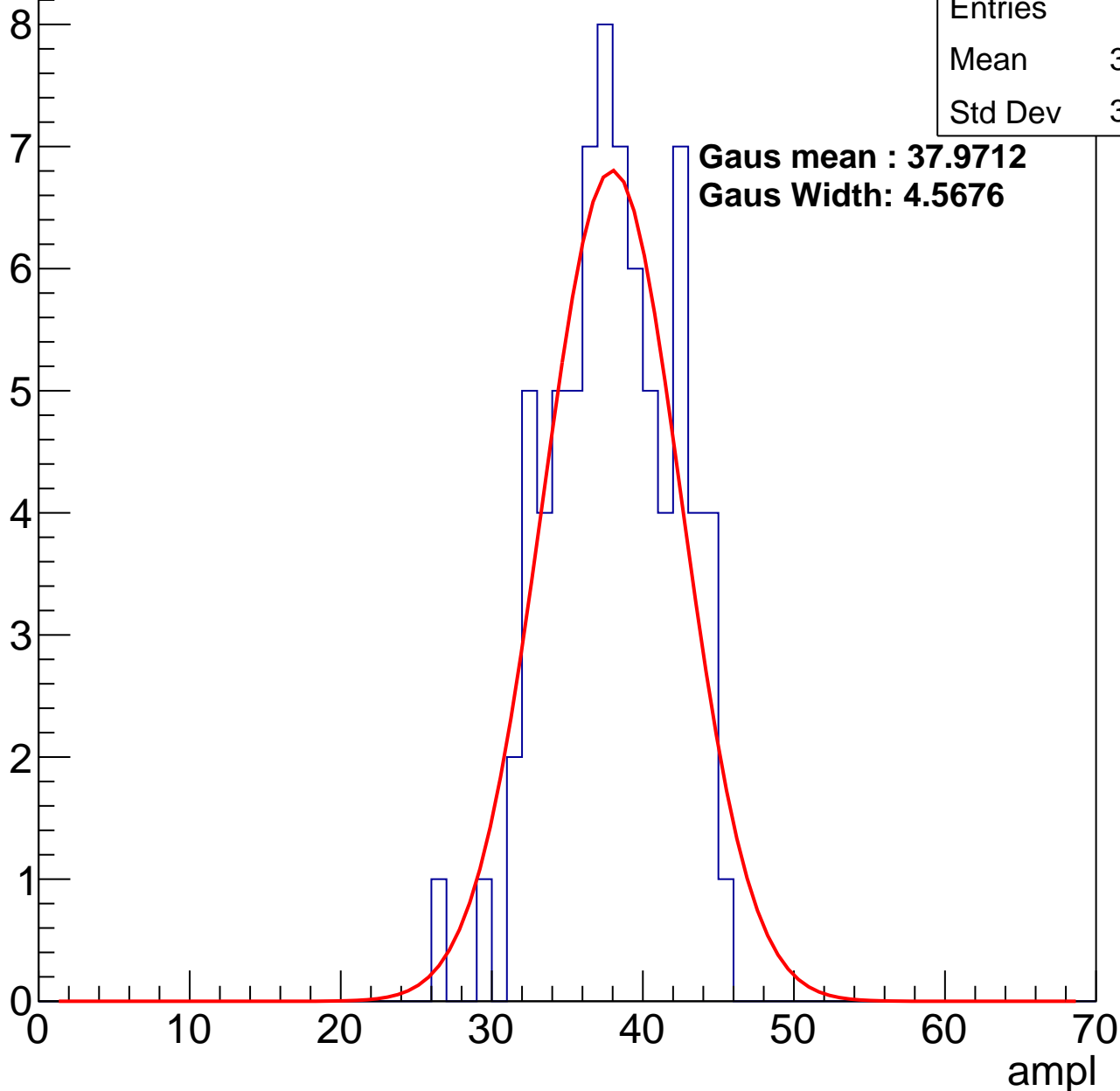
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.54
Std Dev	3.998

Gaus mean : 37.9712

Gaus Width: 4.5676



B1L101S, U23-ch86, adc2

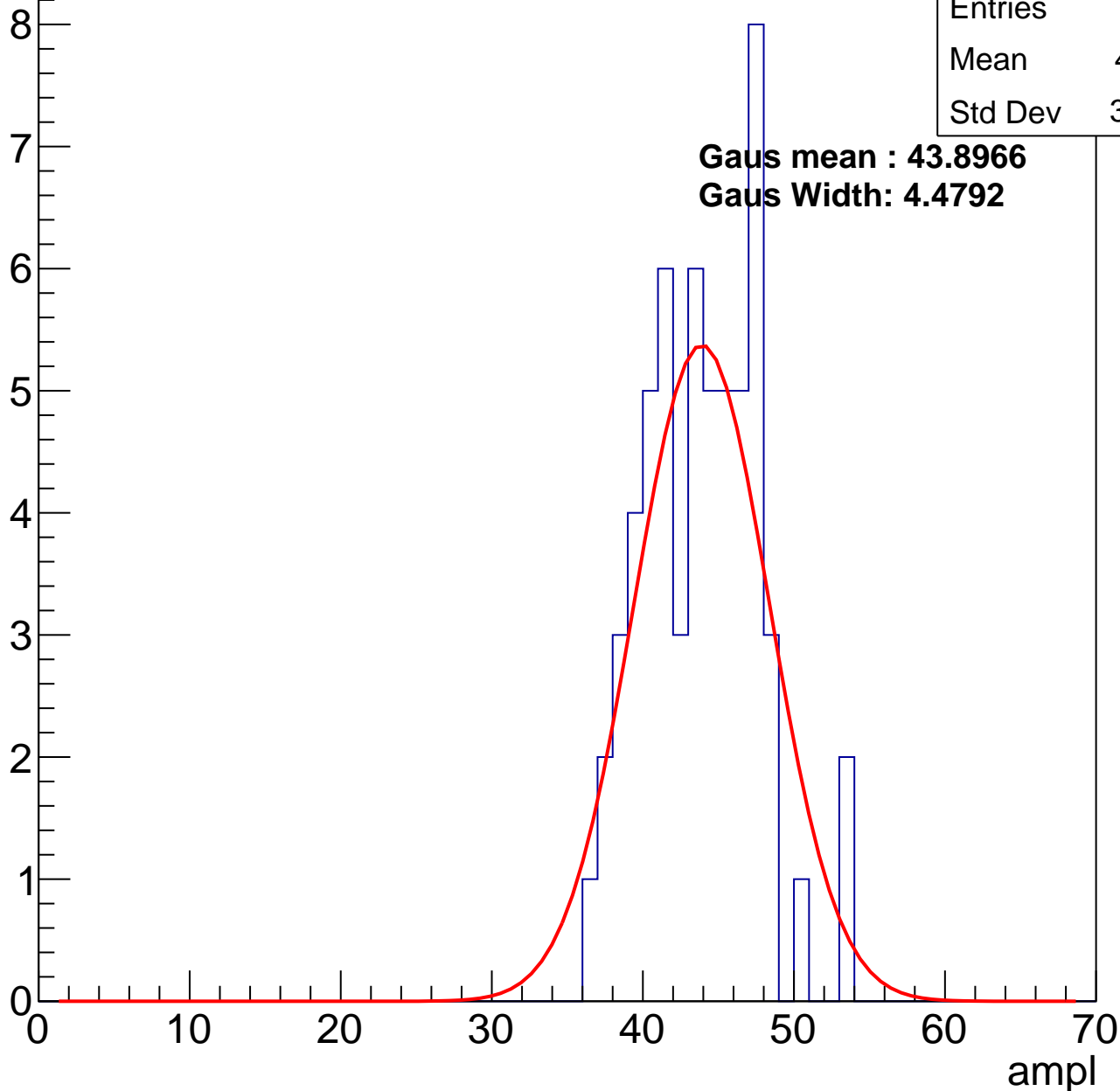
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.41
Std Dev	3.783

Gaus mean : 43.8966

Gaus Width: 4.4792

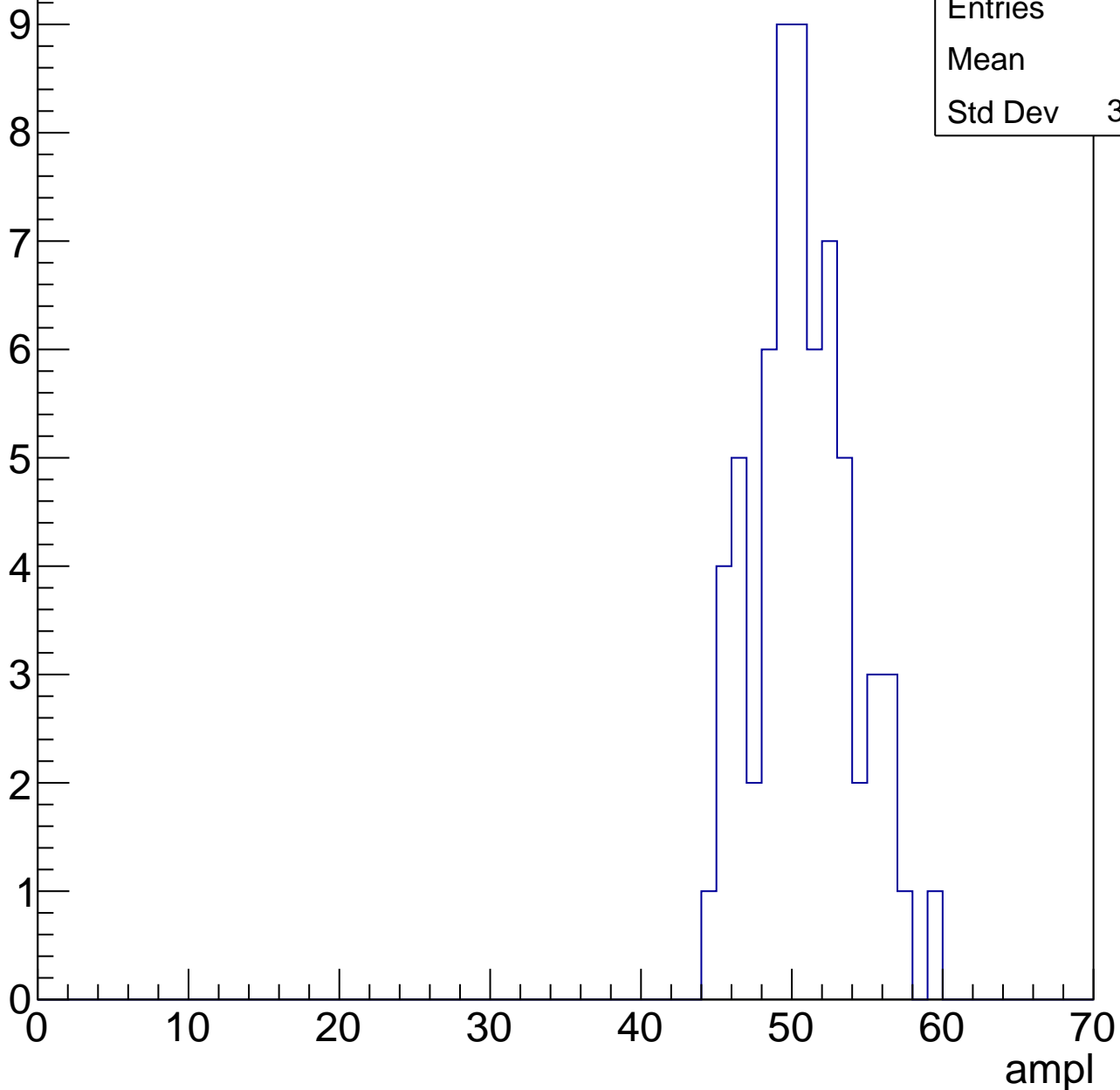


B1L101S, U23-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.3
Std Dev	3.272

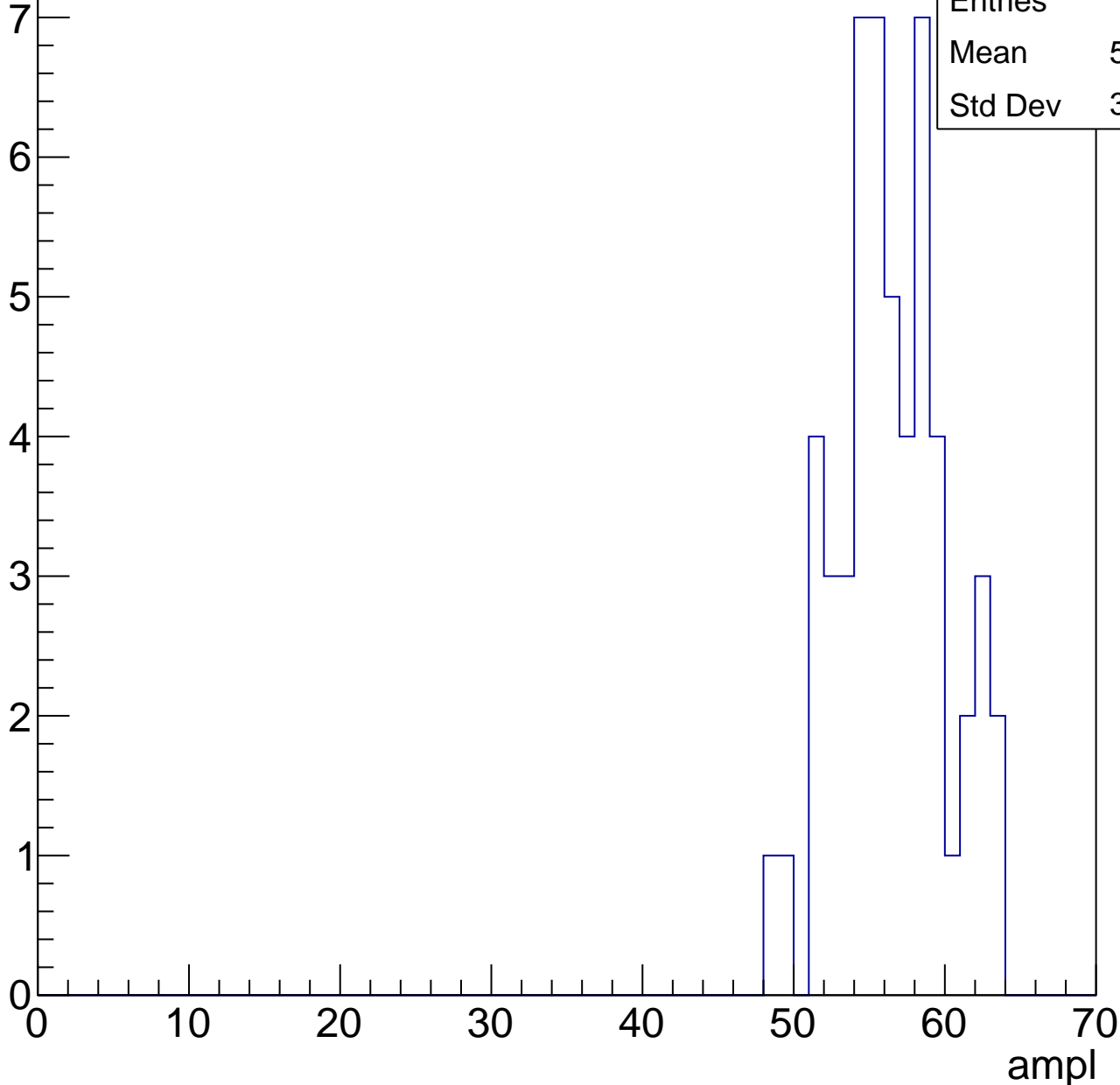


B1L101S, U23-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.98
Std Dev	3.504



B1L101S, U23-ch86, adc5

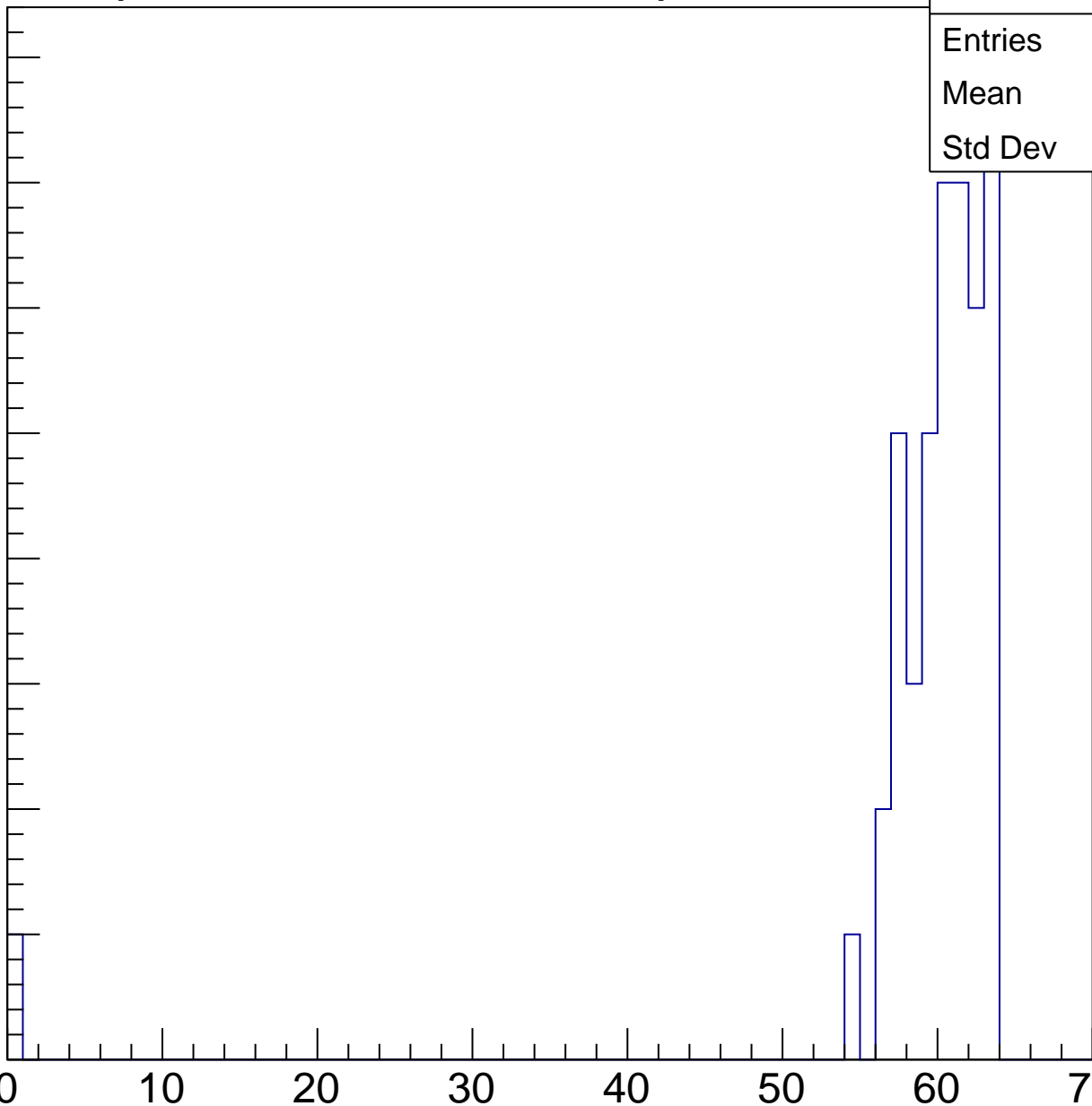
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	58.73
Std Dev	9.142

ampl



B1L101S, U23-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	60.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L101S, U23-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch87, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	32.7
Std Dev	4.932

Gaus mean : 33.8818

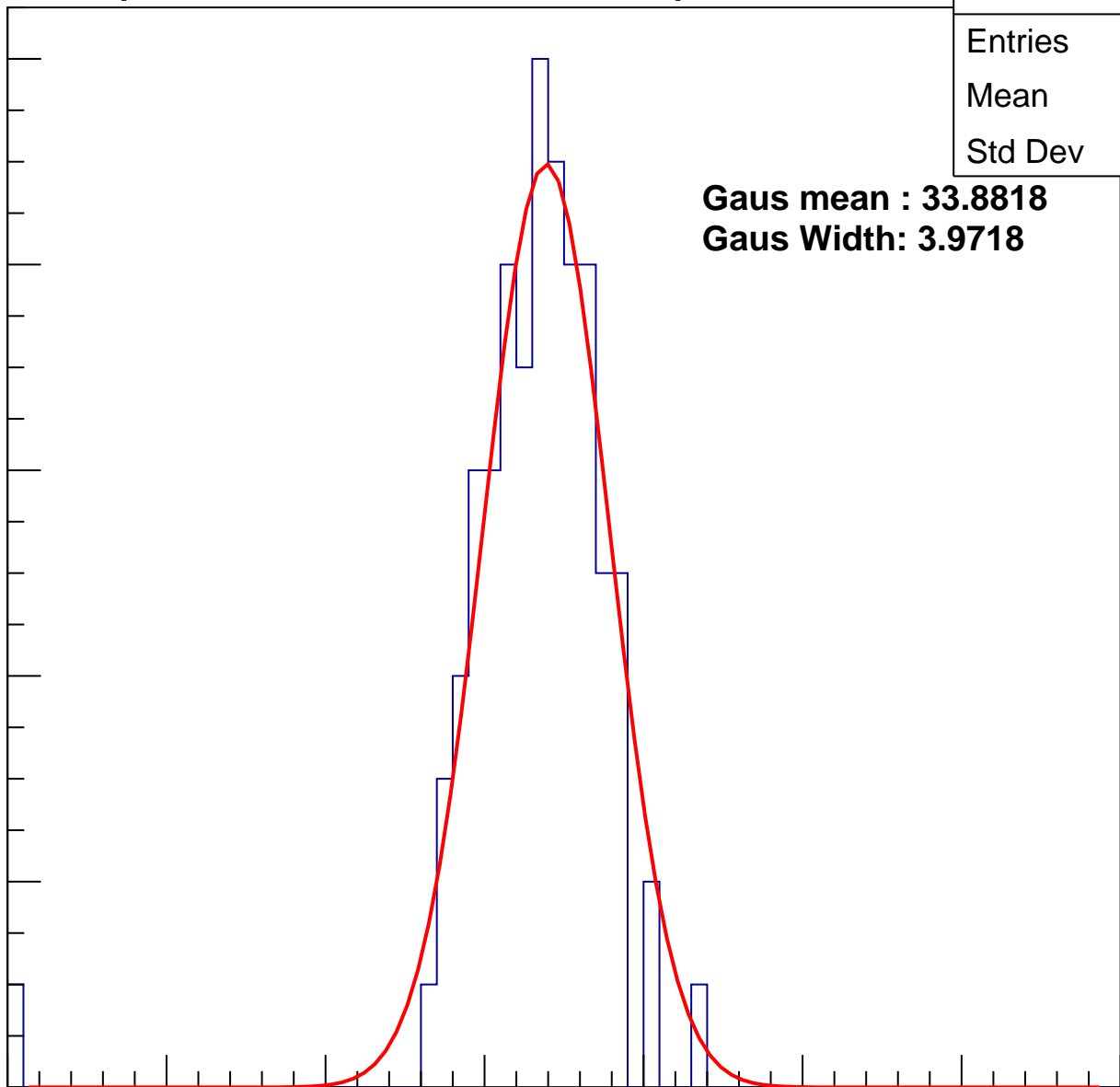
Gaus Width: 3.9718

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch87, adc1

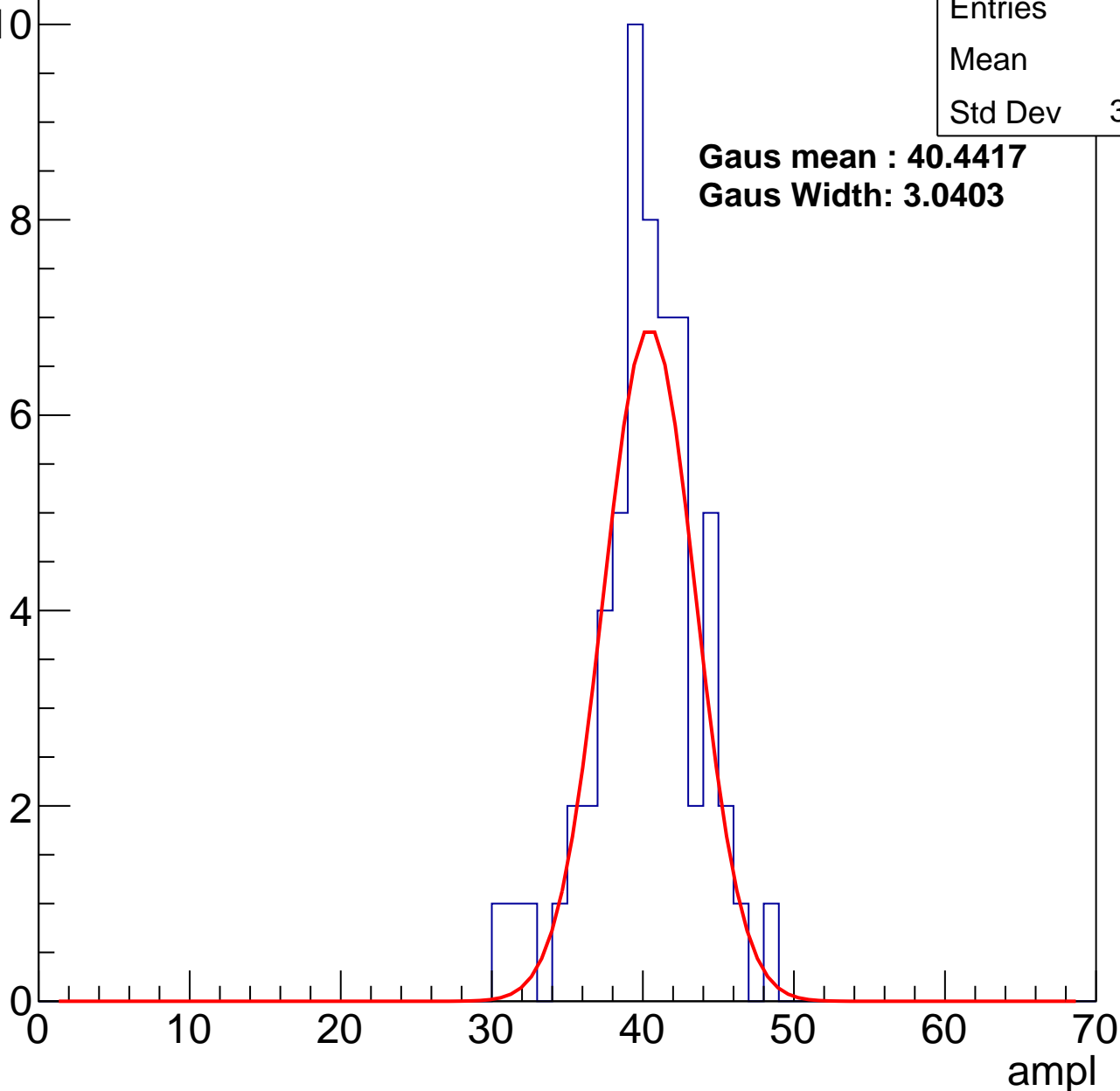
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	39.8
Std Dev	3.439

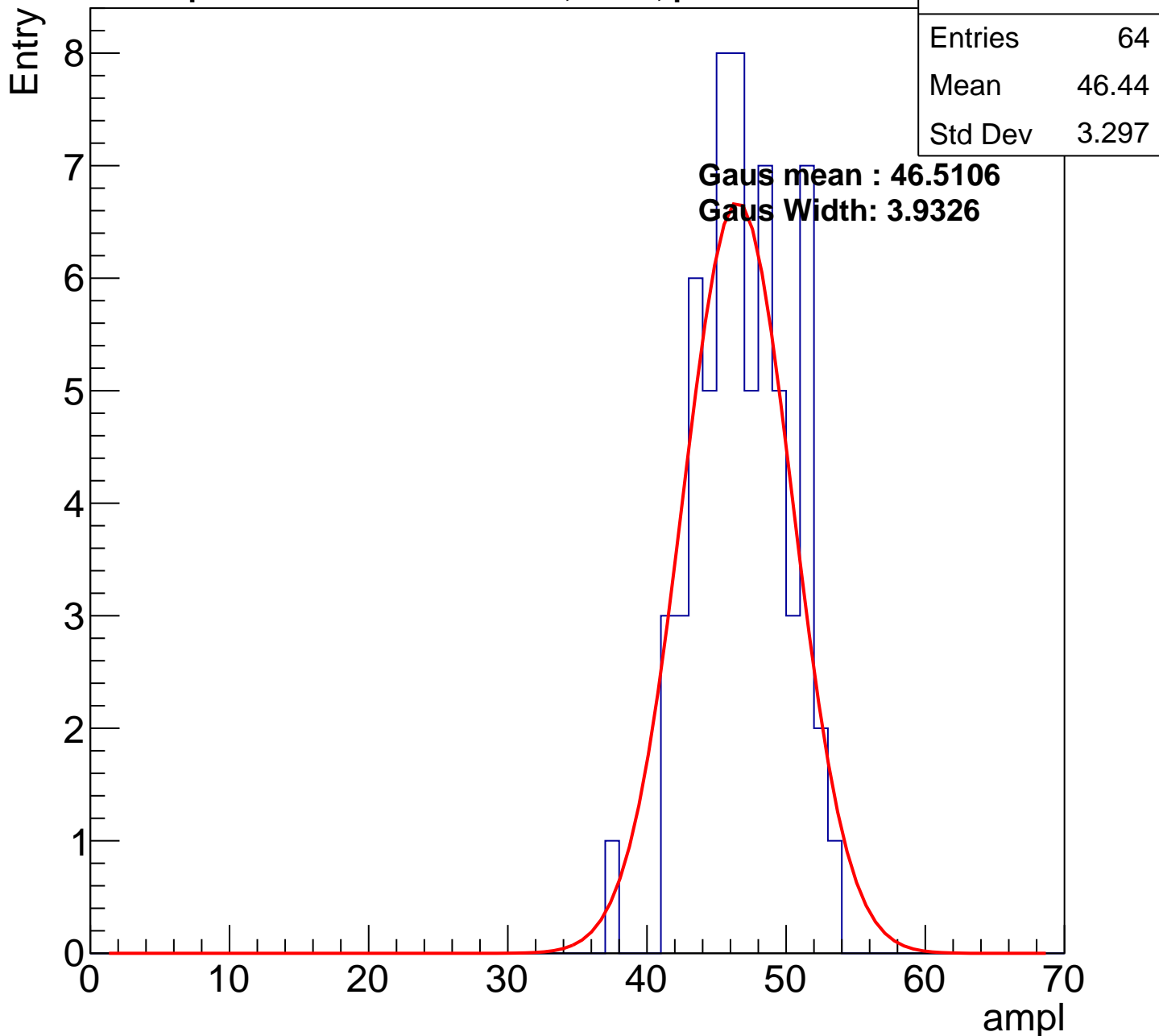
Gaus mean : 40.4417

Gaus Width: 3.0403



B1L101S, U23-ch87, adc2

calib_packv5_042523_0143.root, FC#0, port D2

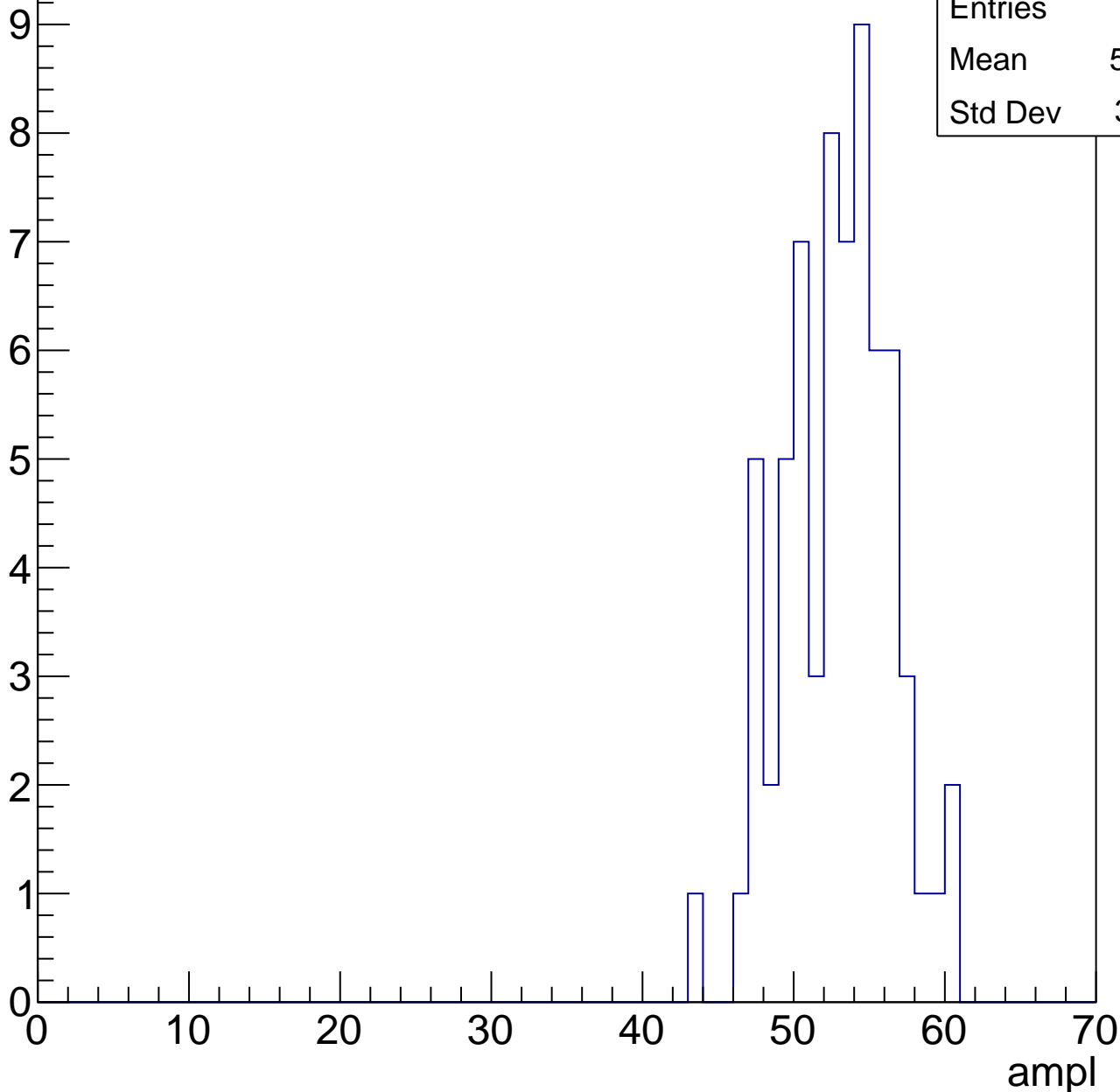


B1L101S, U23-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	52.46
Std Dev	3.491

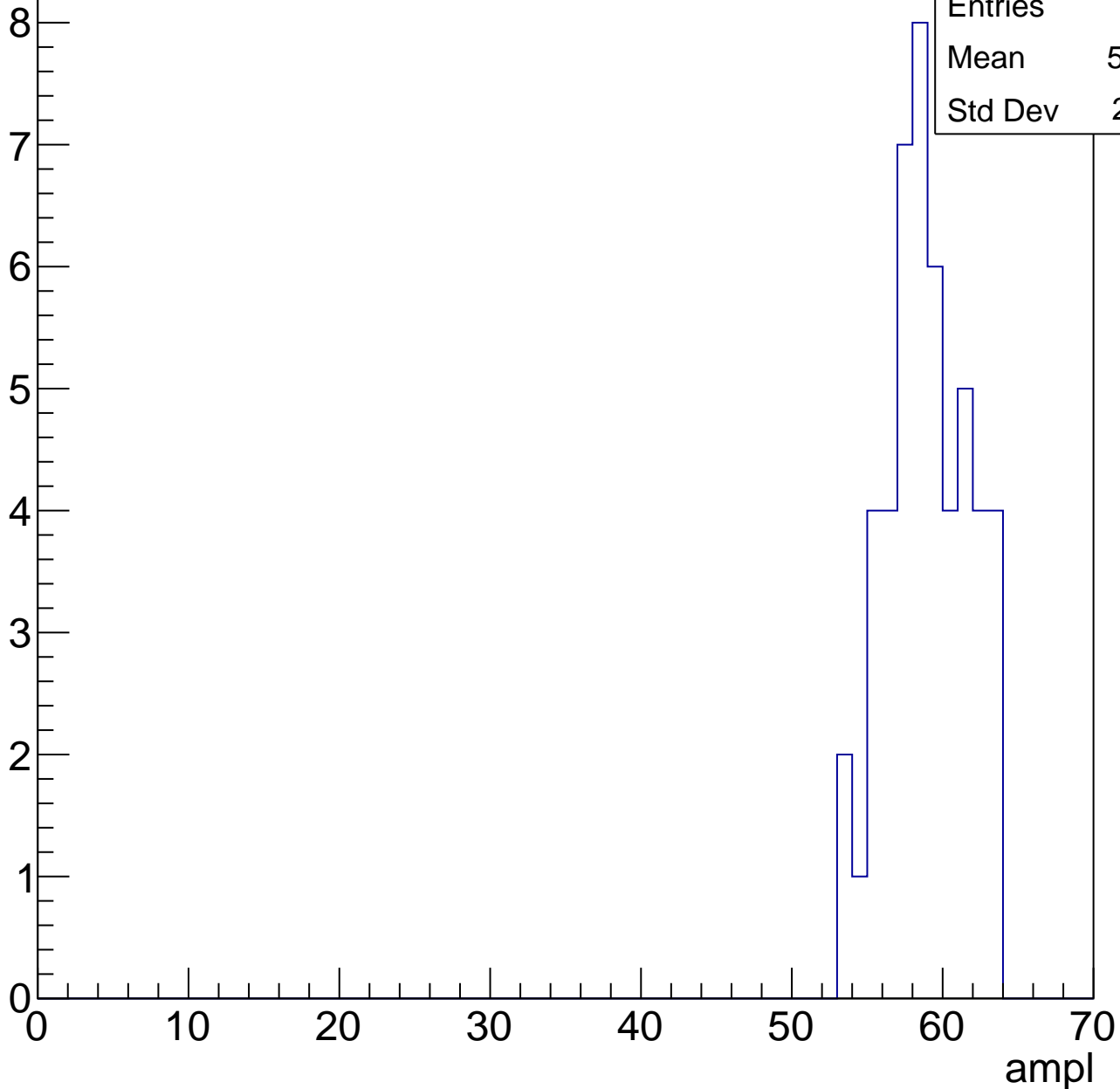


B1L101S, U23-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

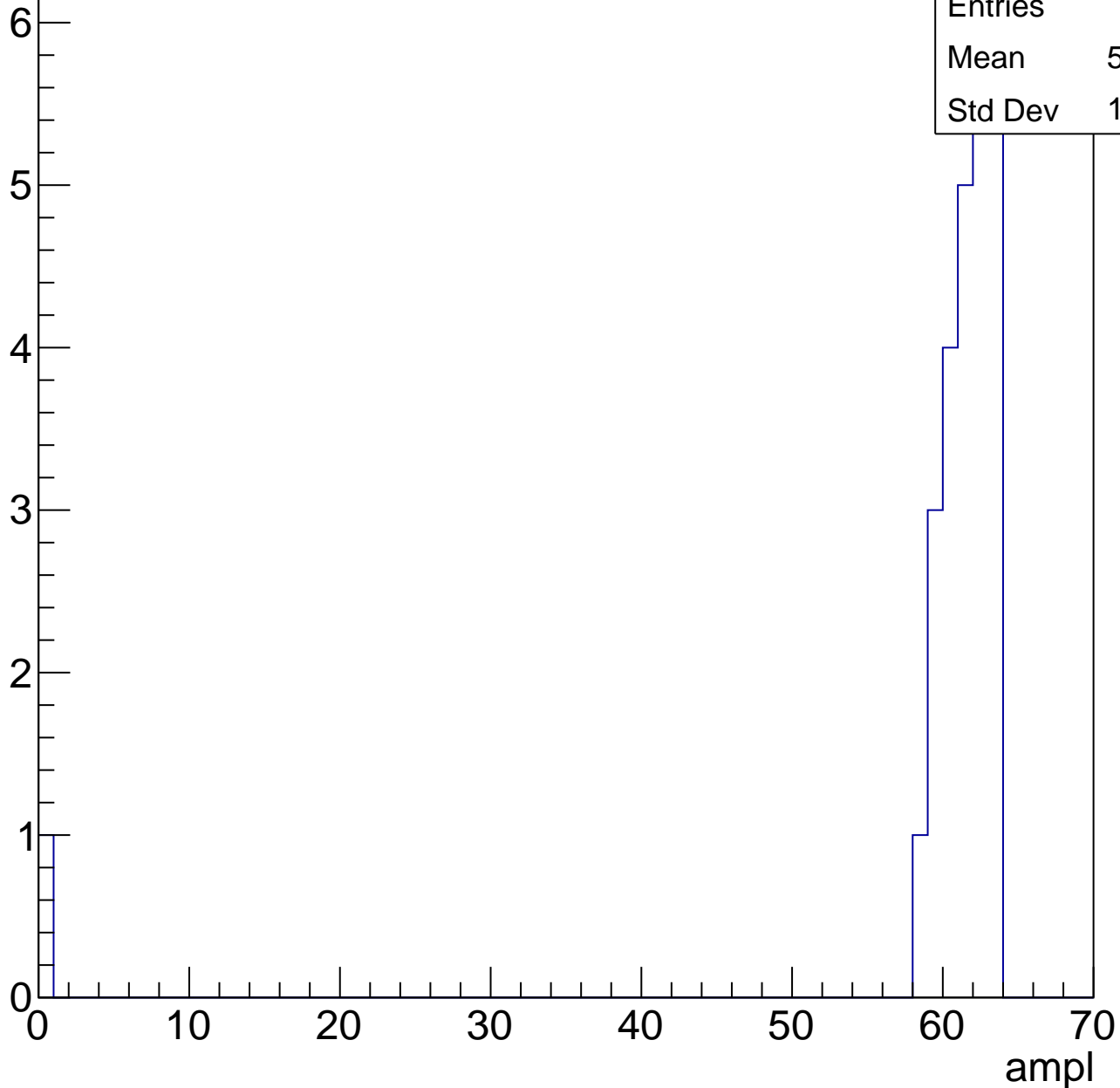
Entries	49
Mean	58.49
Std Dev	2.651



B1L101S, U23-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch88, adc0

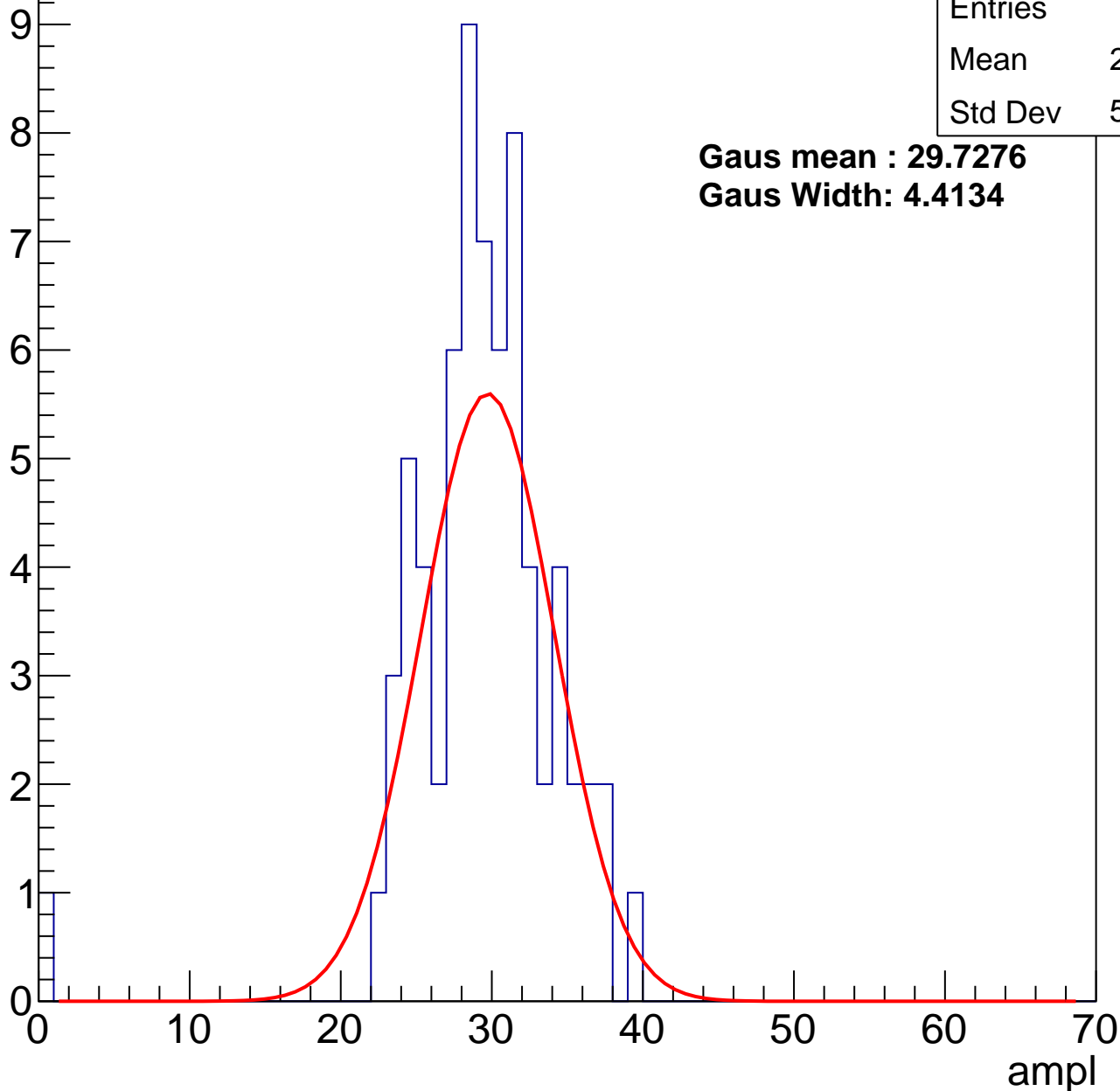
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.88
Std Dev	5.165

Gaus mean : 29.7276

Gaus Width: 4.4134



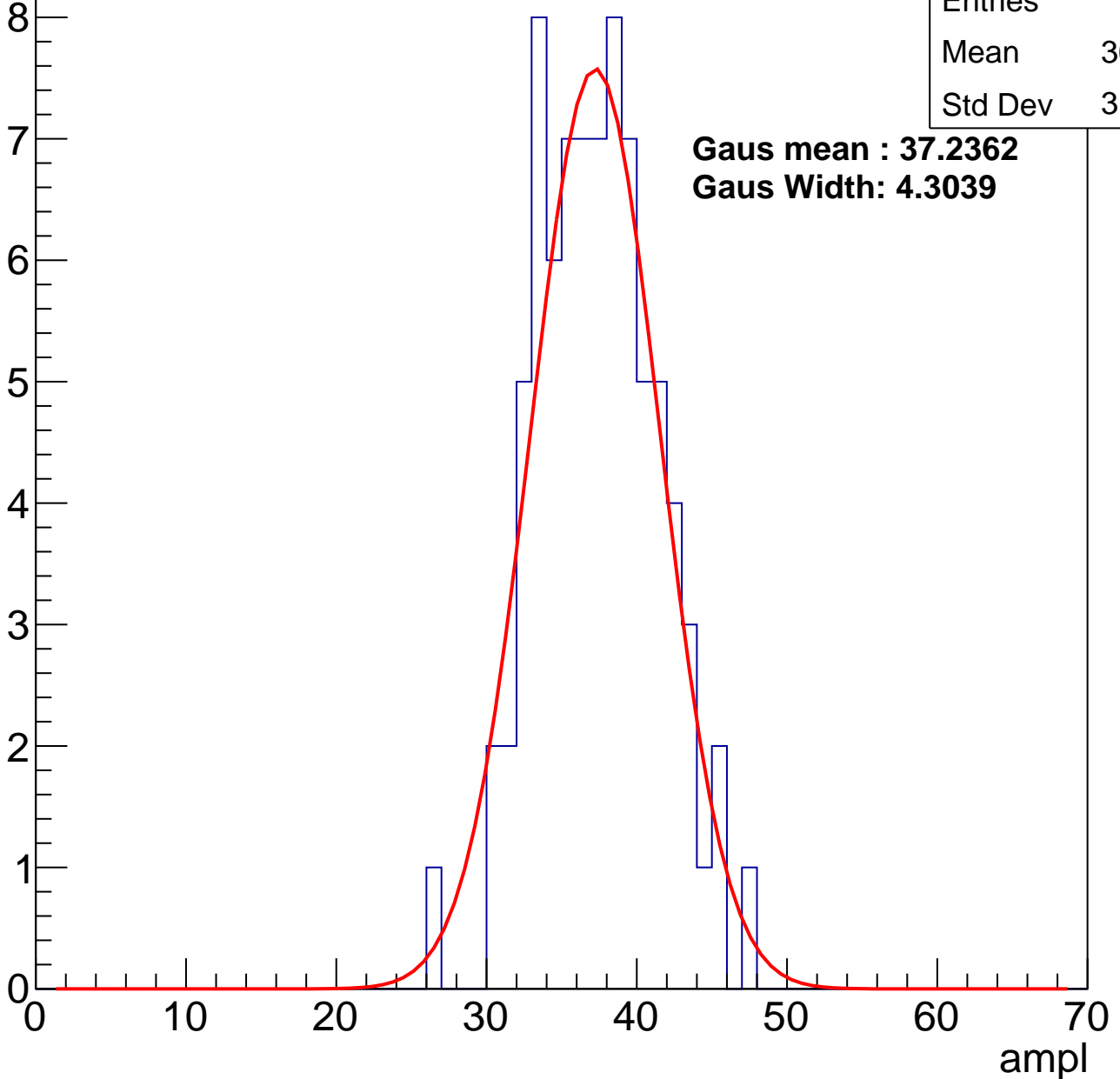
B1L101S, U23-ch88, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	36.94
Std Dev	3.973

Gaus mean : 37.2362
Gaus Width: 4.3039

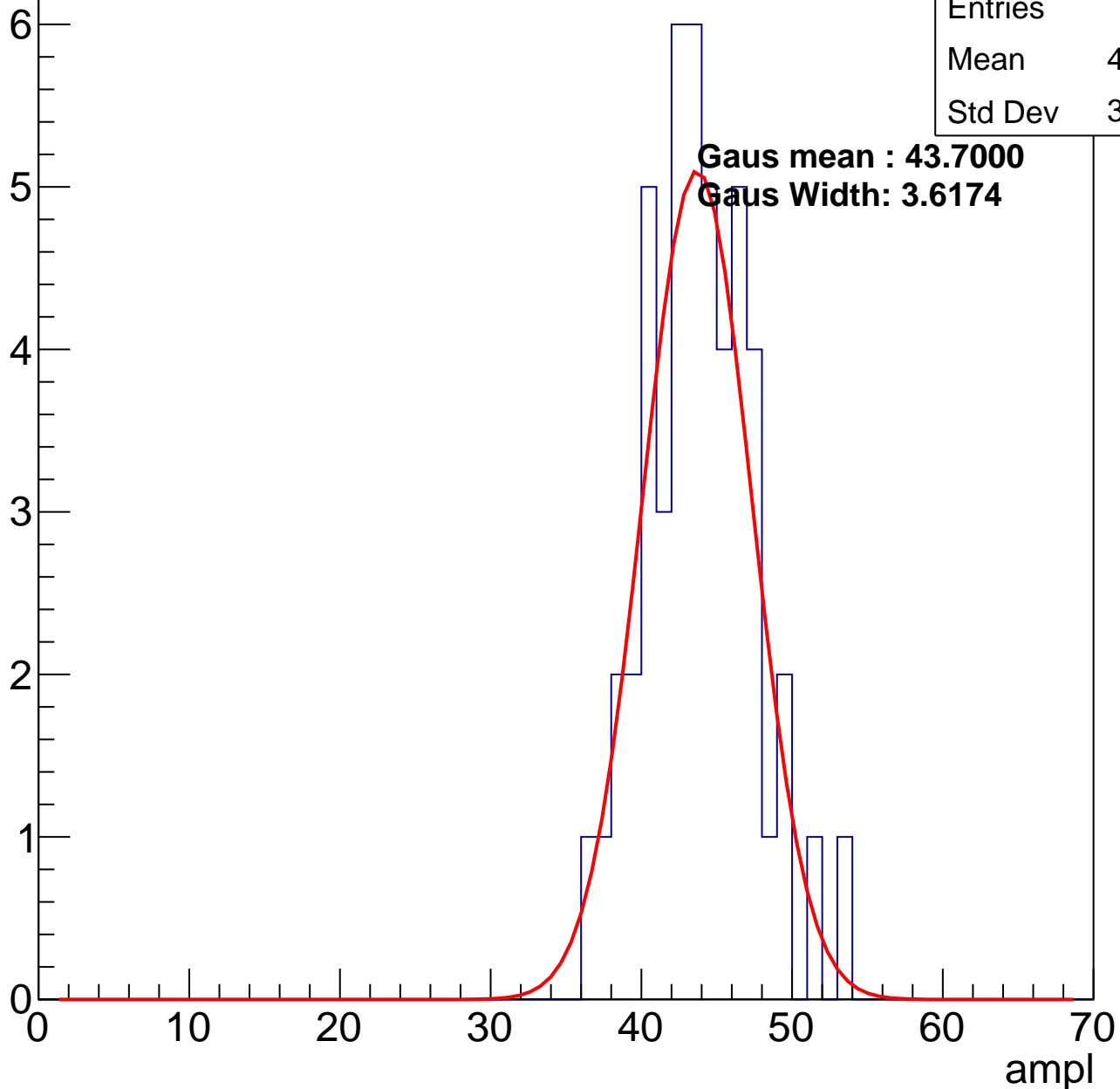


B1L101S, U23-ch88, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	43.43
Std Dev	3.534

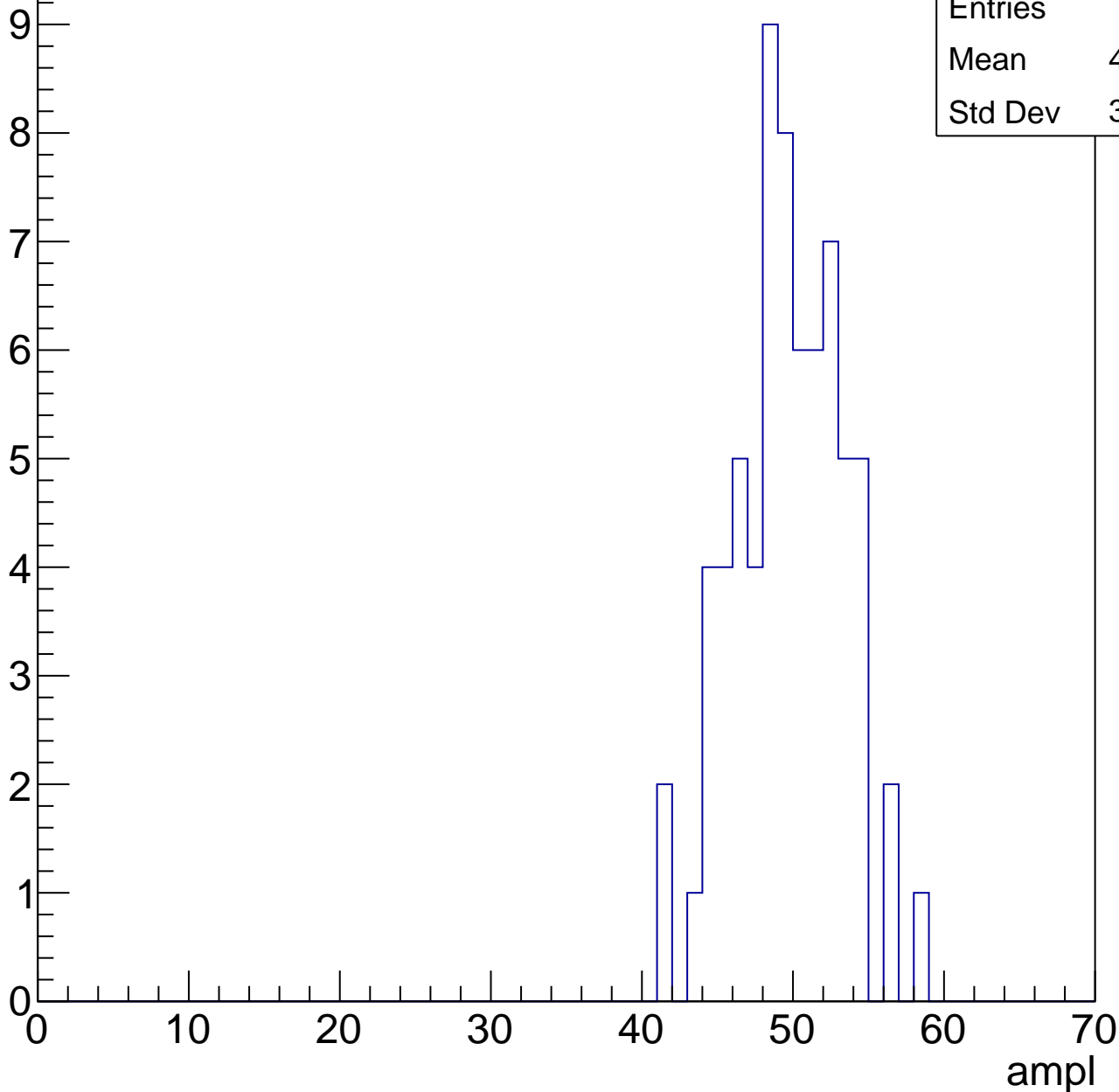


B1L101S, U23-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.25
Std Dev	3.553

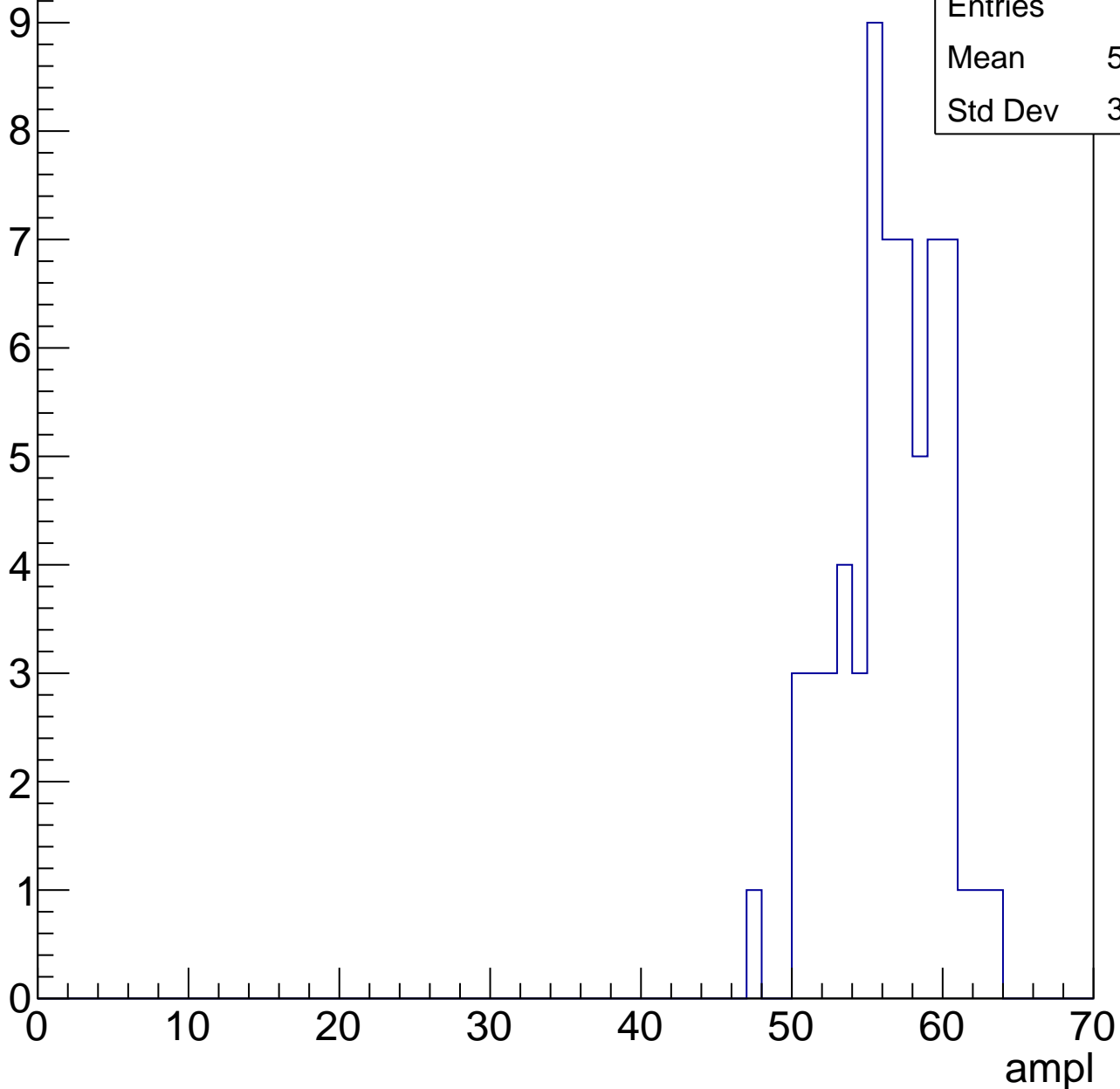


B1L101S, U23-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.05
Std Dev	3.314

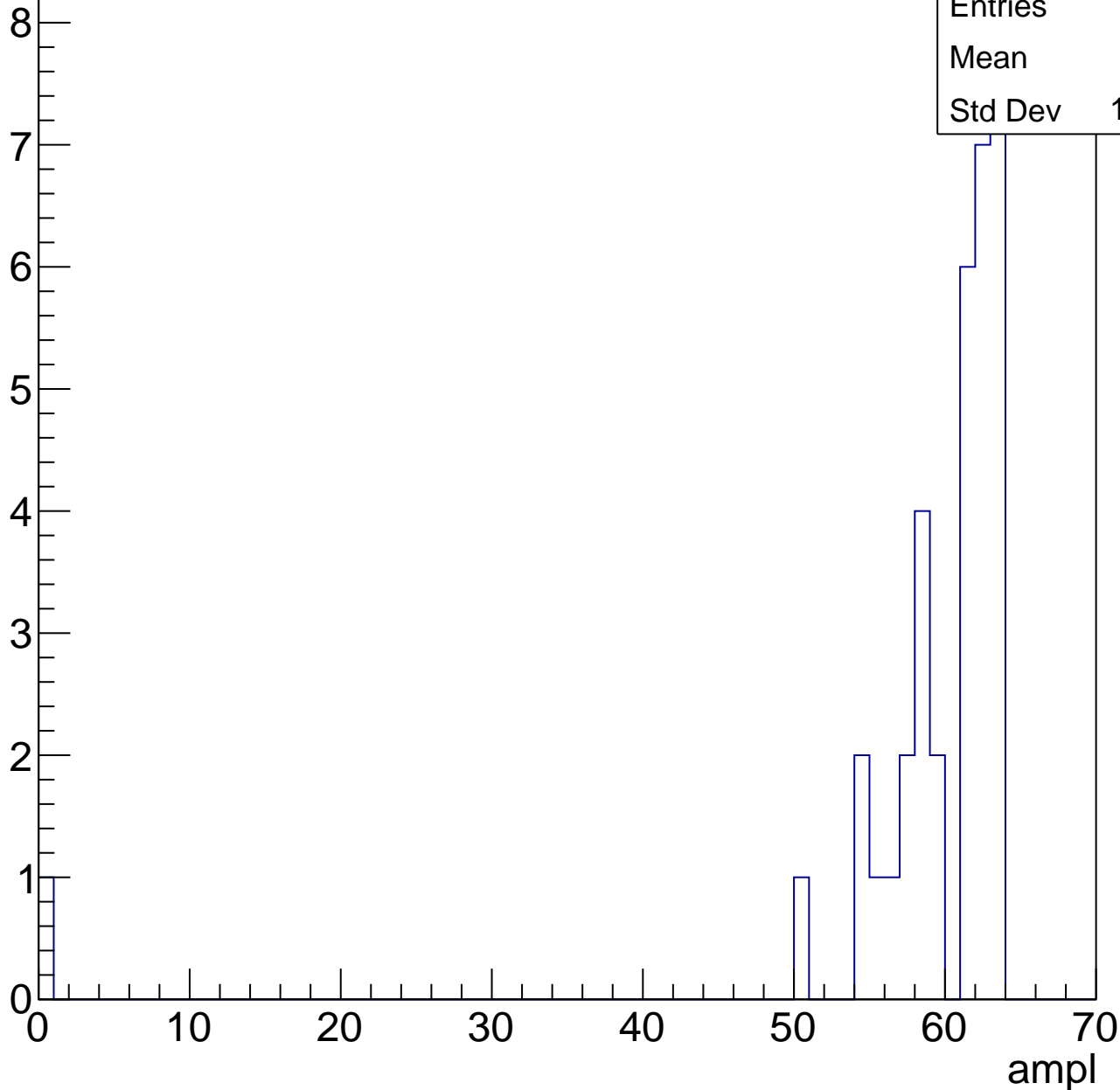


B1L101S, U23-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.2
Std Dev	10.48



B1L101S, U23-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	60.55
Std Dev	1.559

B1L101S, U23-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch89, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	30.24
Std Dev	3.958

Gaus mean : 30.9183

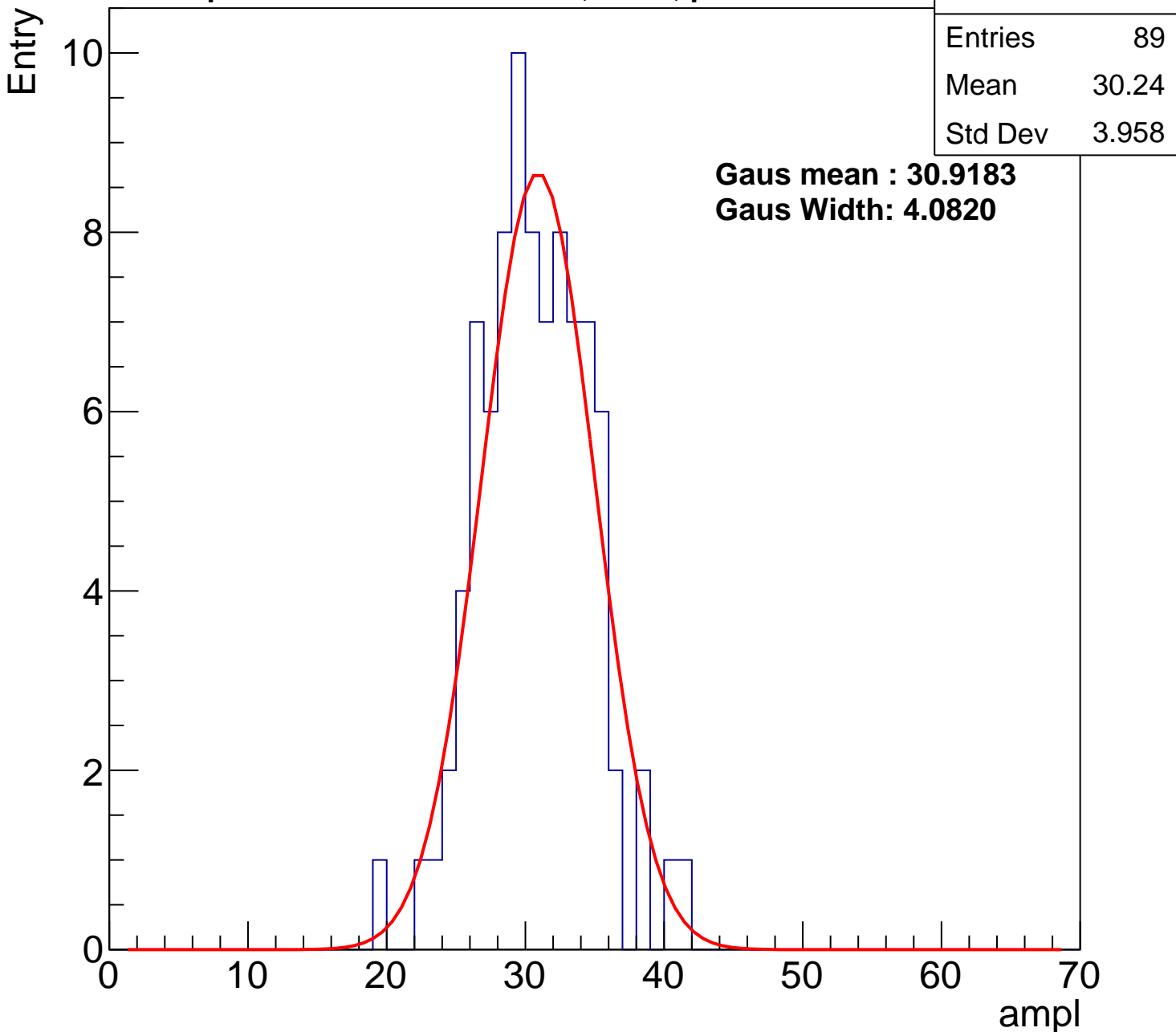
Gaus Width: 4.0820

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch89, adc1

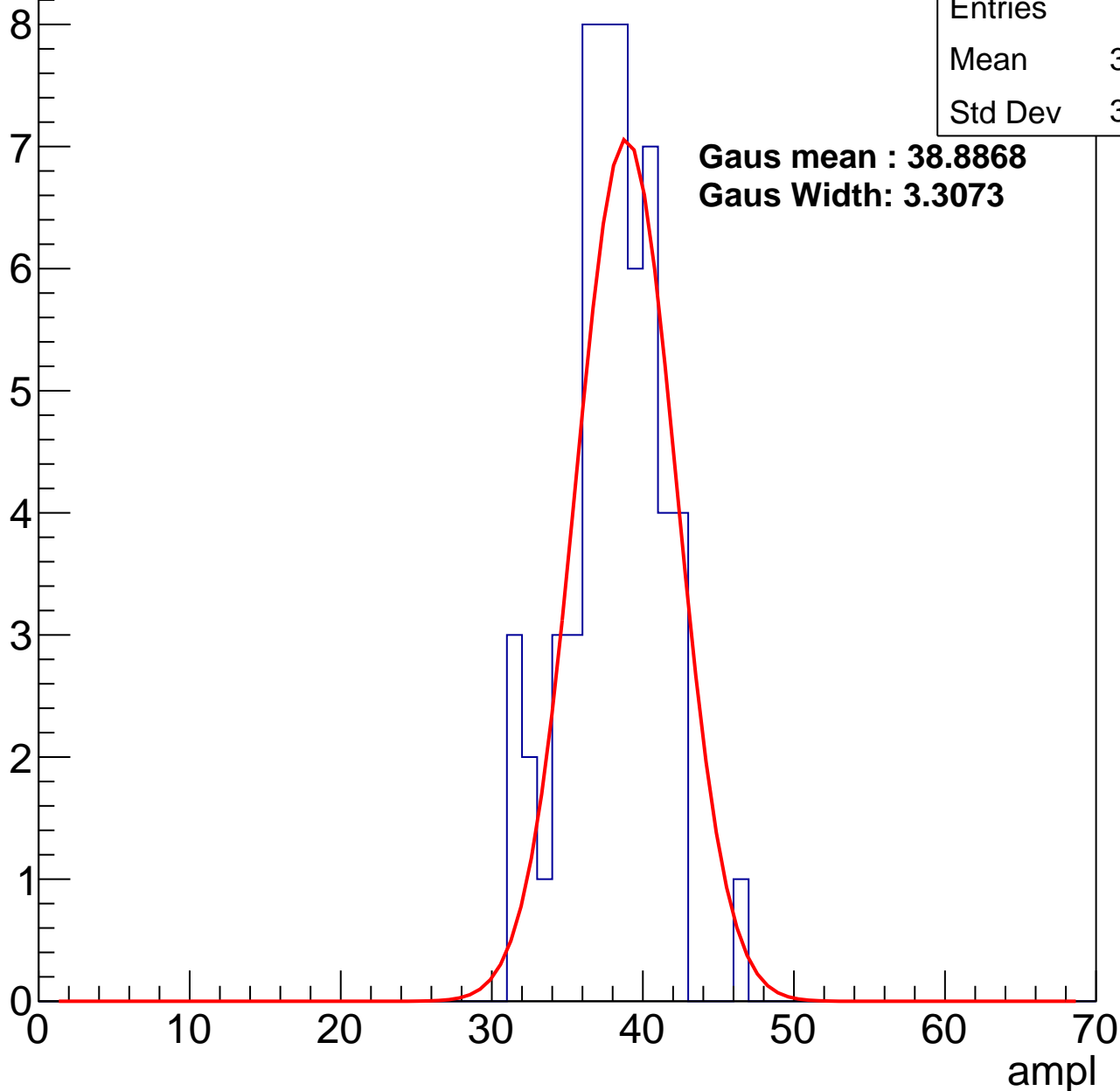
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.53
Std Dev	3.064

Gaus mean : 38.8868

Gaus Width: 3.3073



B1L101S, U23-ch89, adc2

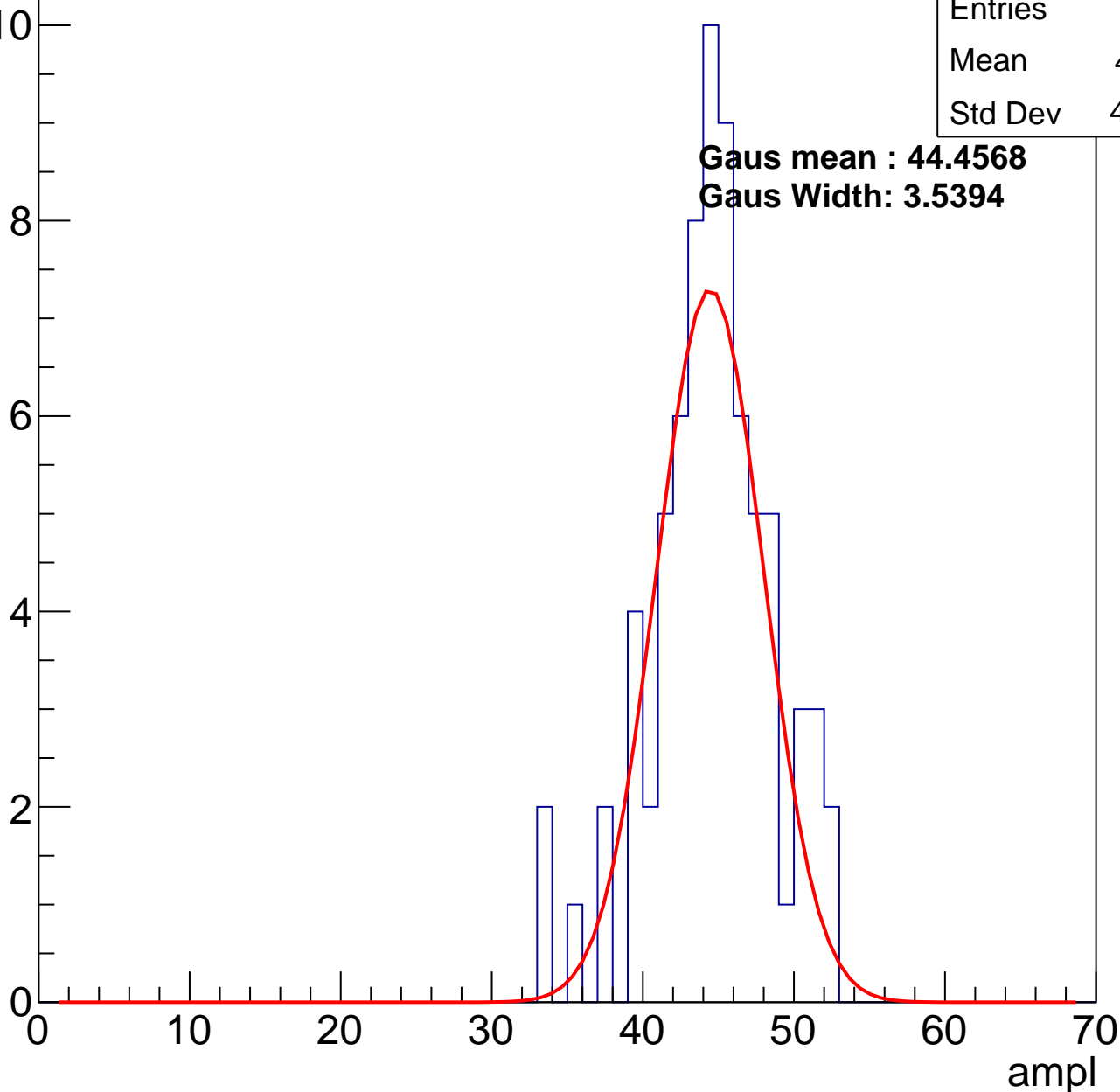
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.11
Std Dev	4.019

Gaus mean : 44.4568

Gaus Width: 3.5394

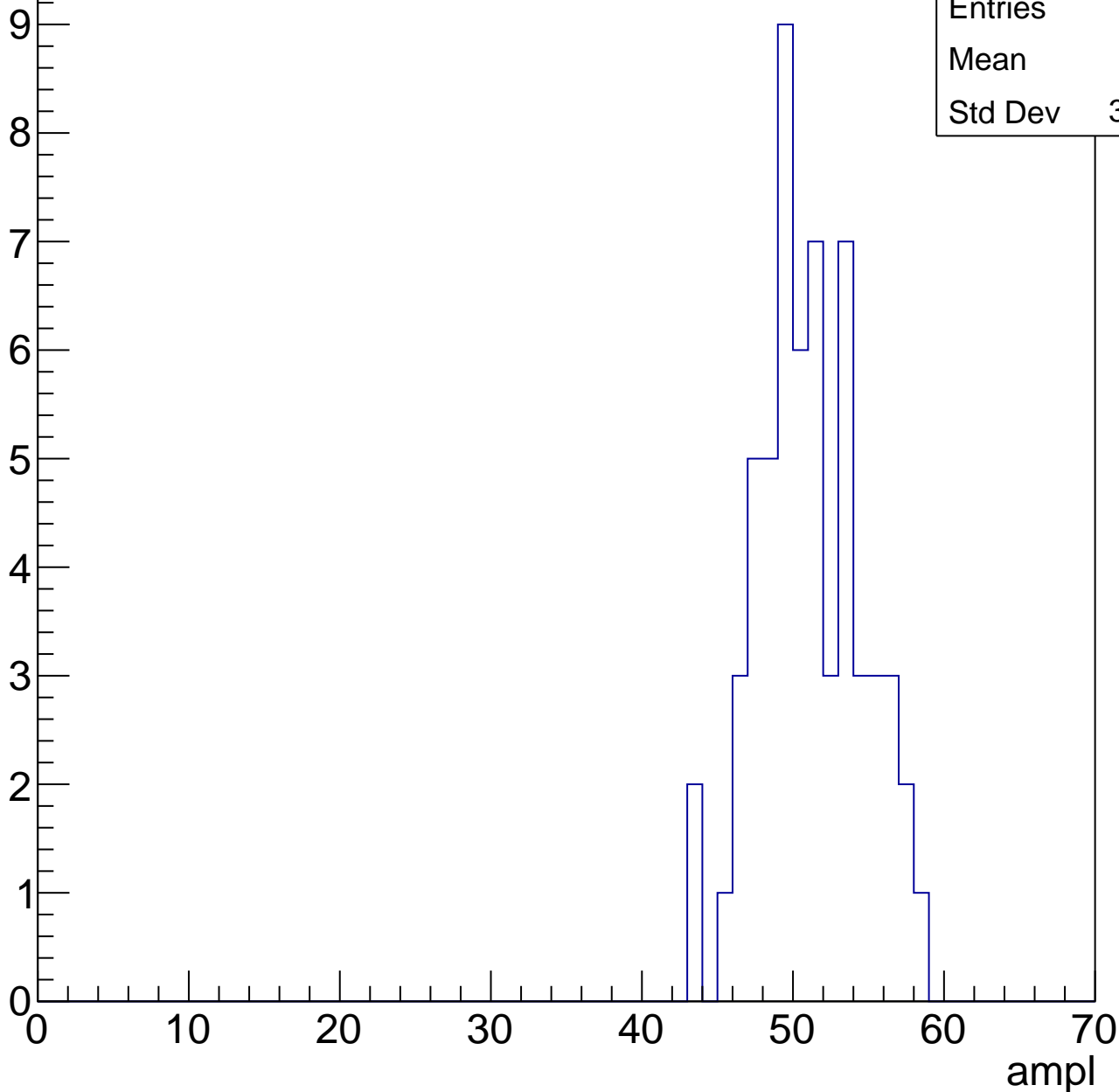


B1L101S, U23-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.6
Std Dev	3.412

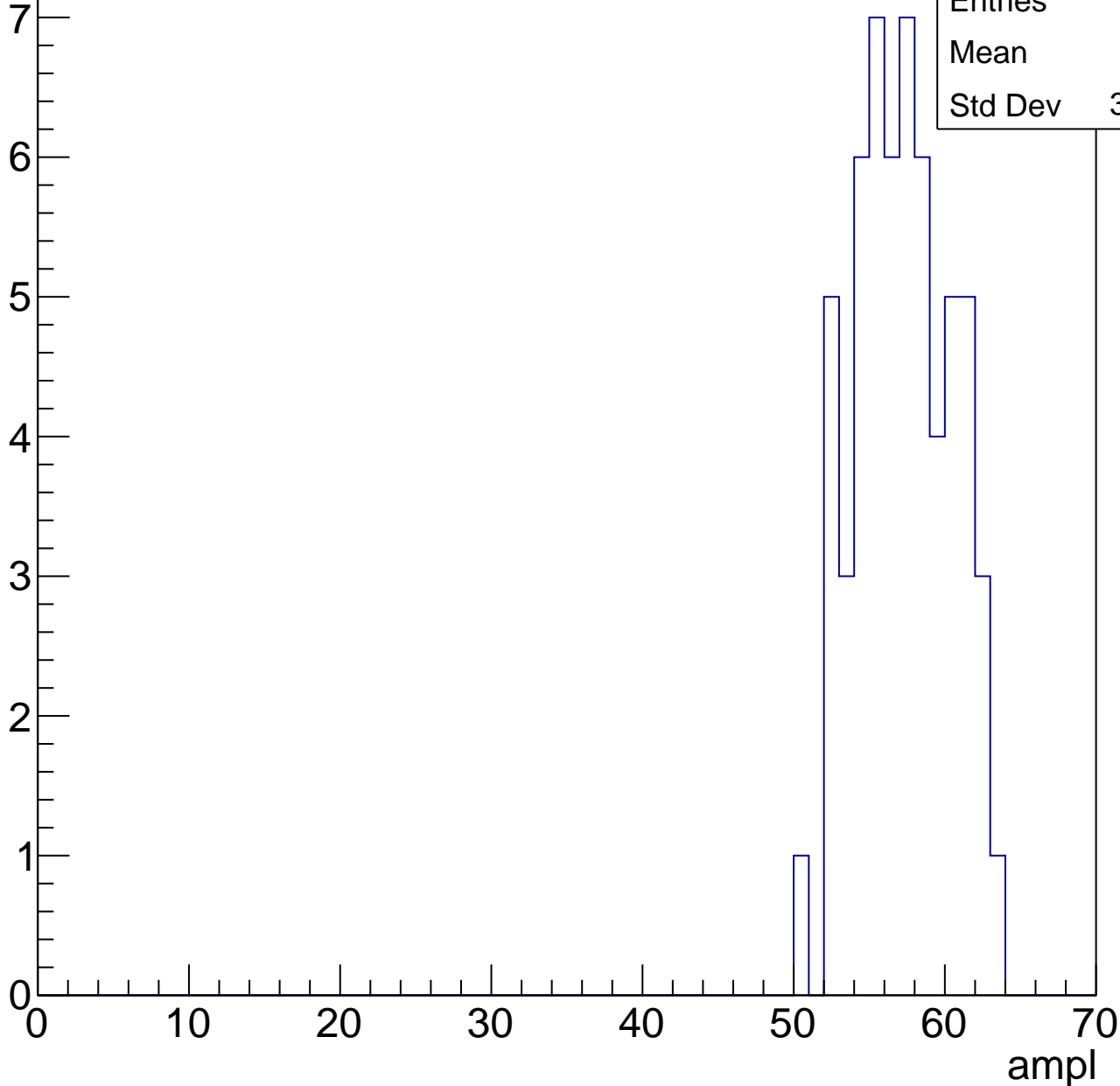


B1L101S, U23-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.8
Std Dev	3.096

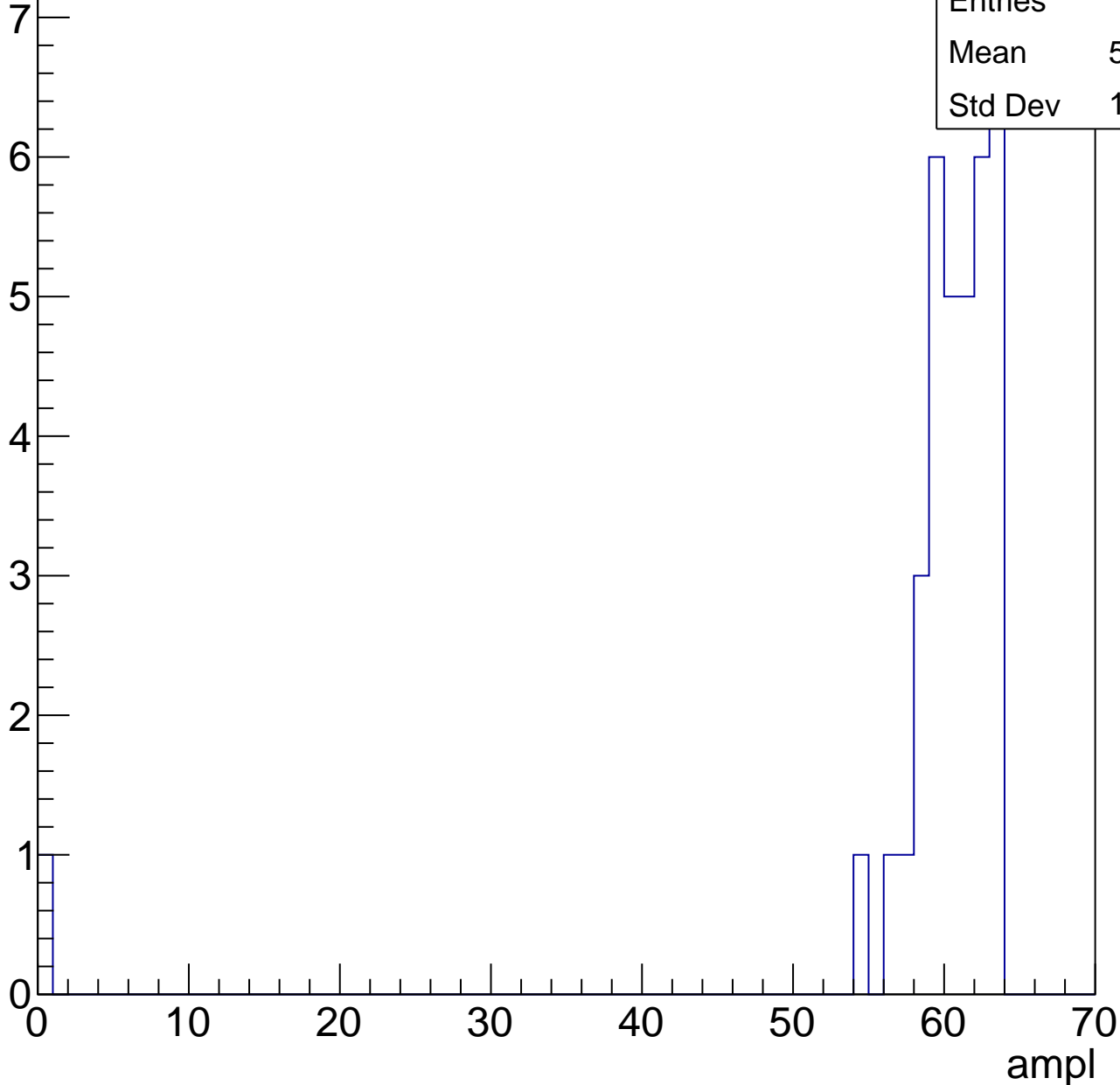


B1L101S, U23-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.69
Std Dev	10.15



B1L101S, U23-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch90, adc0

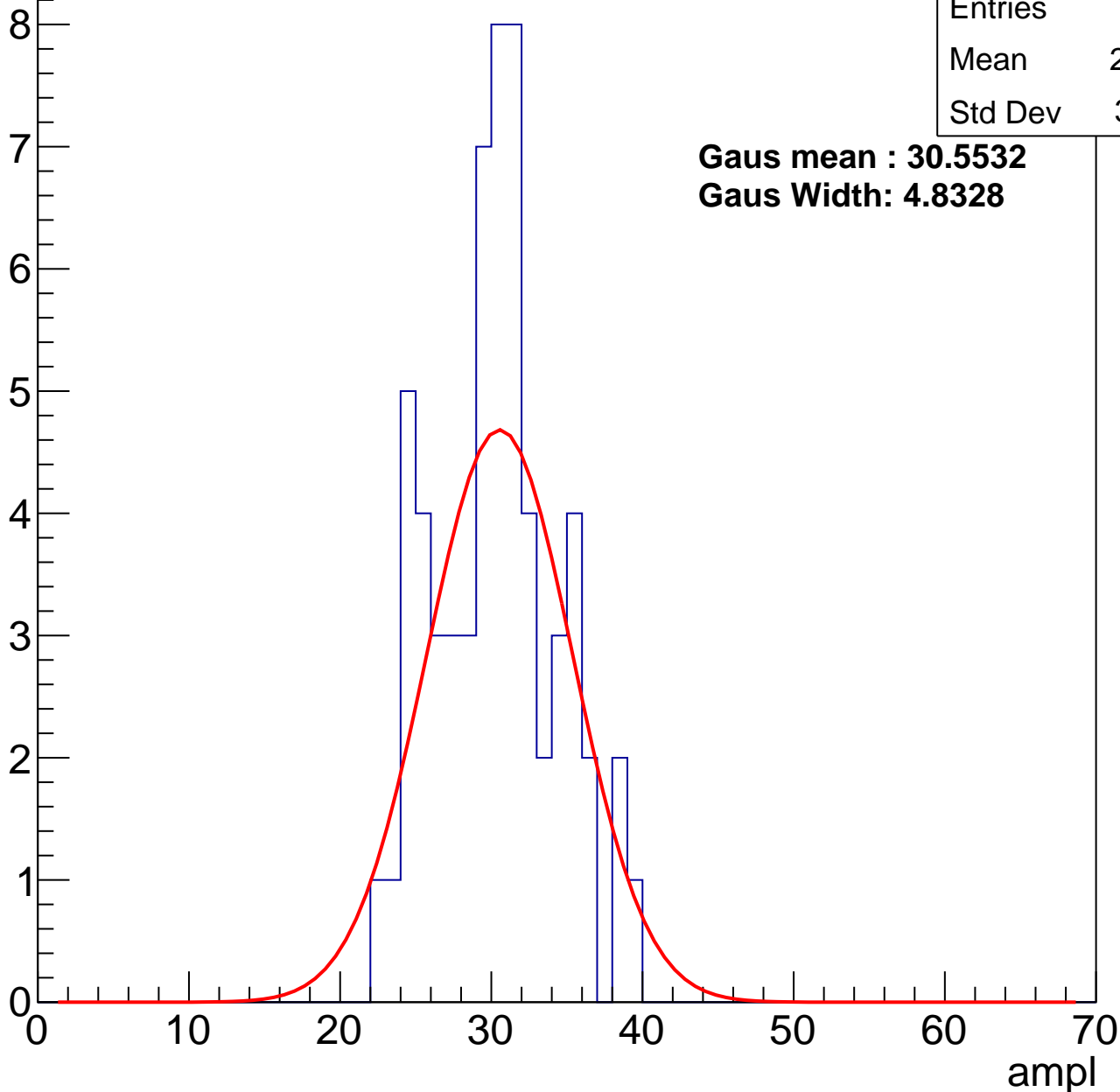
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.87
Std Dev	3.961

Gaus mean : 30.5532

Gaus Width: 4.8328



B1L101S, U23-ch90, adc1

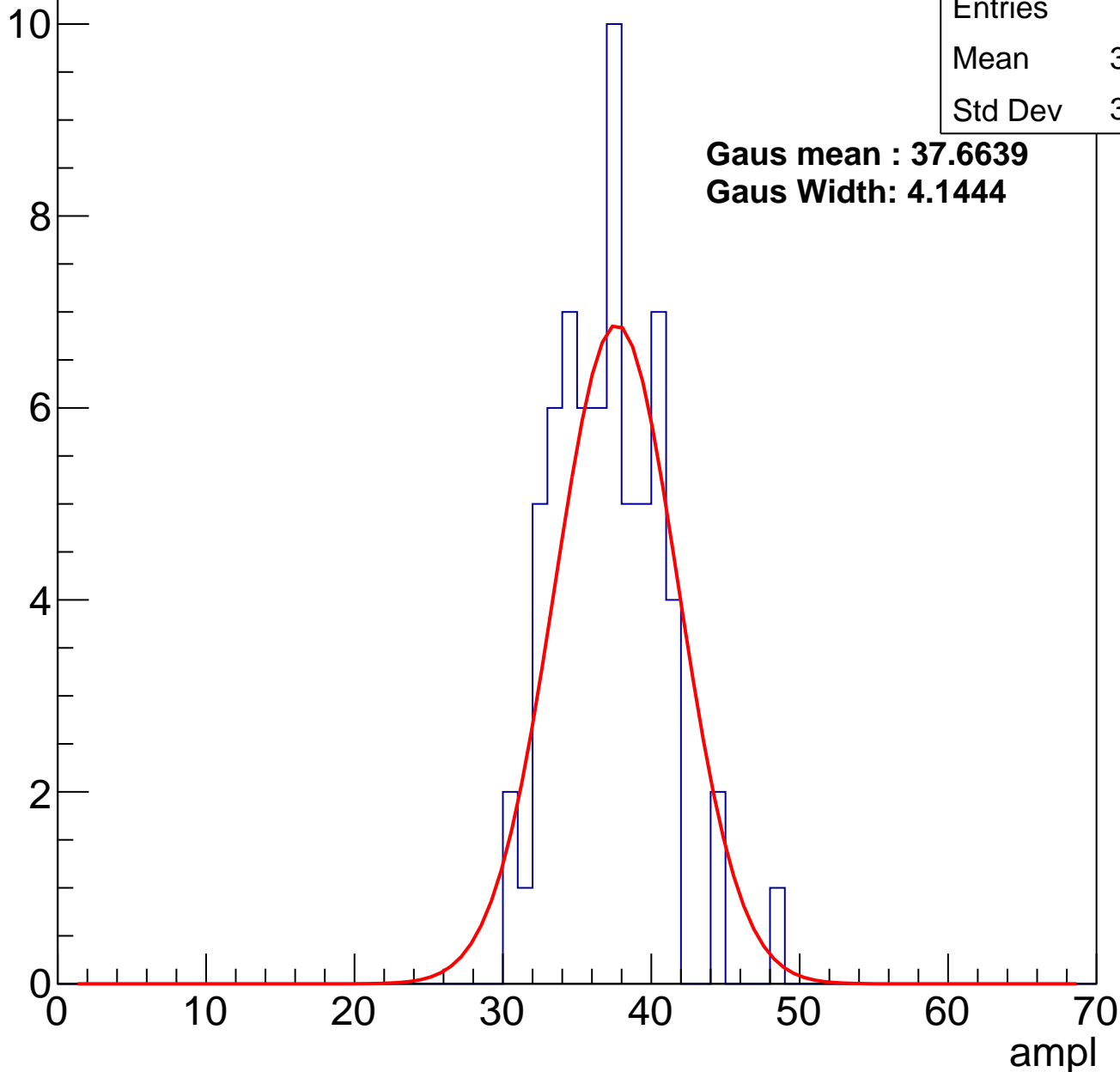
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	36.54
Std Dev	3.466

Gaus mean : 37.6639

Gaus Width: 4.1444

Entry



B1L101S, U23-ch90, adc2

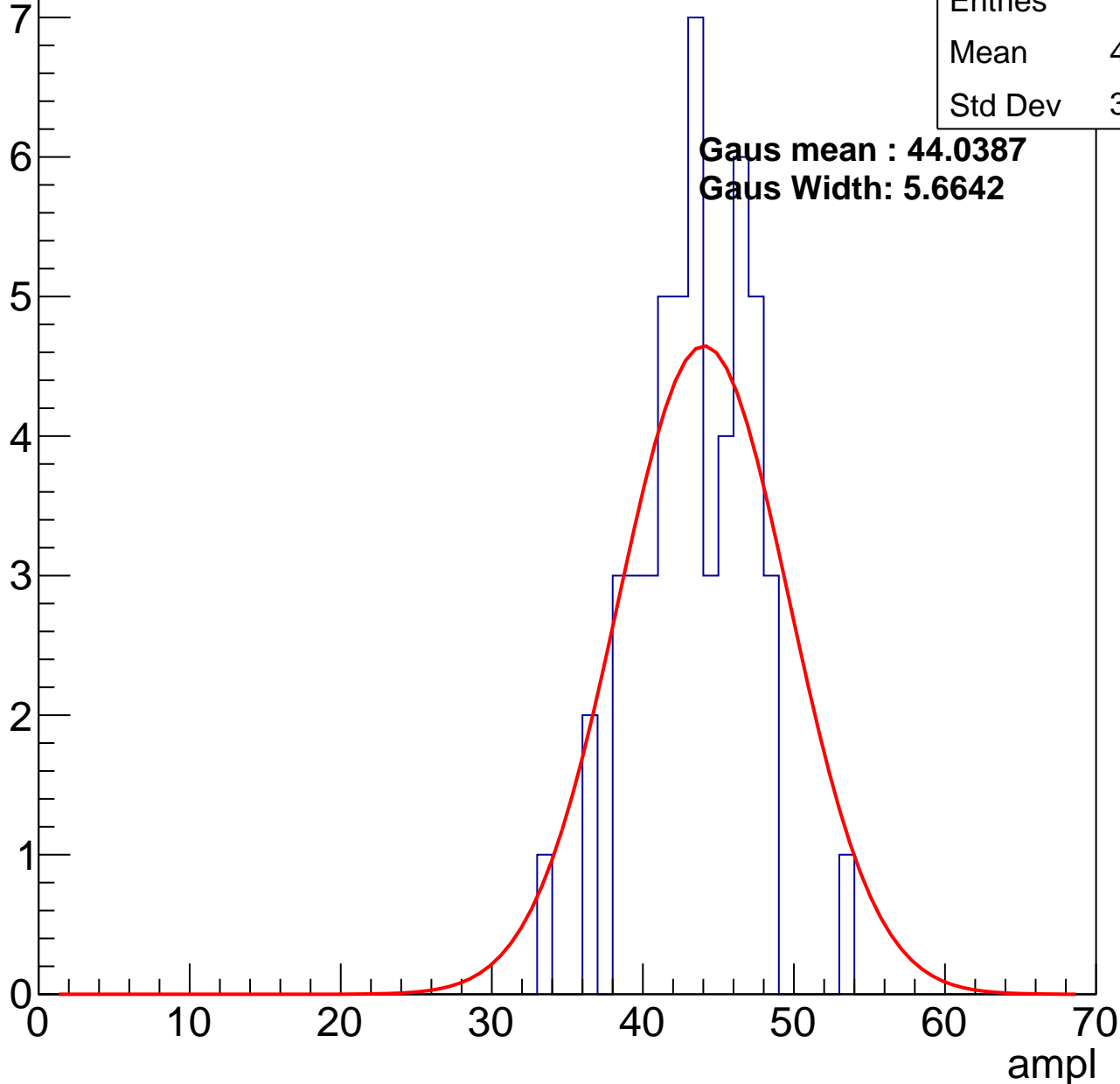
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.98
Std Dev	3.707

Gaus mean : 44.0387

Gaus Width: 5.6642

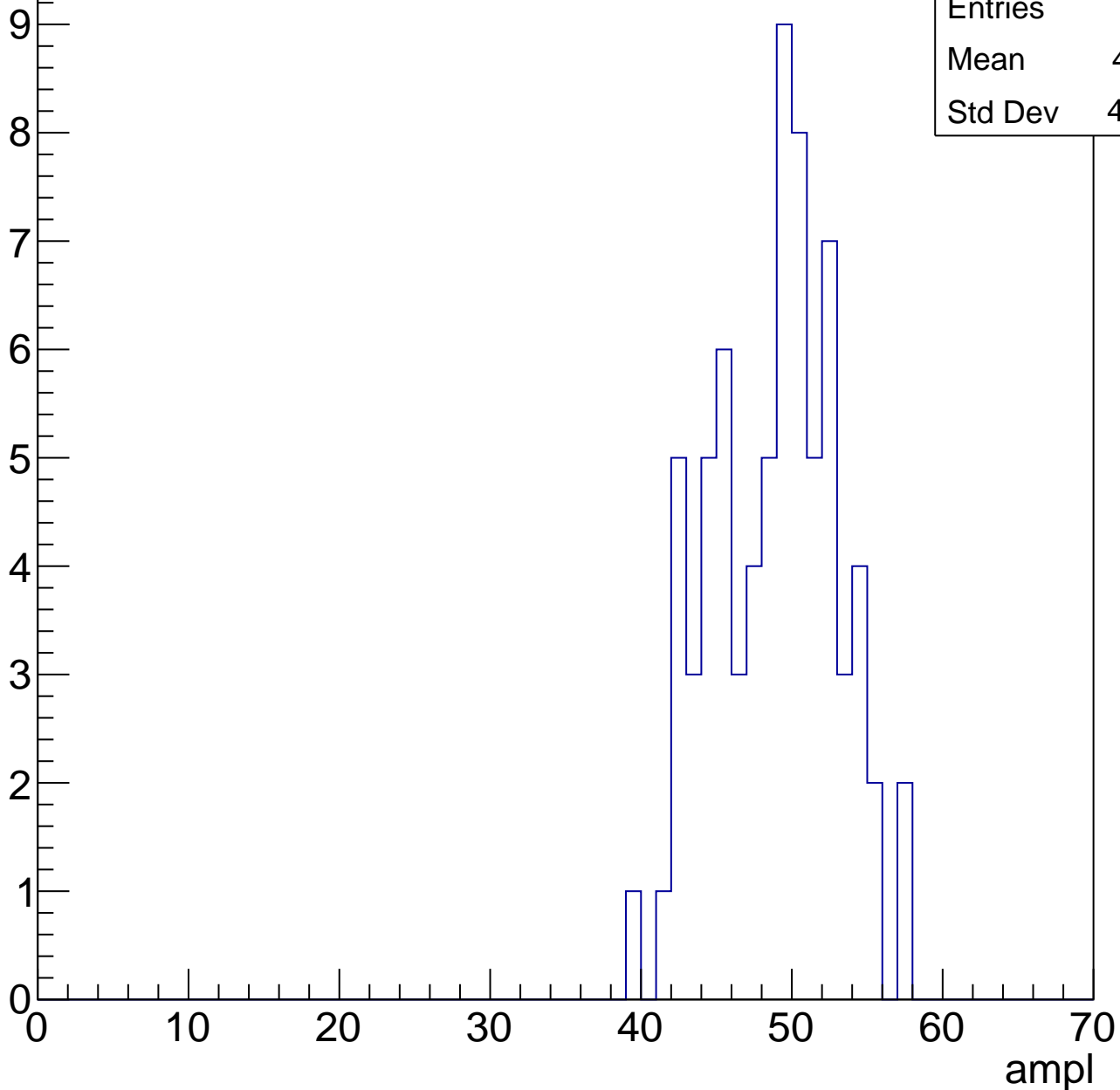


B1L101S, U23-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

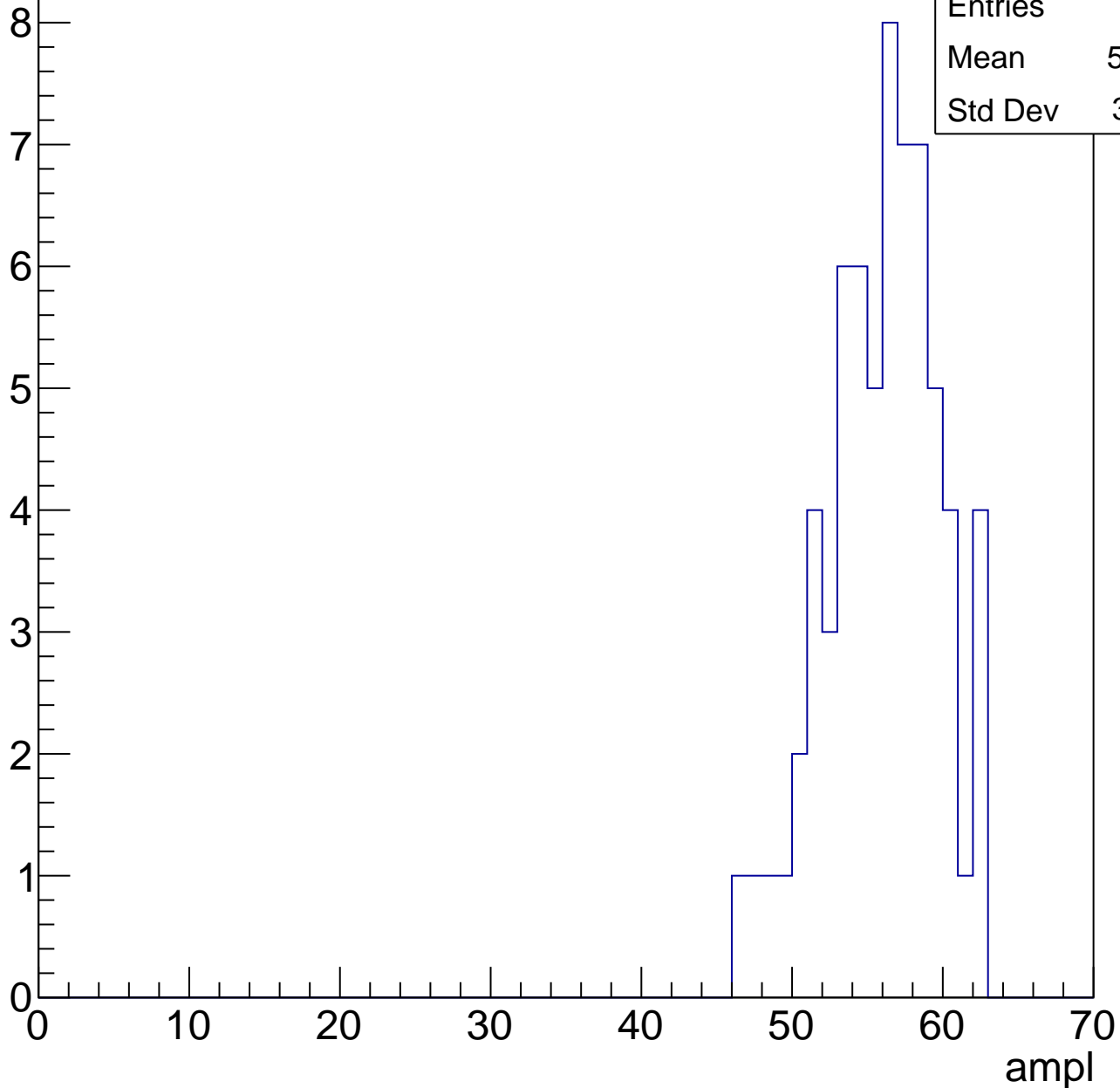
Entries	73
Mean	48.41
Std Dev	4.054



B1L101S, U23-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



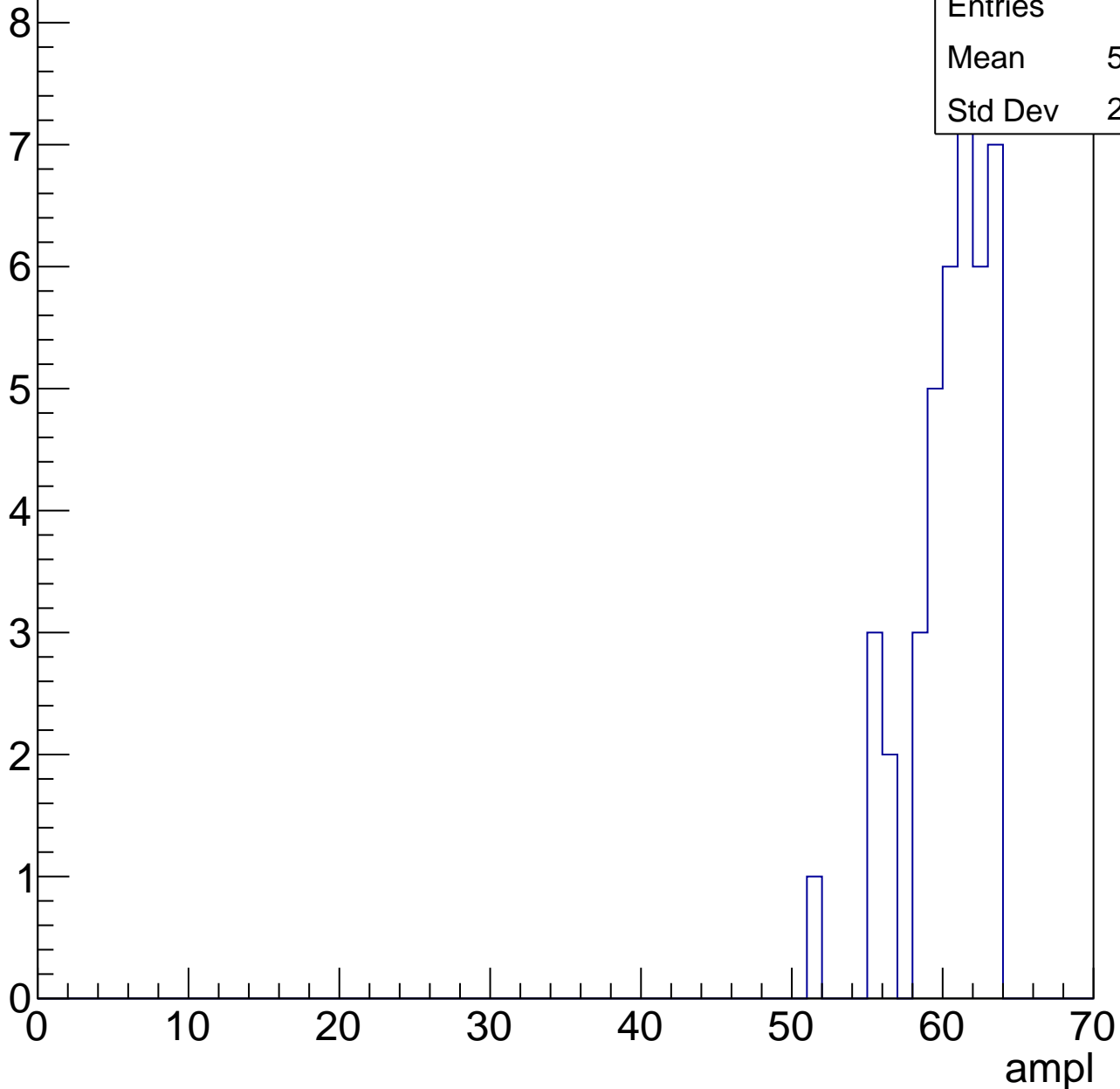
Entries	66
Mean	55.52
Std Dev	3.681

B1L101S, U23-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

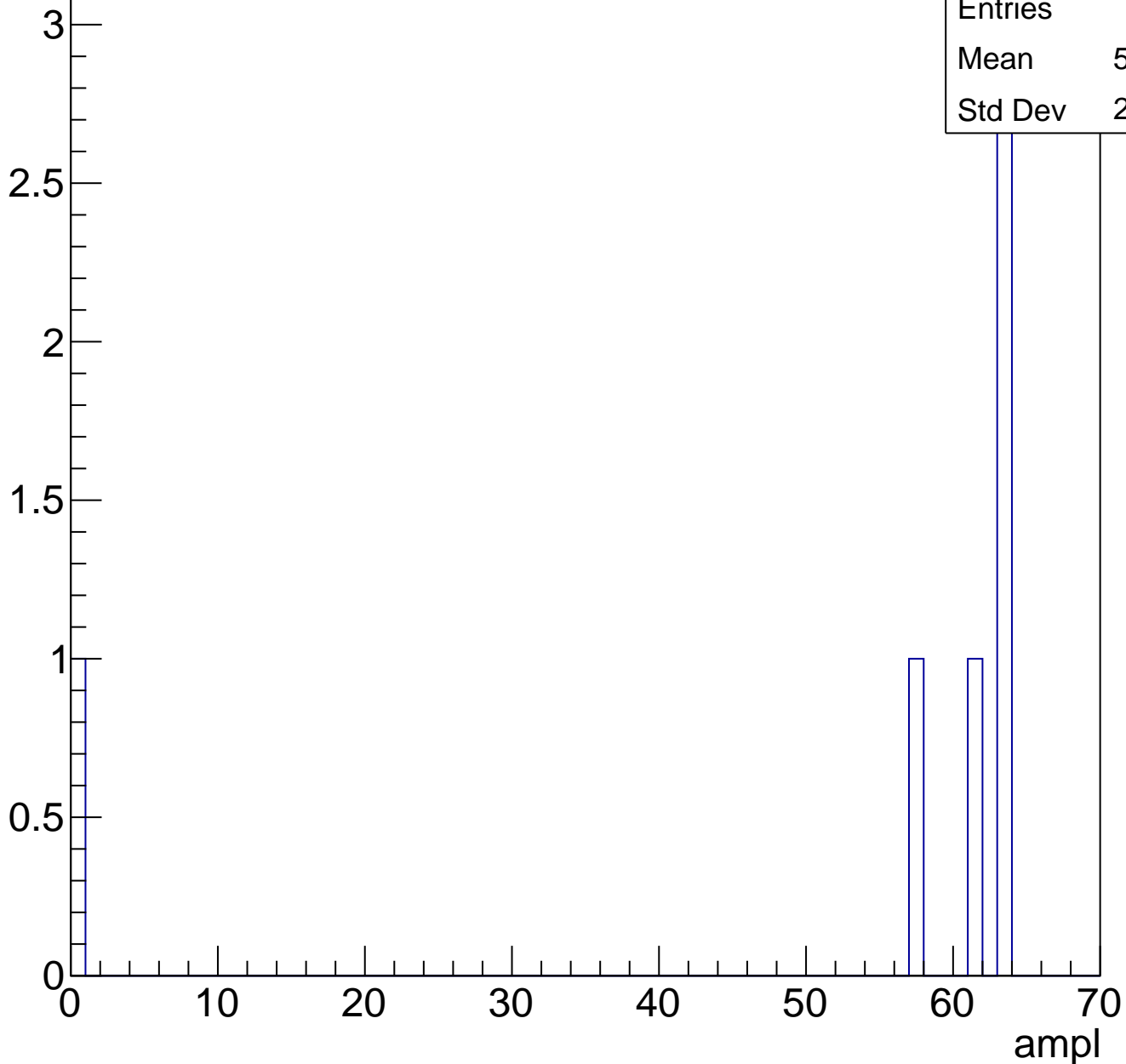
Entries	41
Mean	59.95
Std Dev	2.705



B1L101S, U23-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch91, adc0

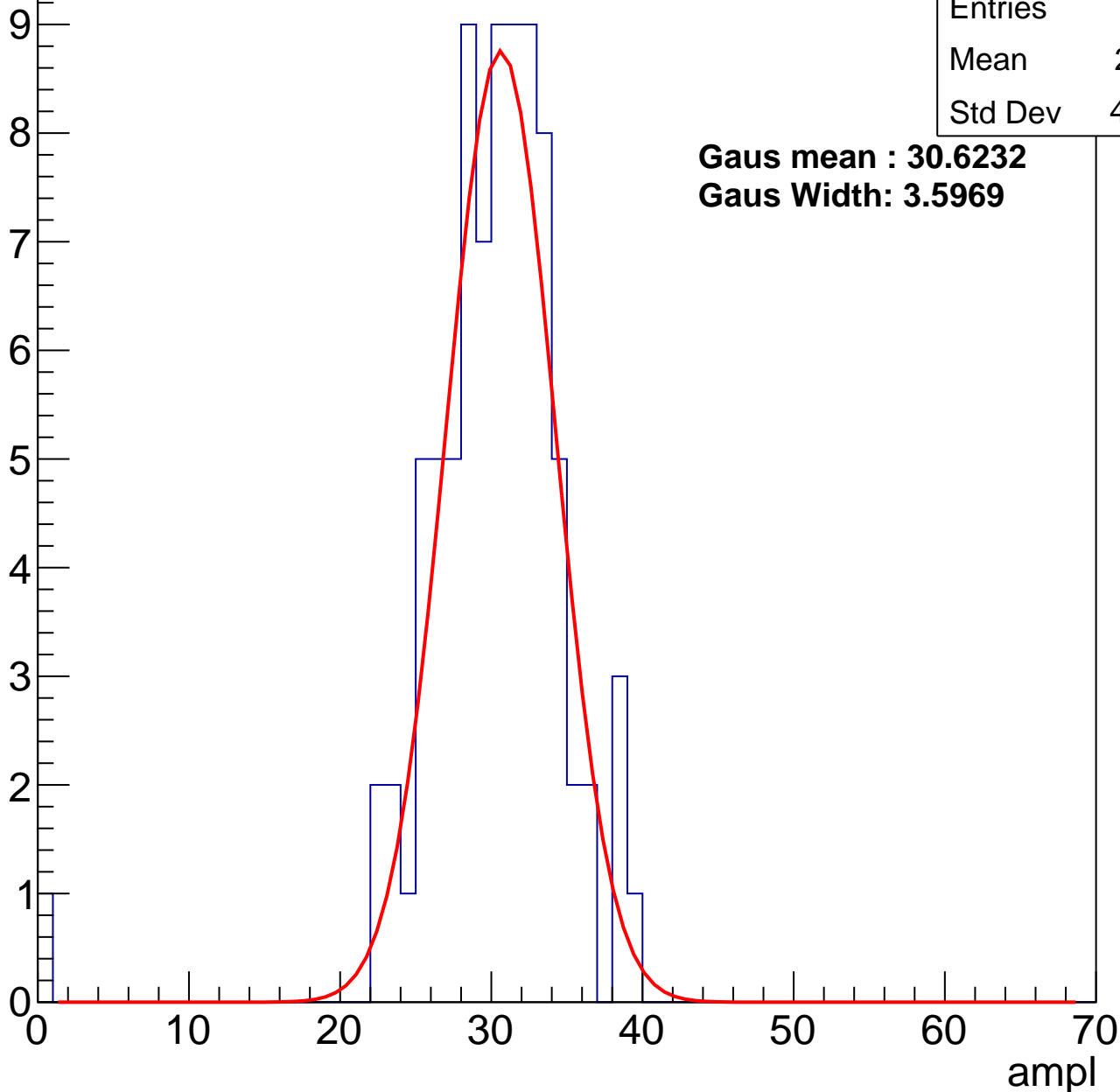
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	29.71
Std Dev	4.899

Gaus mean : 30.6232

Gaus Width: 3.5969



B1L101S, U23-ch91, adc1

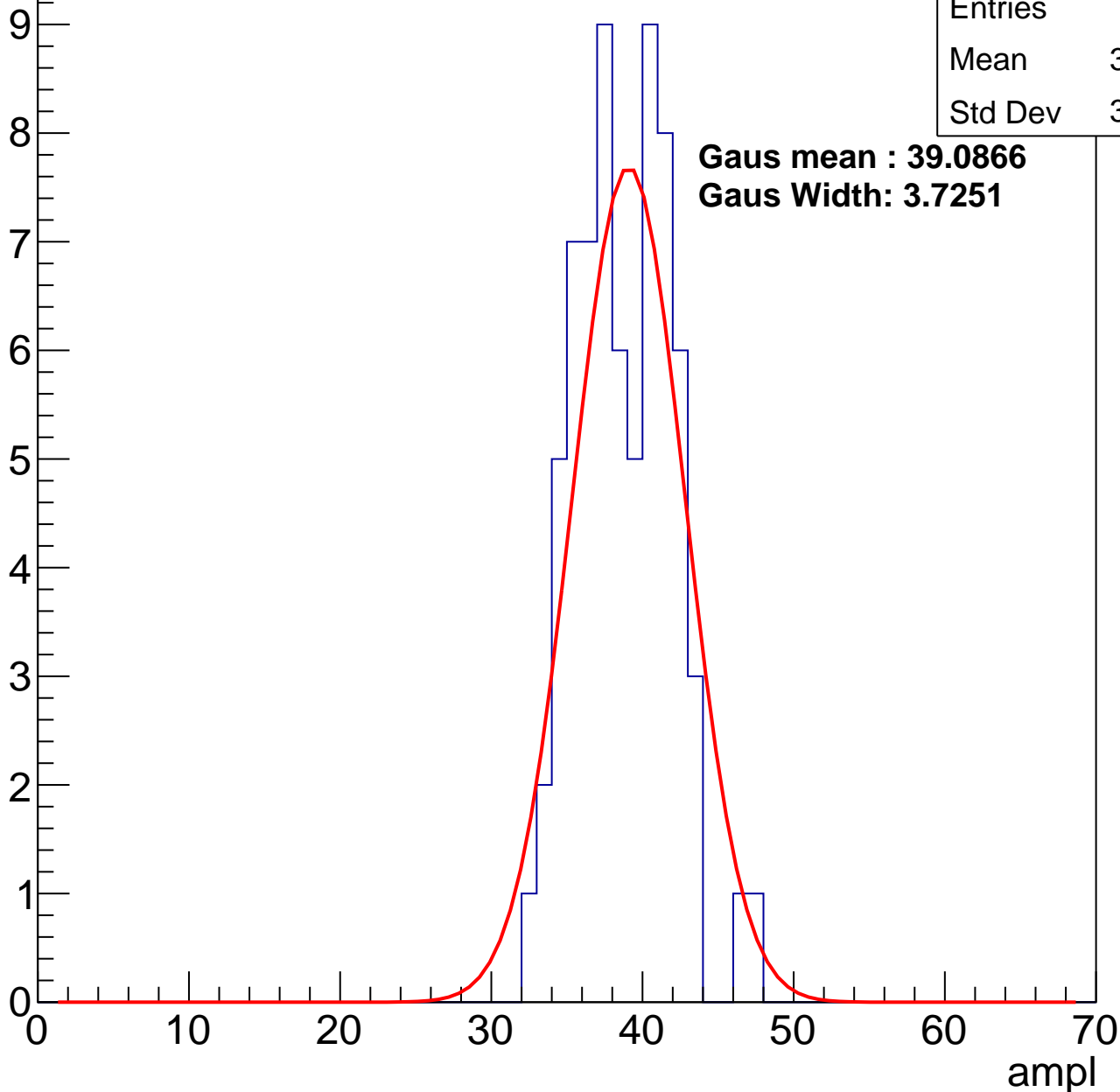
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	38.33
Std Dev	3.134

Gaus mean : 39.0866

Gaus Width: 3.7251



B1L101S, U23-ch91, adc2

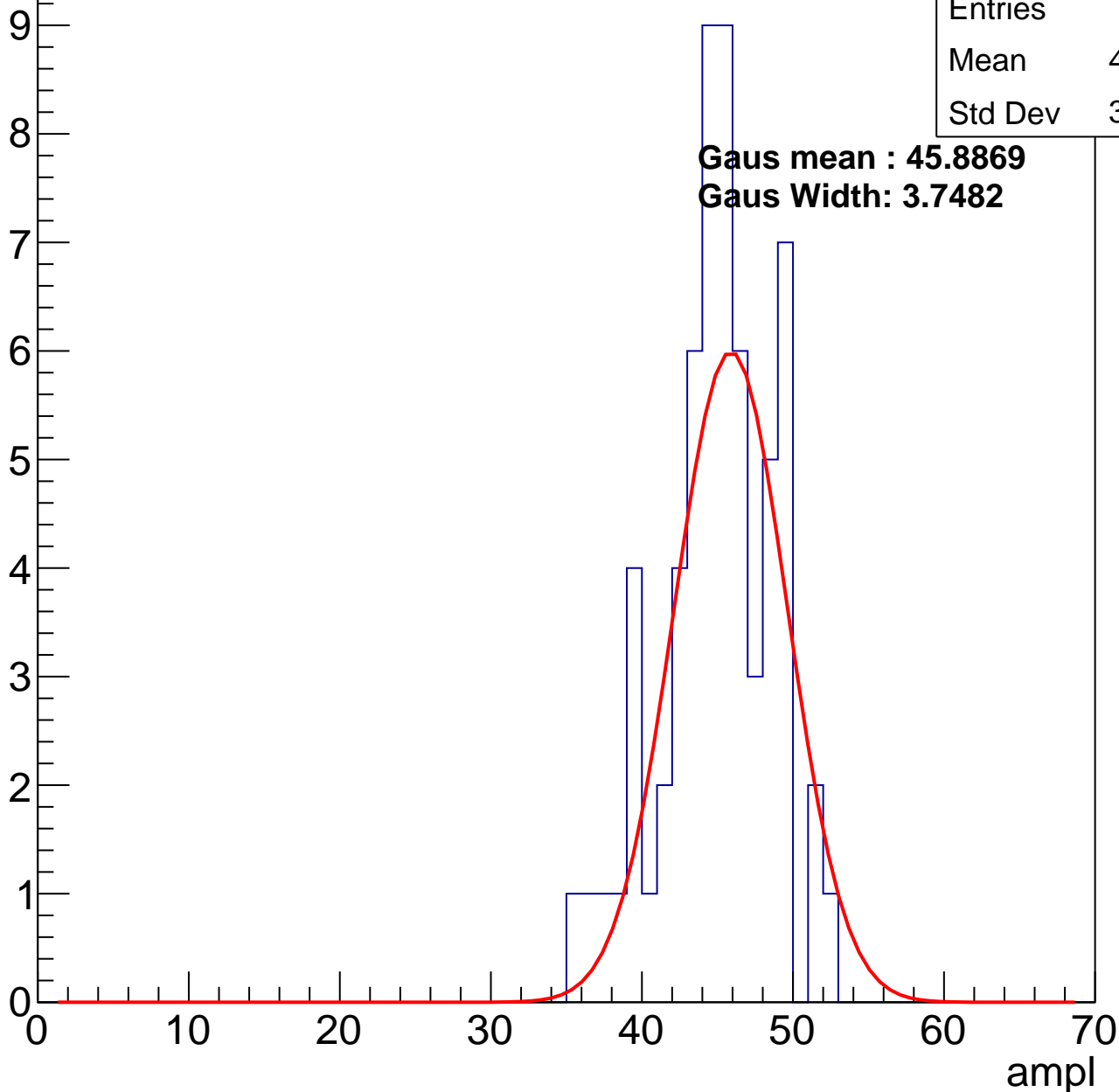
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.52
Std Dev	3.664

Gaus mean : 45.8869

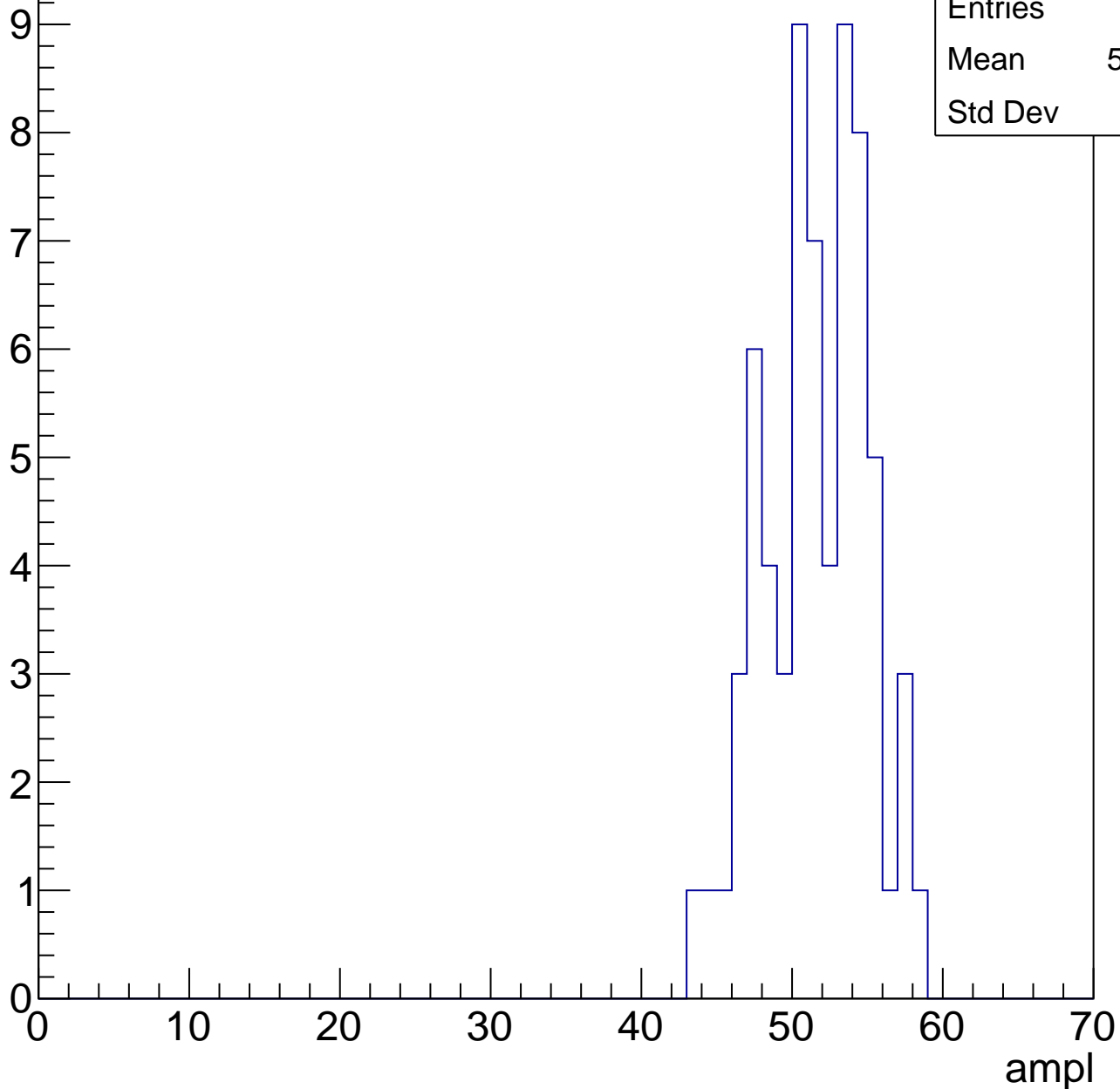
Gaus Width: 3.7482



B1L101S, U23-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



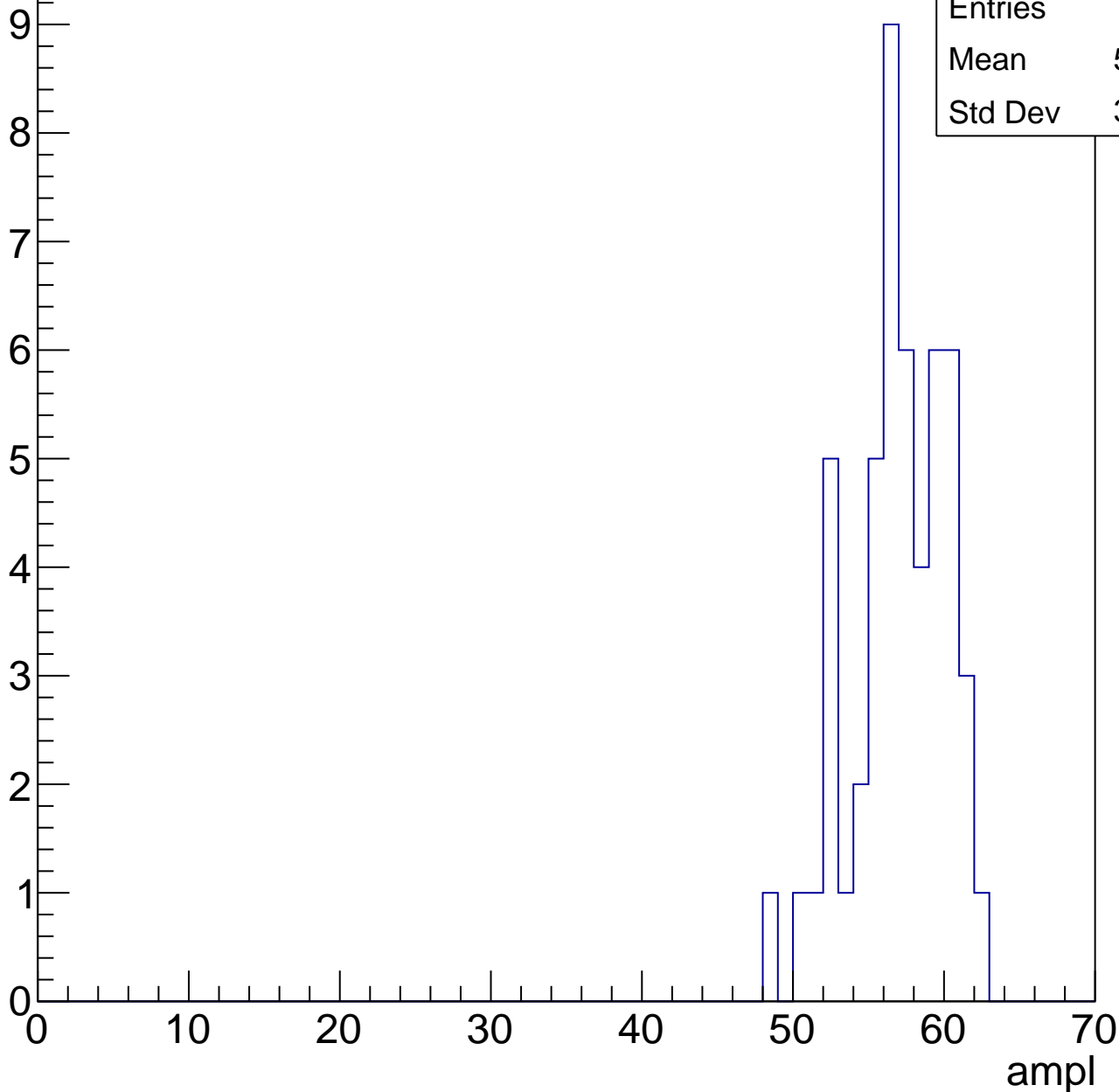
Entries	66
Mean	51.14
Std Dev	3.37

B1L101S, U23-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.51
Std Dev	3.121

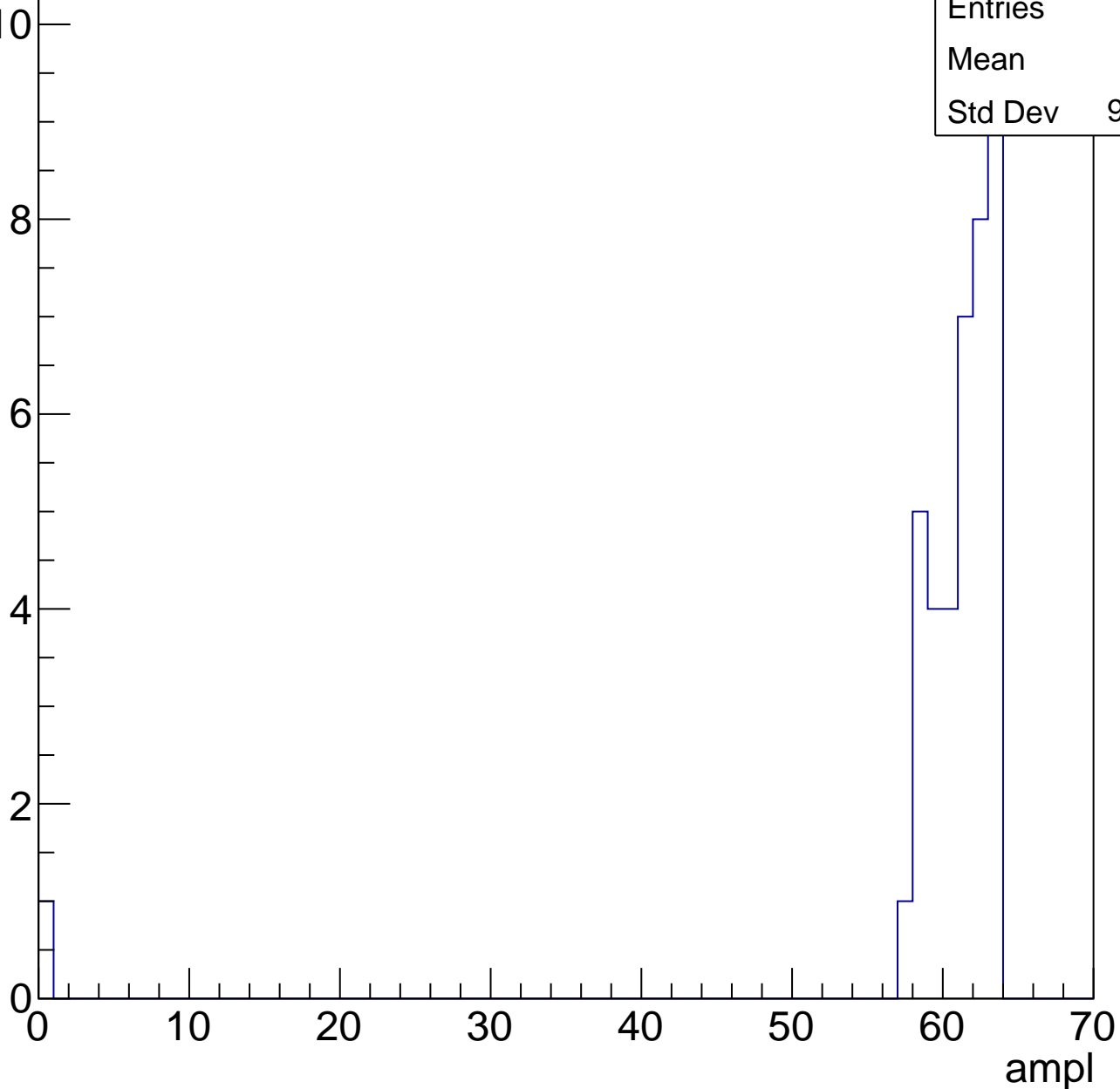


B1L101S, U23-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.4
Std Dev	9.679



B1L101S, U23-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch92, adc0

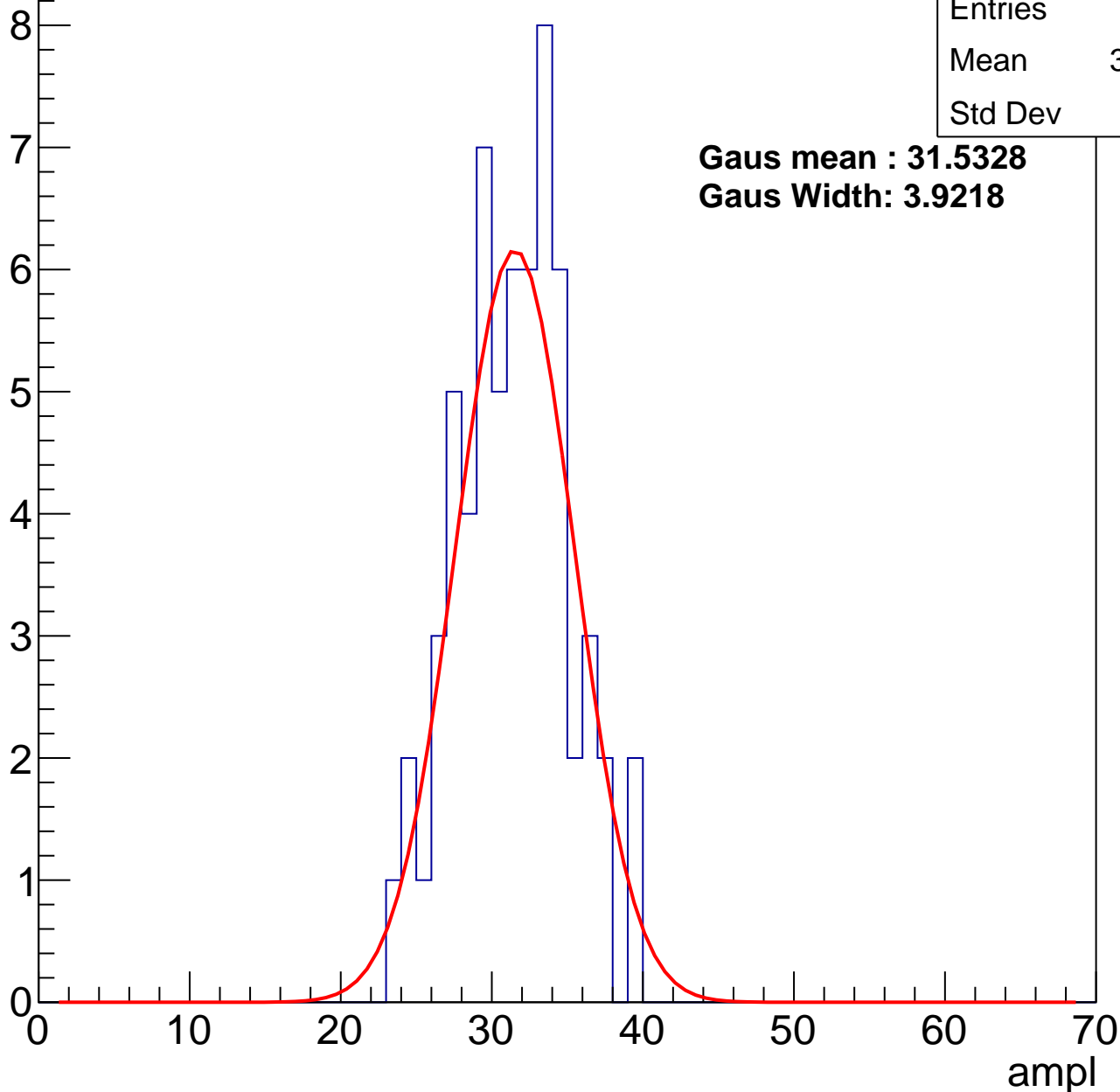
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.95
Std Dev	3.61

Gaus mean : 31.5328

Gaus Width: 3.9218



B1L101S, U23-ch92, adc1

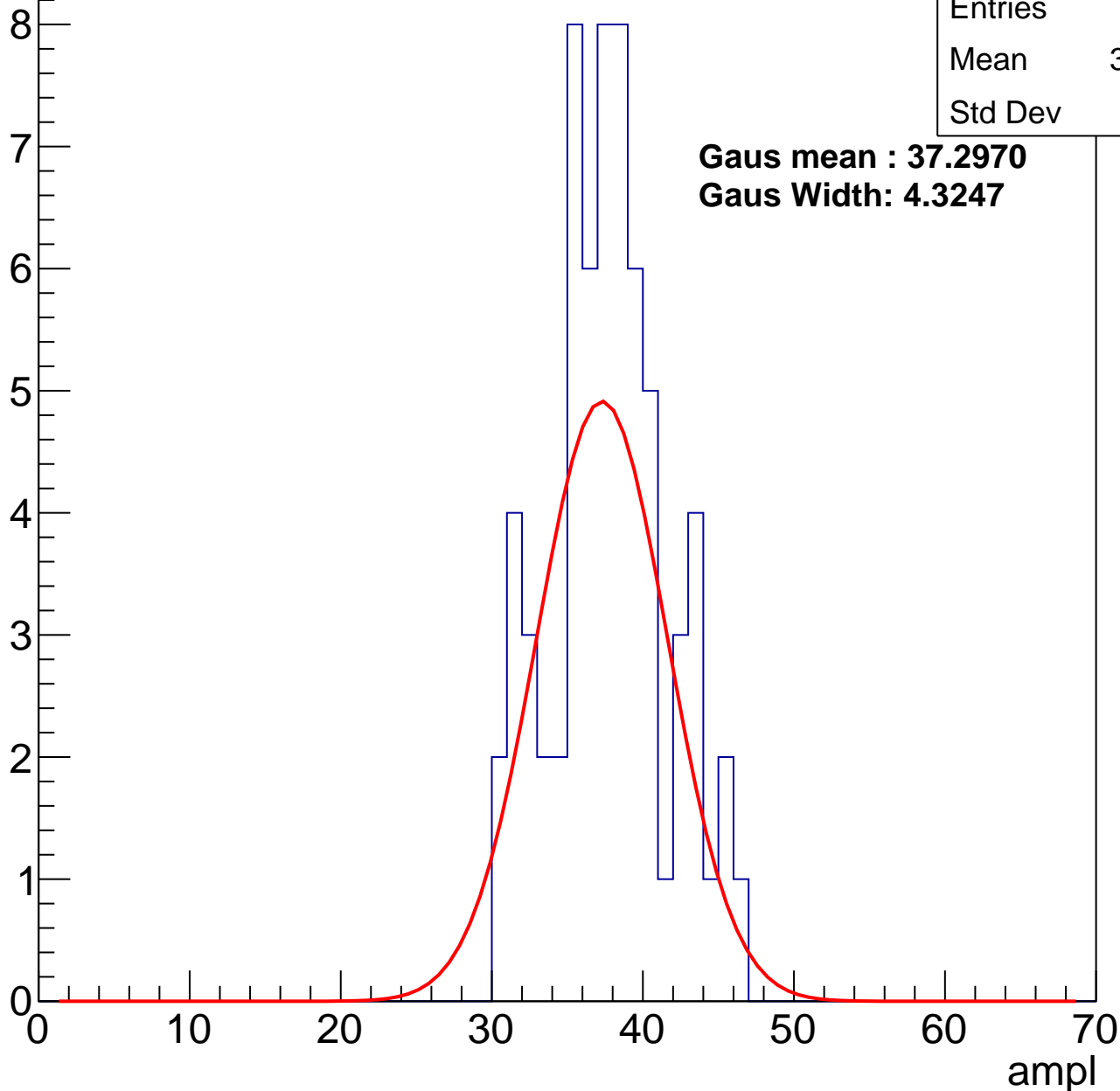
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.32
Std Dev	3.83

Gaus mean : 37.2970

Gaus Width: 4.3247



B1L101S, U23-ch92, adc2

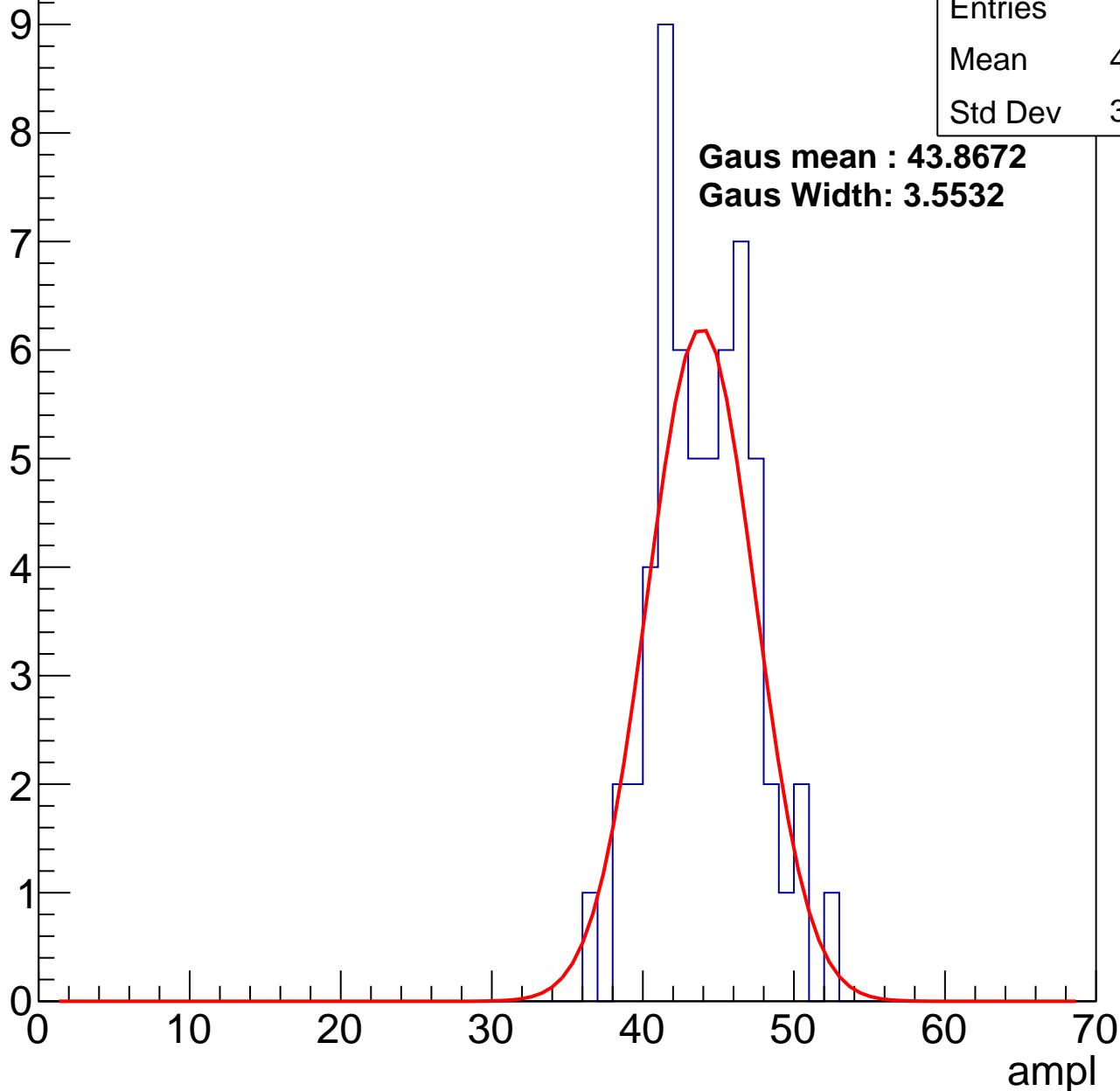
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.62
Std Dev	3.279

Gaus mean : 43.8672

Gaus Width: 3.5532



B1L101S, U23-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	50.47
Std Dev	3.749

Entry

10

8

6

4

2

0

0

10

20

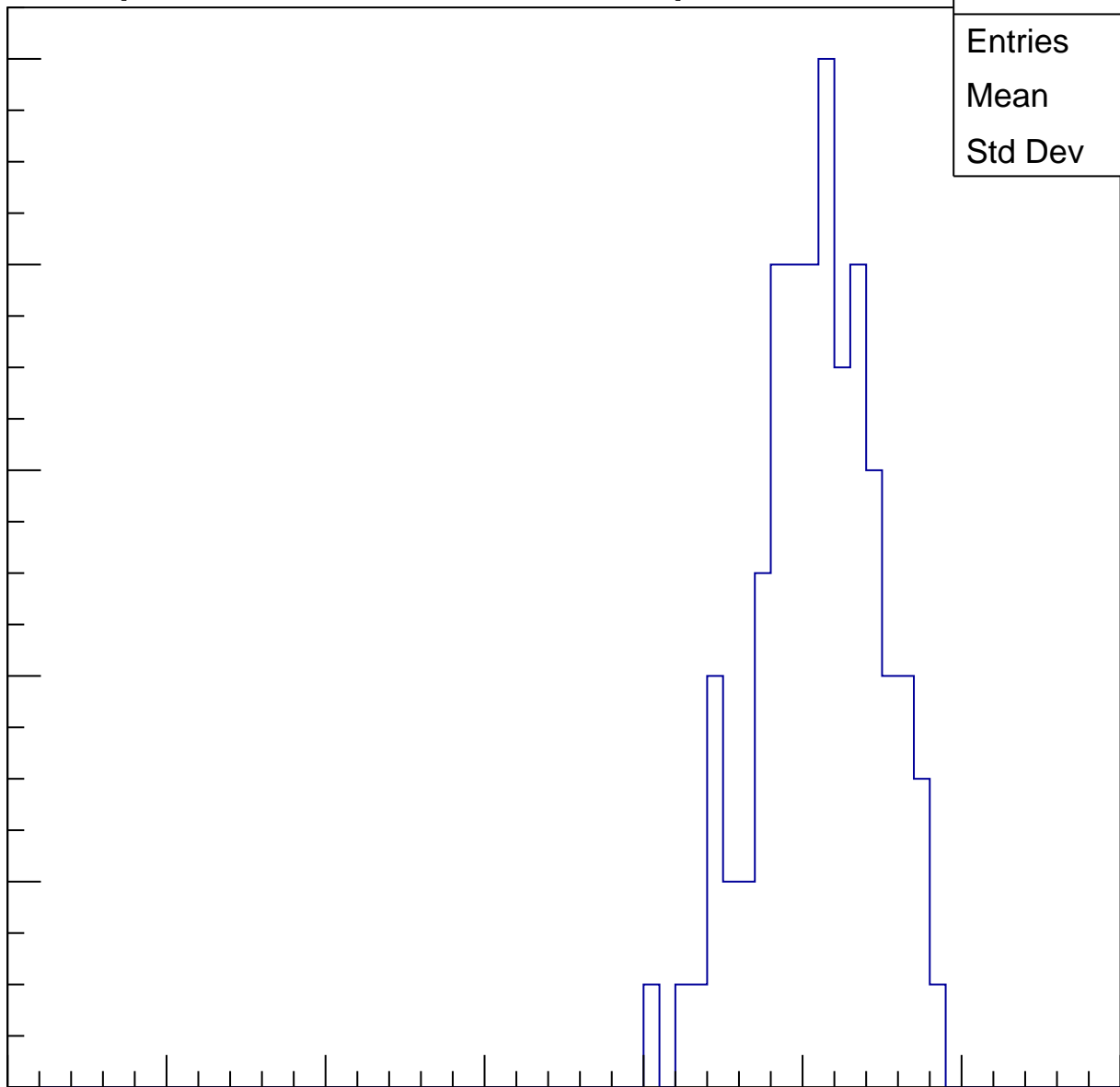
30

40

50

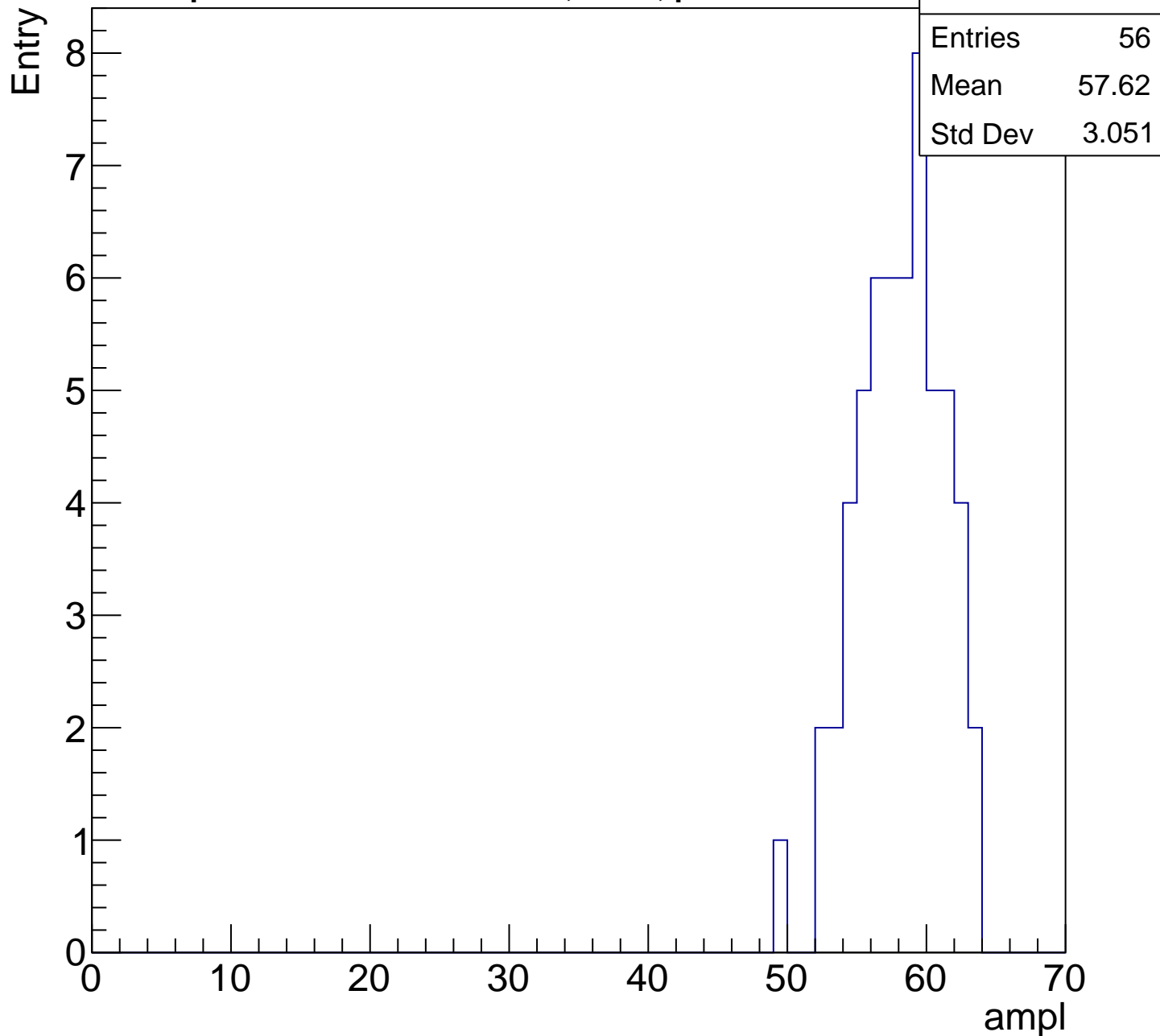
60

ampl



B1L101S, U23-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

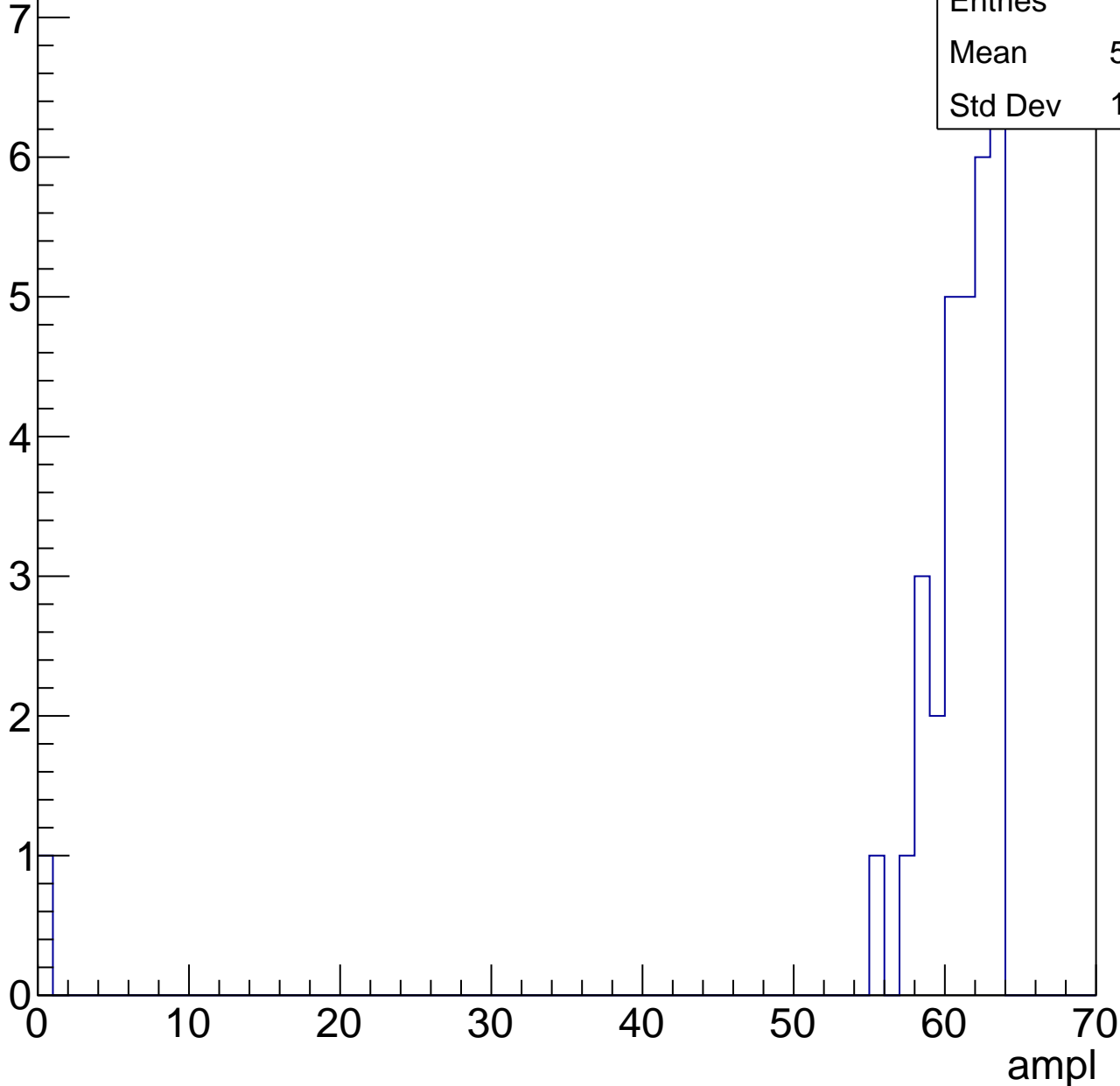


B1L101S, U23-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.77
Std Dev	10.92



B1L101S, U23-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L101S, U23-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch93, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	32.56
Std Dev	3.359

Gaus mean : 32.8255

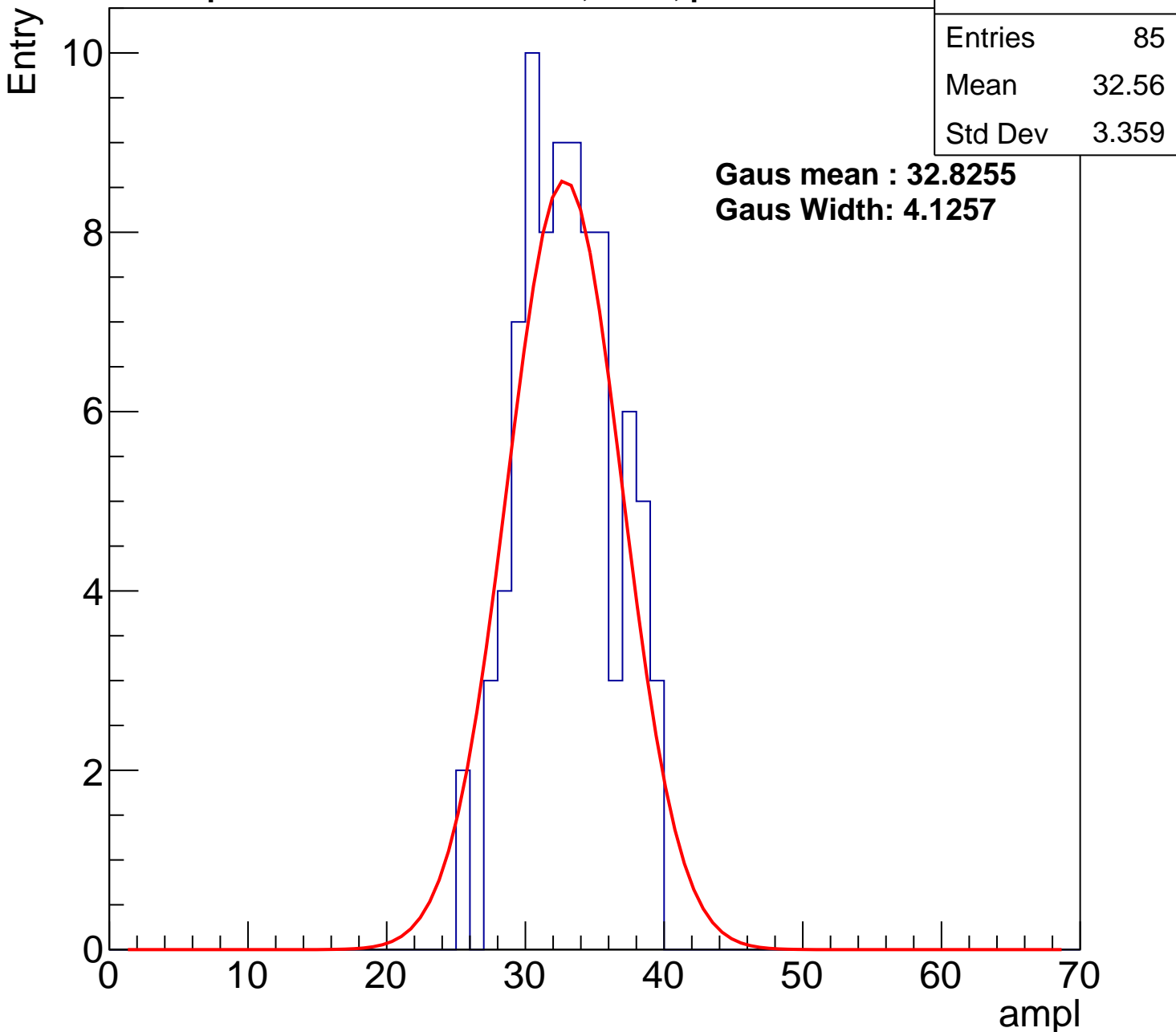
Gaus Width: 4.1257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch93, adc1

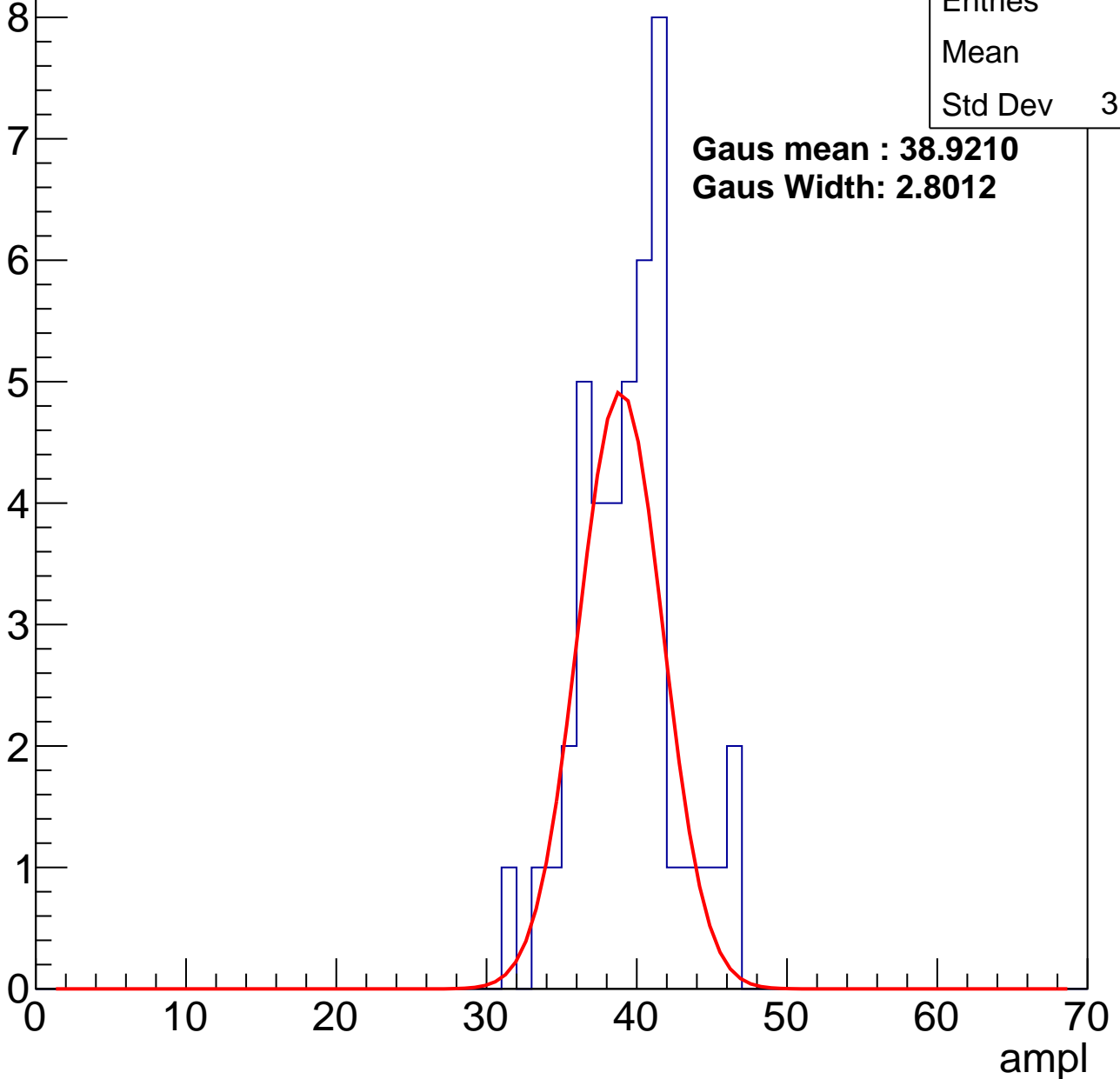
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	39
Std Dev	3.213

Gaus mean : 38.9210

Gaus Width: 2.8012



B1L101S, U23-ch93, adc2

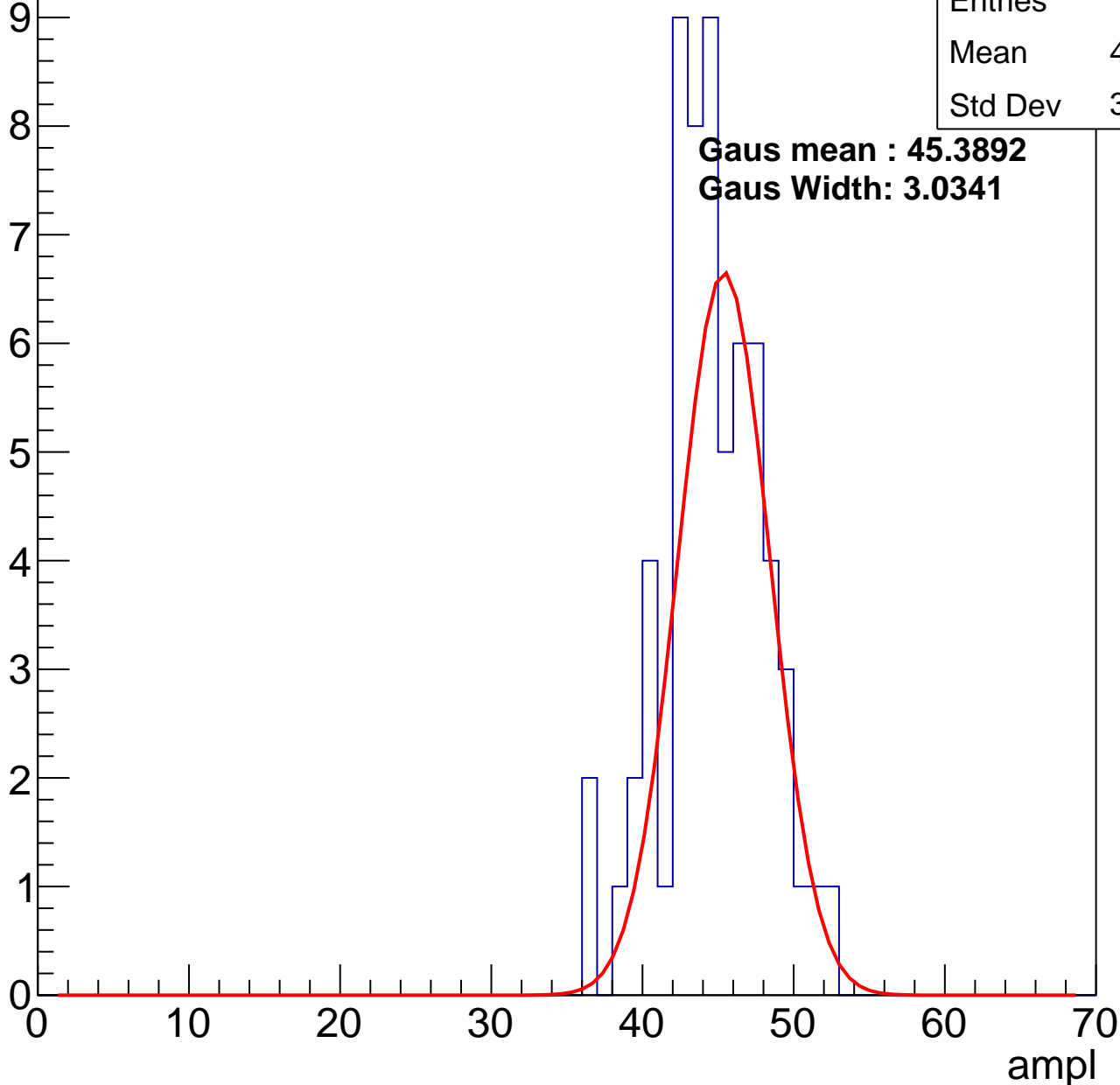
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.16
Std Dev	3.334

Gaus mean : 45.3892

Gaus Width: 3.0341

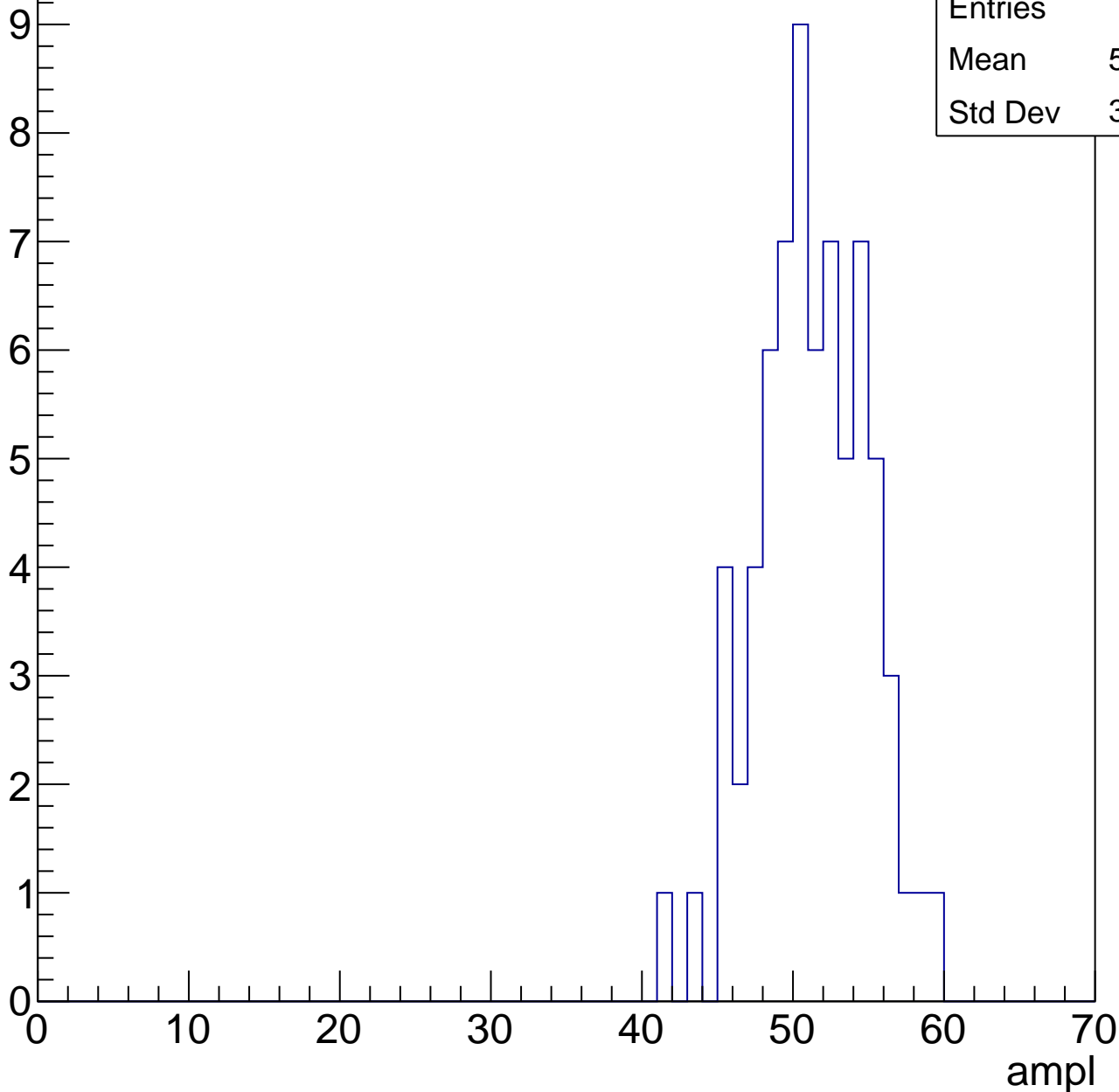


B1L101S, U23-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

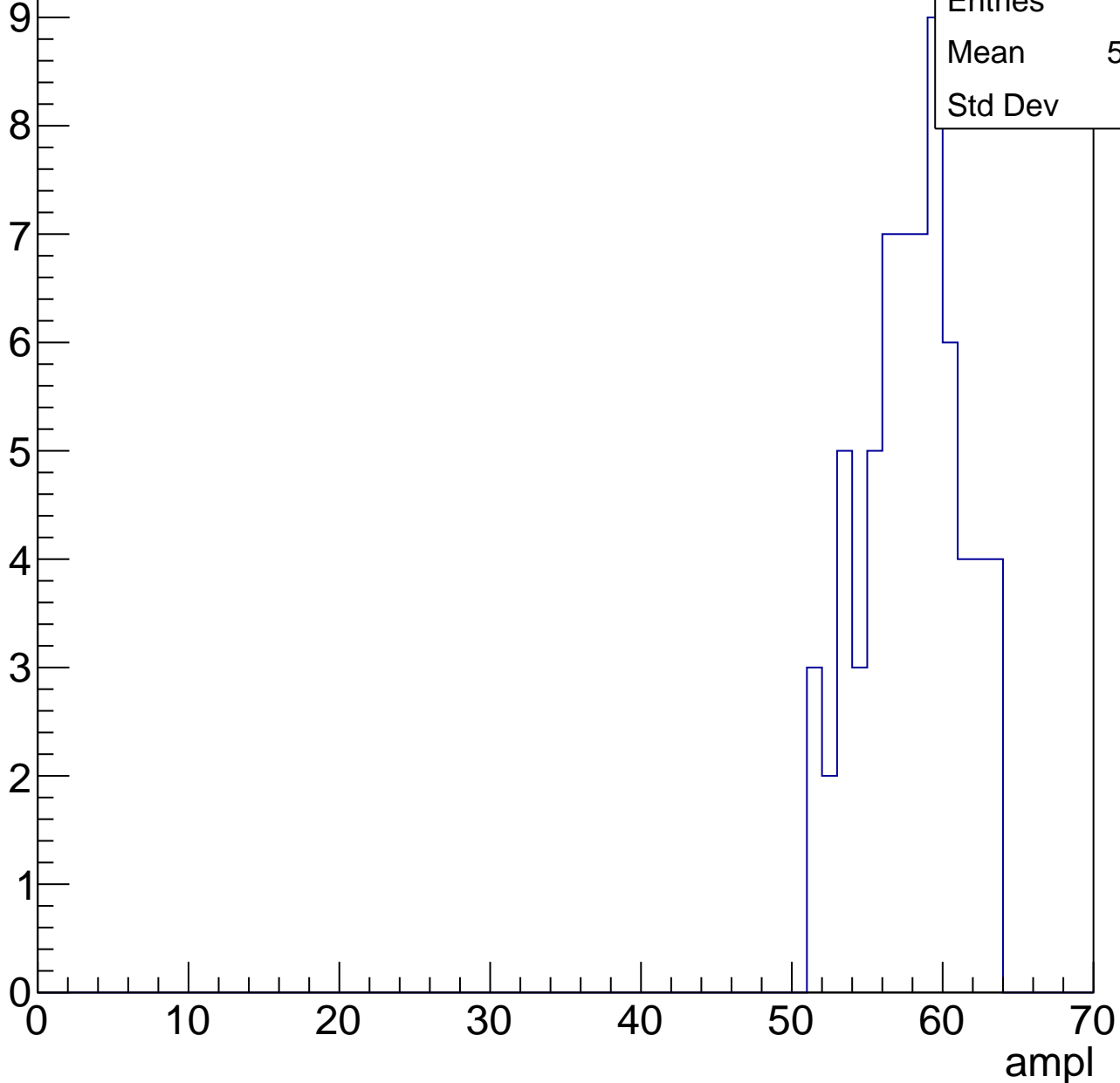
Entries	70
Mean	50.79
Std Dev	3.593



B1L101S, U23-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

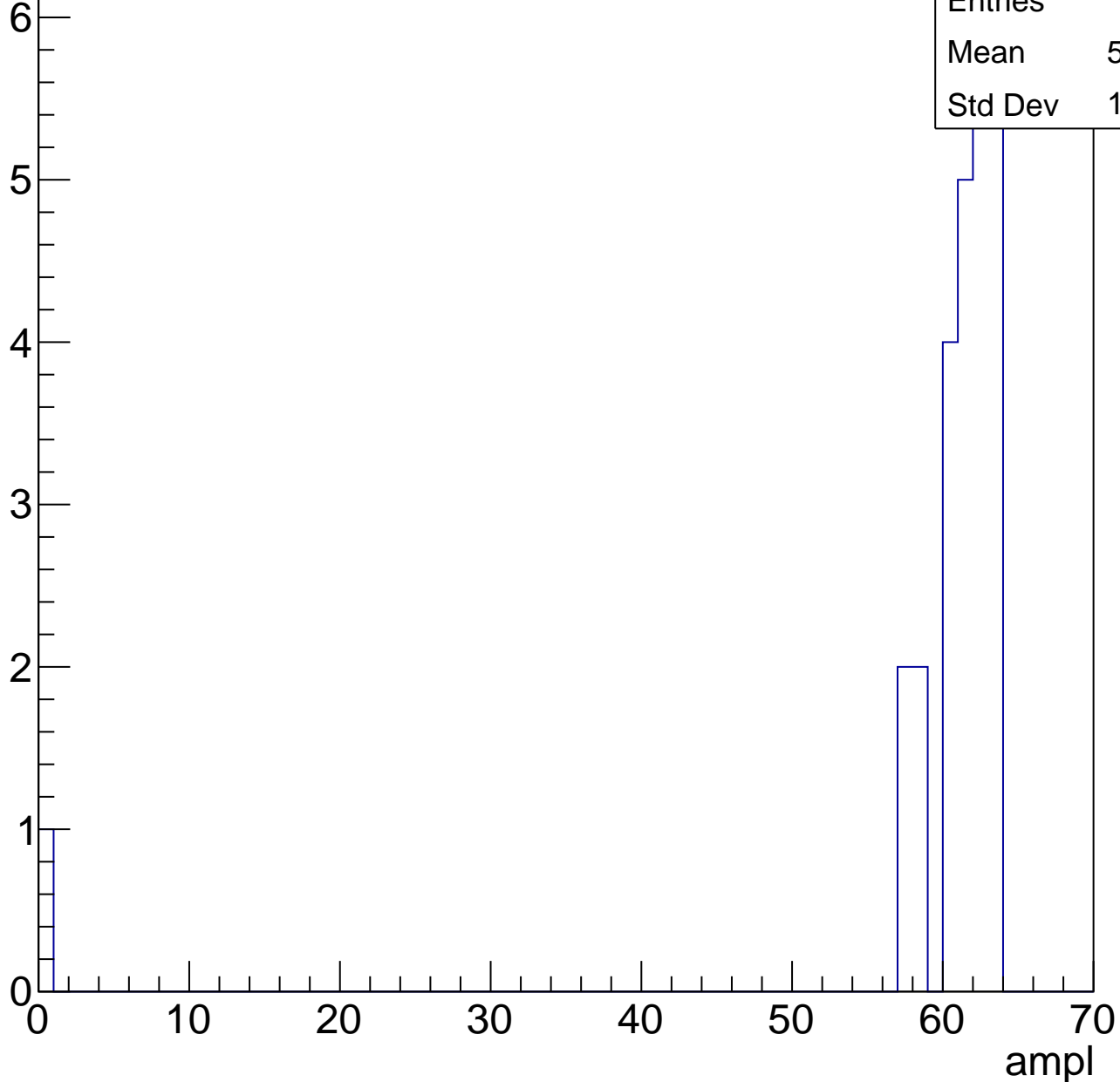
Entry



B1L101S, U23-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch94, adc0

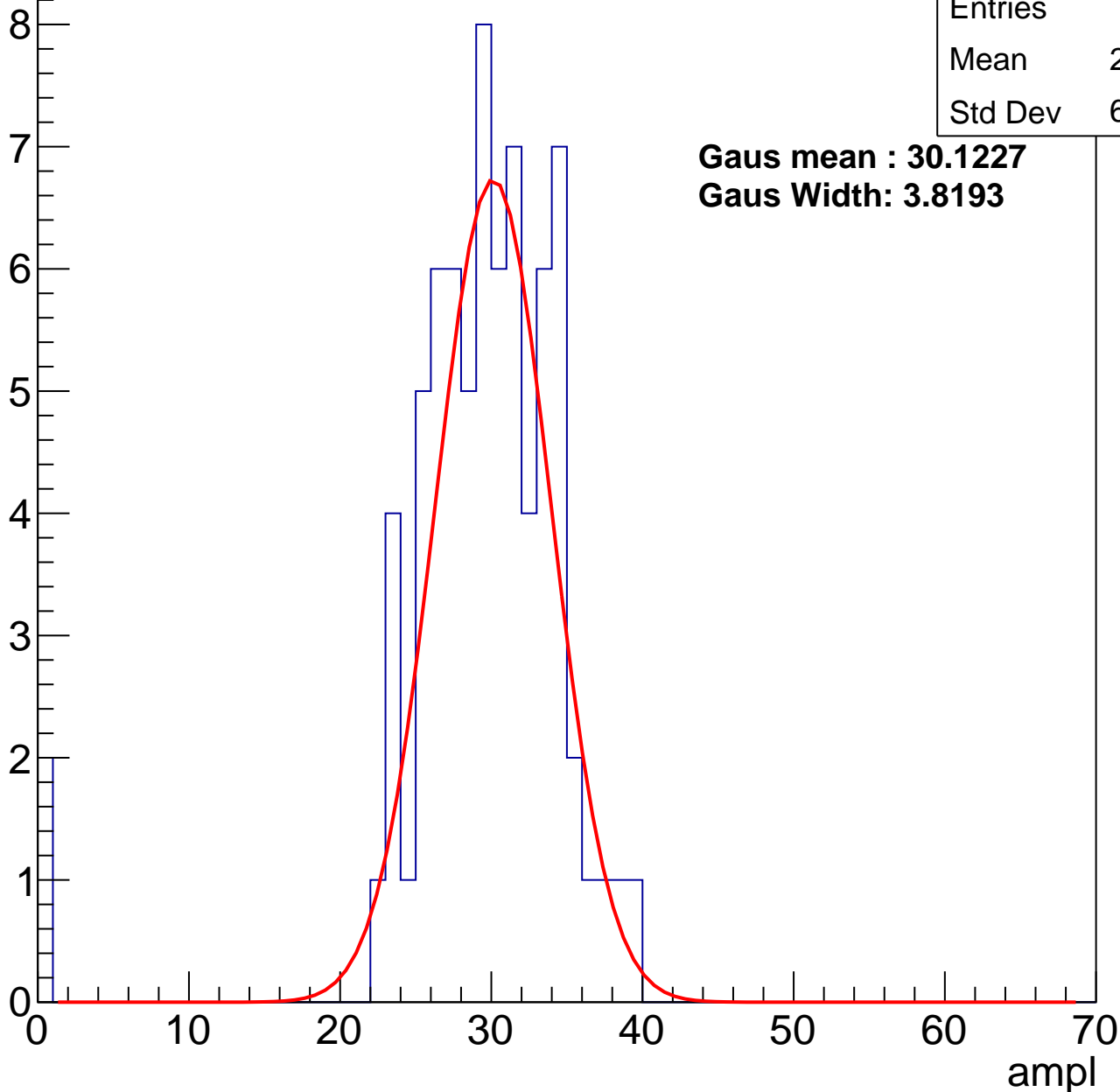
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.84
Std Dev	6.116

Gaus mean : 30.1227

Gaus Width: 3.8193



B1L101S, U23-ch94, adc1

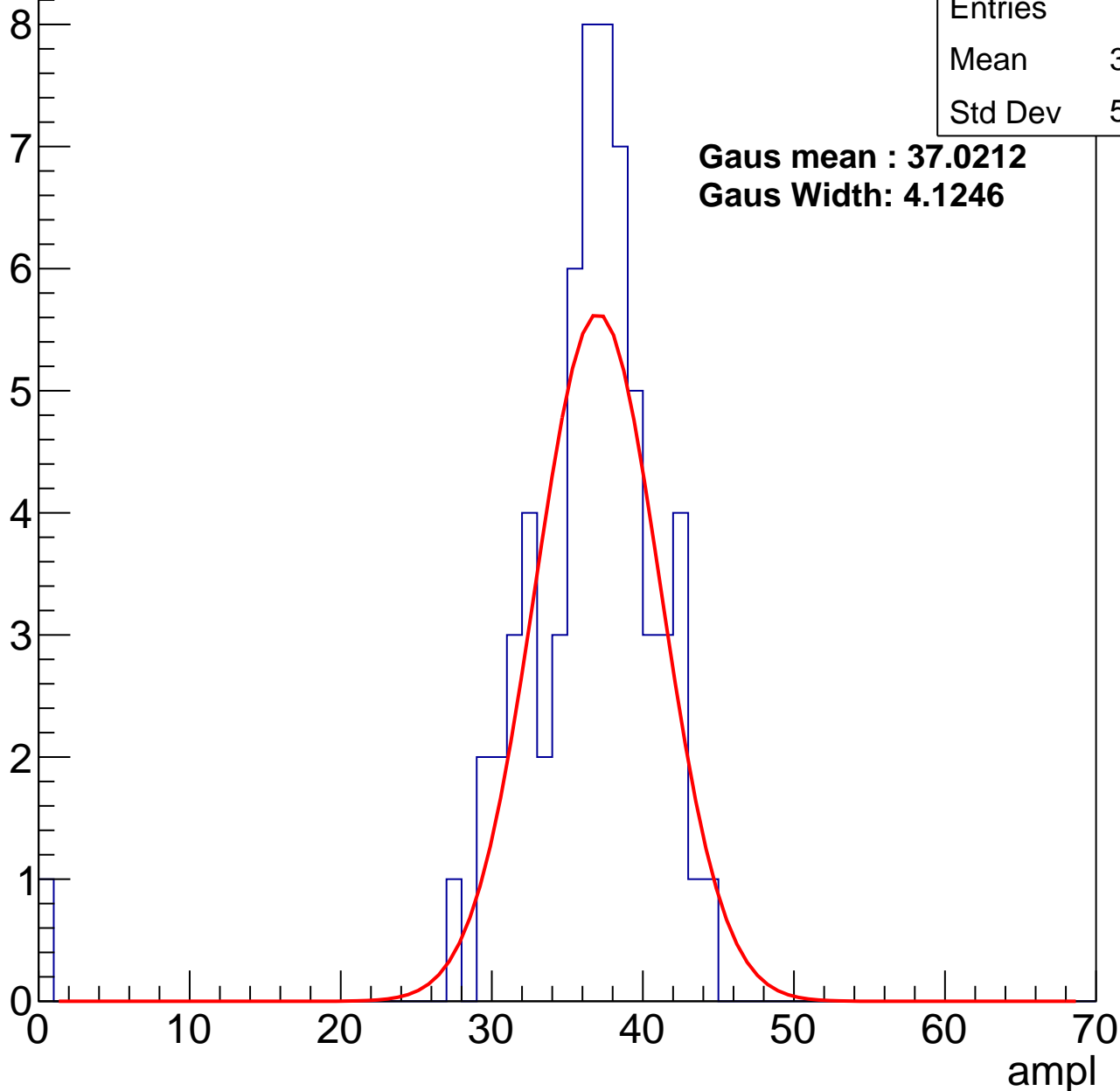
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.73
Std Dev	5.834

Gaus mean : 37.0212

Gaus Width: 4.1246



B1L101S, U23-ch94, adc2

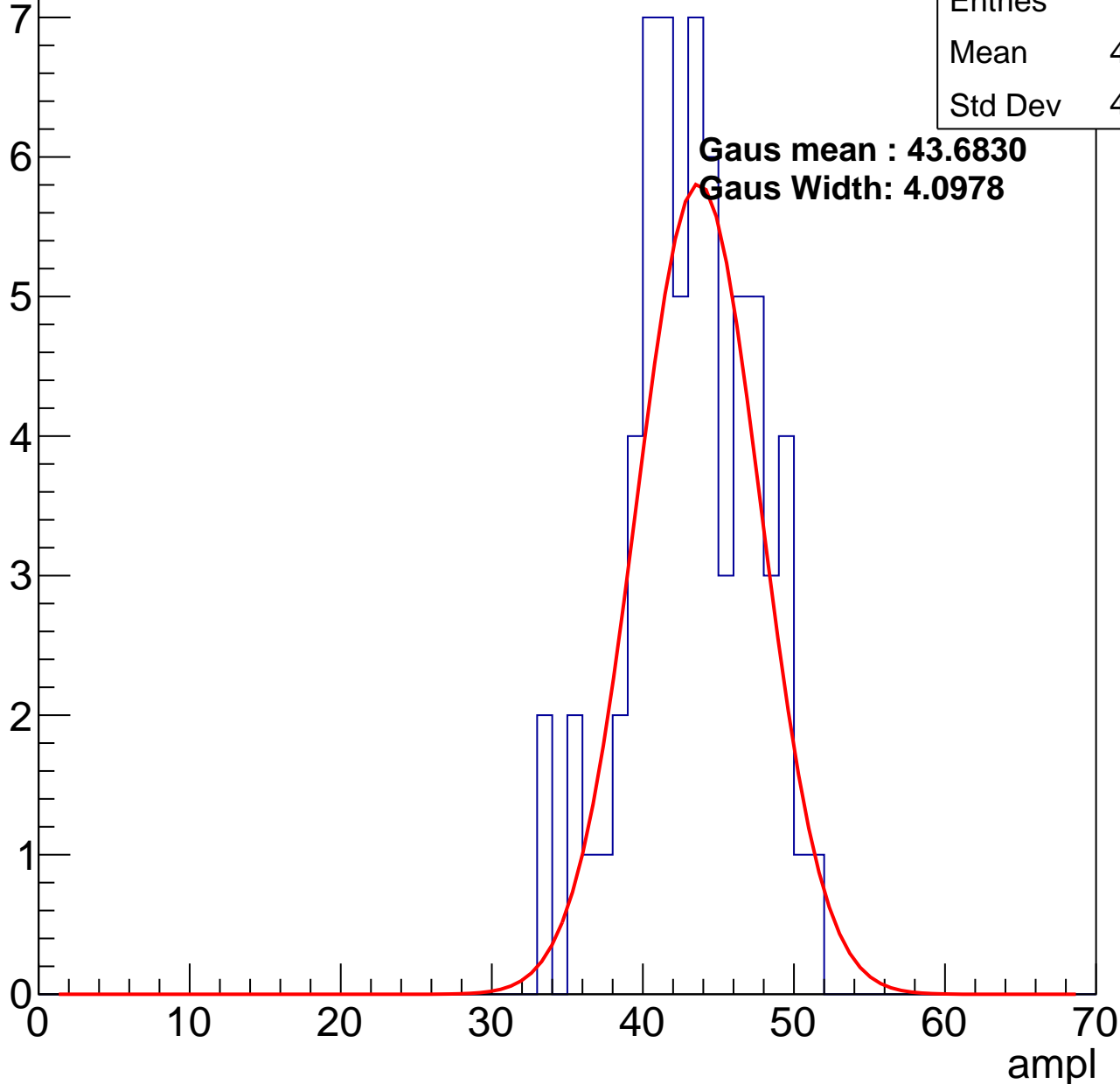
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.79
Std Dev	4.073

Gaus mean : 43.6830

Gaus Width: 4.0978

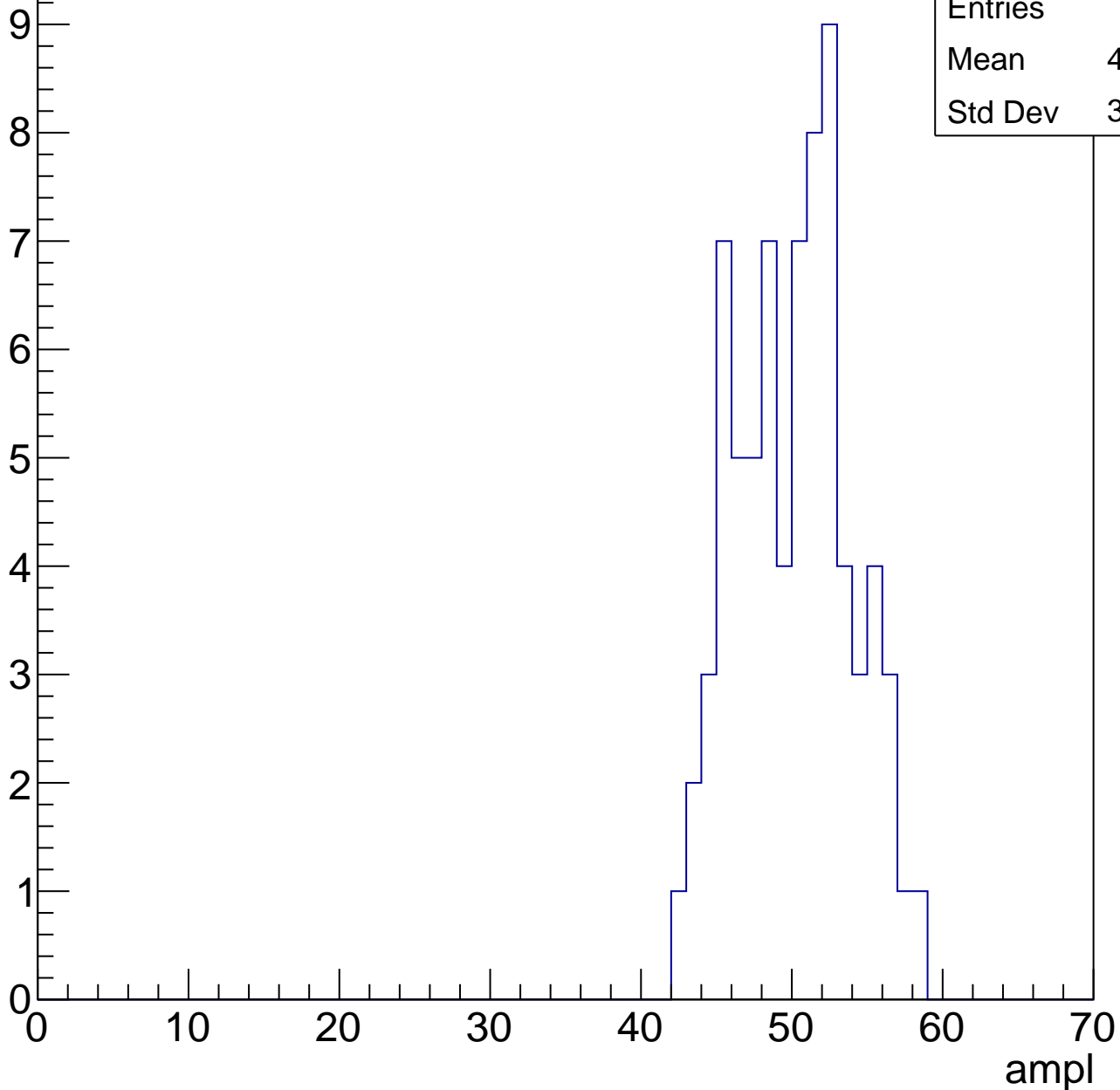


B1L101S, U23-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	49.66
Std Dev	3.753

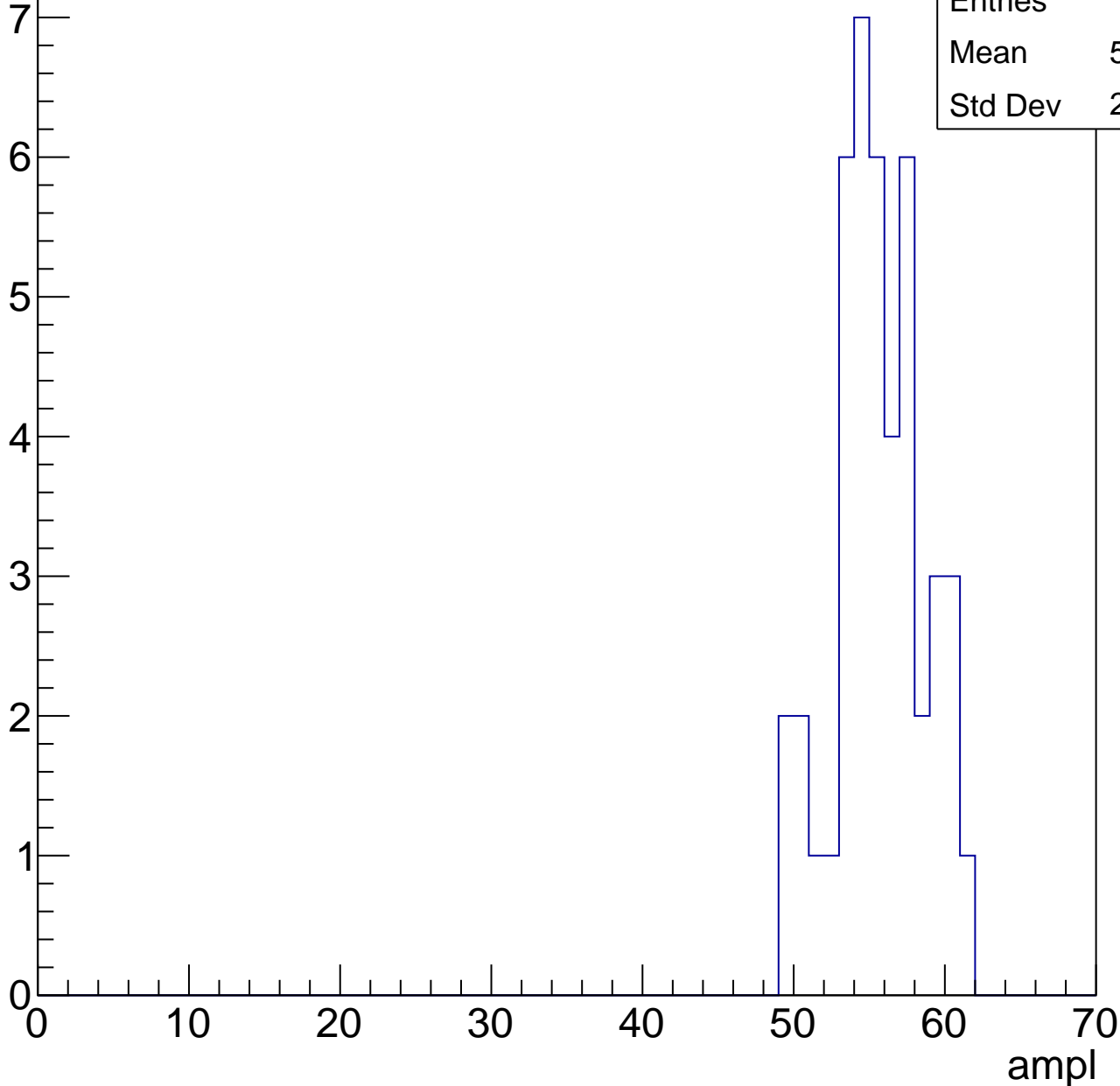


B1L101S, U23-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	55.16
Std Dev	2.946



B1L101S, U23-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	50
Mean	60.2
Std Dev	2.163

Entry

10

8

6

4

2

0

0

10

20

30

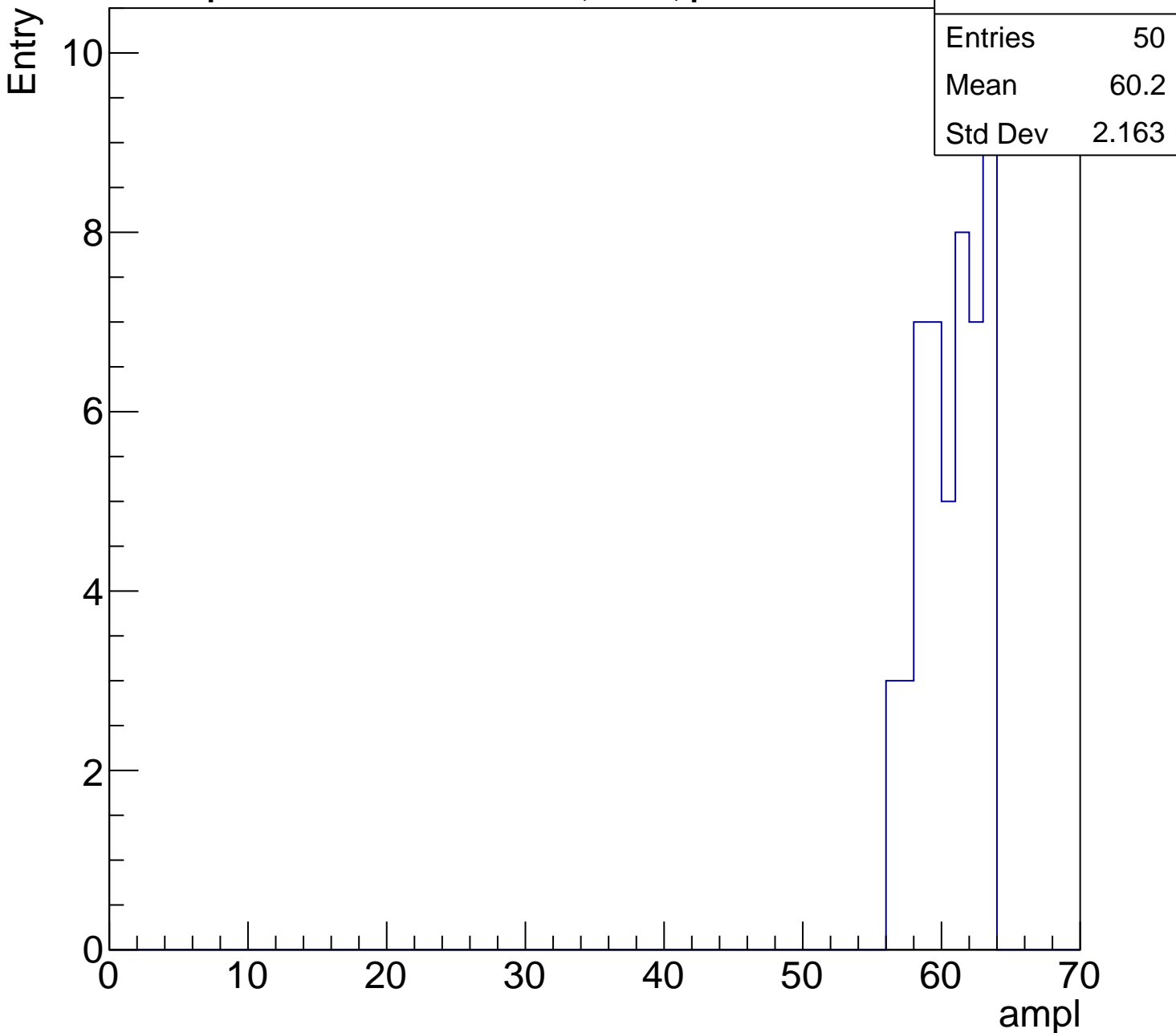
40

50

60

ampl

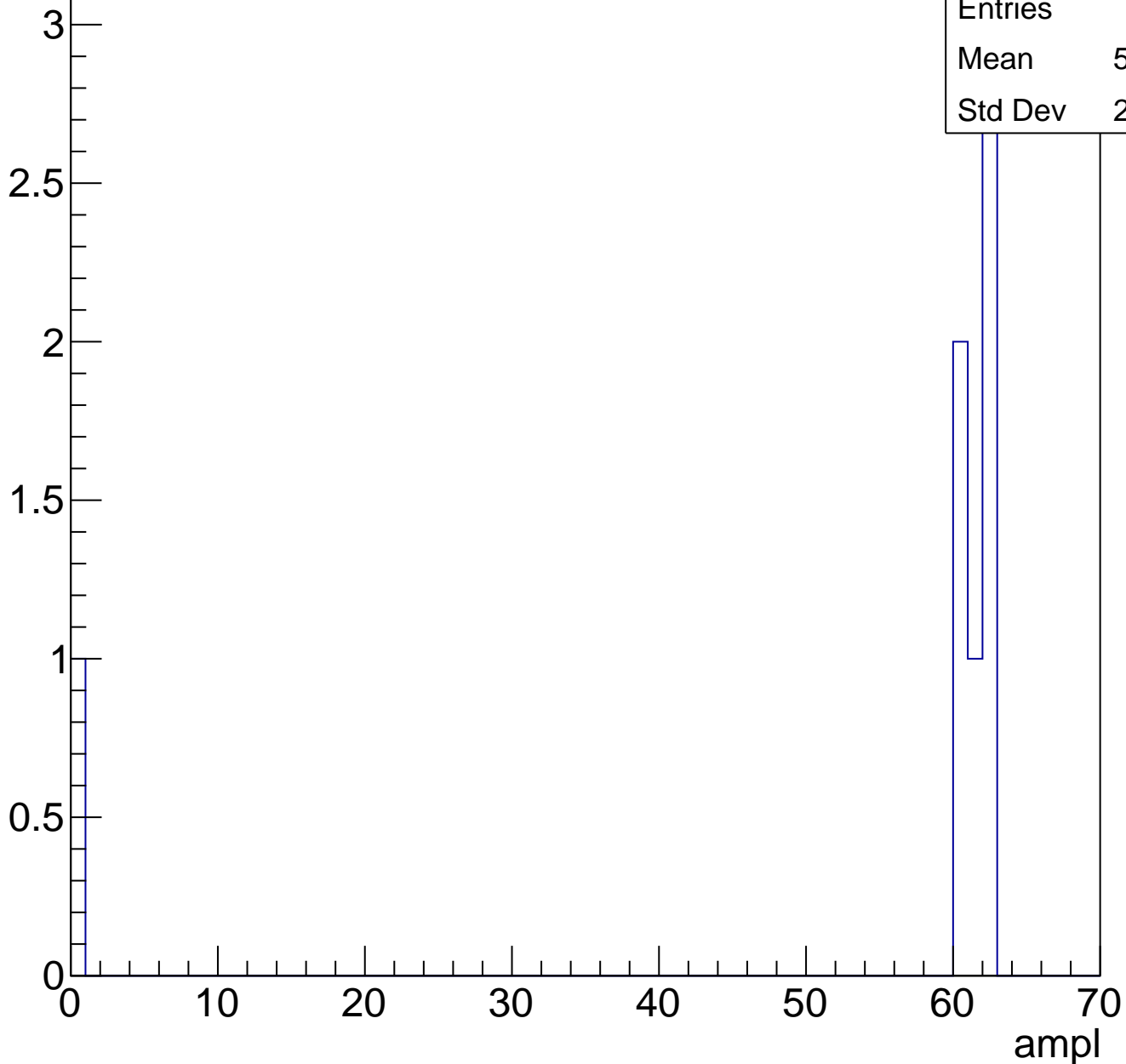
70



B1L101S, U23-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch95, adc0

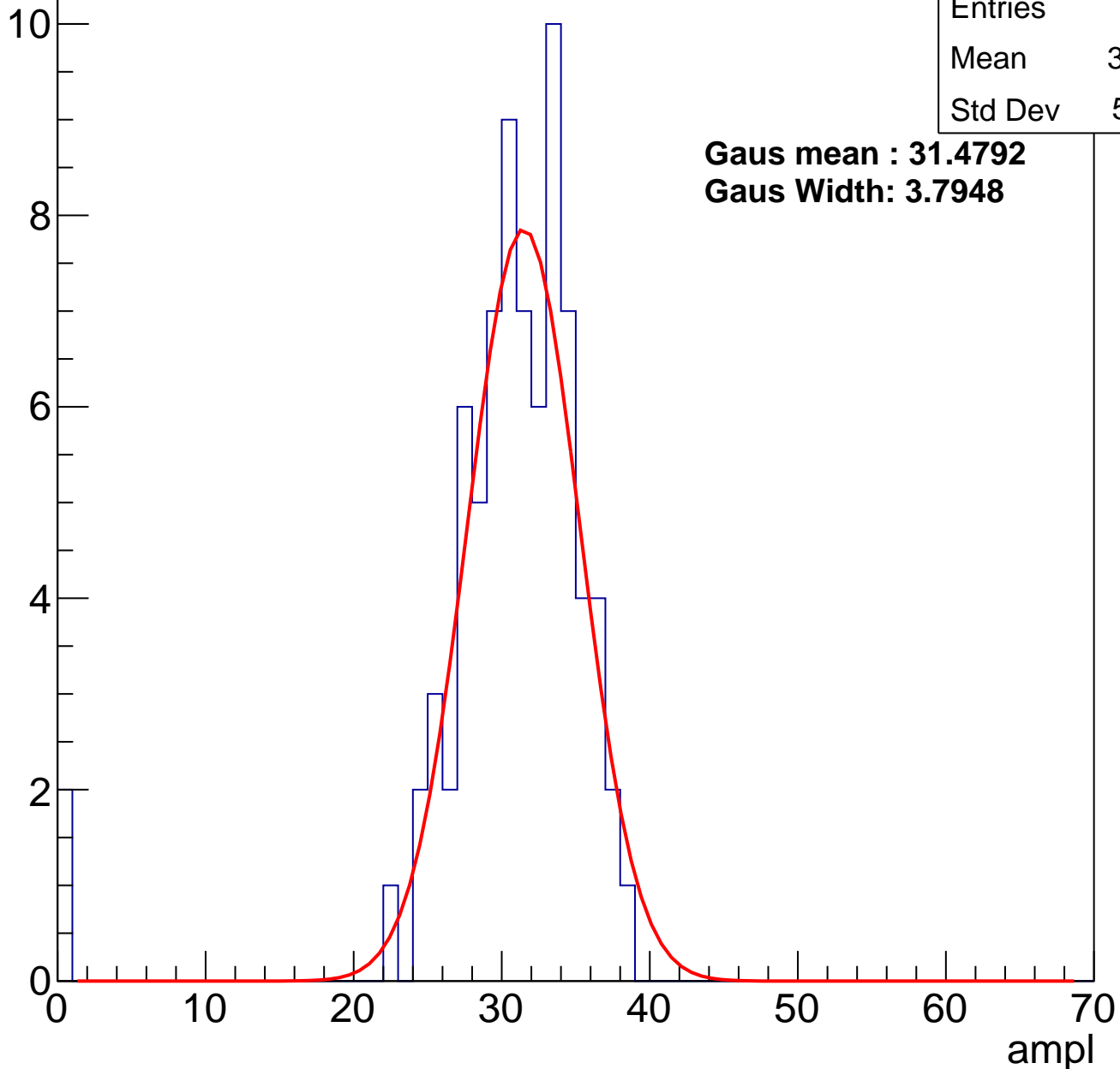
calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	30.06
Std Dev	5.951

Gaus mean : 31.4792

Gaus Width: 3.7948

Entry



B1L101S, U23-ch95, adc1

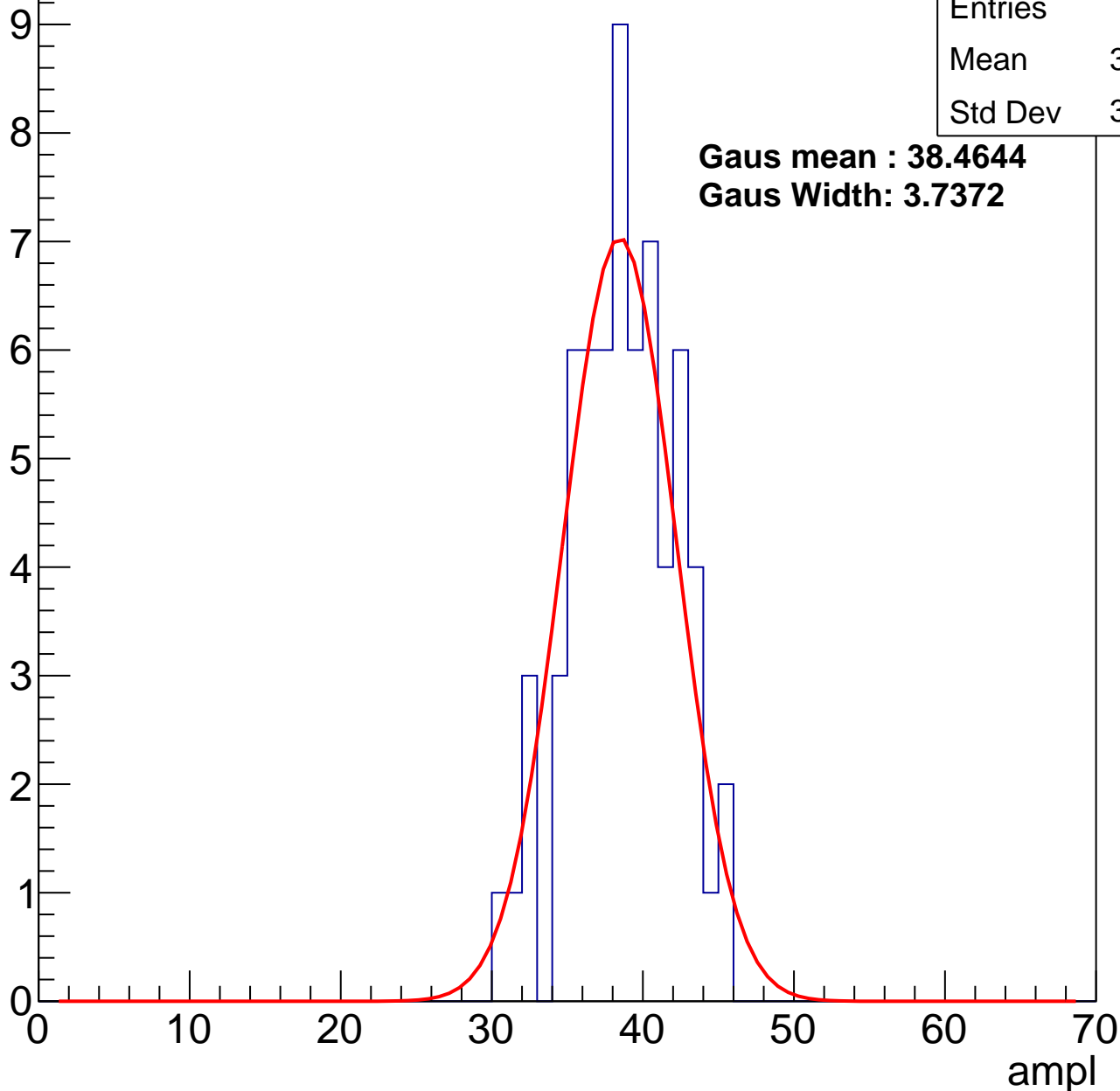
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	38.23
Std Dev	3.396

Gaus mean : 38.4644

Gaus Width: 3.7372



B1L101S, U23-ch95, adc2

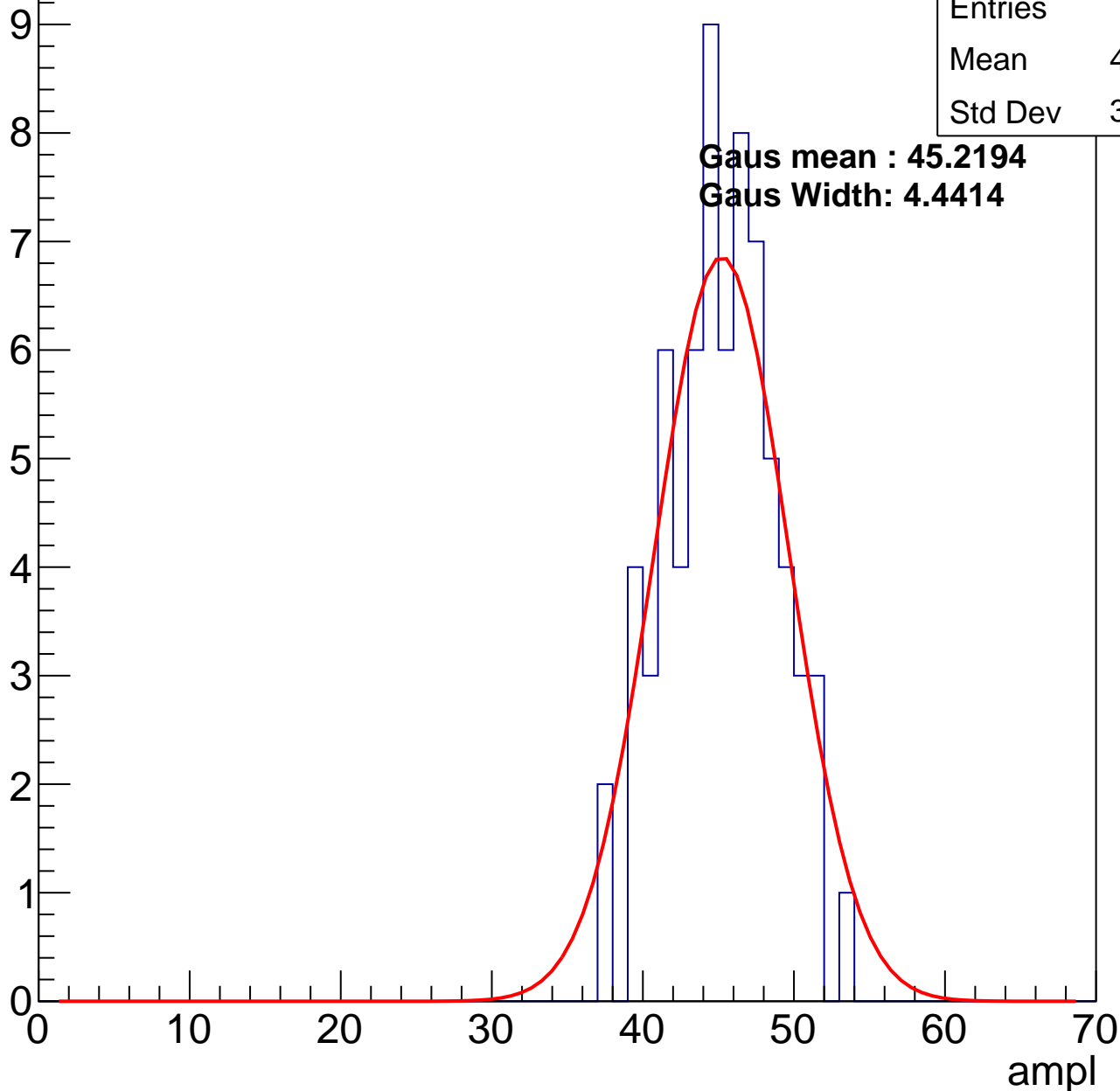
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.75
Std Dev	3.559

Gaus mean : 45.2194

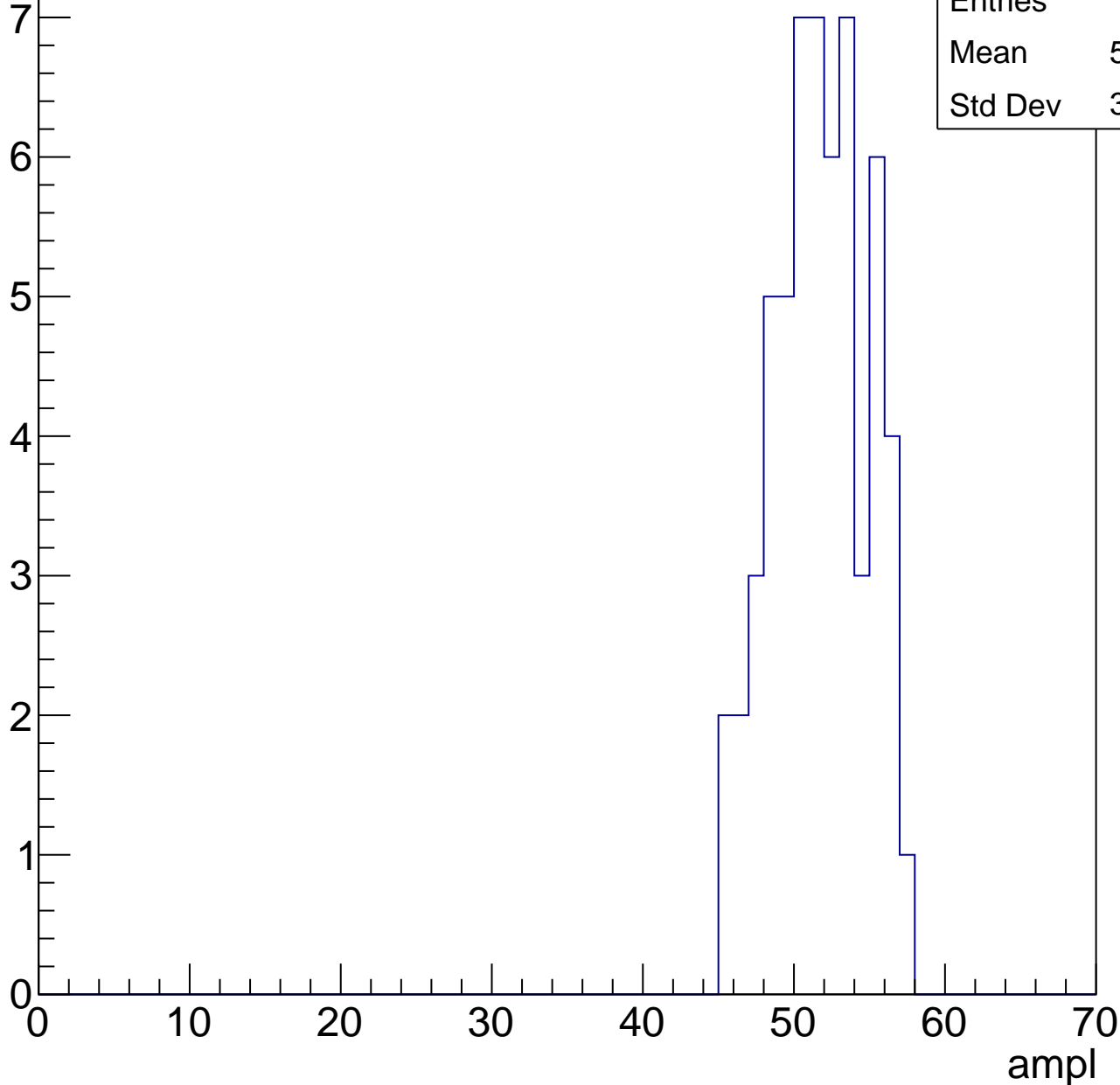
Gaus Width: 4.4414



B1L101S, U23-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



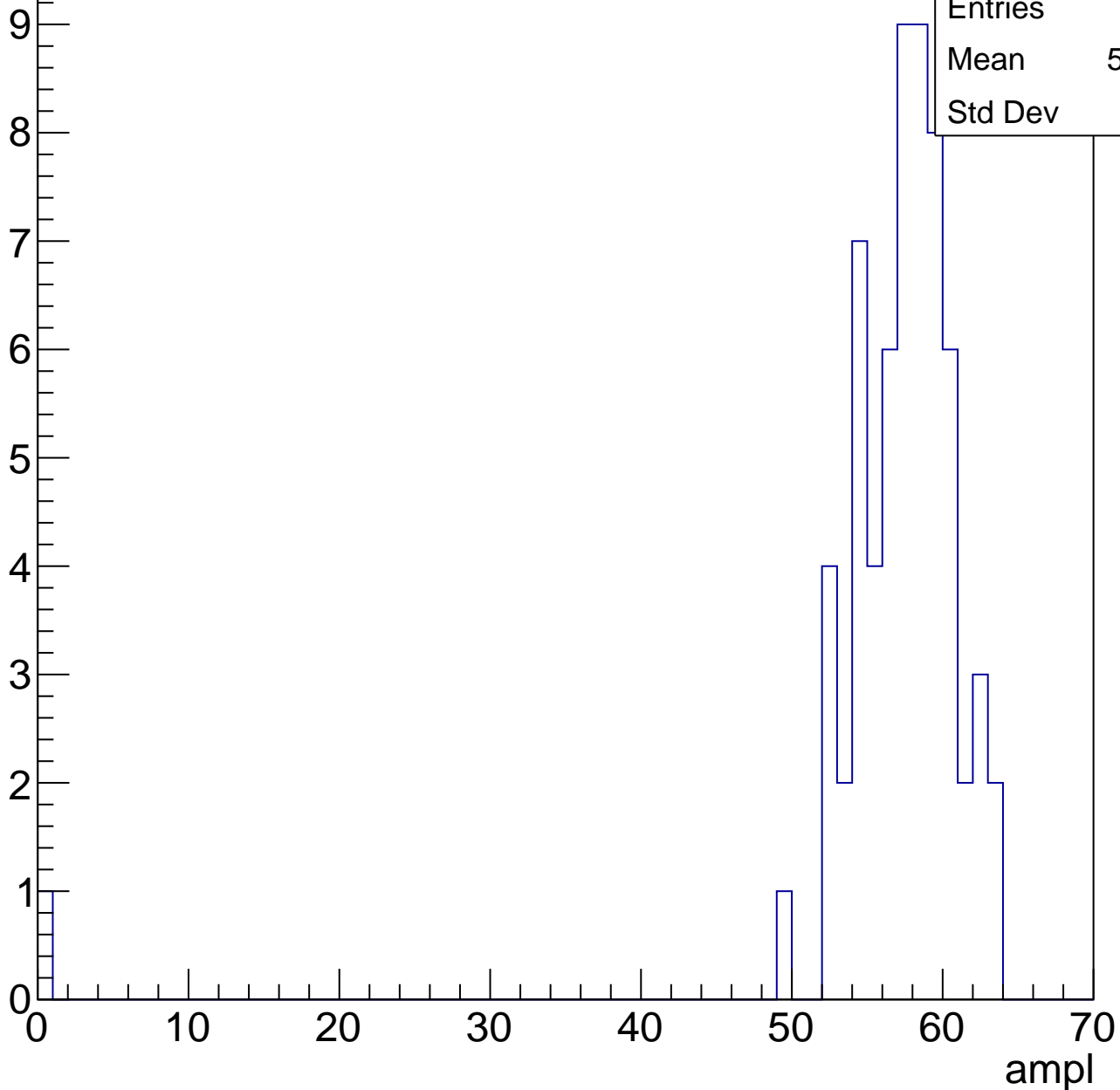
Entries	58
Mean	51.22
Std Dev	3.029

B1L101S, U23-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

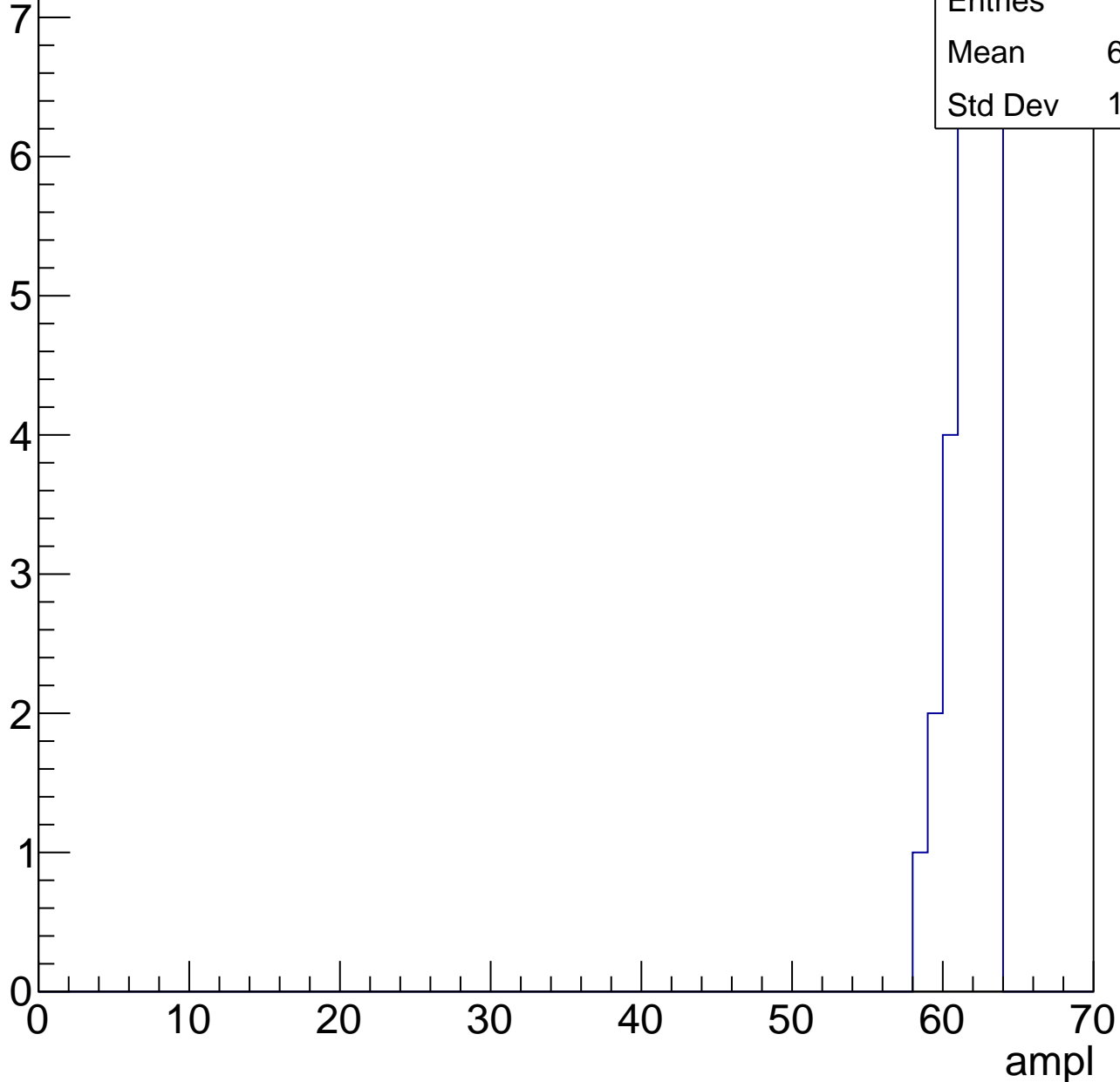
Entries	64
Mean	56.22
Std Dev	7.67



B1L101S, U23-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch96, adc0

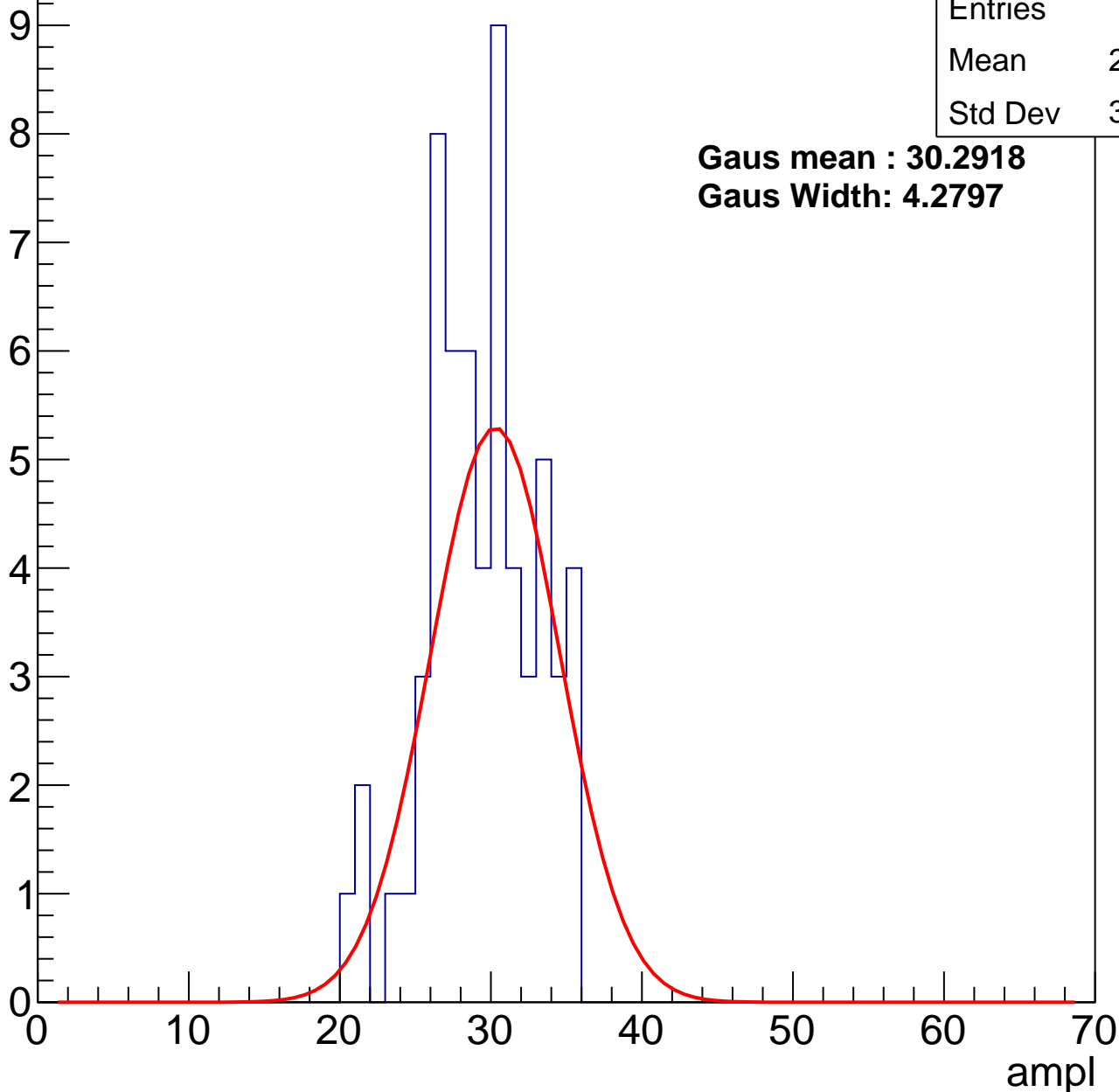
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.92
Std Dev	3.584

Gaus mean : 30.2918

Gaus Width: 4.2797



B1L101S, U23-ch96, adc1

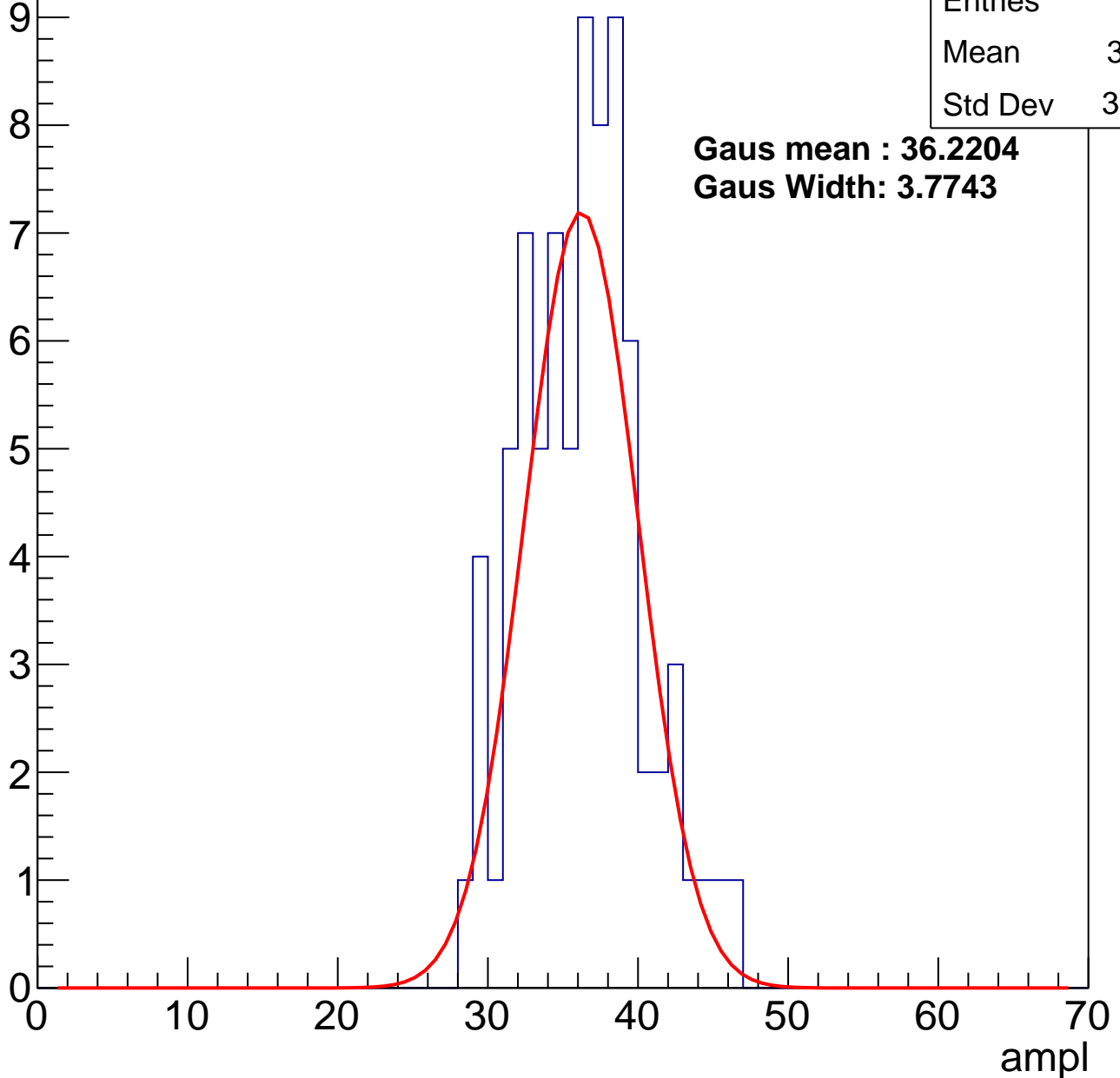
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.81
Std Dev	3.906

Gaus mean : 36.2204

Gaus Width: 3.7743



B1L101S, U23-ch96, adc2

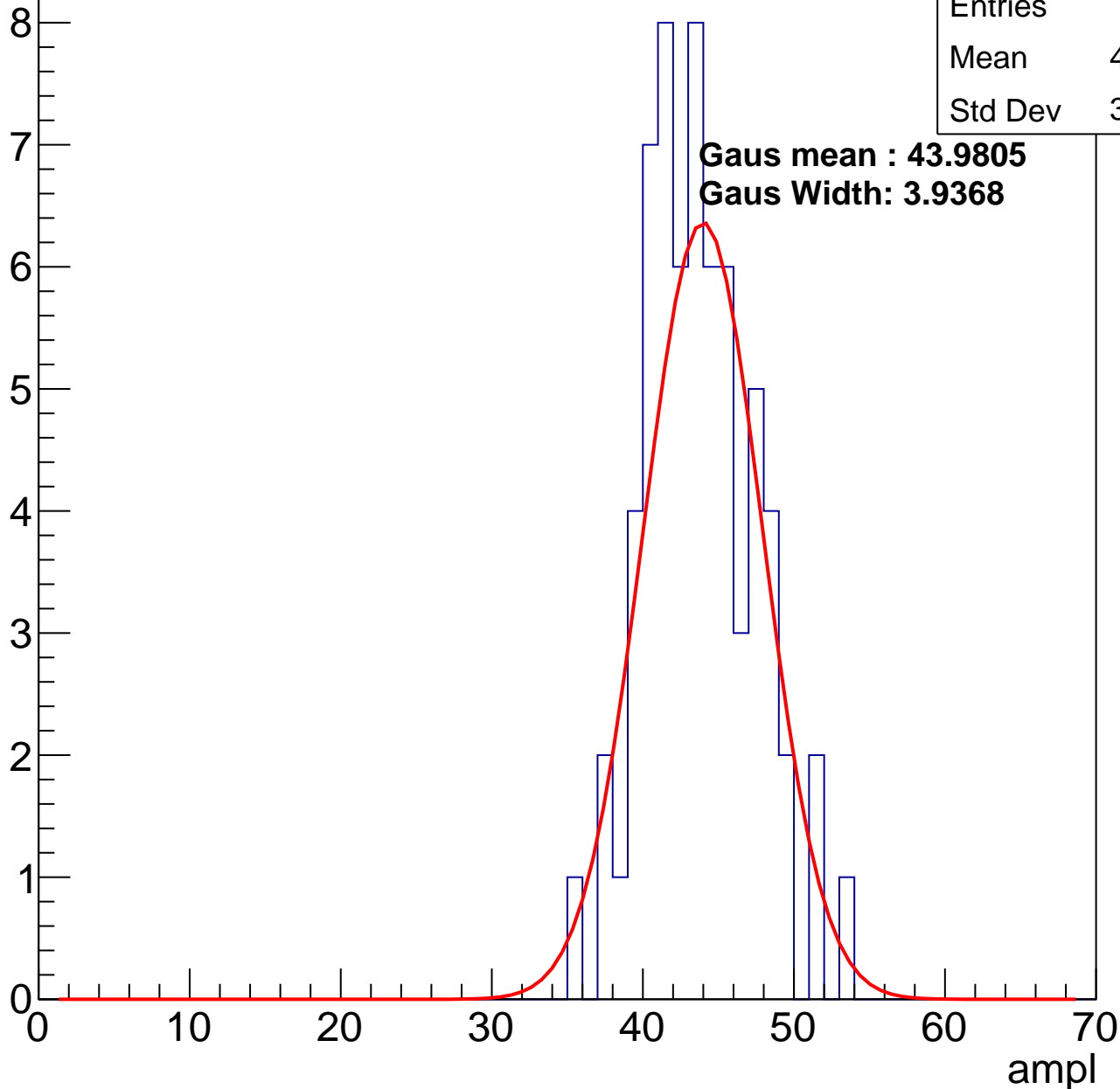
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.32
Std Dev	3.602

Gaus mean : 43.9805

Gaus Width: 3.9368

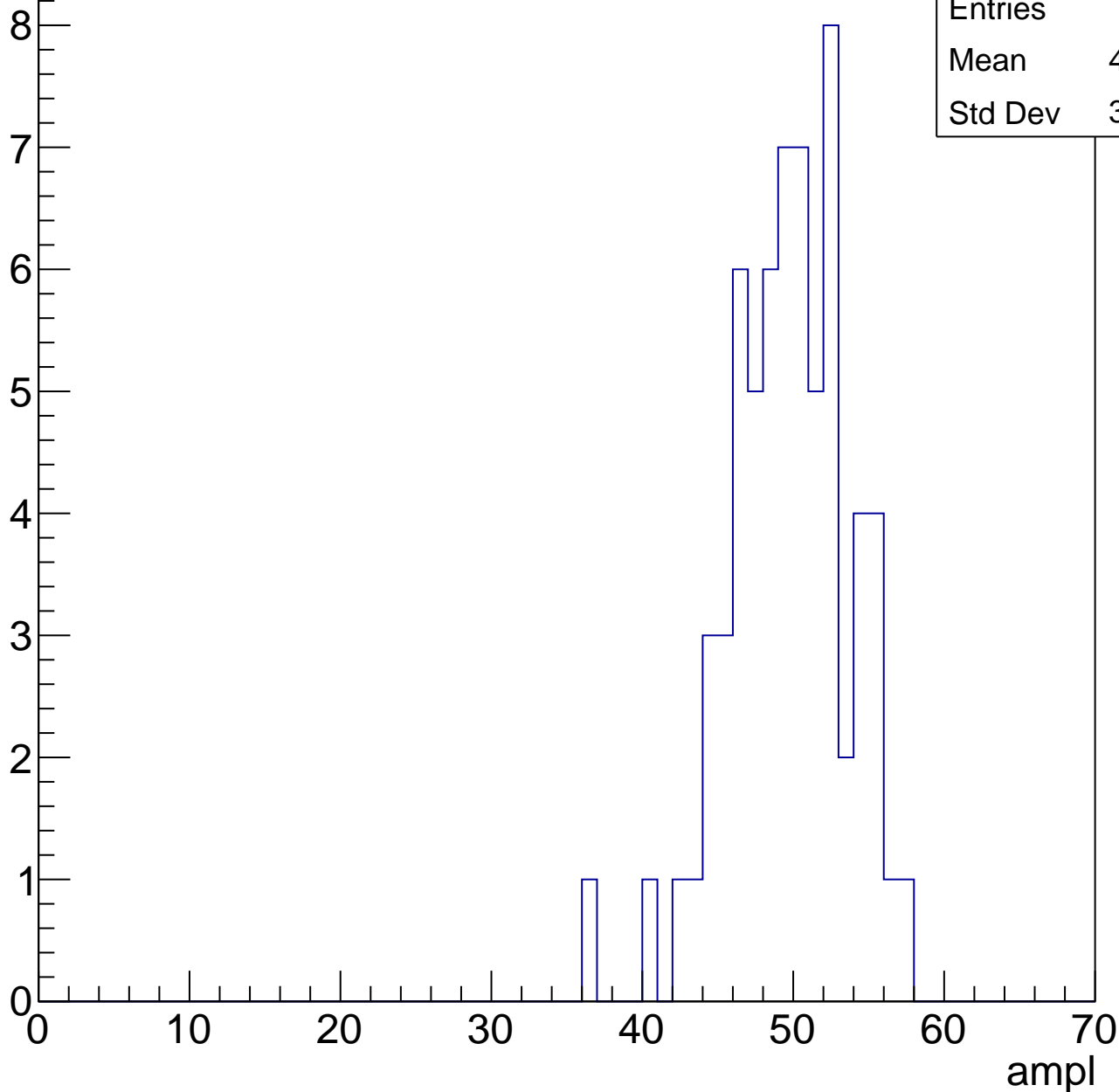


B1L101S, U23-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	49.18
Std Dev	3.939



B1L101S, U23-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 55.33

Std Dev 3.156

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

6

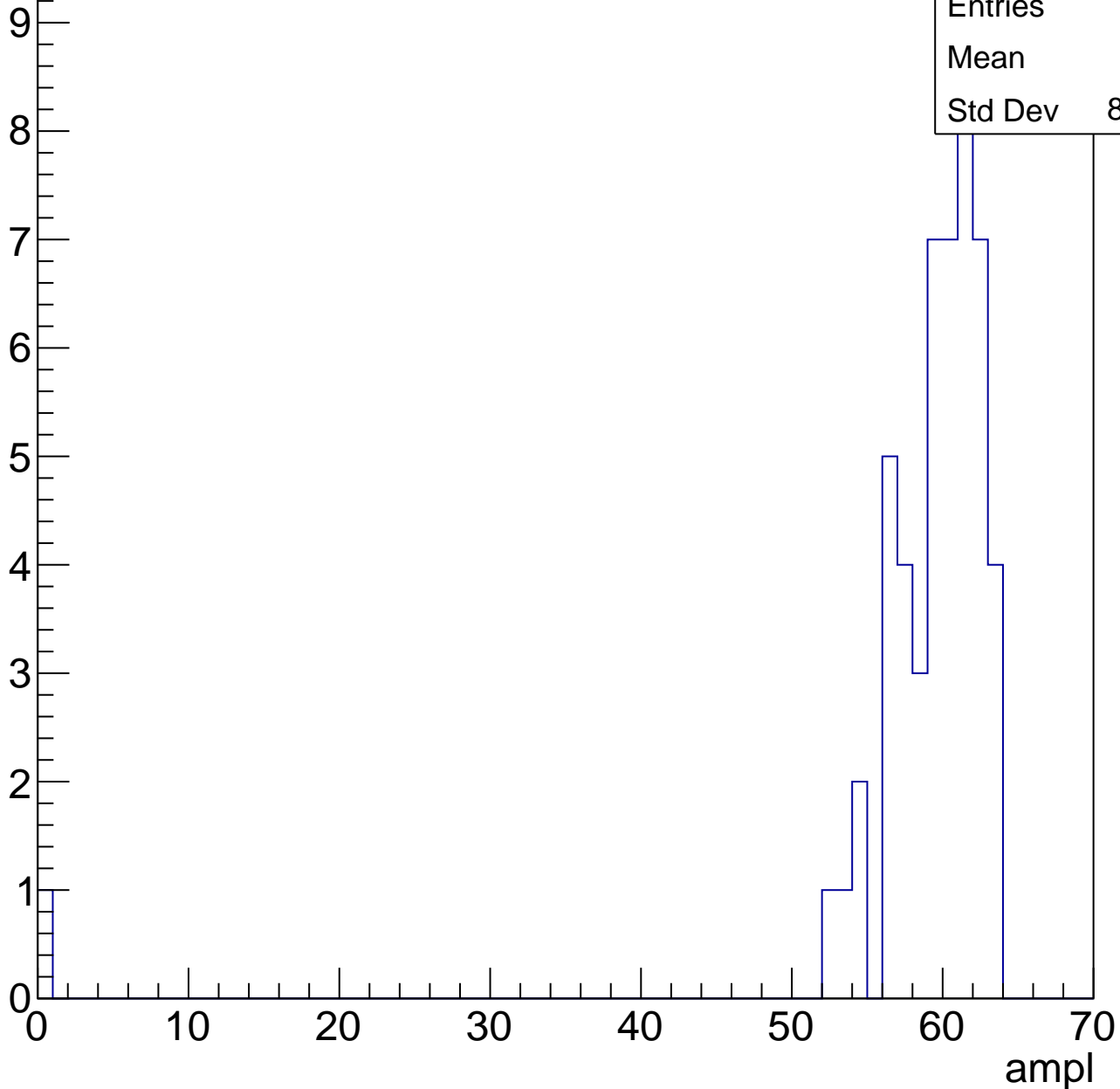
7

B1L101S, U23-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.1
Std Dev	8.642

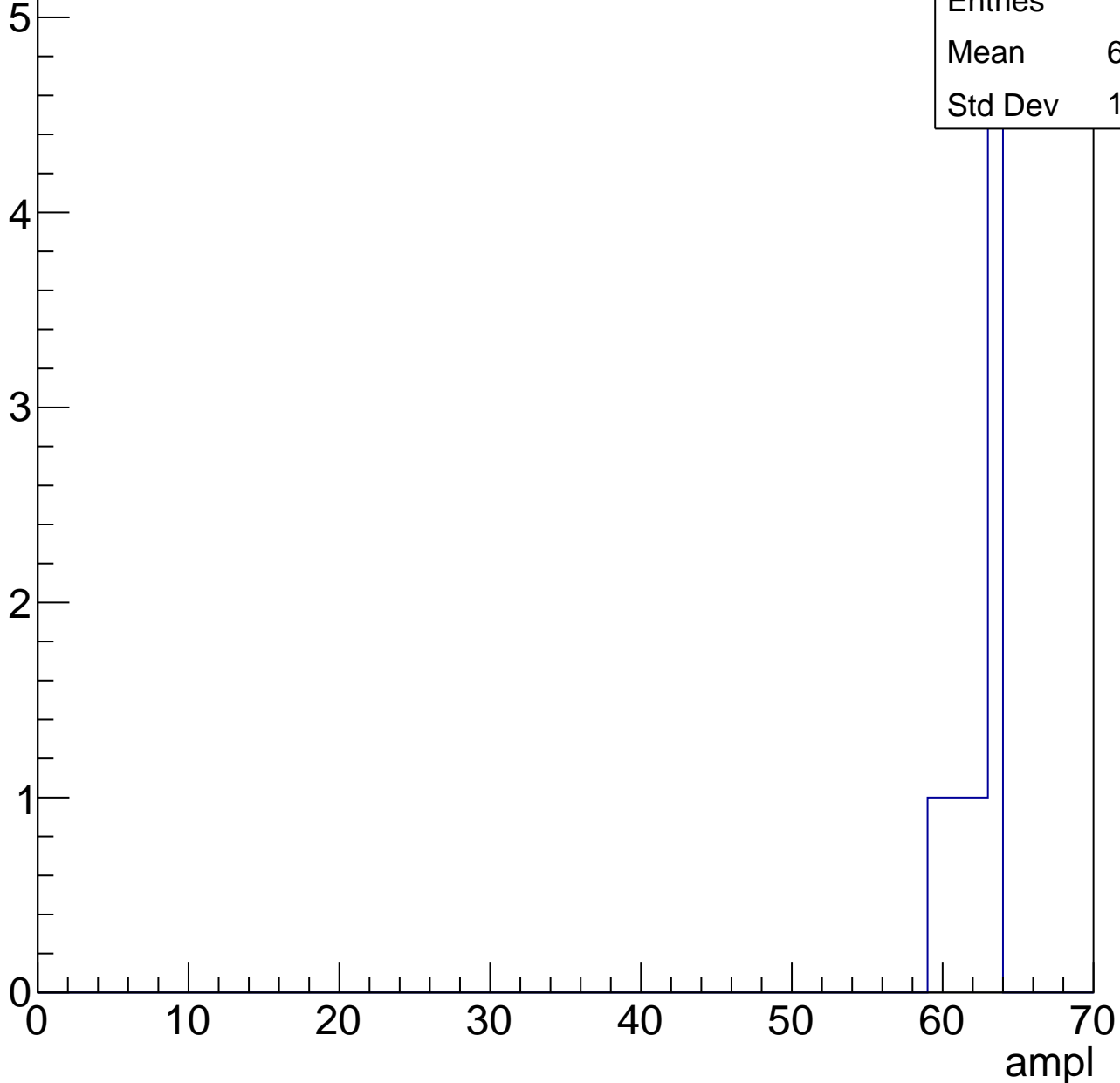


B1L101S, U23-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	61.89
Std Dev	1.449



B1L101S, U23-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch97, adc0

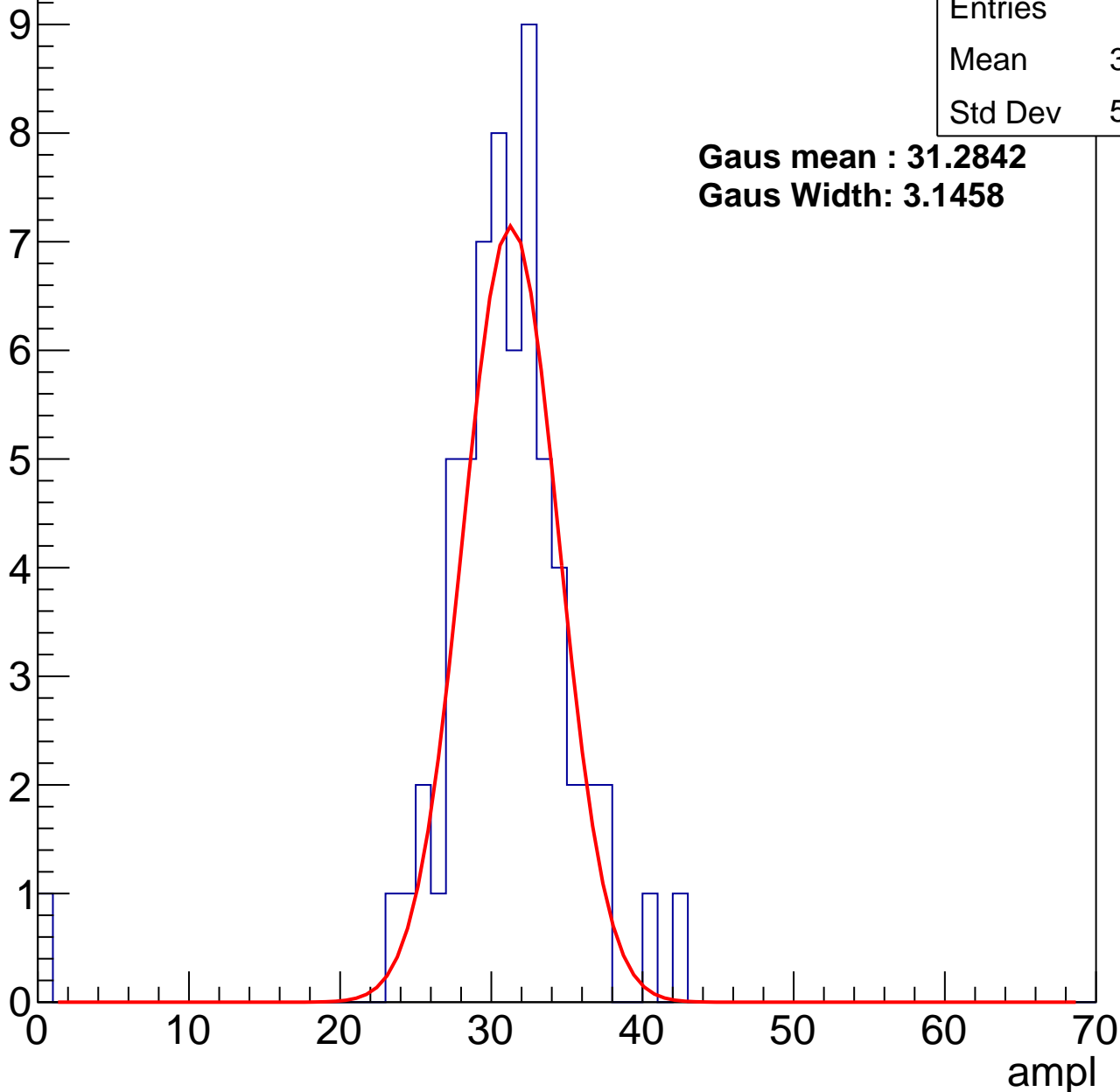
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.38
Std Dev	5.239

Gaus mean : 31.2842

Gaus Width: 3.1458



B1L101S, U23-ch97, adc1

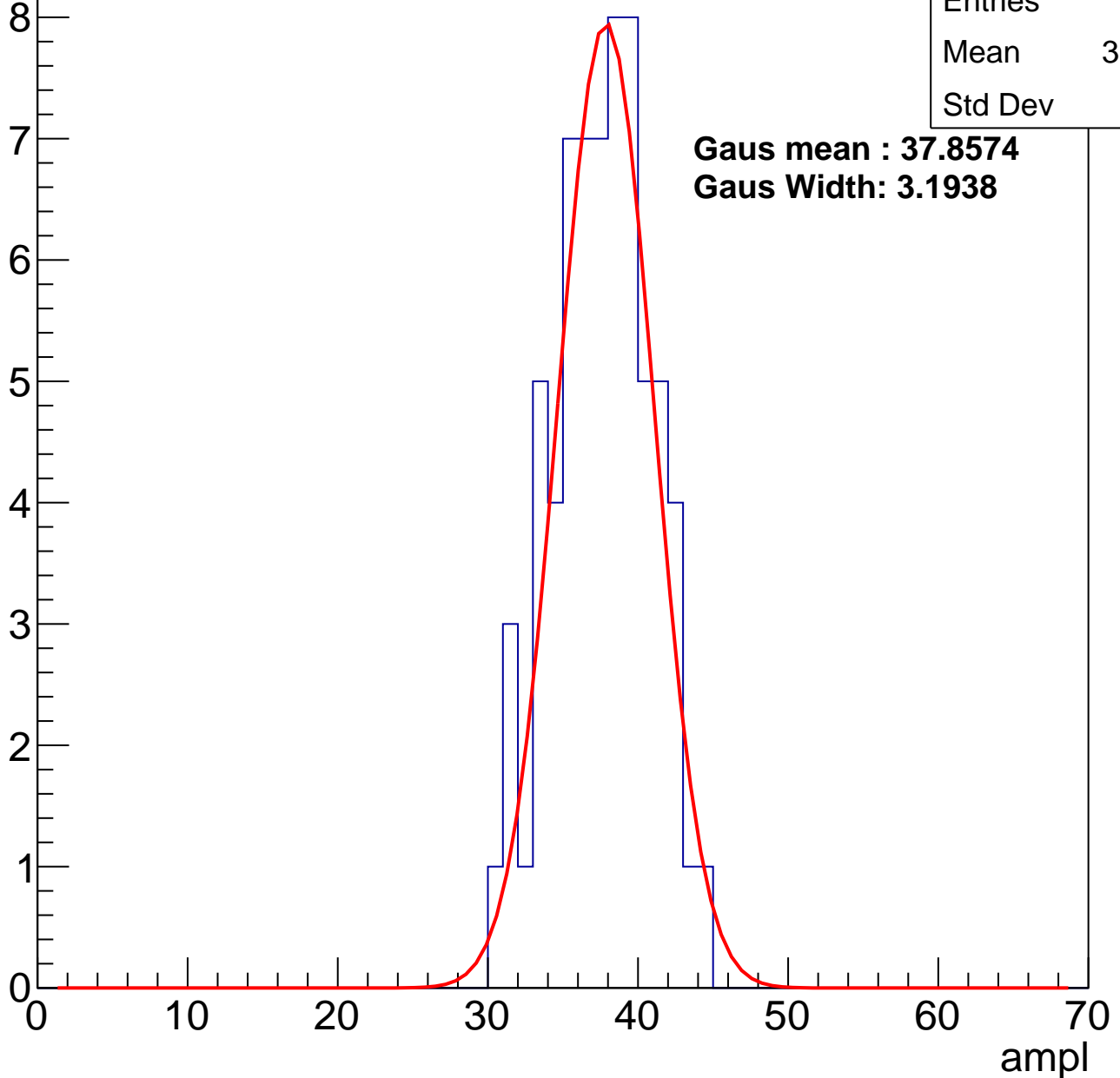
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	37.13
Std Dev	3.19

Gaus mean : 37.8574

Gaus Width: 3.1938



B1L101S, U23-ch97, adc2

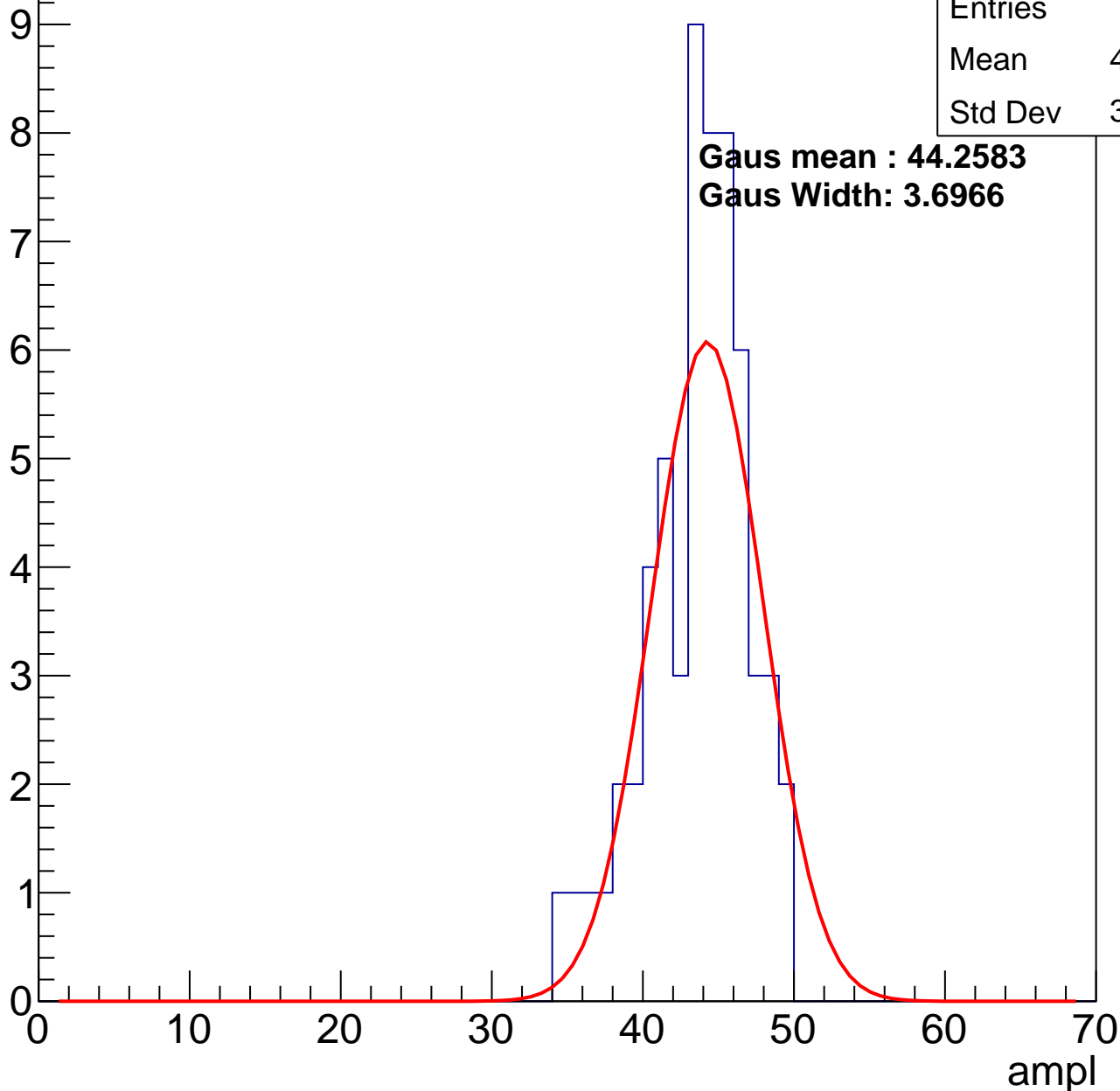
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.14
Std Dev	3.342

Gaus mean : 44.2583

Gaus Width: 3.6966

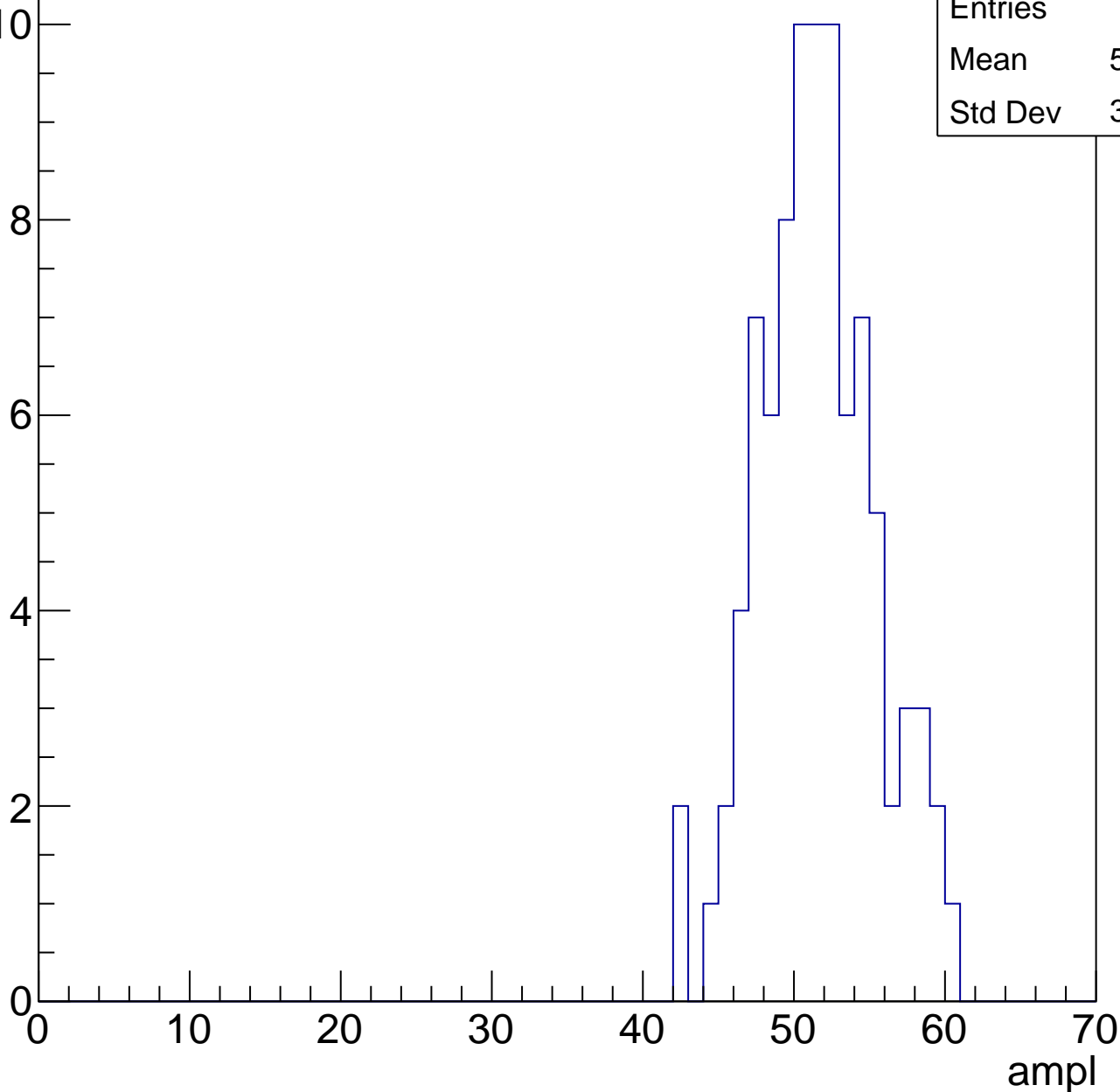


B1L101S, U23-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	51.09
Std Dev	3.794

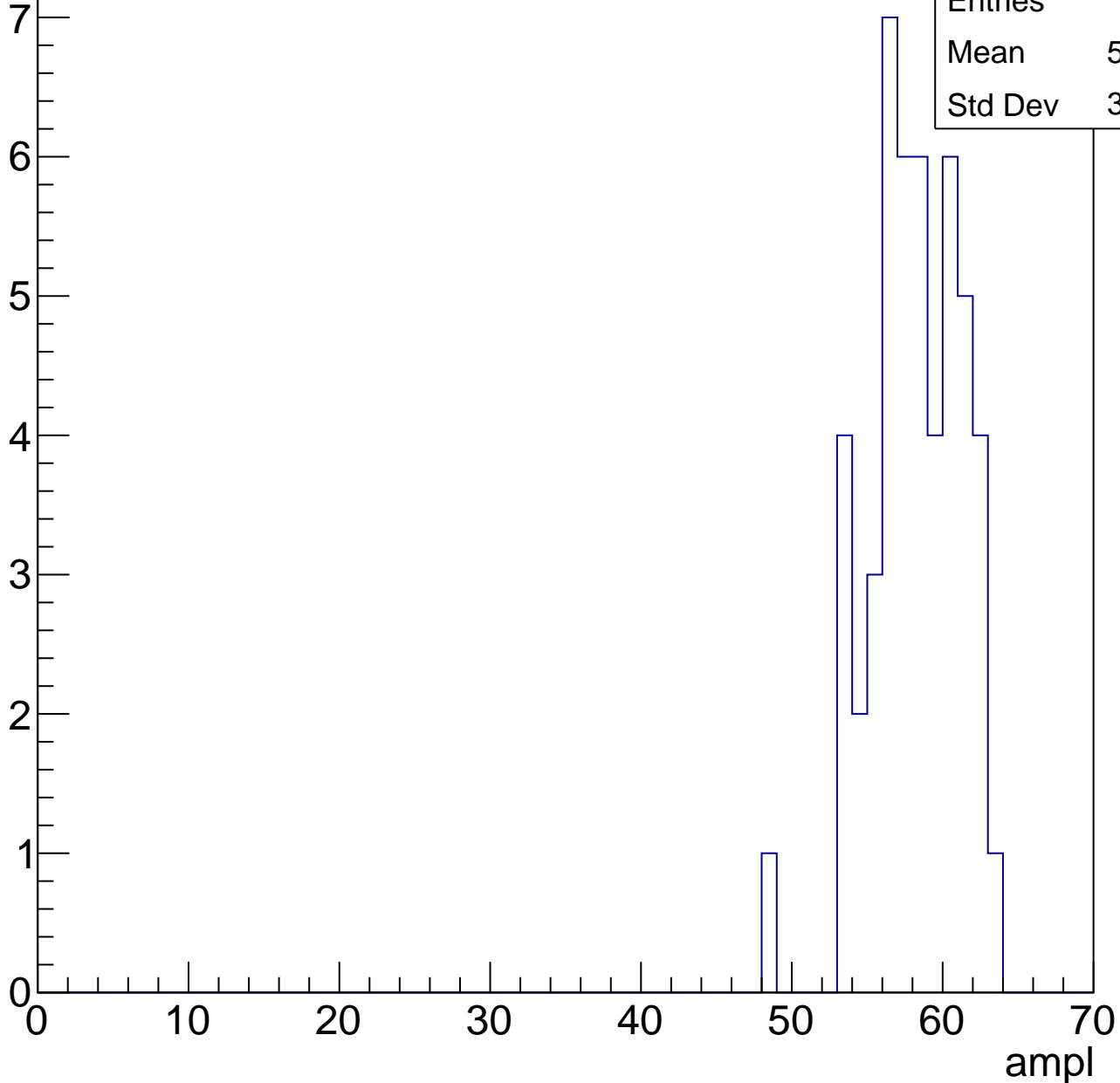


B1L101S, U23-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.69
Std Dev	3.032

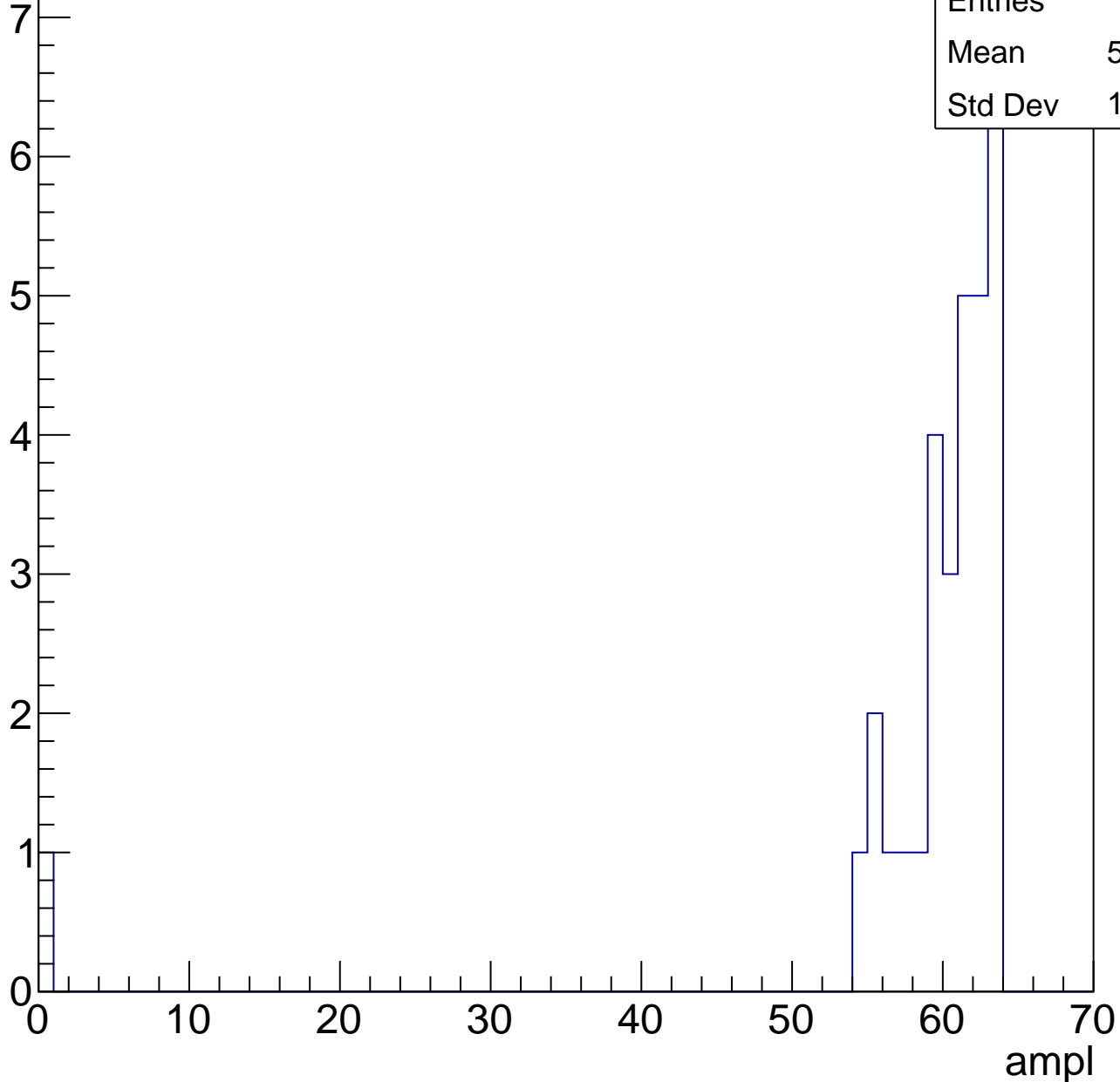


B1L101S, U23-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.29
Std Dev	10.95



B1L101S, U23-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch98, adc0

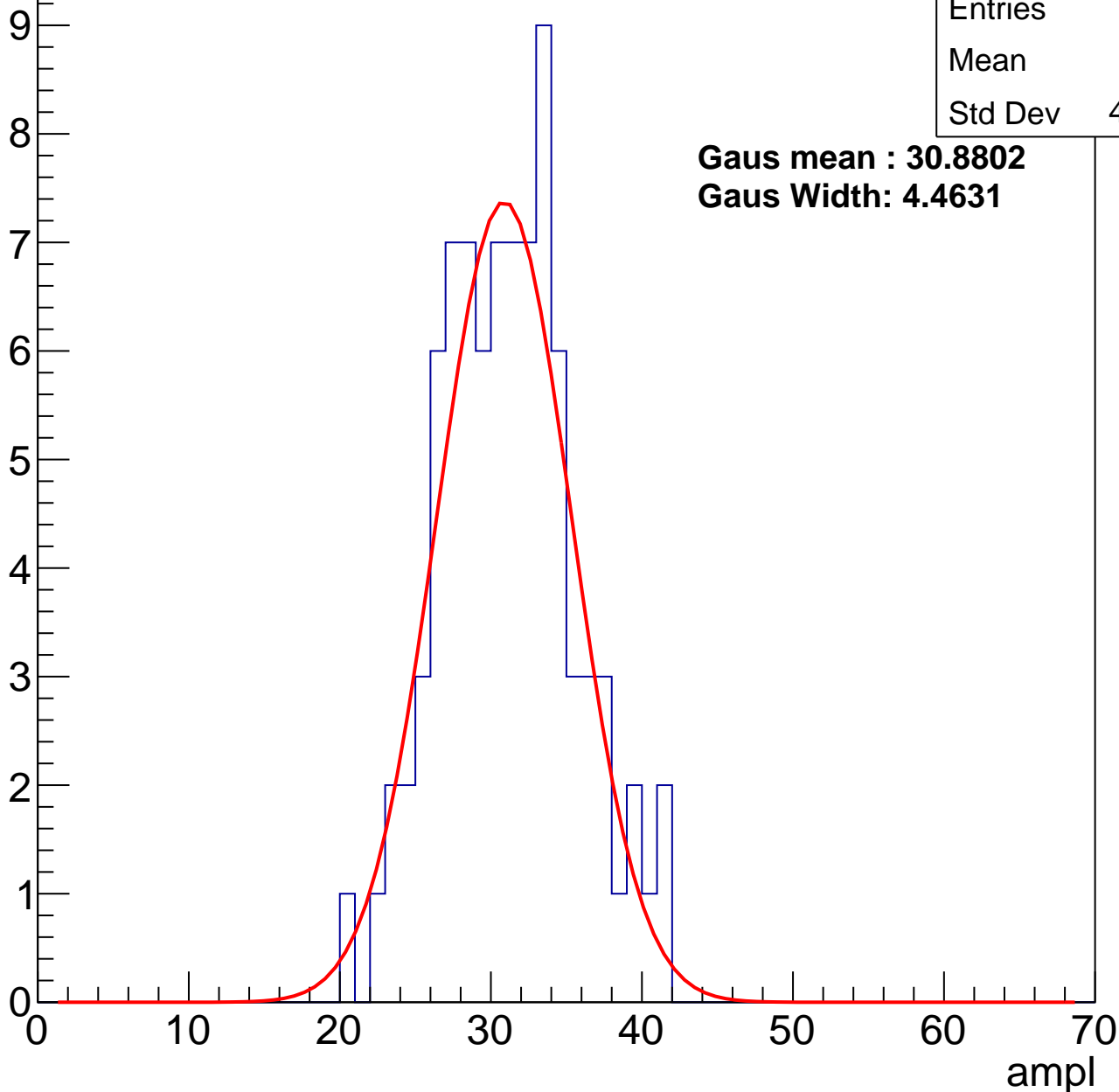
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	30.7
Std Dev	4.407

Gaus mean : 30.8802

Gaus Width: 4.4631



B1L101S, U23-ch98, adc1

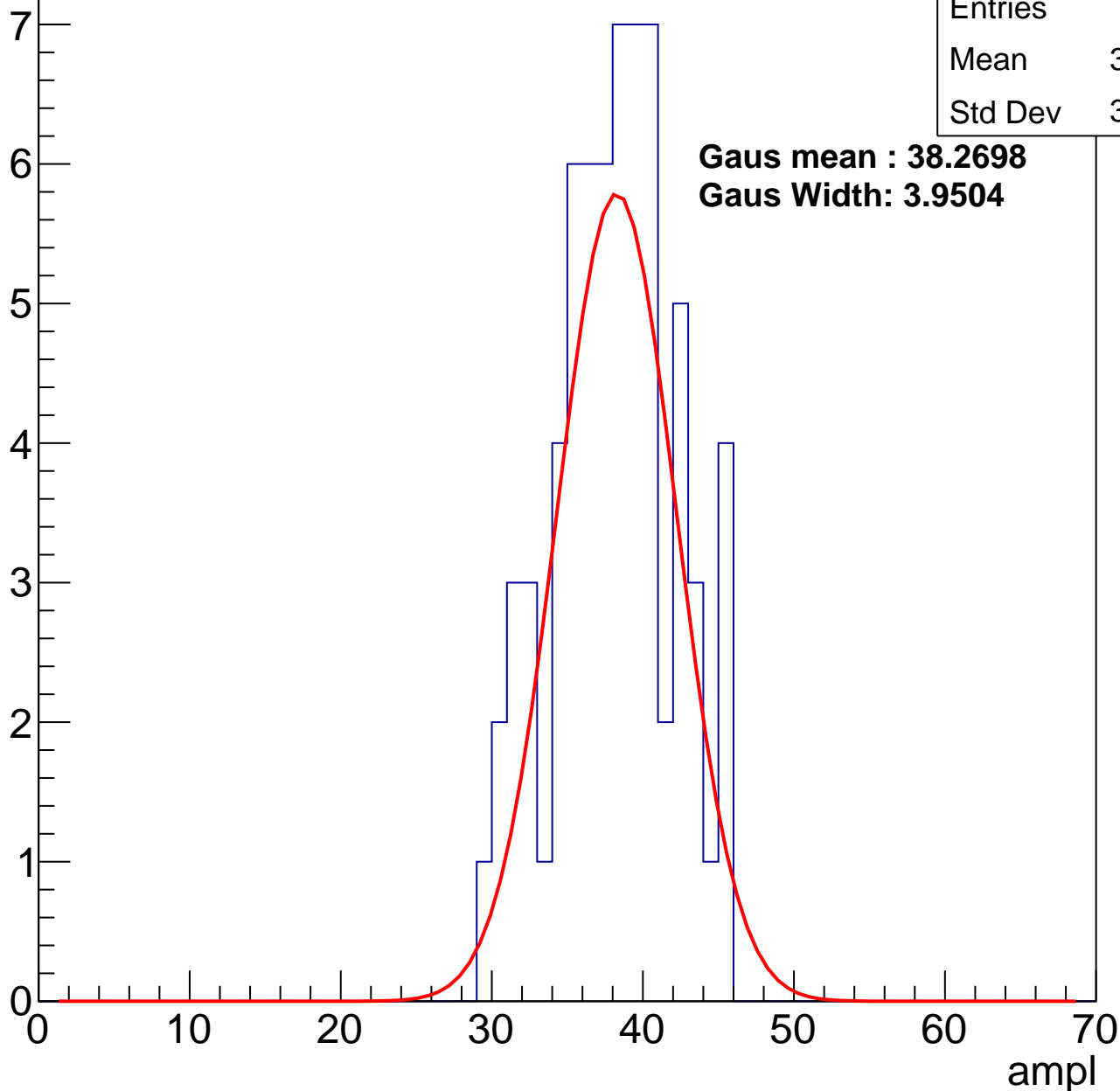
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.63
Std Dev	3.963

Gaus mean : 38.2698

Gaus Width: 3.9504



B1L101S, U23-ch98, adc2

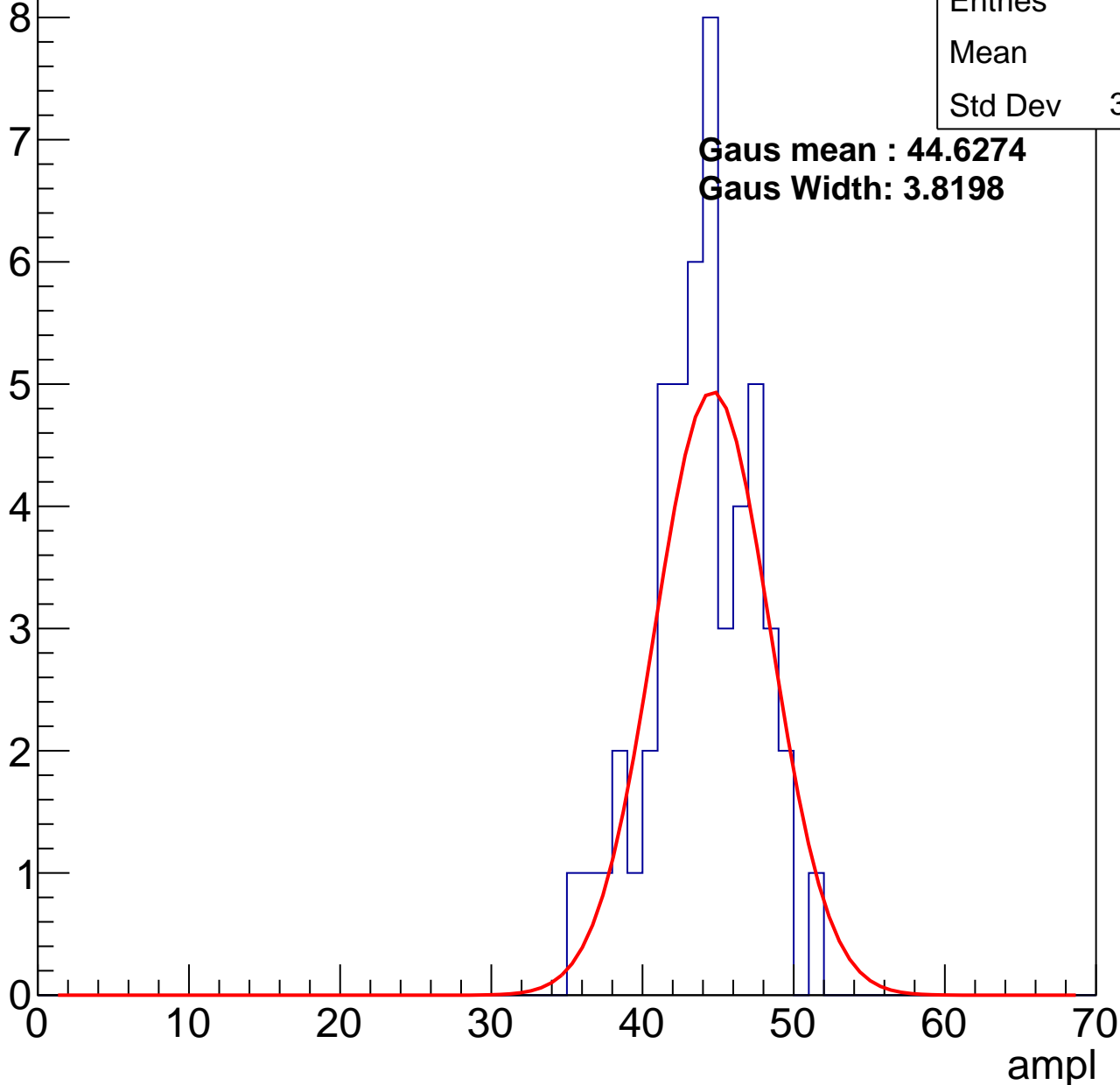
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	43.5
Std Dev	3.442

Gaus mean : 44.6274

Gaus Width: 3.8198

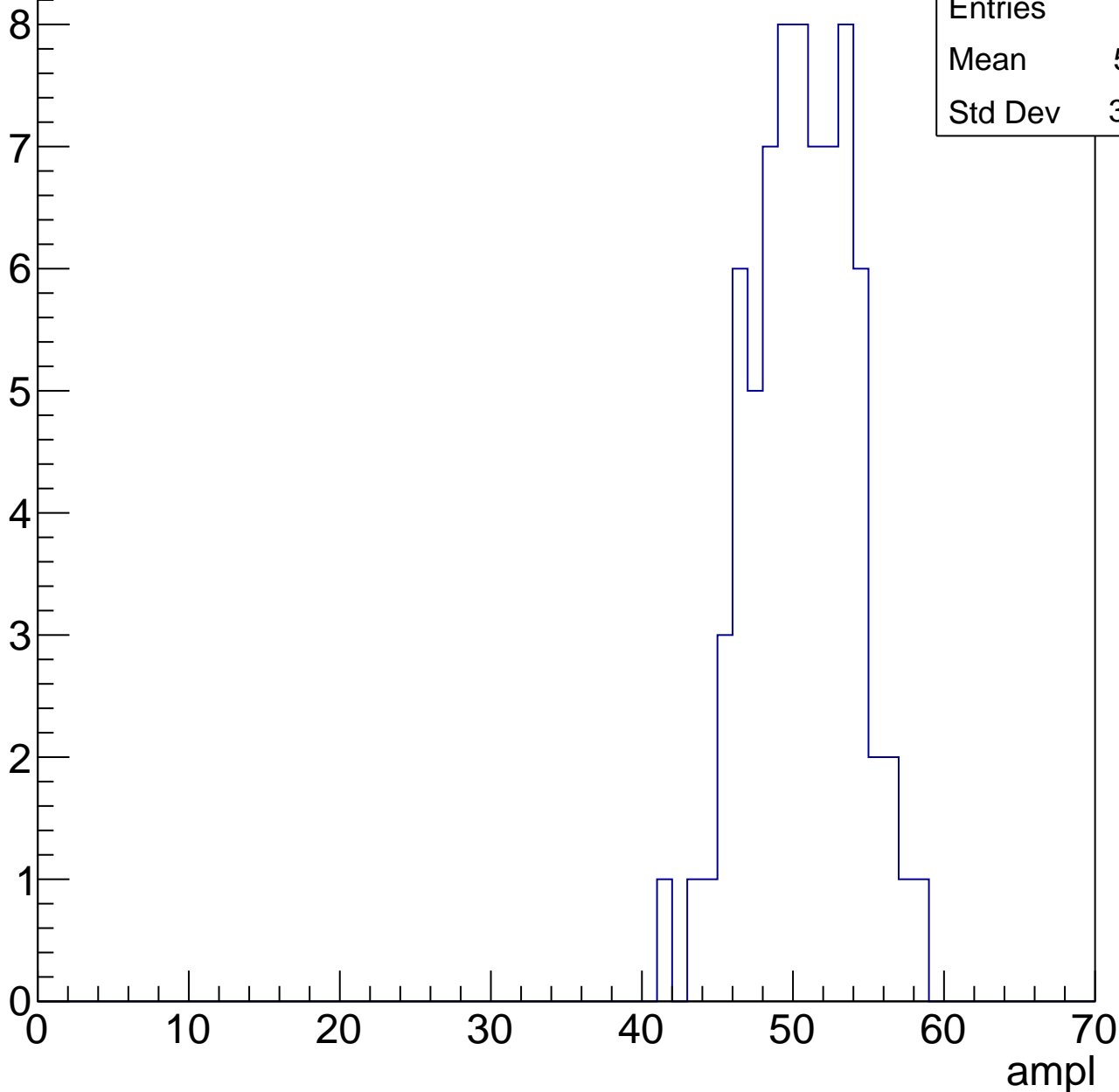


B1L101S, U23-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	50.11
Std Dev	3.399

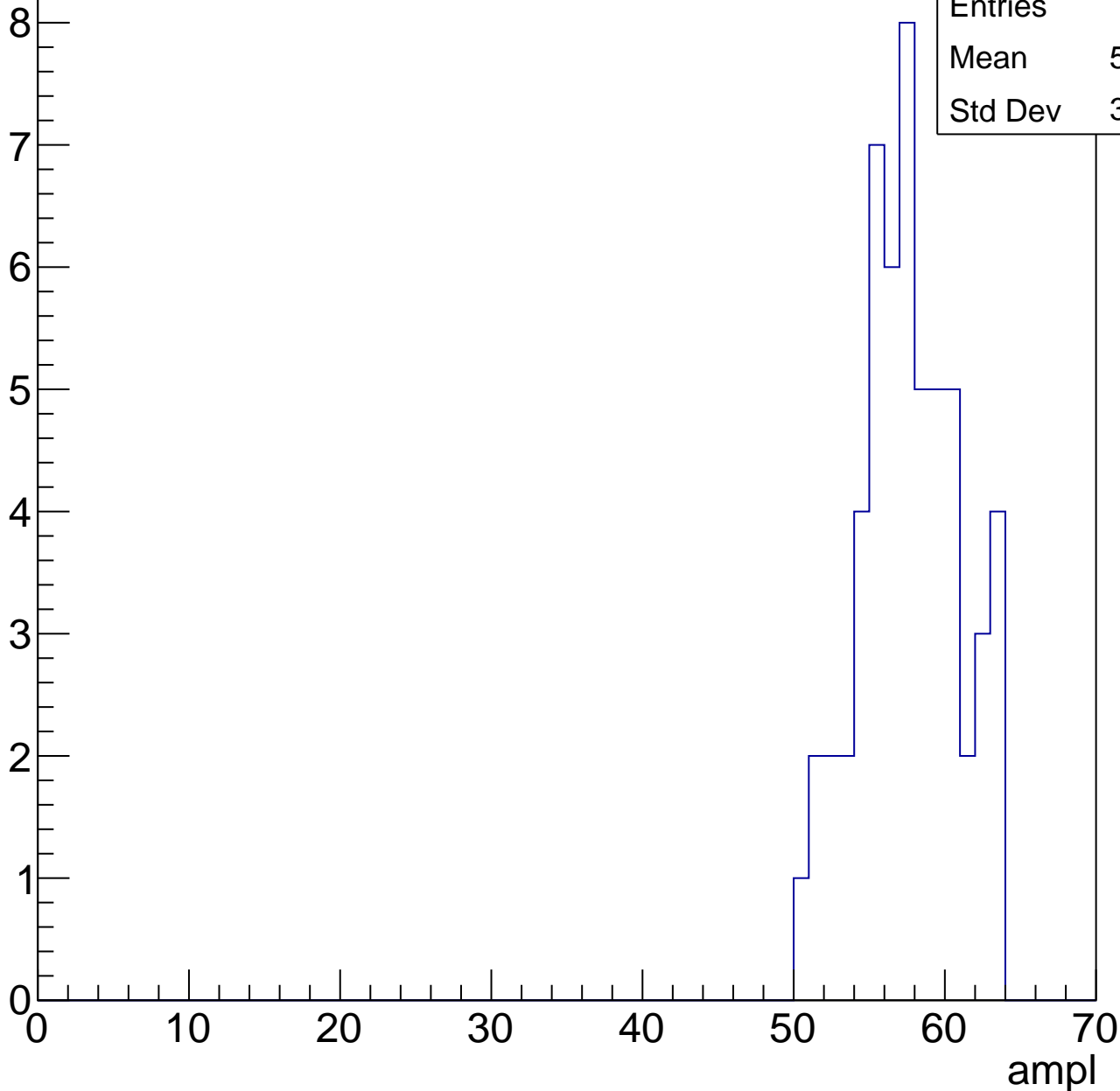


B1L101S, U23-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

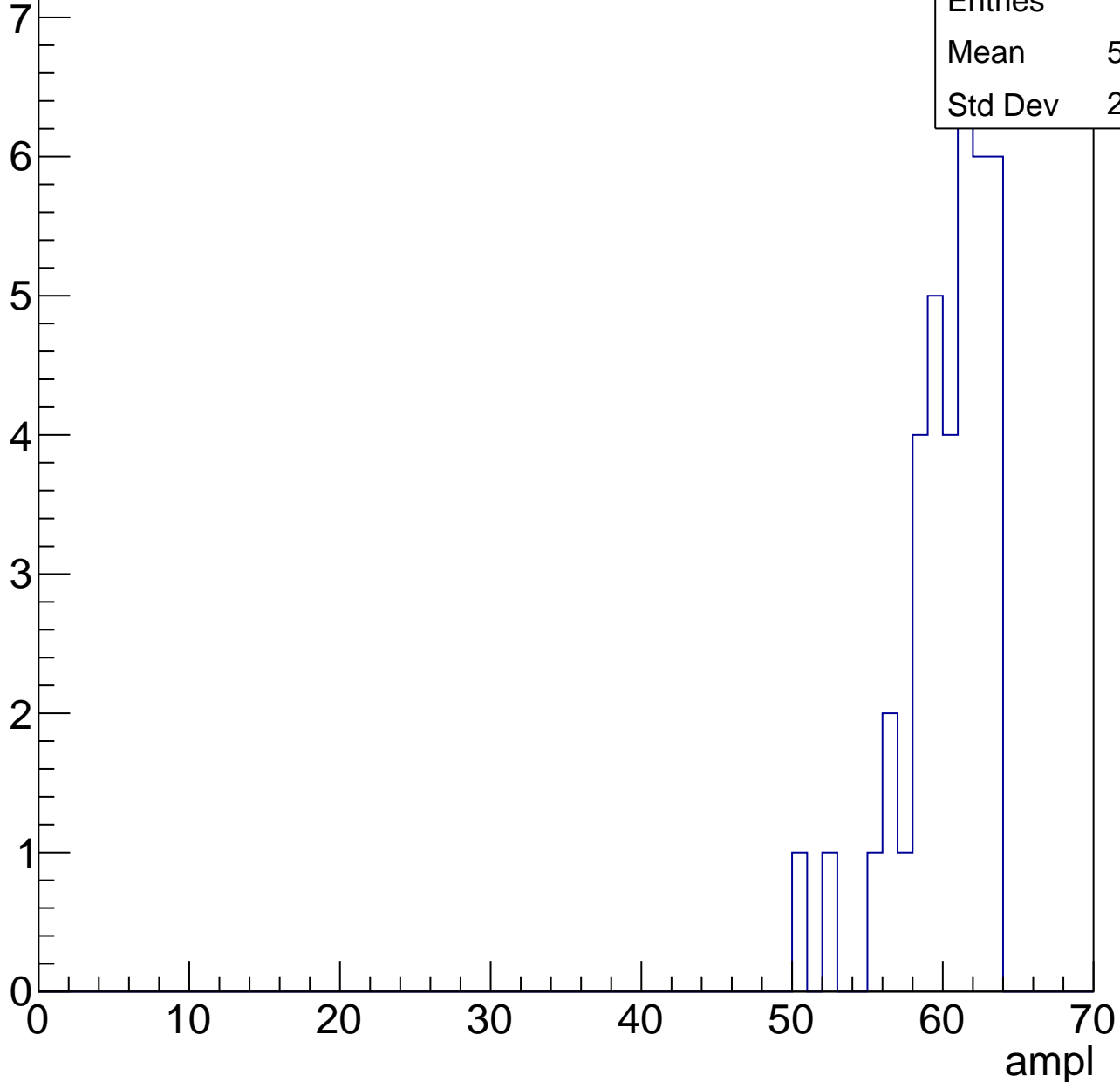
Entries	56
Mean	57.14
Std Dev	3.254



B1L101S, U23-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

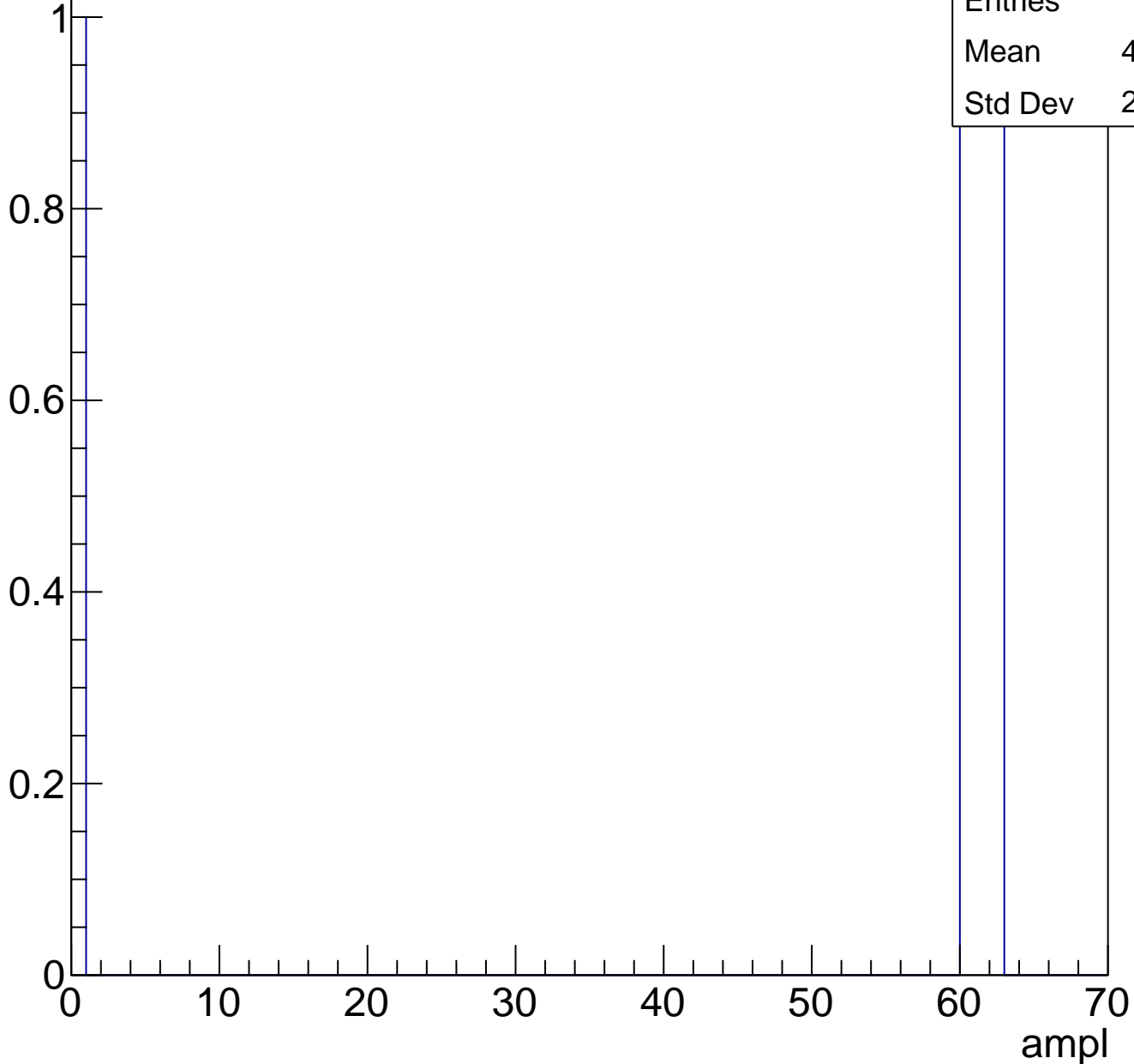
Entry



B1L101S, U23-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch99, adc0

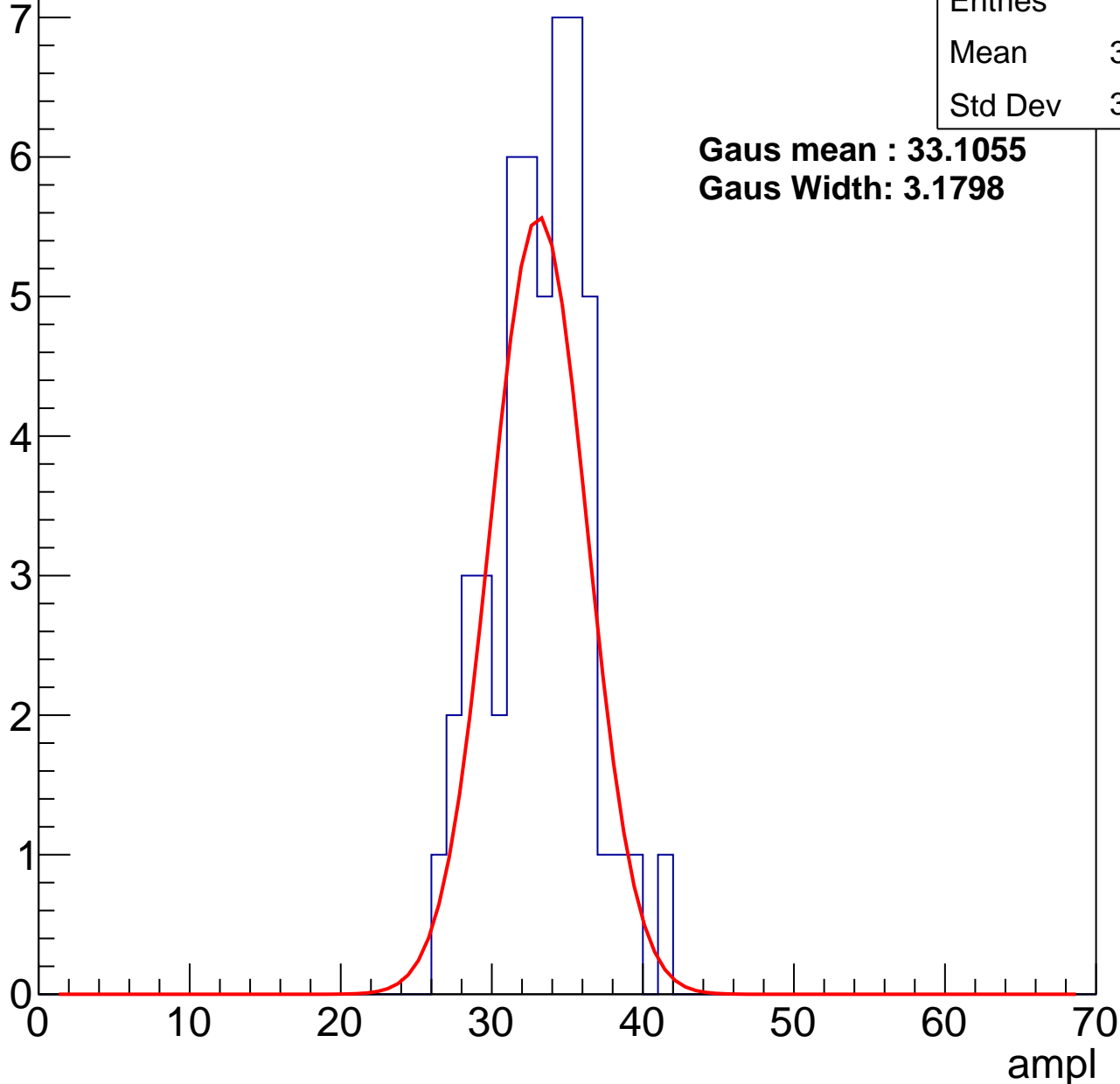
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	32.78
Std Dev	3.164

Gaus mean : 33.1055

Gaus Width: 3.1798



B1L101S, U23-ch99, adc1

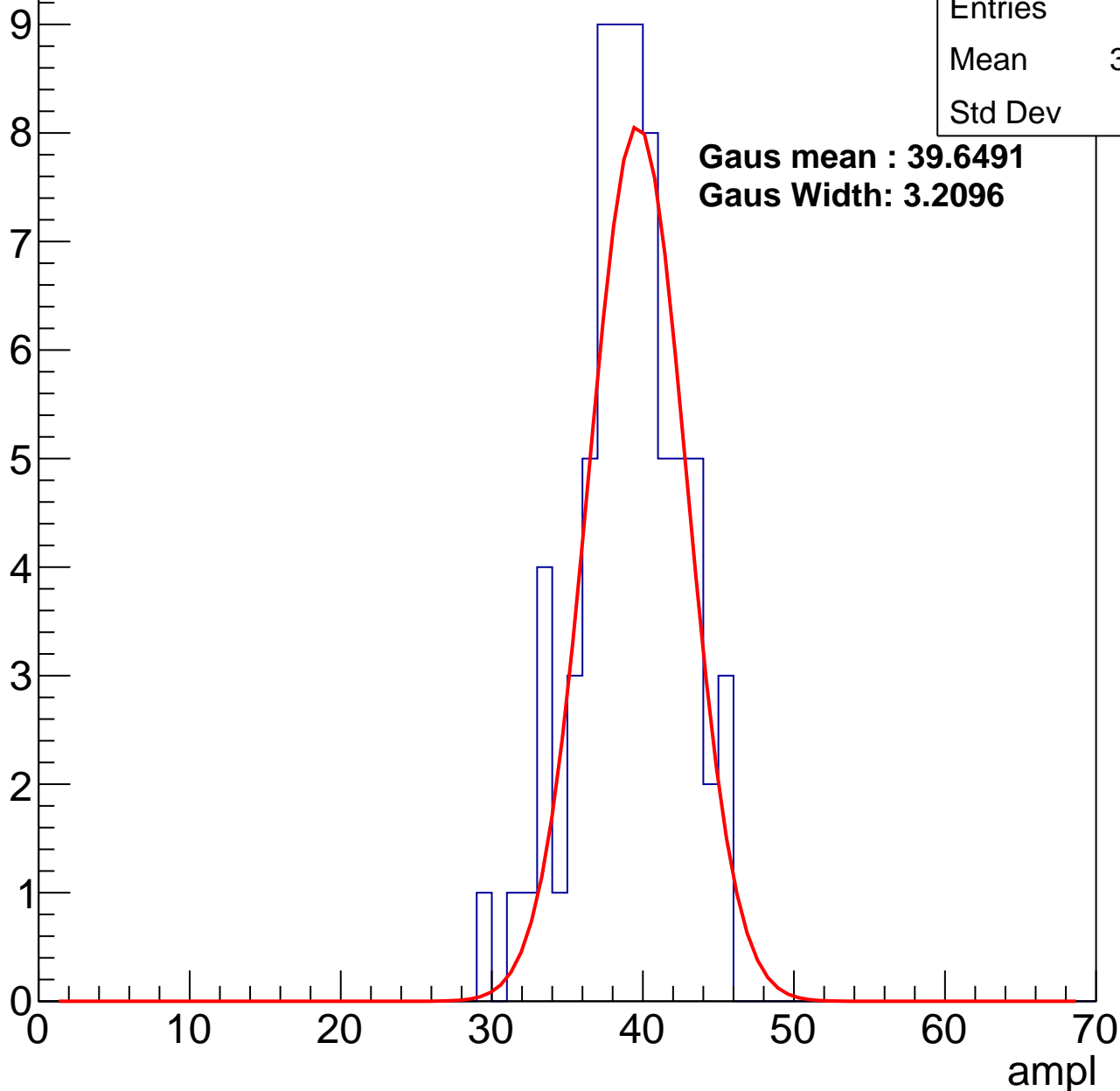
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	38.62
Std Dev	3.4

Gaus mean : 39.6491

Gaus Width: 3.2096



B1L101S, U23-ch99, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	46.22
Std Dev	3.12

Gaus mean : 46.5731

Gaus Width: 3.4931

Entry

10

8

6

4

2

0

0

10

20

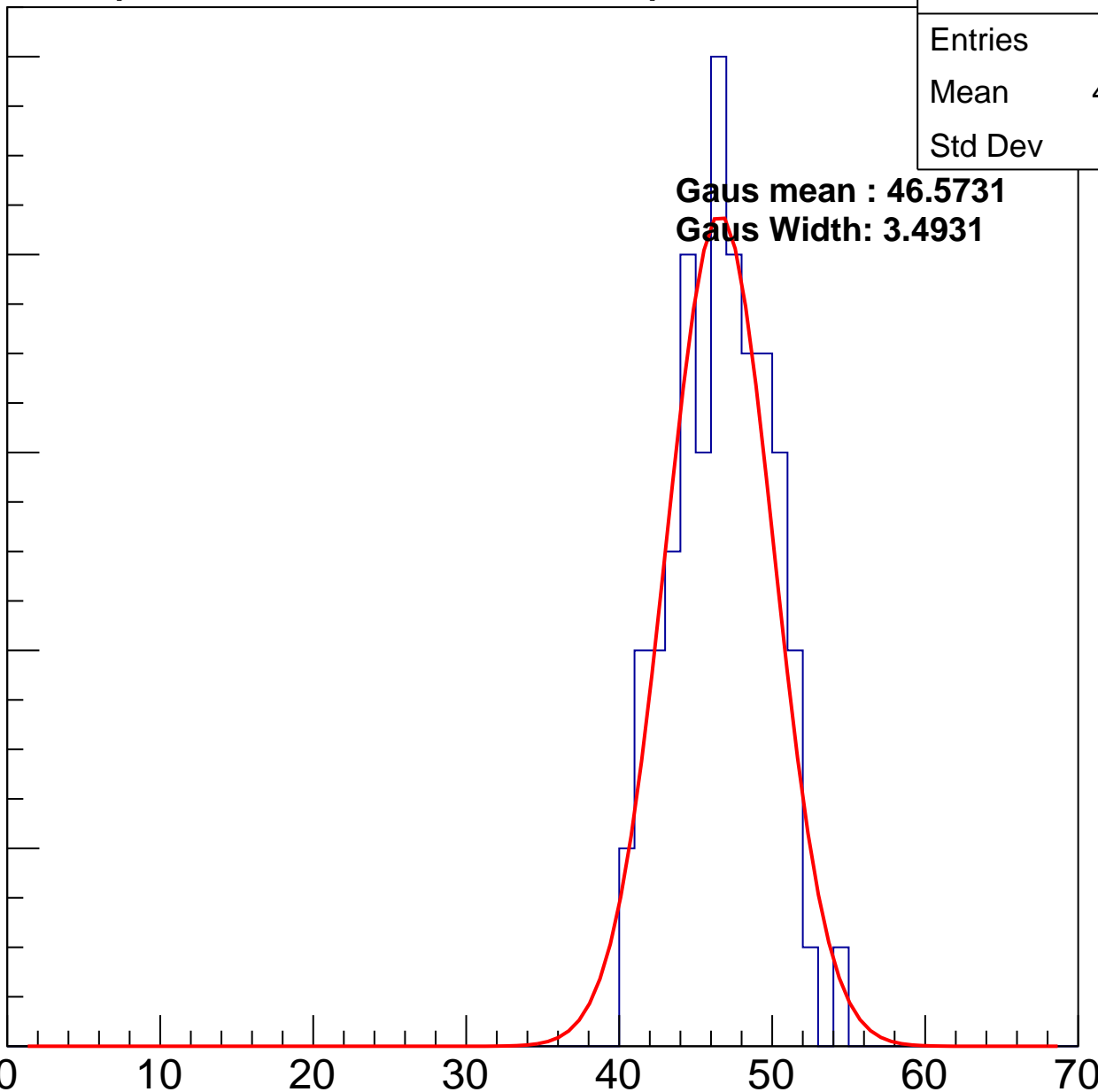
30

40

50

60

ampl

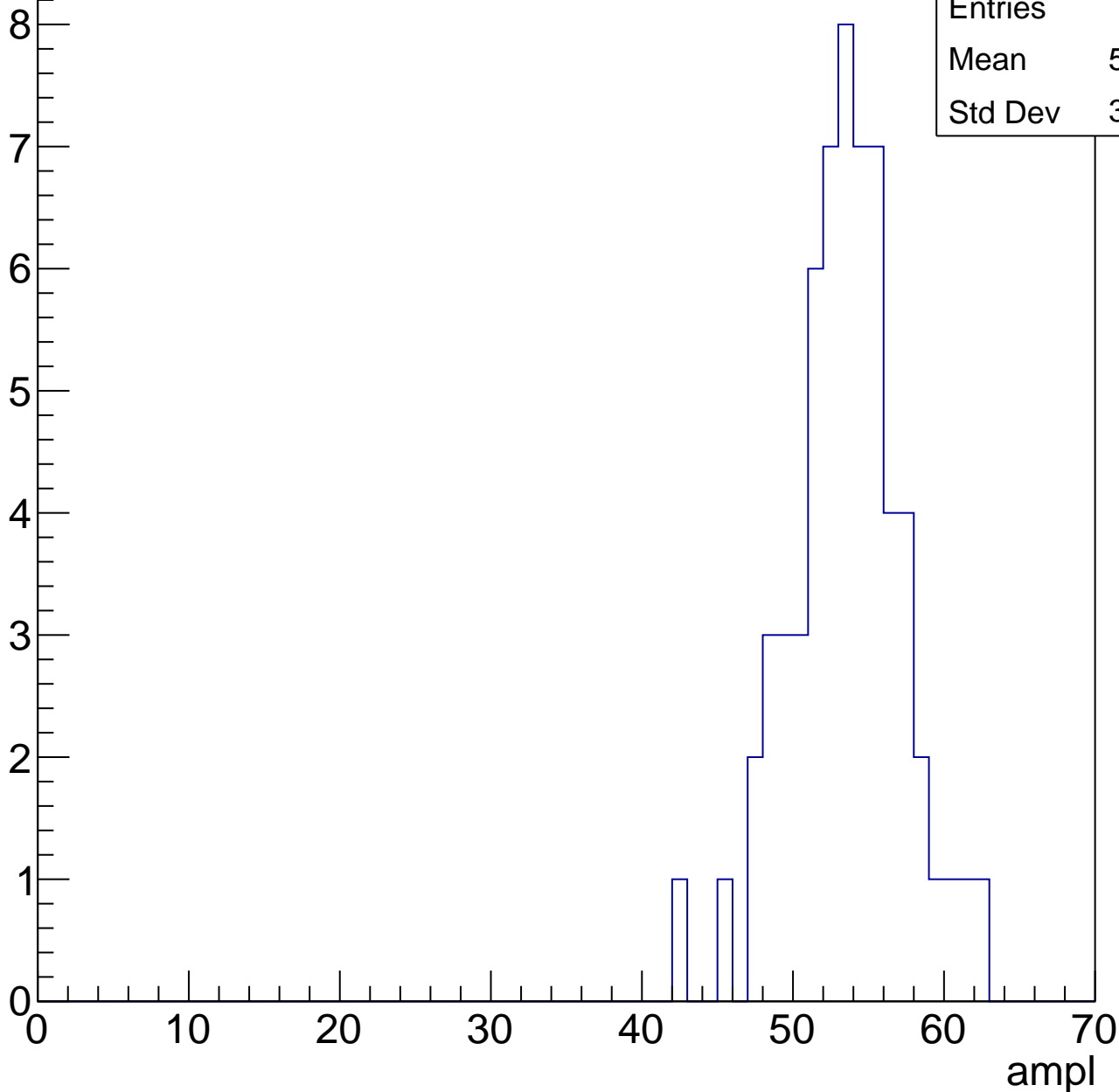


B1L101S, U23-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	53.05
Std Dev	3.709

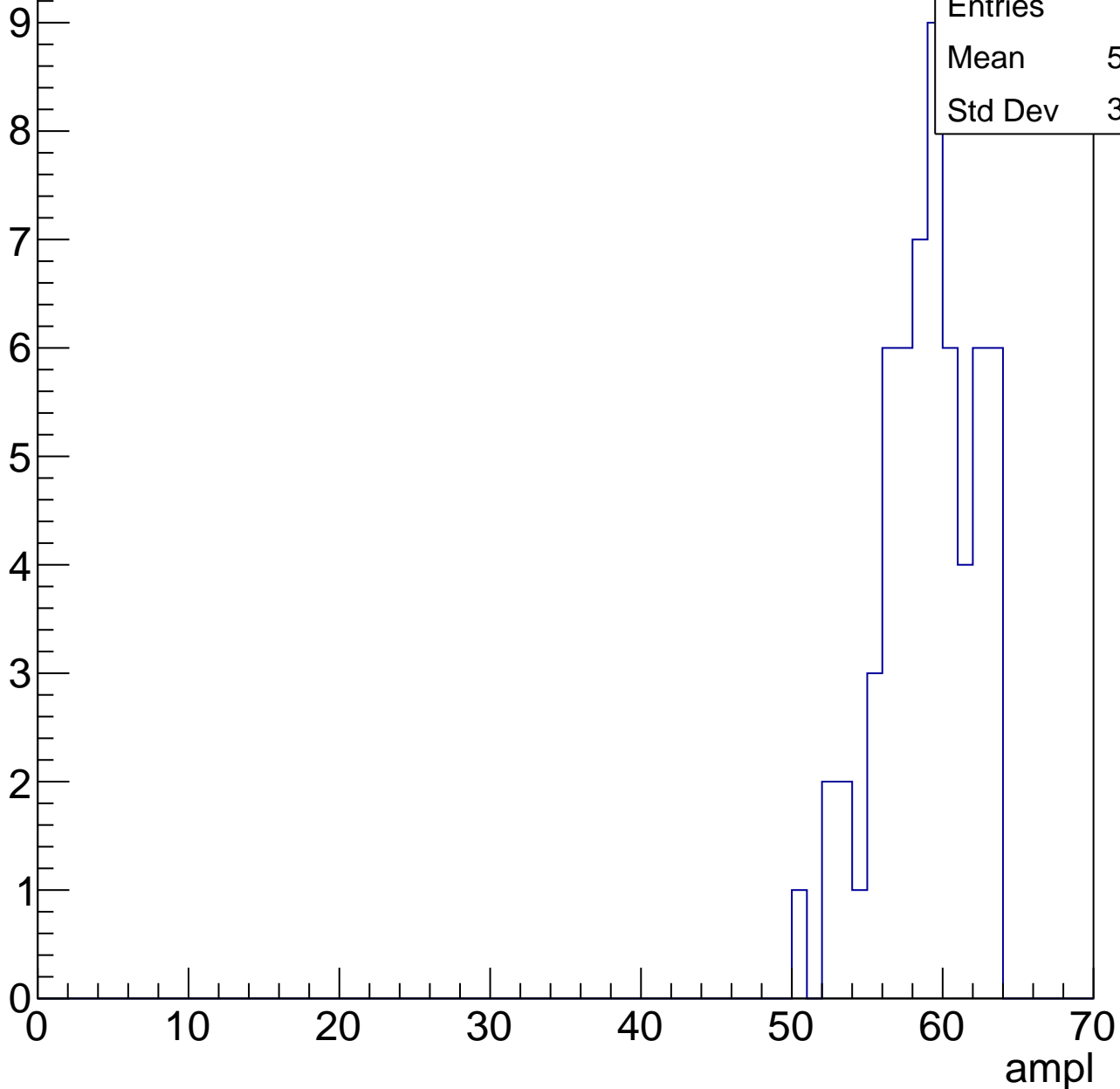


B1L101S, U23-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	58.44
Std Dev	3.088

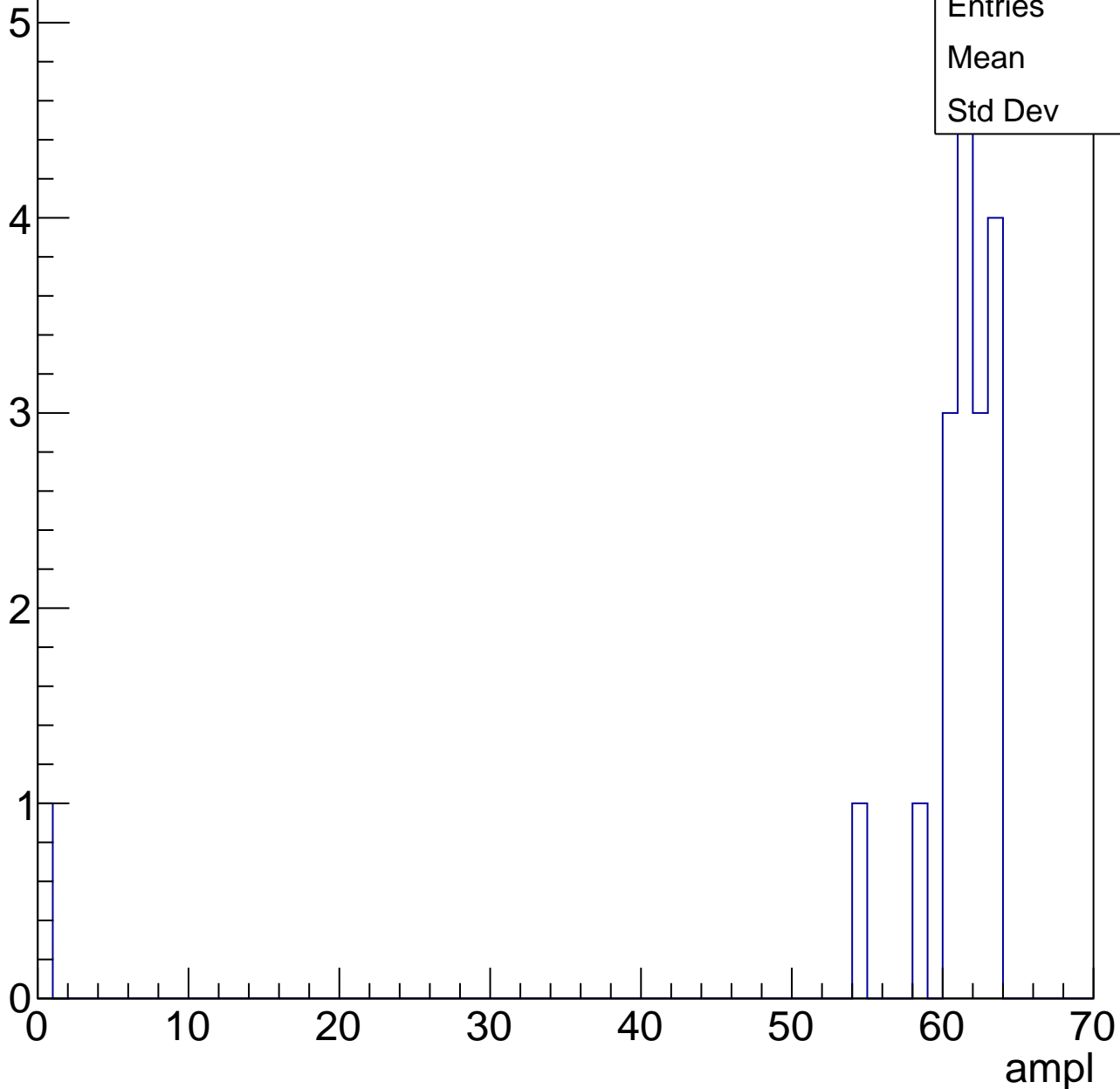


B1L101S, U23-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	57.5
Std Dev	14.1



B1L101S, U23-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L101S, U23-ch100, adc0

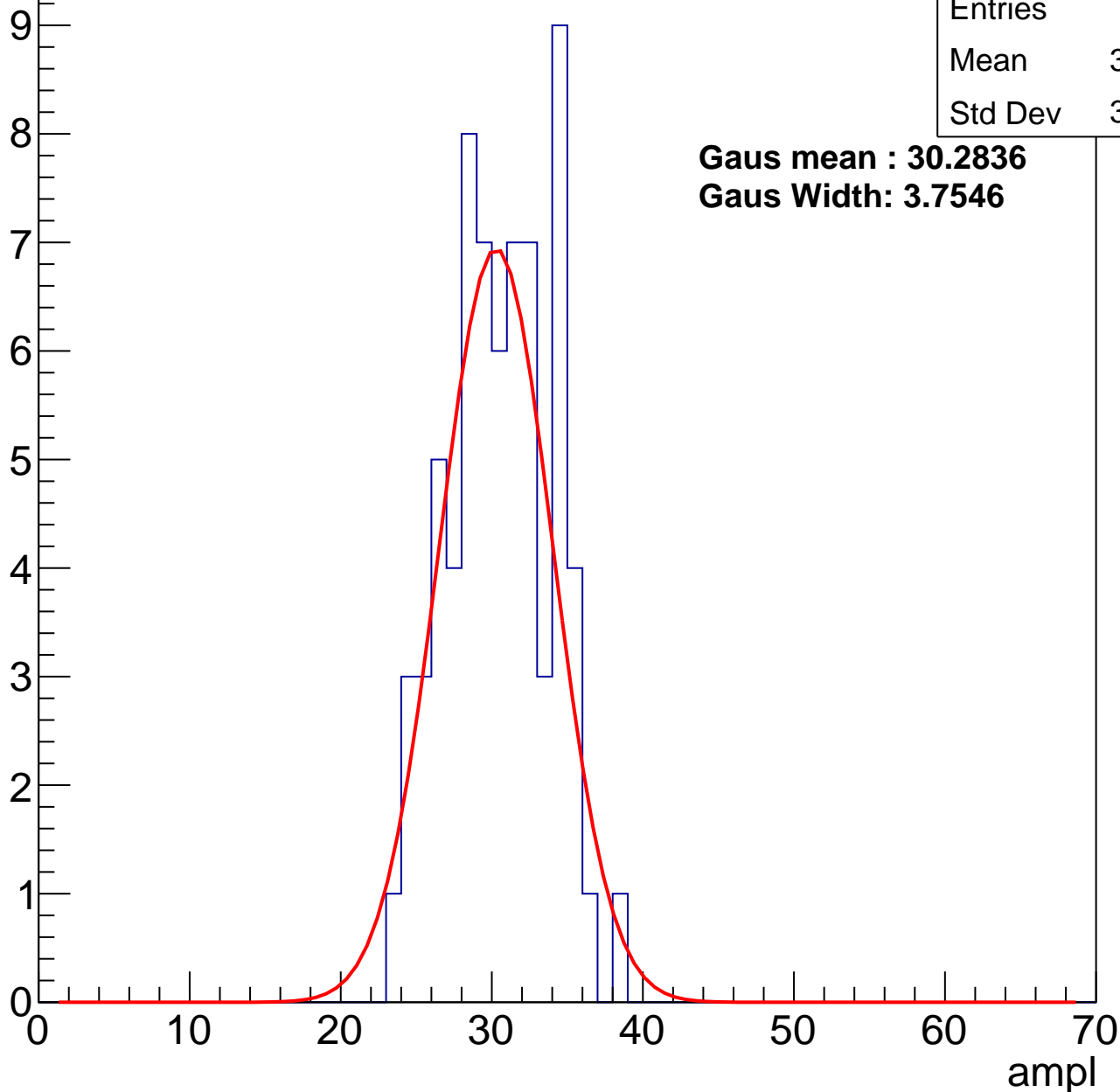
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.07
Std Dev	3.389

Gaus mean : 30.2836

Gaus Width: 3.7546



B1L101S, U23-ch100, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	36.96
Std Dev	3.866

Gaus mean : 38.5694

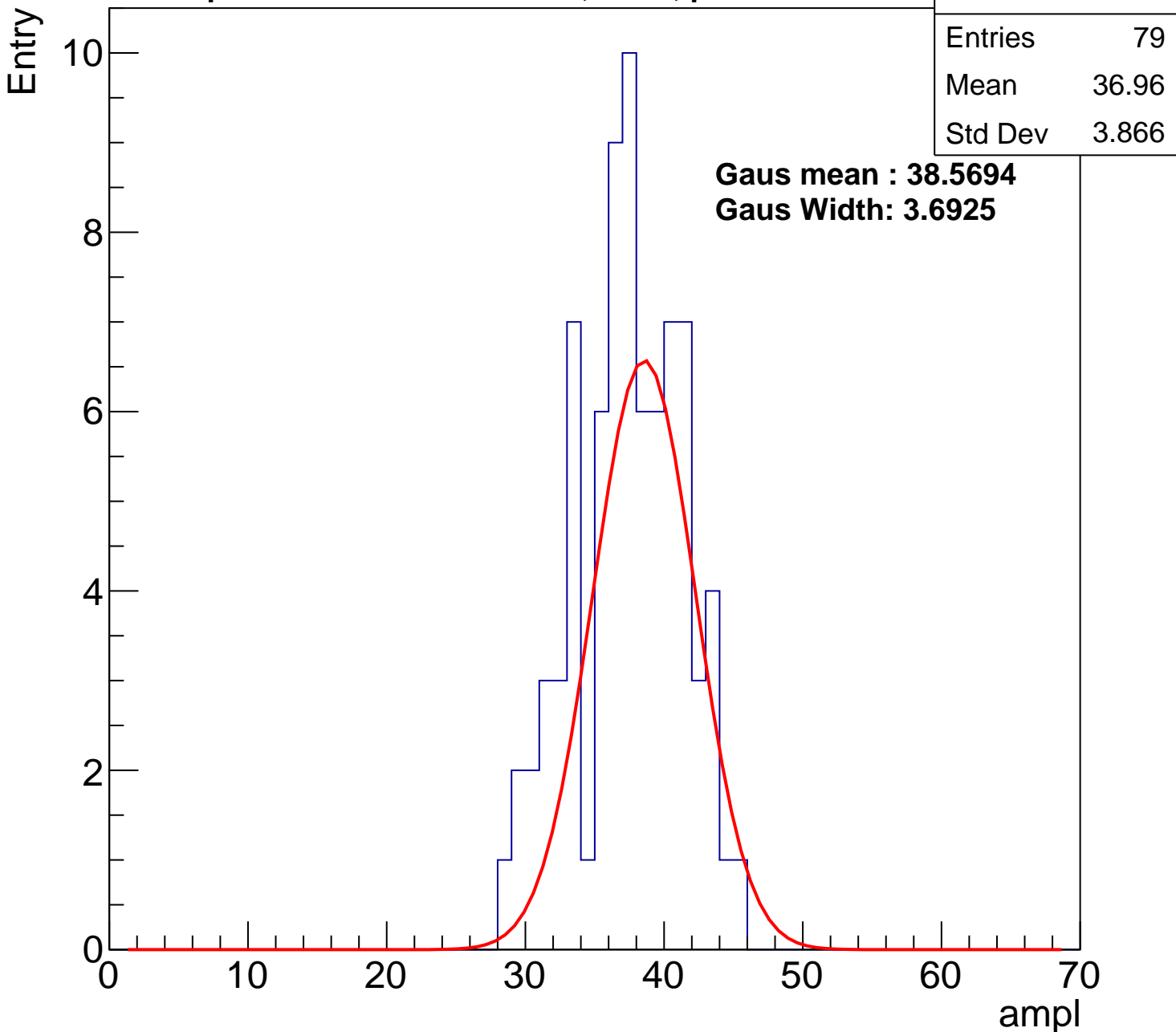
Gaus Width: 3.6925

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch100, adc2

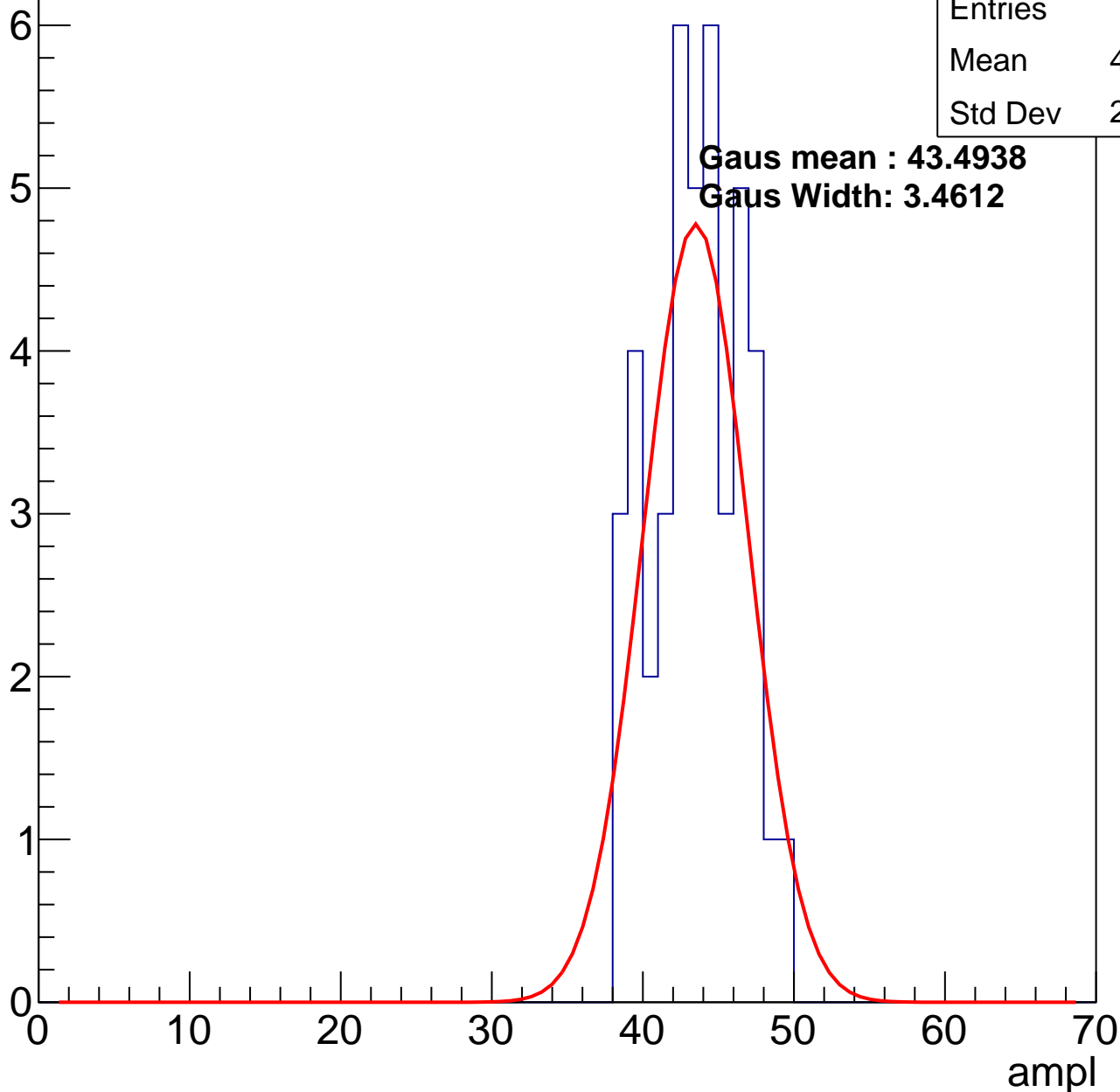
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	43.12
Std Dev	2.903

Gaus mean : 43.4938

Gaus Width: 3.4612

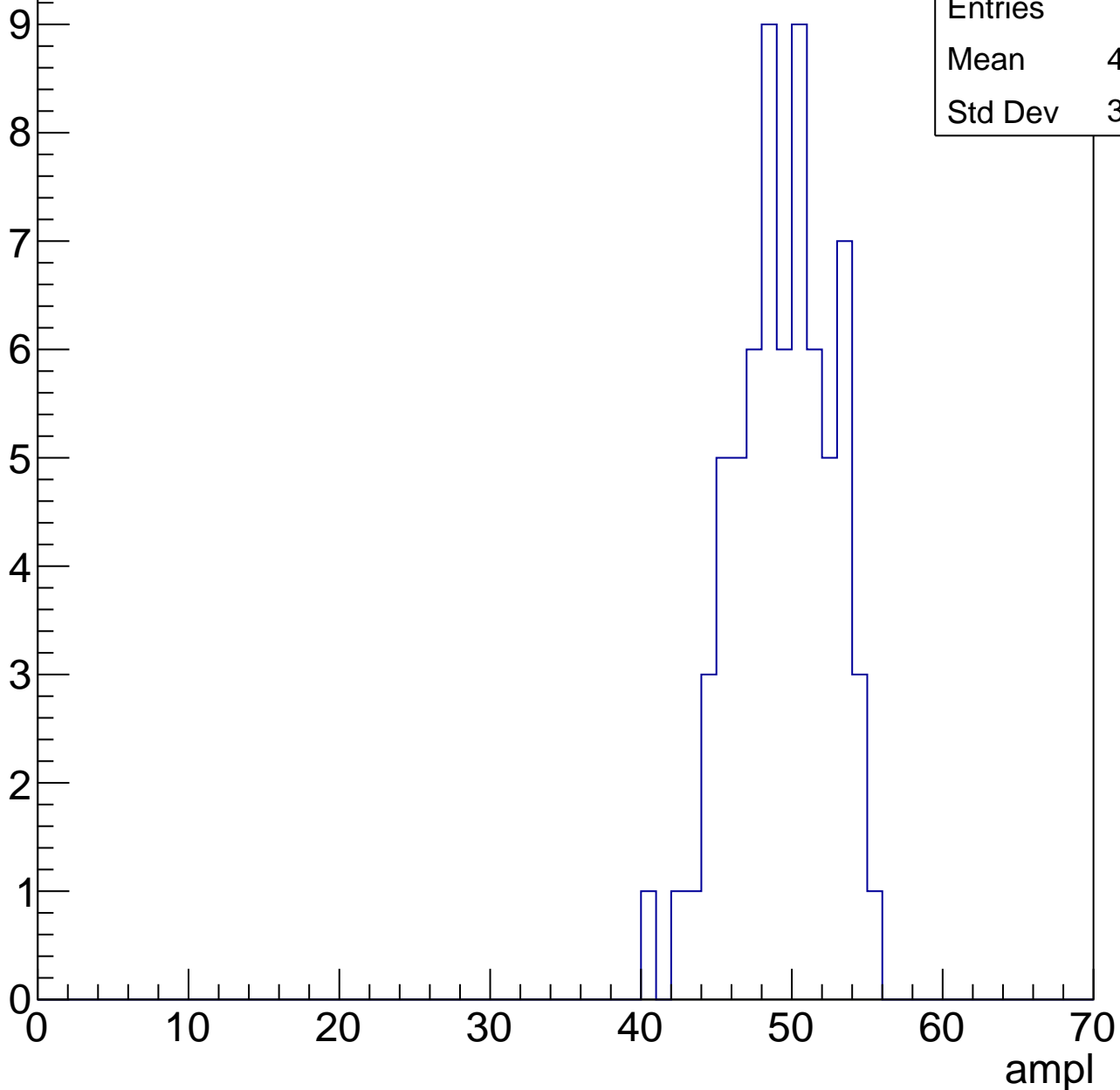


B1L101S, U23-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.88
Std Dev	3.206

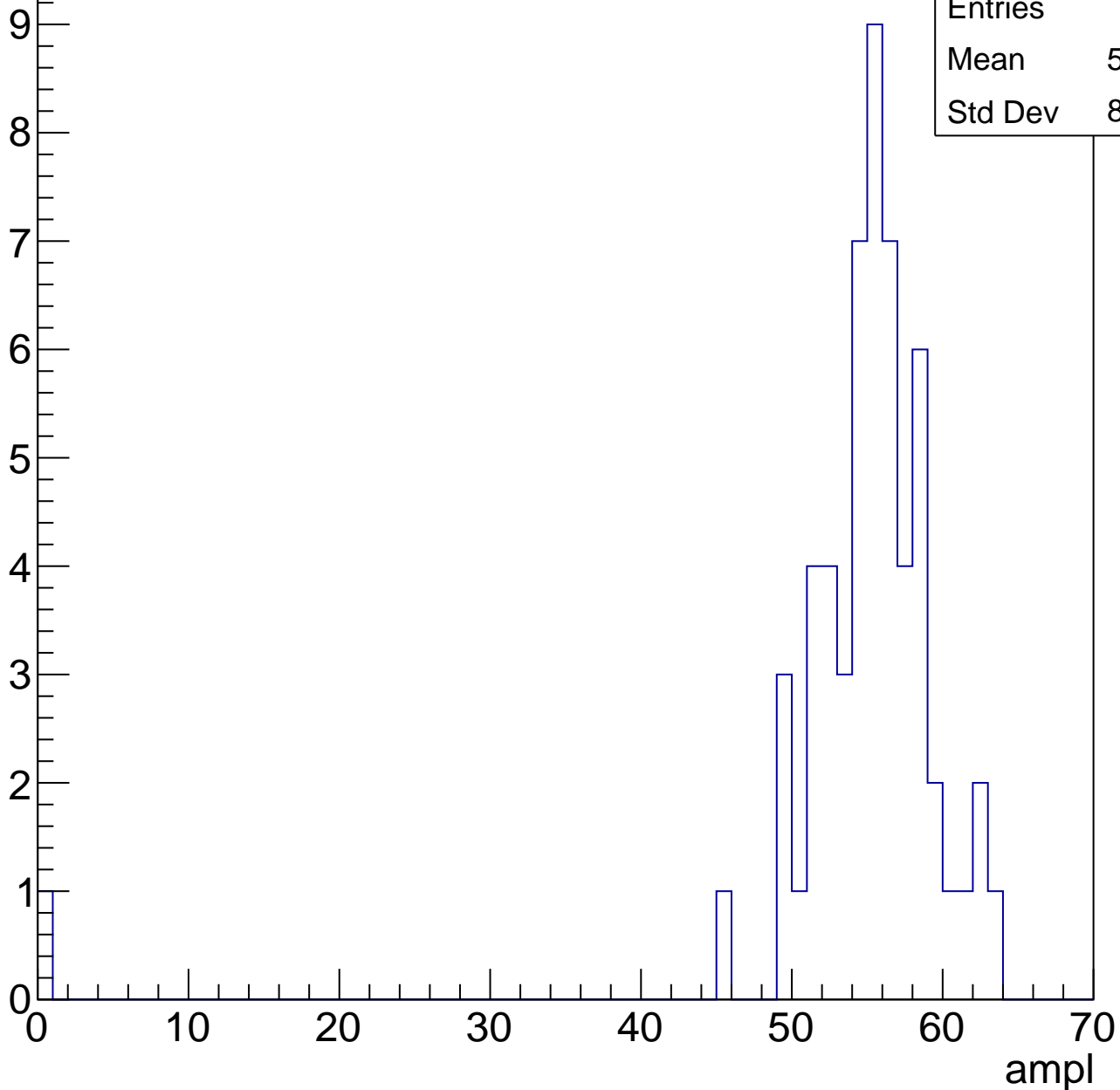


B1L101S, U23-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	54.04
Std Dev	8.009

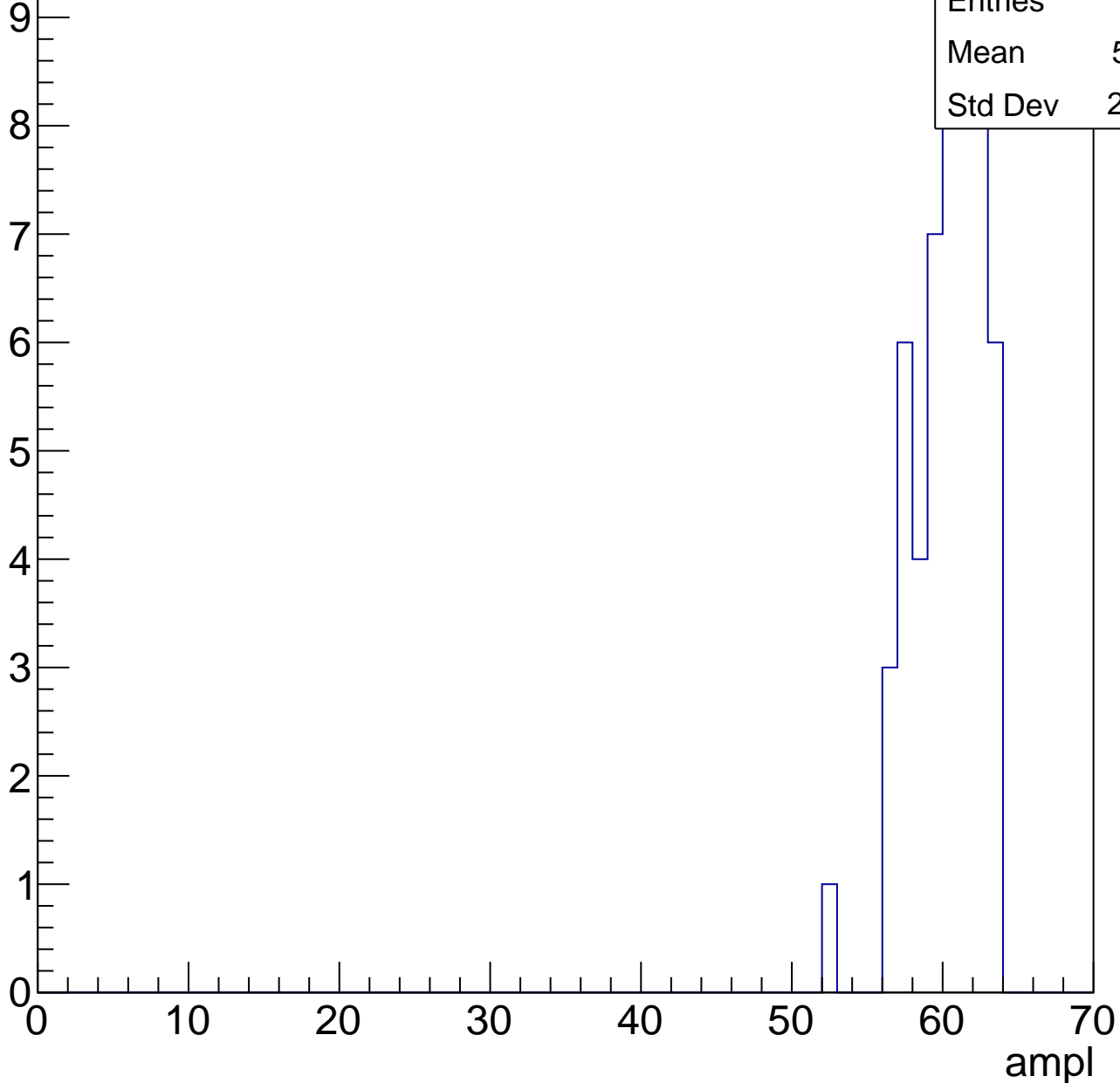


B1L101S, U23-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	59.81
Std Dev	2.307



B1L101S, U23-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch100, adc7

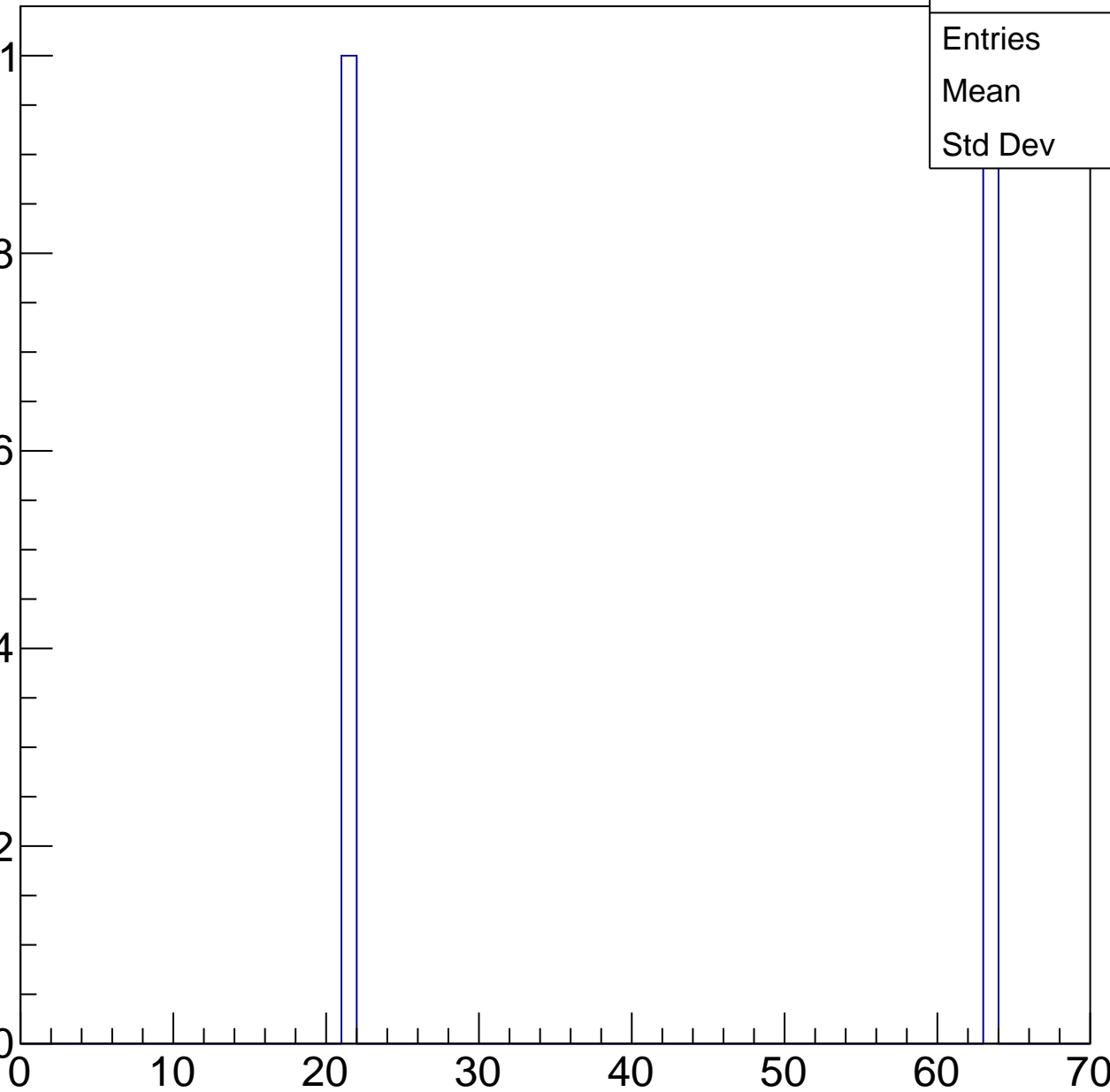
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42
Std Dev	21

ampl



B1L101S, U23-ch101, adc0

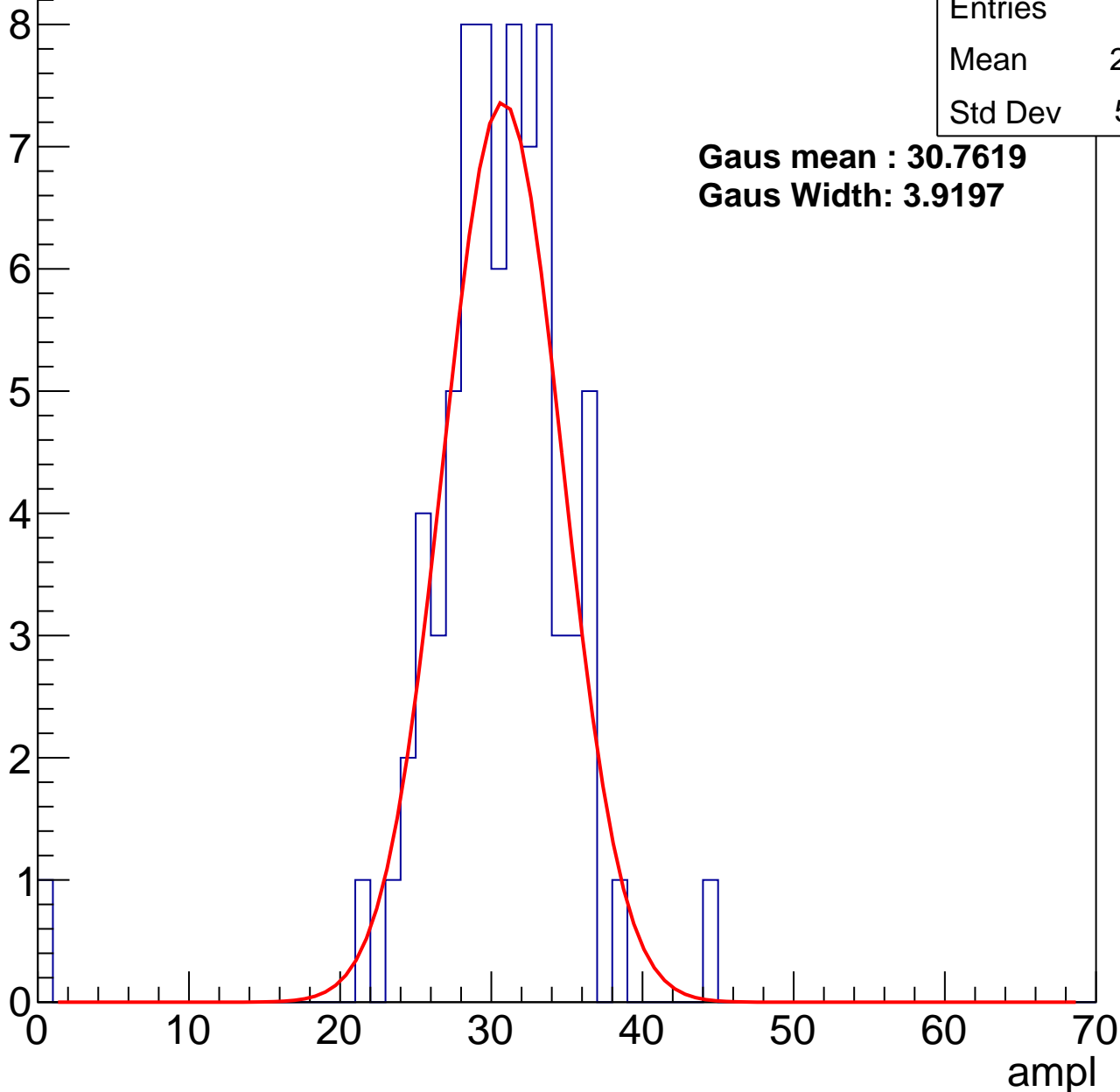
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.95
Std Dev	5.171

Gaus mean : 30.7619

Gaus Width: 3.9197



B1L101S, U23-ch101, adc1

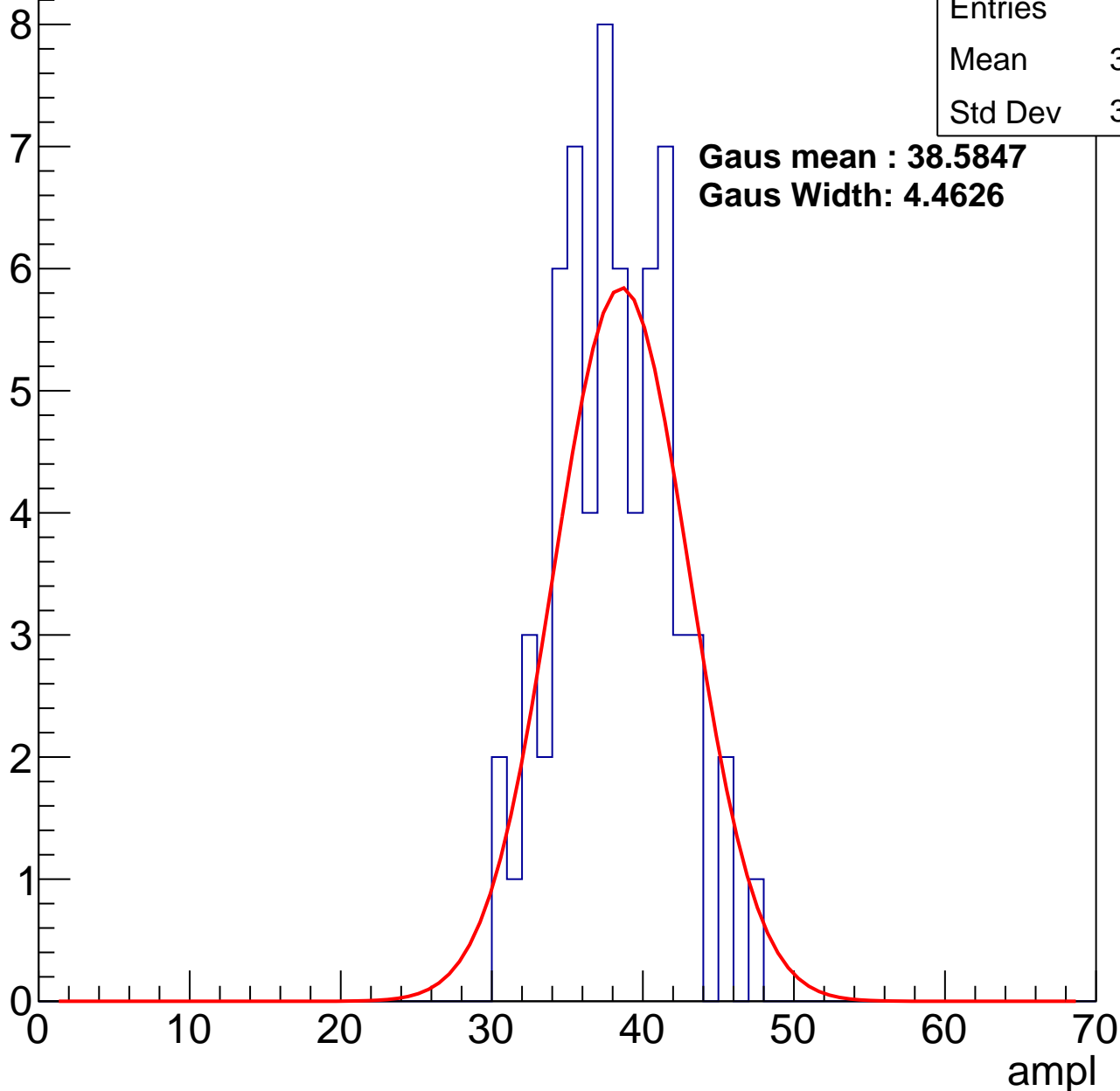
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.62
Std Dev	3.724

Gaus mean : 38.5847

Gaus Width: 4.4626



B1L101S, U23-ch101, adc2

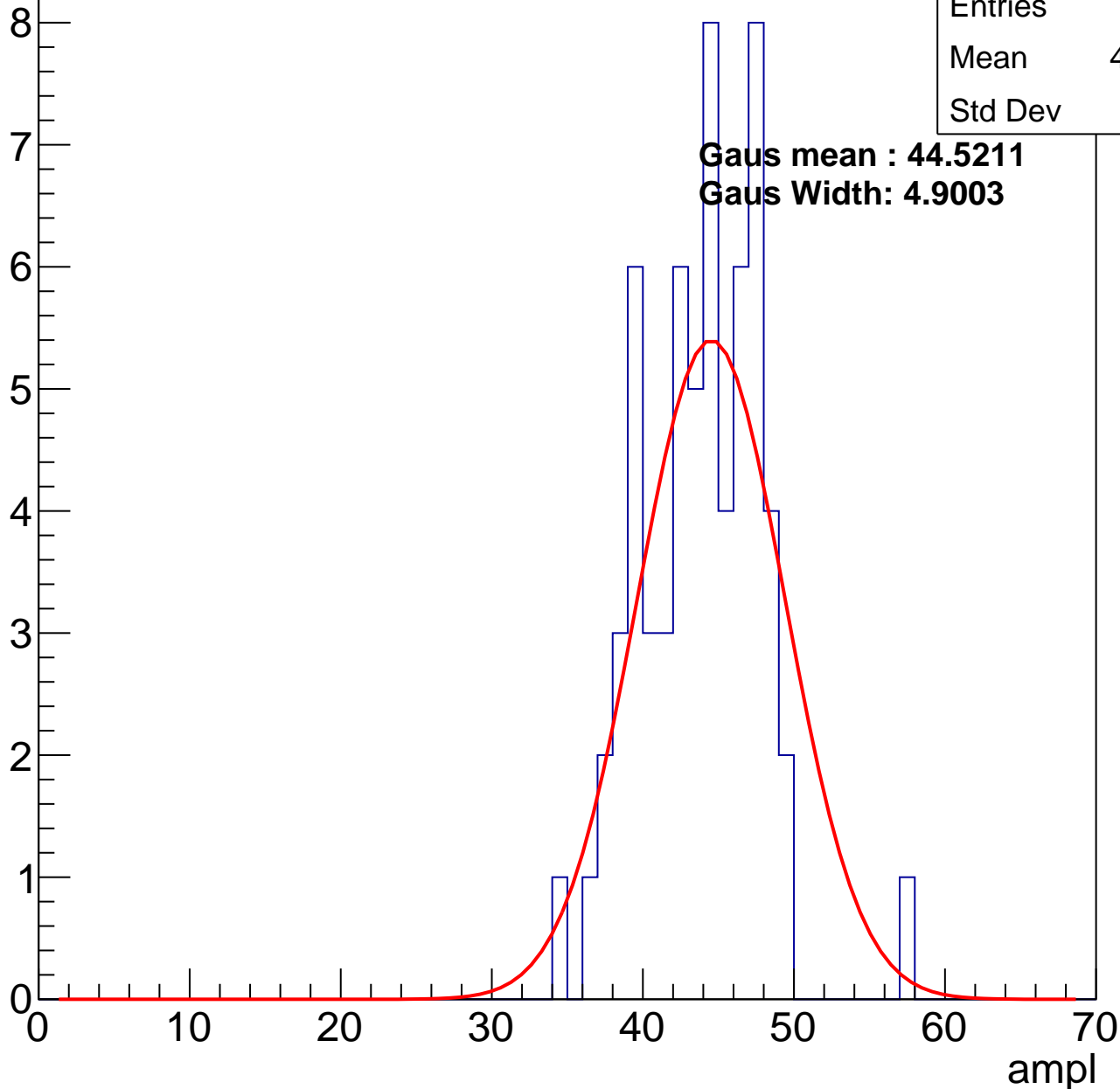
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.38
Std Dev	3.95

Gaus mean : 44.5211

Gaus Width: 4.9003

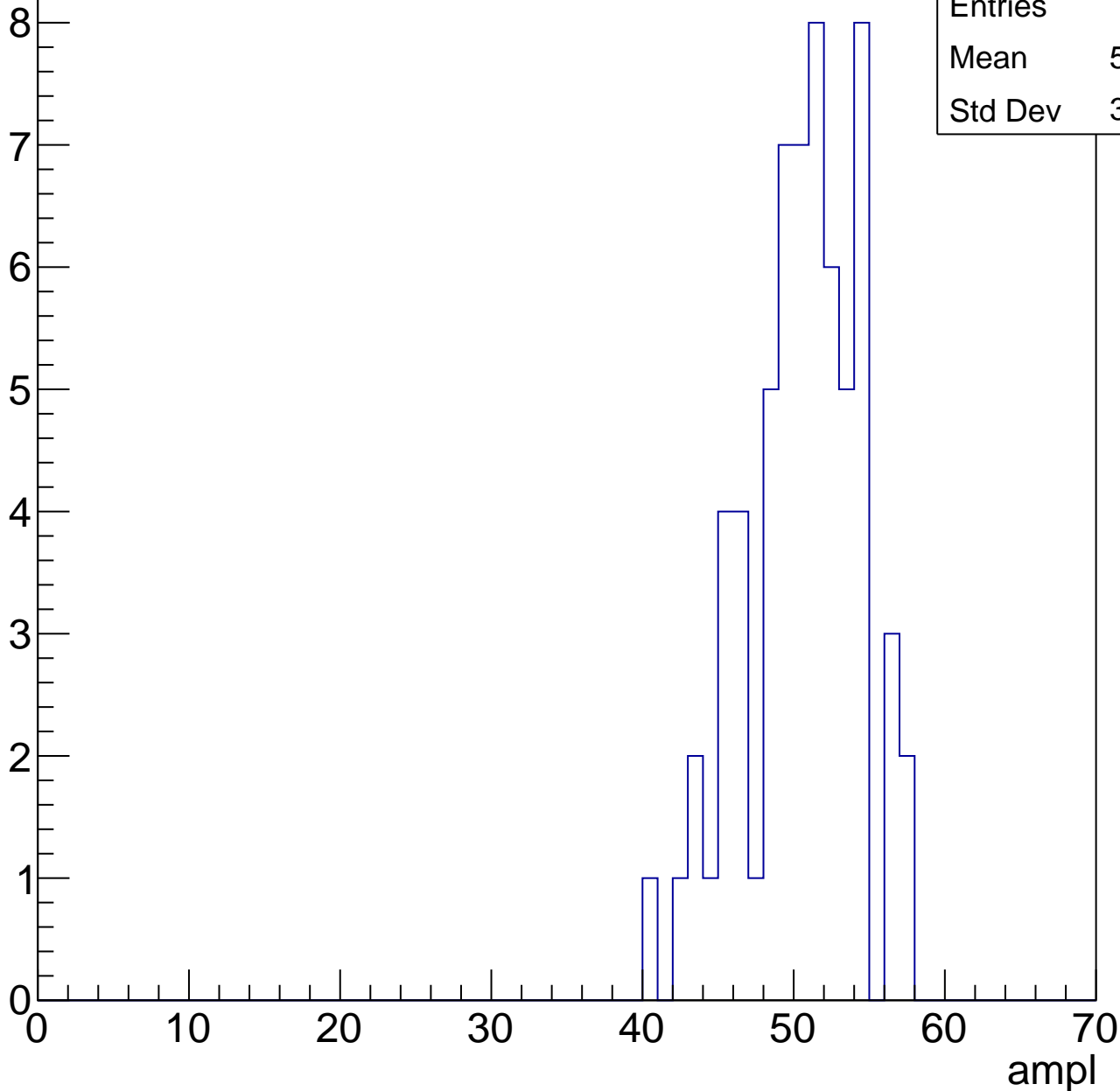


B1L101S, U23-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.08
Std Dev	3.739

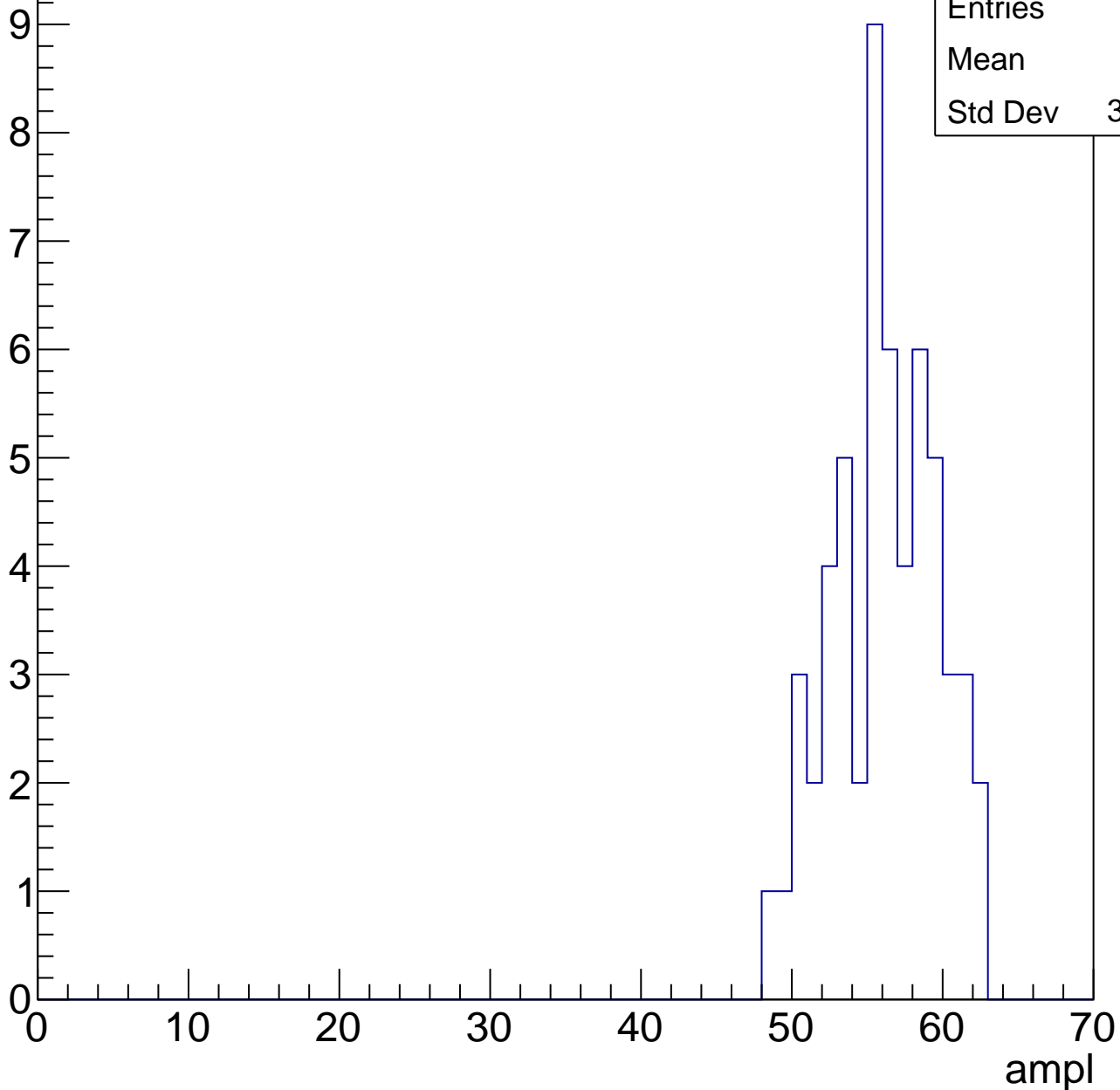


B1L101S, U23-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.7
Std Dev	3.433

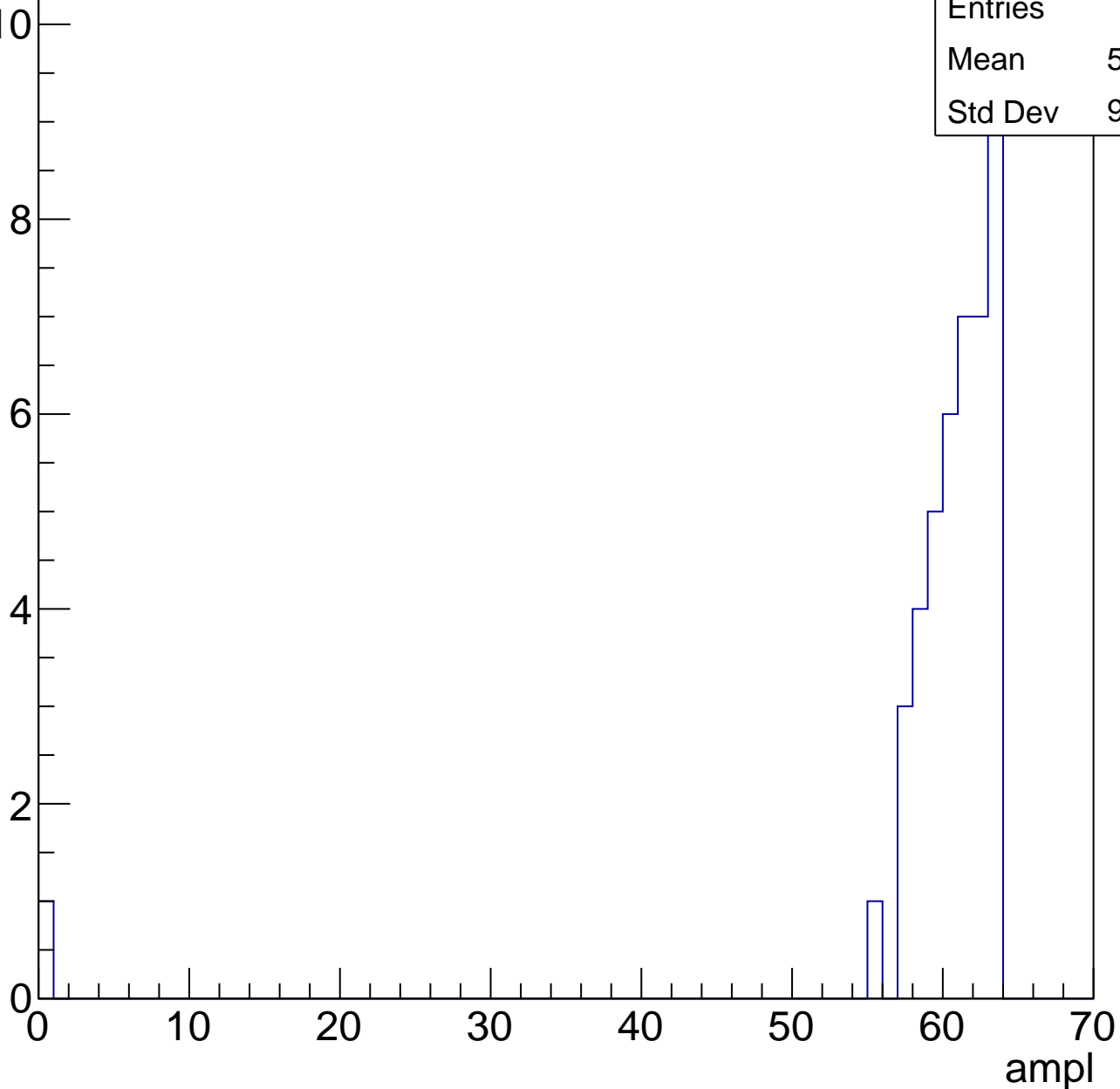


B1L101S, U23-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

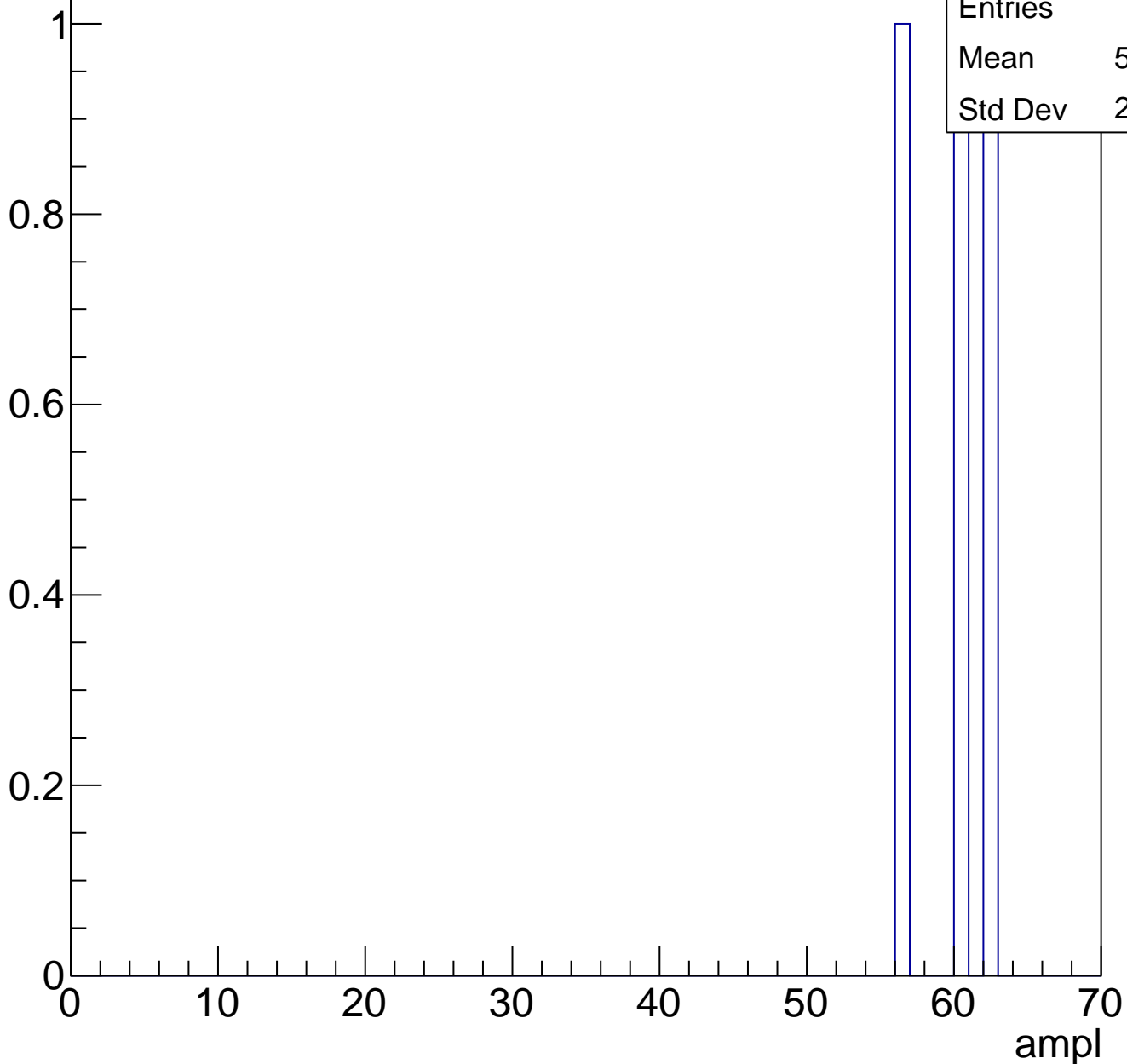
Entries	44
Mean	59.18
Std Dev	9.255



B1L101S, U23-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U23-ch102, adc0

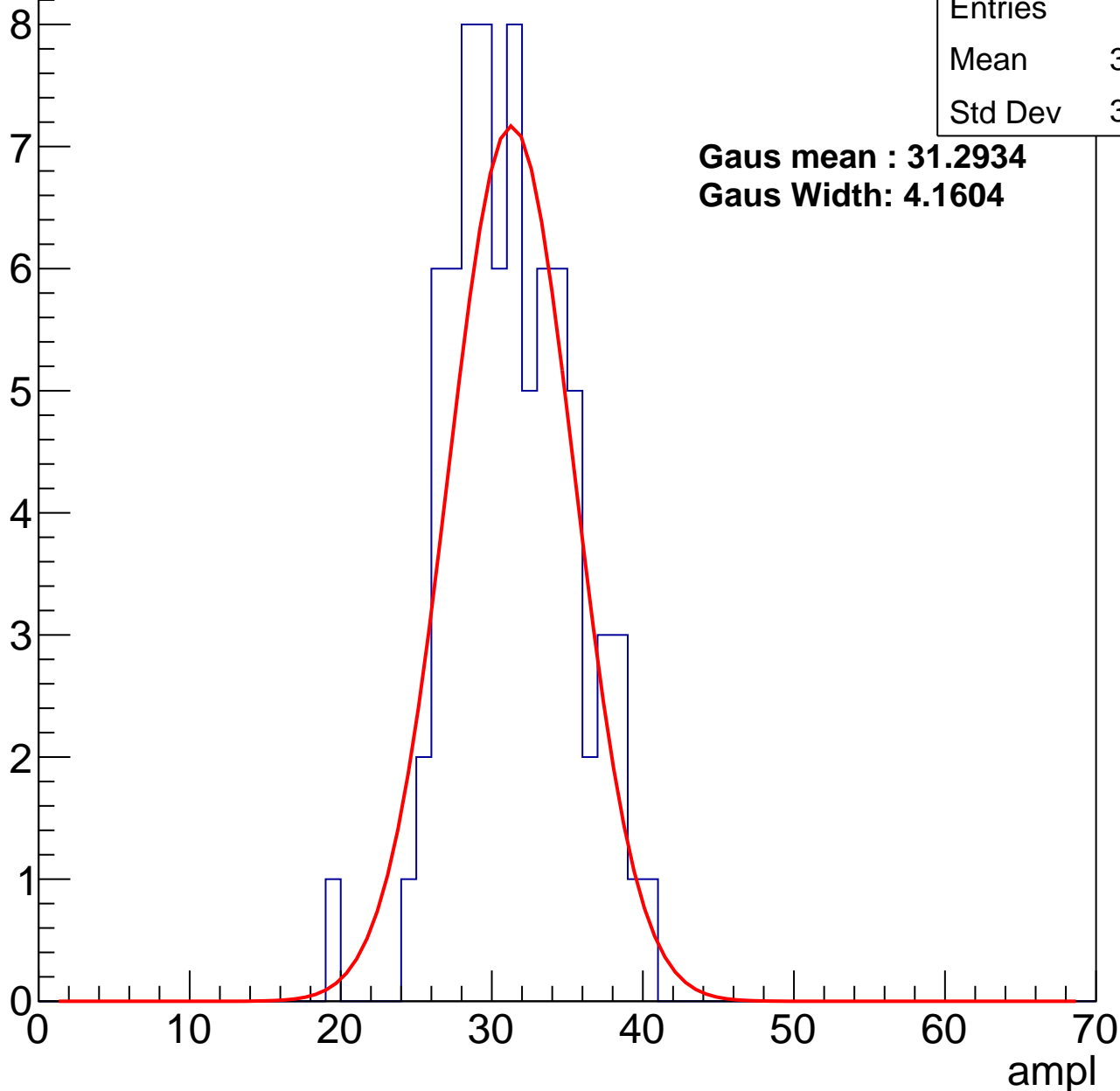
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.87
Std Dev	3.975

Gaus mean : 31.2934

Gaus Width: 4.1604



B1L101S, U23-ch102, adc1

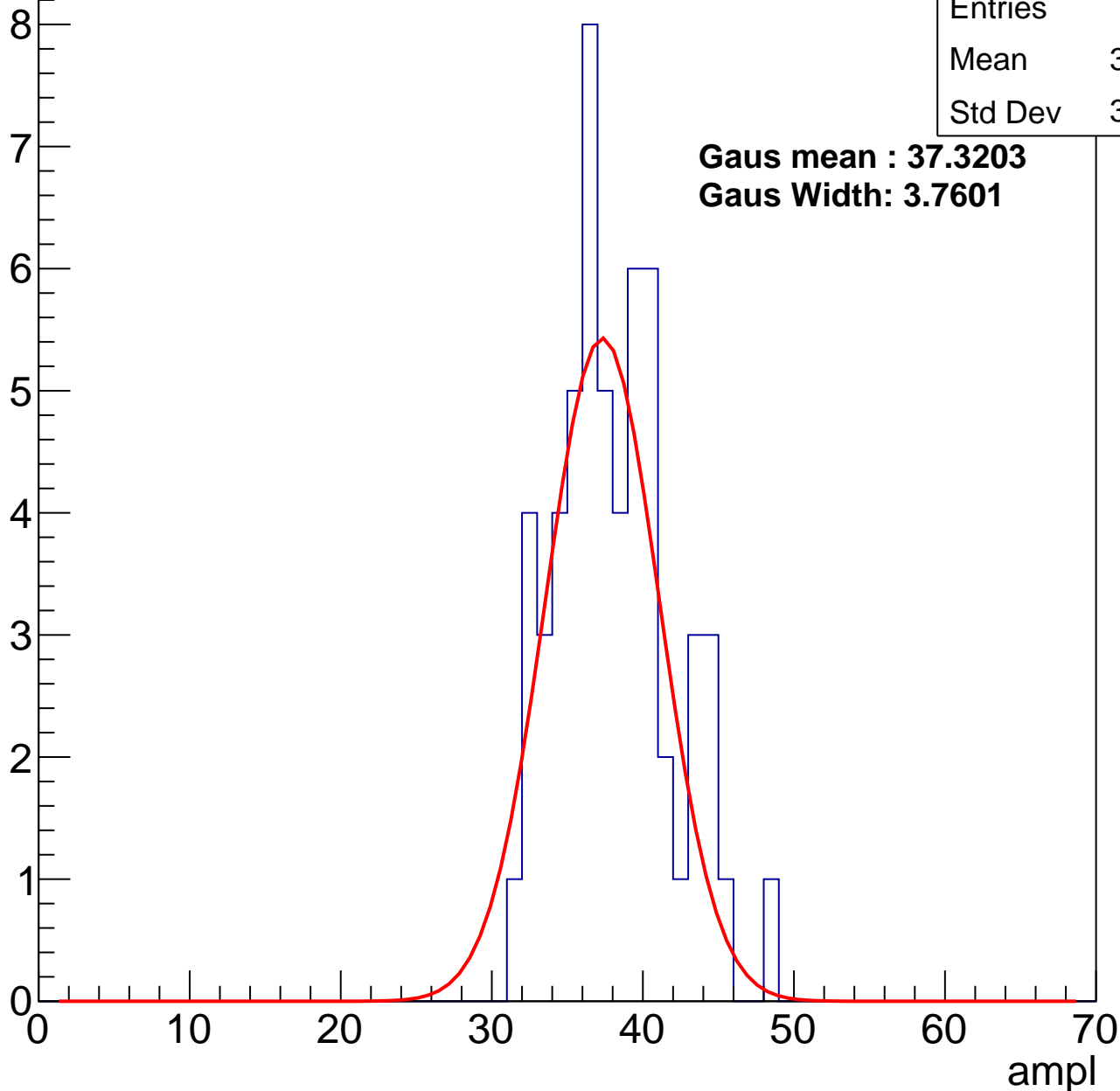
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.65
Std Dev	3.753

Gaus mean : 37.3203

Gaus Width: 3.7601



B1L101S, U23-ch102, adc2

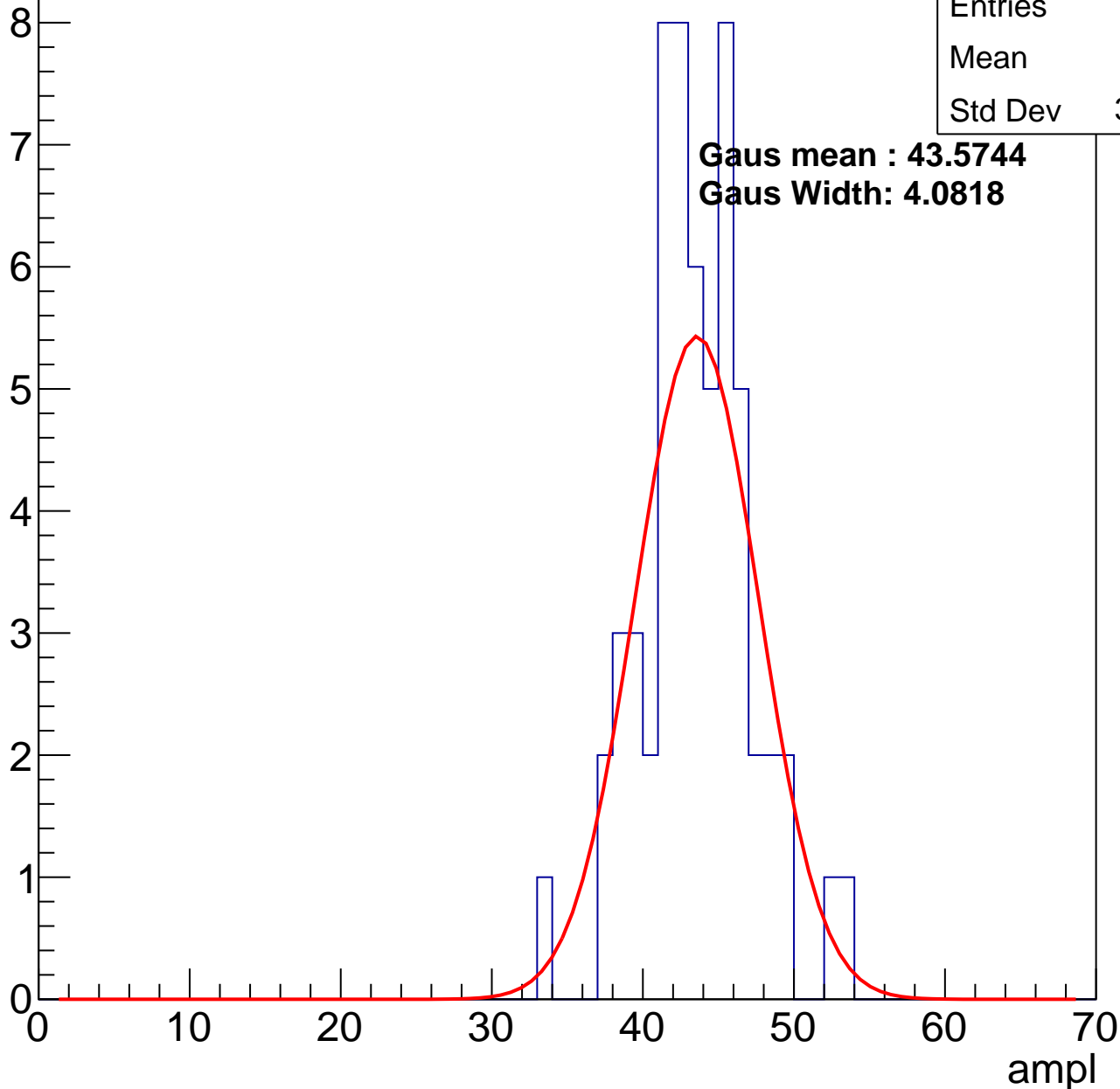
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.1
Std Dev	3.611

Gaus mean : 43.5744

Gaus Width: 4.0818

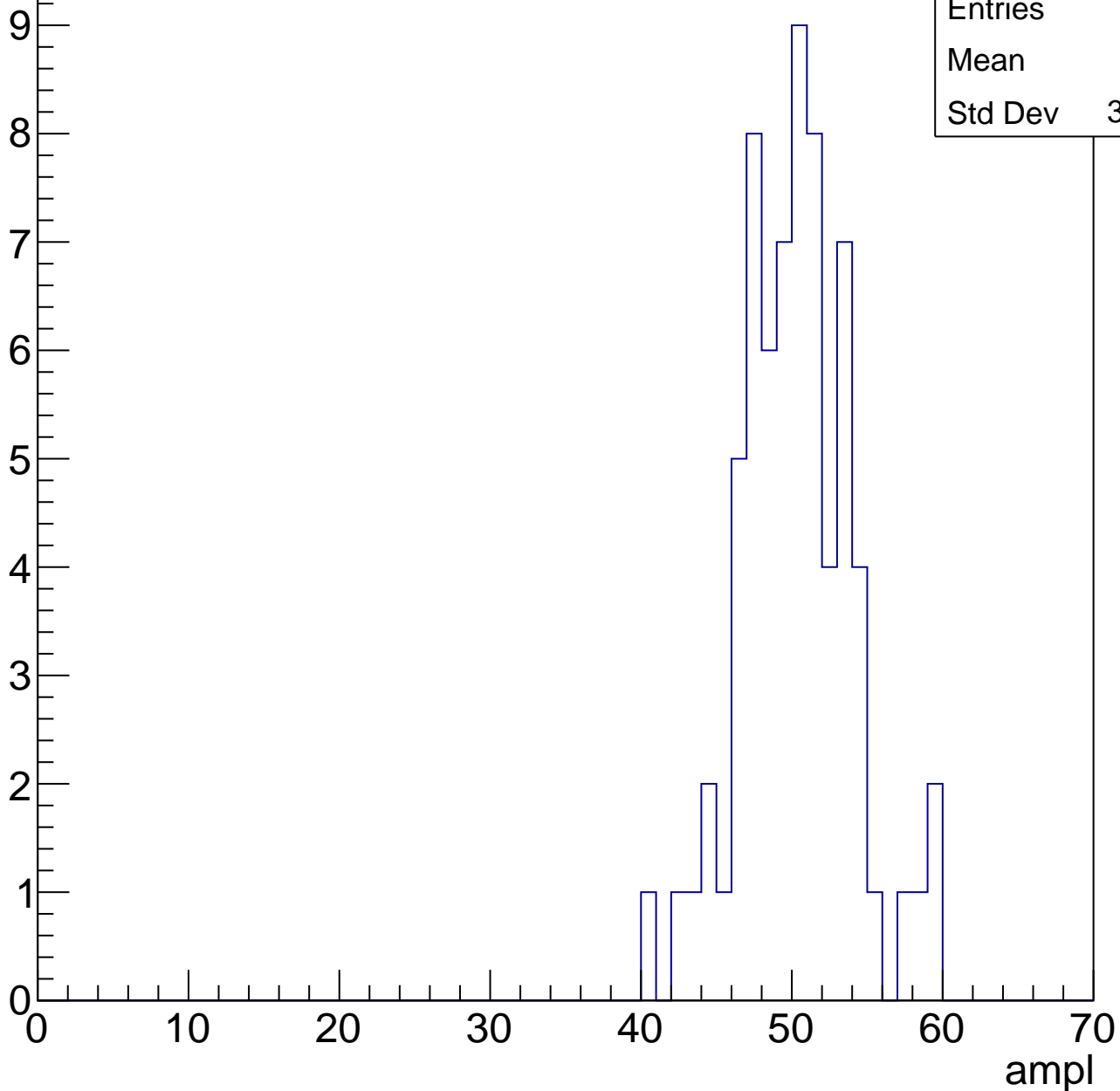


B1L101S, U23-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

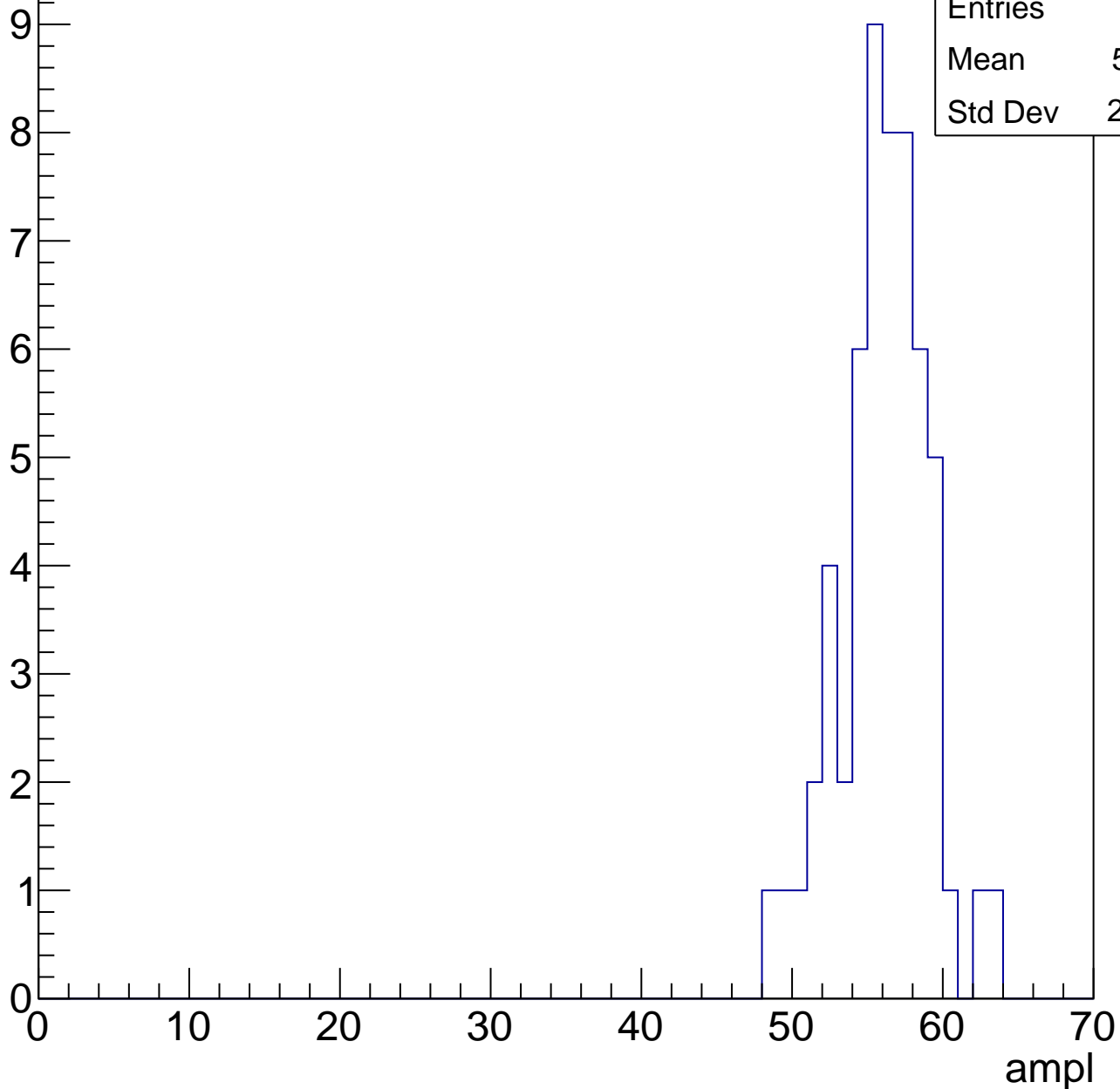
Entries	69
Mean	49.8
Std Dev	3.693



B1L101S, U23-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



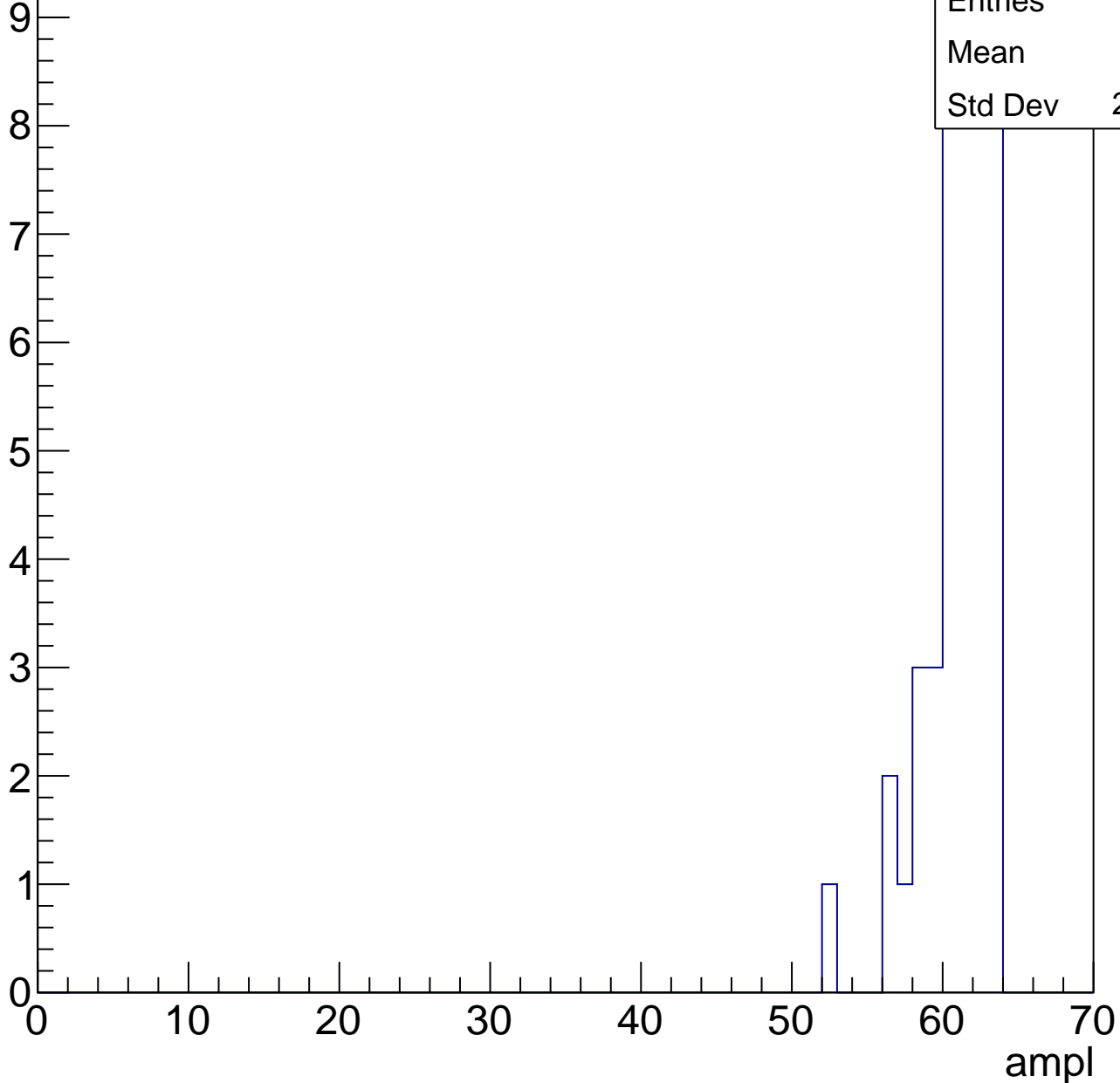
Entries	56
Mean	55.61
Std Dev	2.938

B1L101S, U23-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

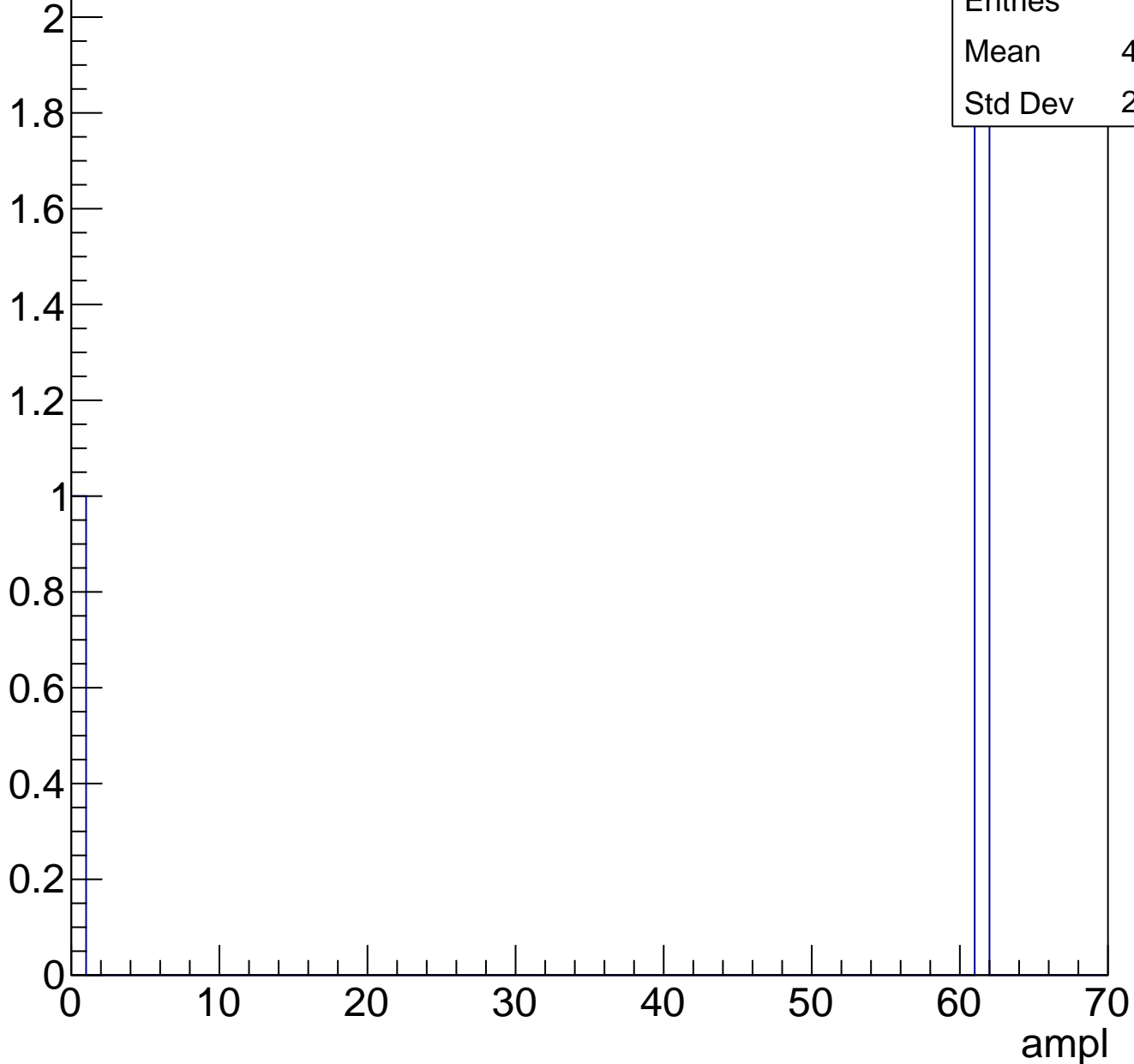
Entries	44
Mean	60.5
Std Dev	2.261



B1L101S, U23-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch103, adc0

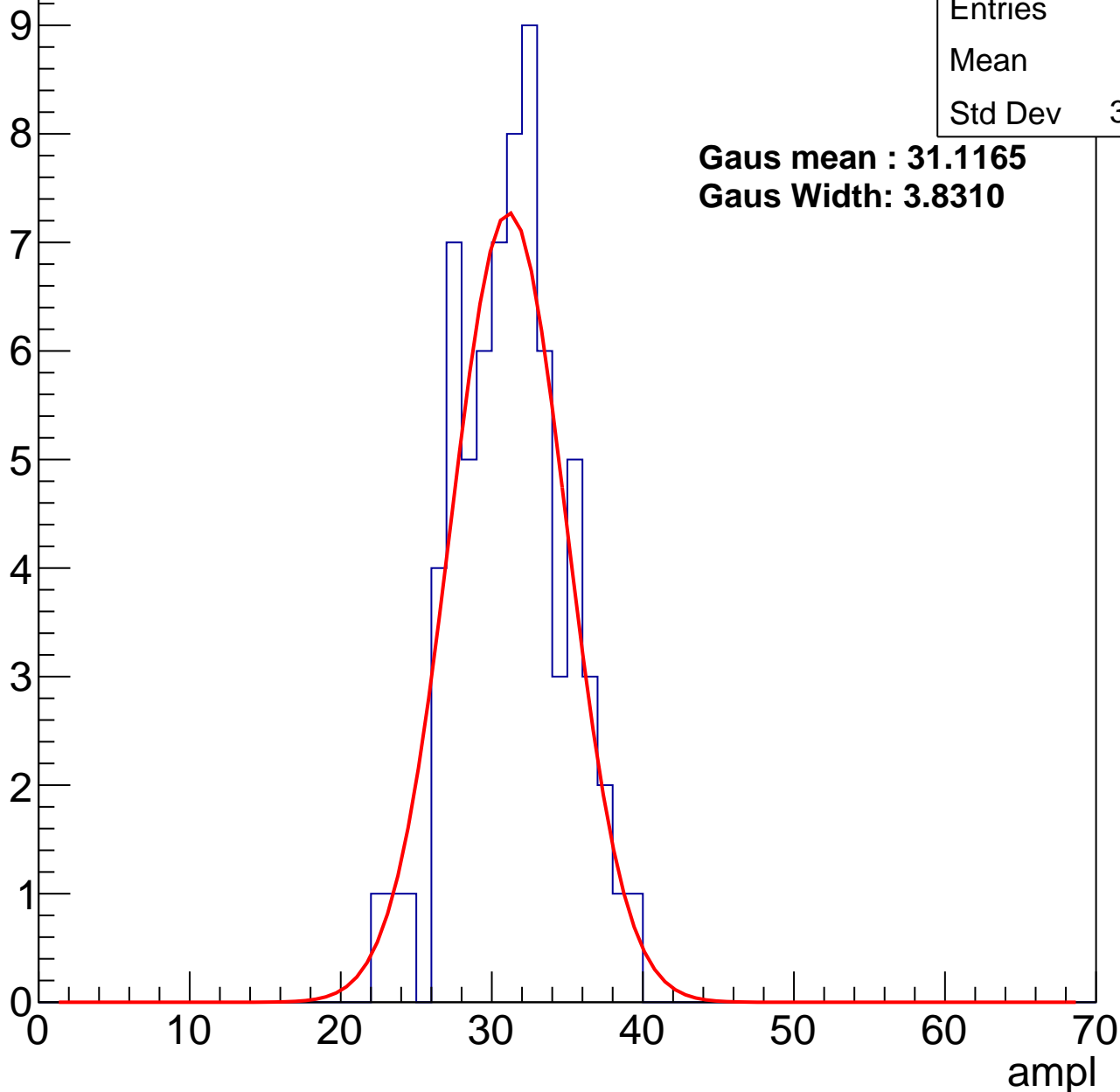
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.8
Std Dev	3.544

Gaus mean : 31.1165

Gaus Width: 3.8310



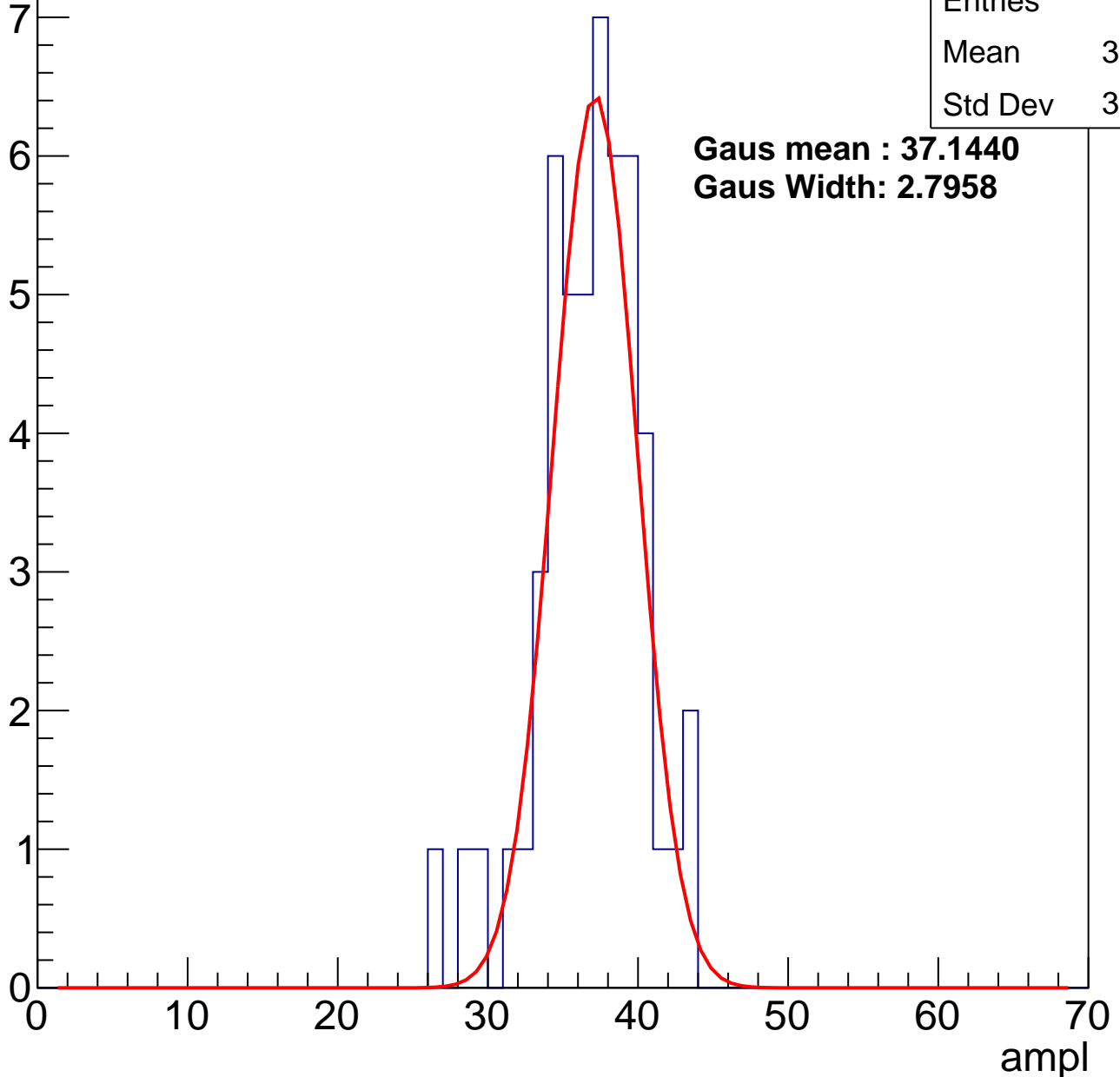
B1L101S, U23-ch103, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	36.35
Std Dev	3.463

Gaus mean : 37.1440
Gaus Width: 2.7958



B1L101S, U23-ch103, adc2

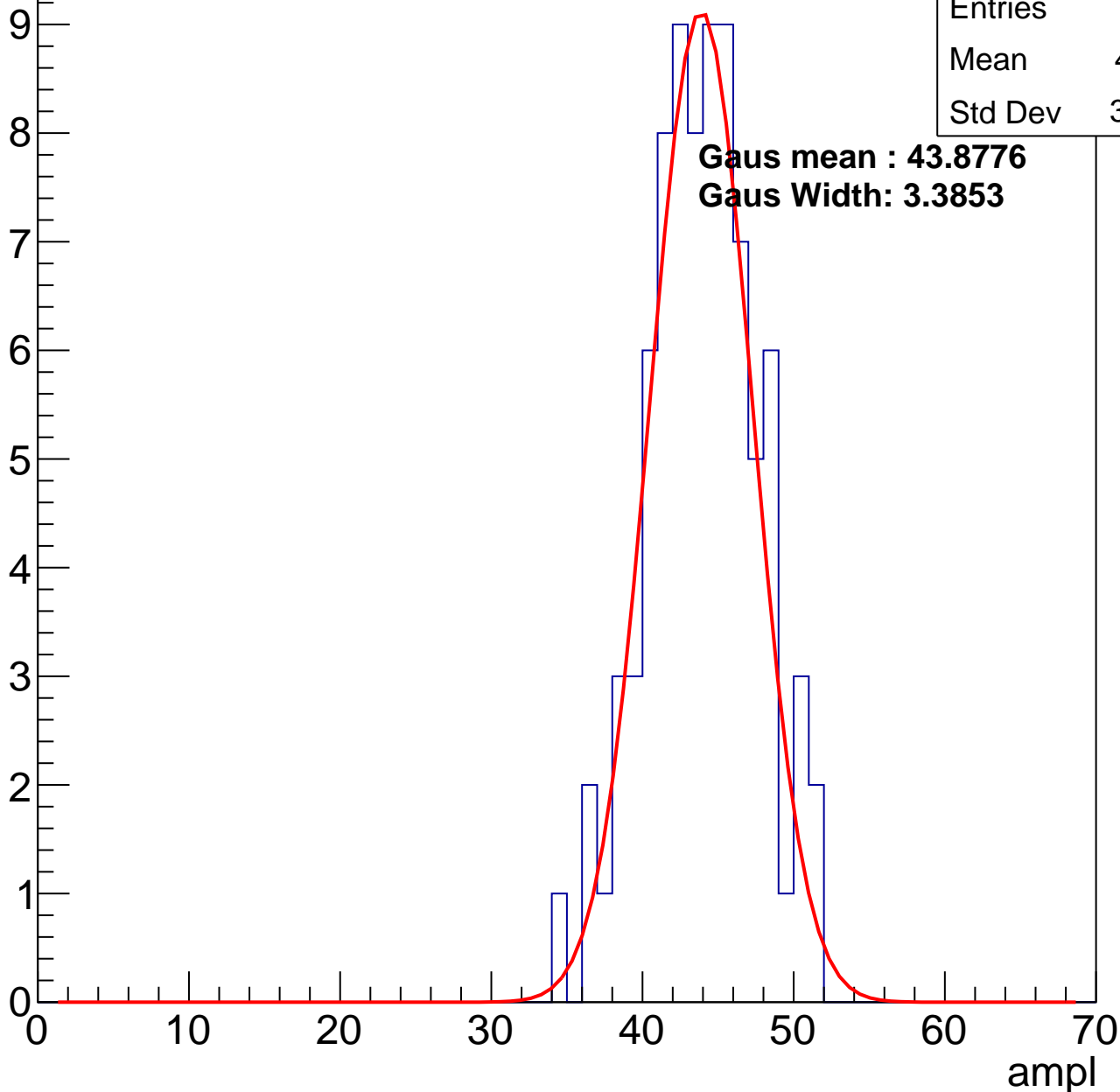
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	43.51
Std Dev	3.575

Gaus mean : 43.8776

Gaus Width: 3.3853

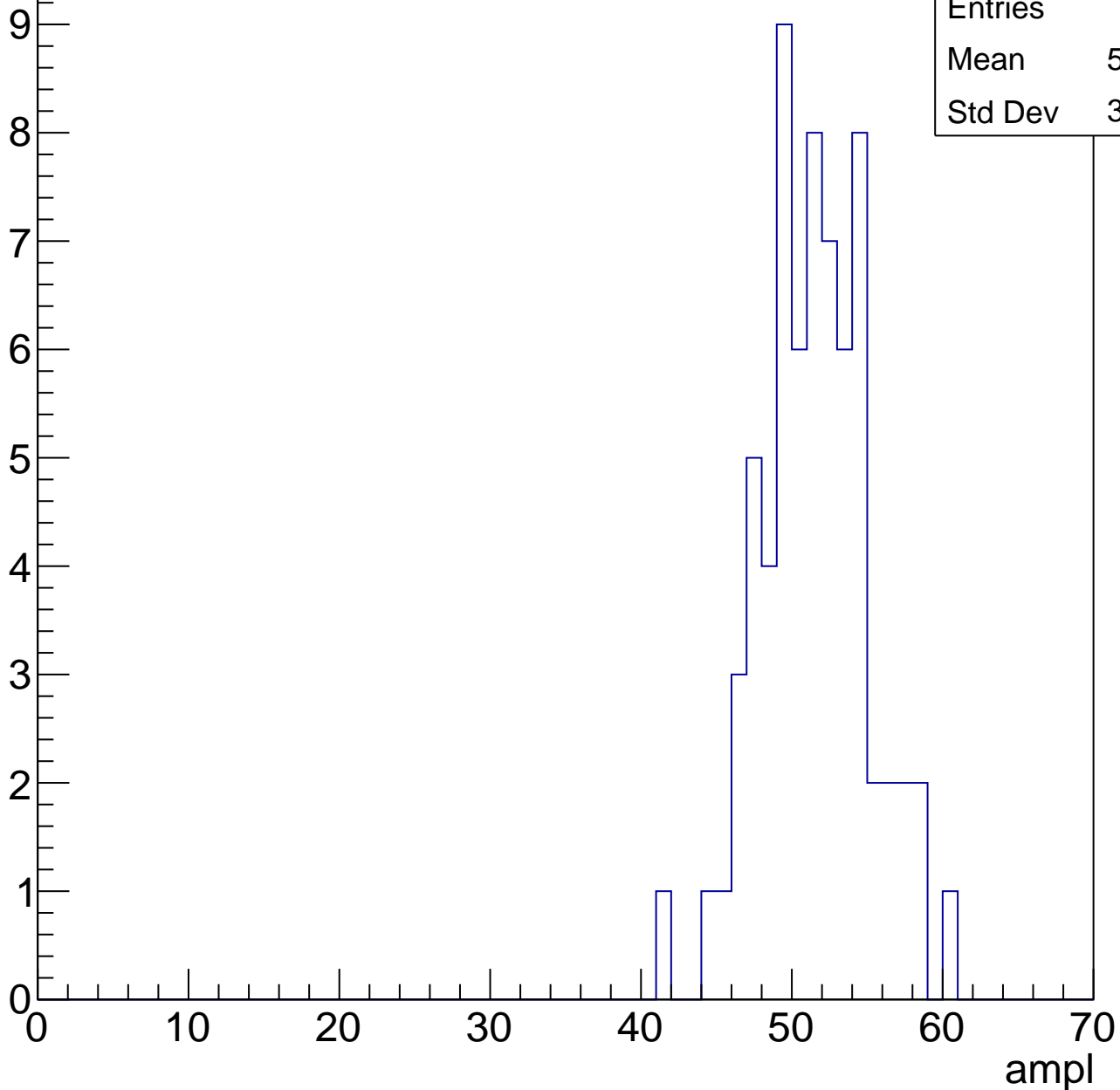


B1L101S, U23-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51.03
Std Dev	3.544

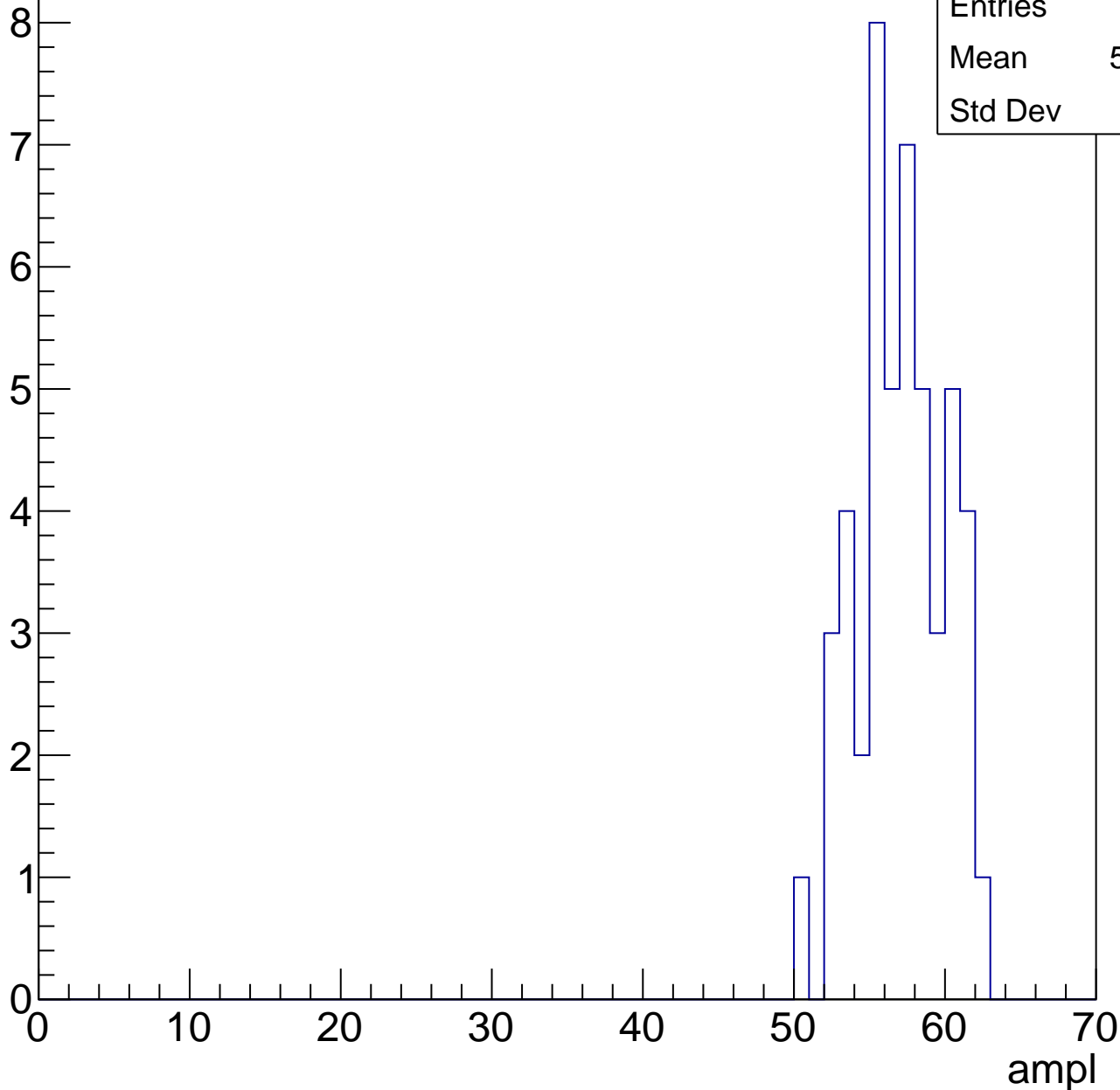


B1L101S, U23-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	56.62
Std Dev	2.84

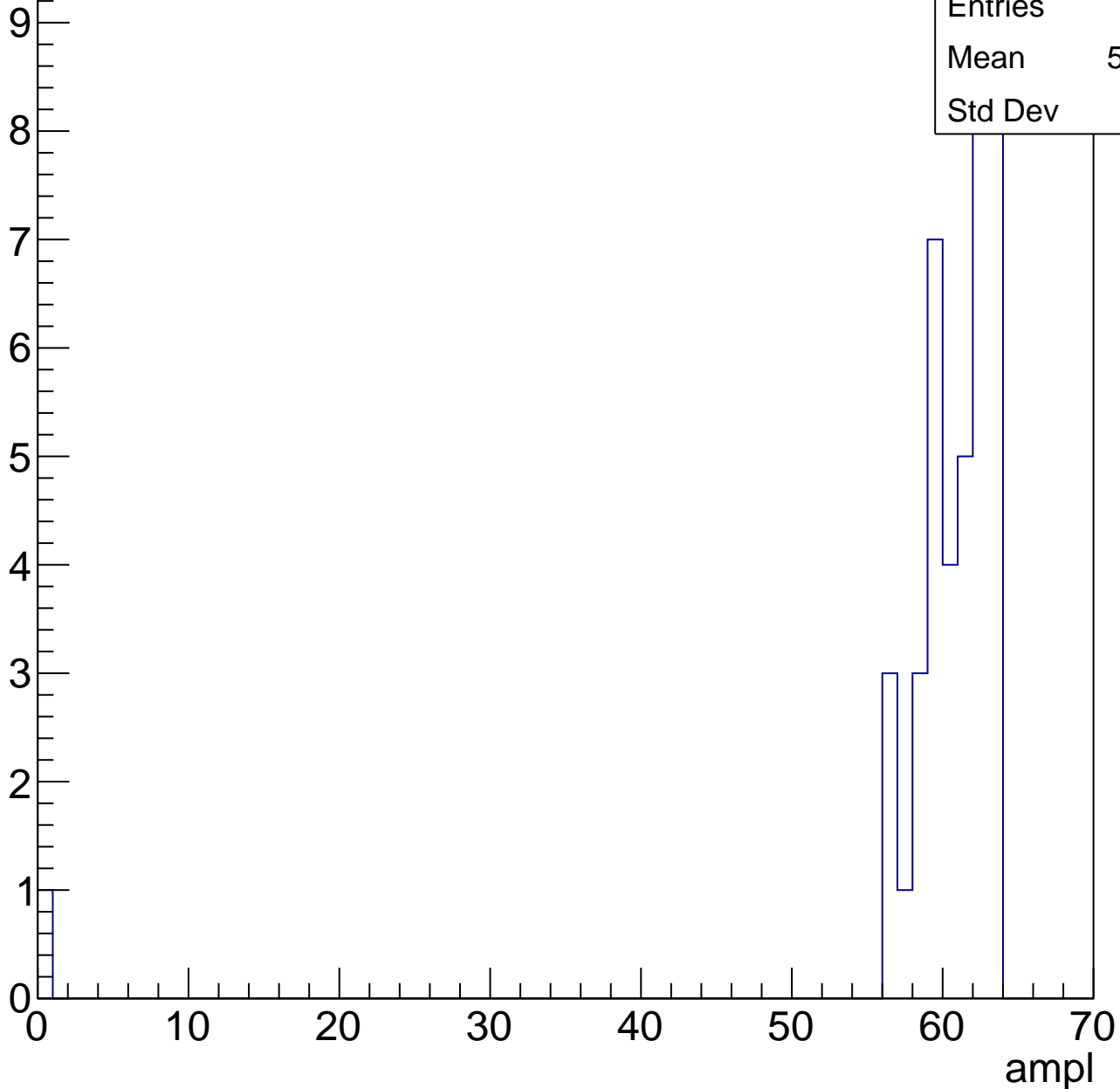


B1L101S, U23-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.02
Std Dev	9.57



B1L101S, U23-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

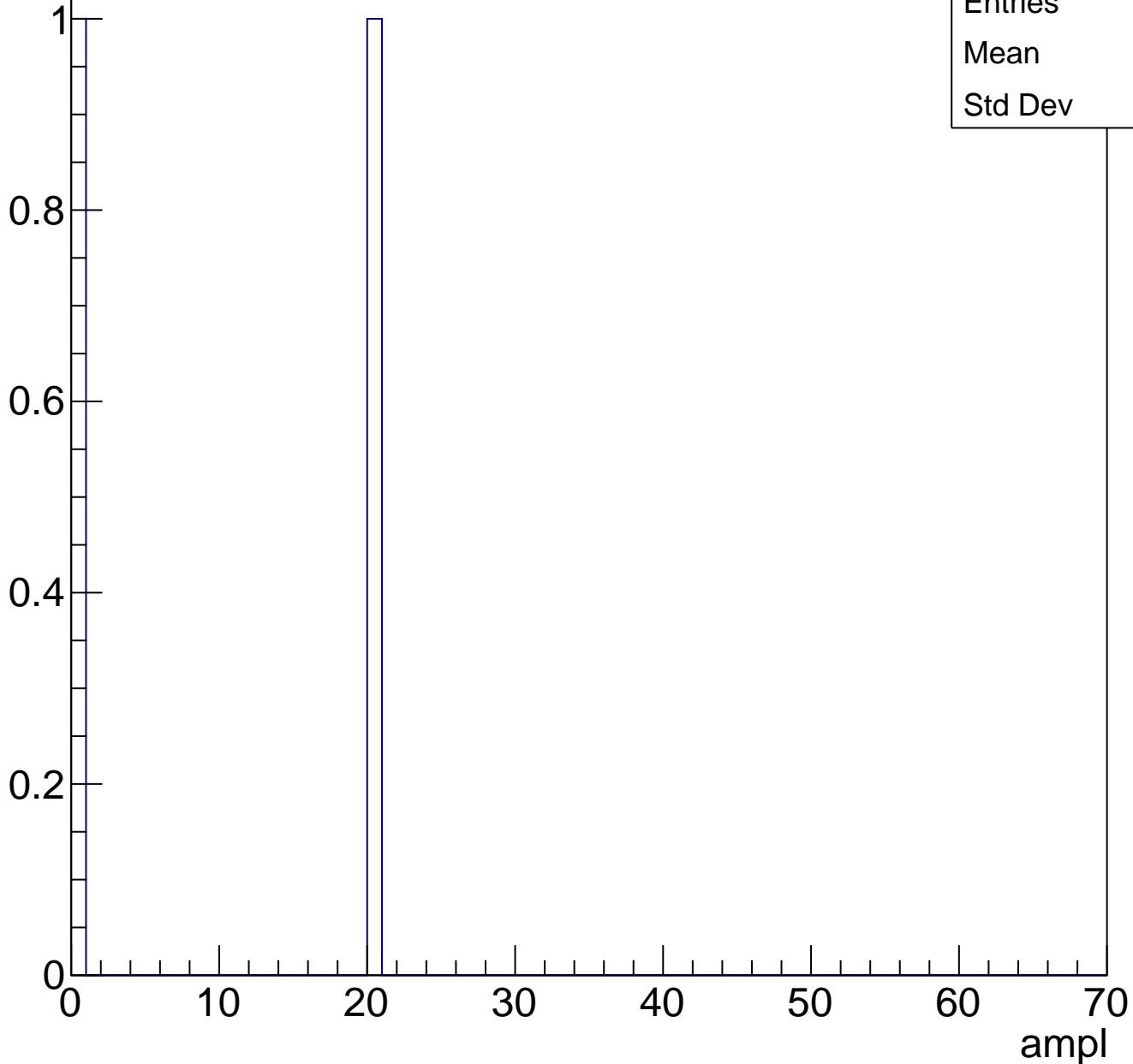
Entry



B1L101S, U23-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	10
Std Dev	10

B1L101S, U23-ch104, adc0

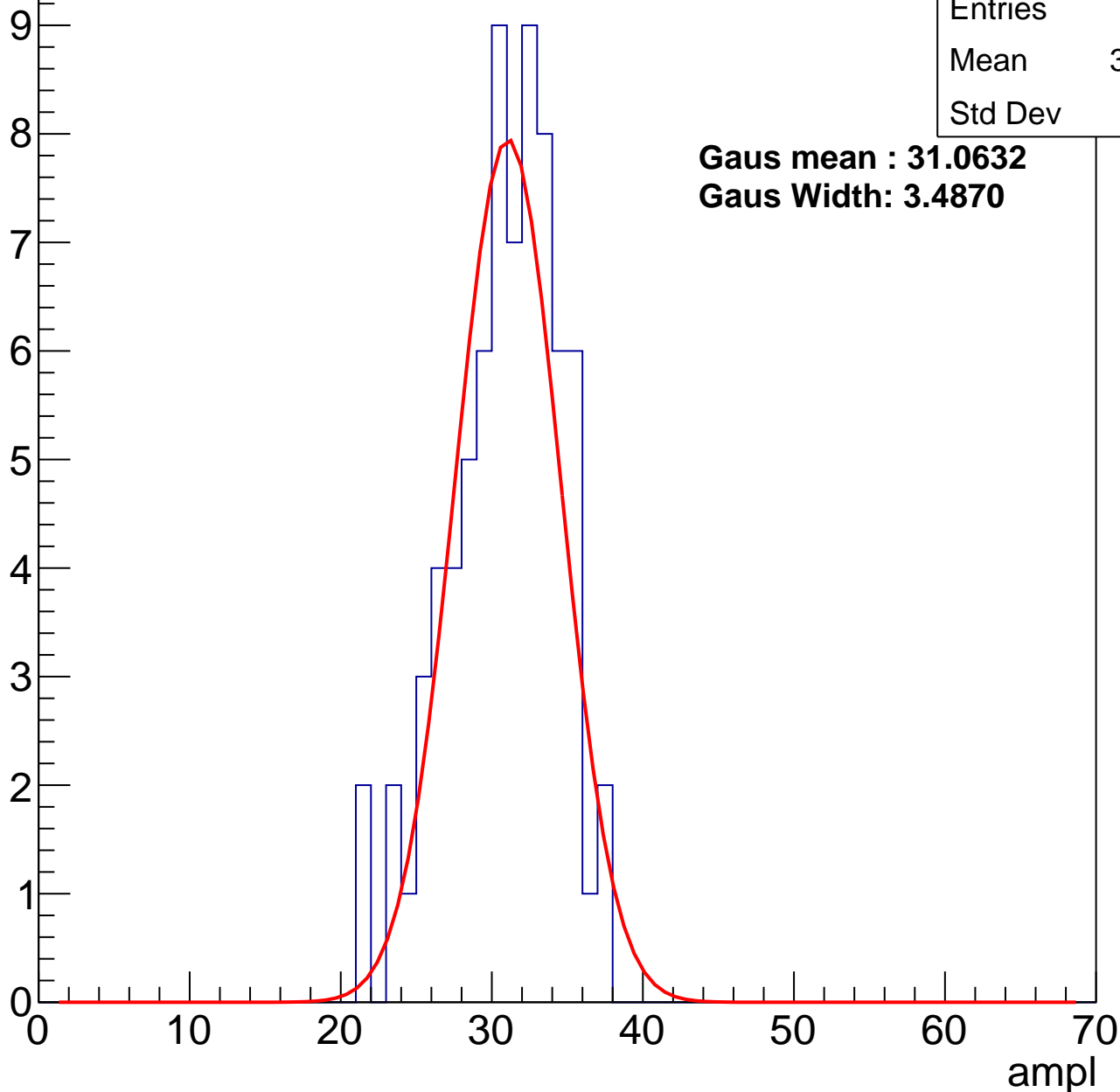
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.35
Std Dev	3.62

Gaus mean : 31.0632

Gaus Width: 3.4870



B1L101S, U23-ch104, adc1

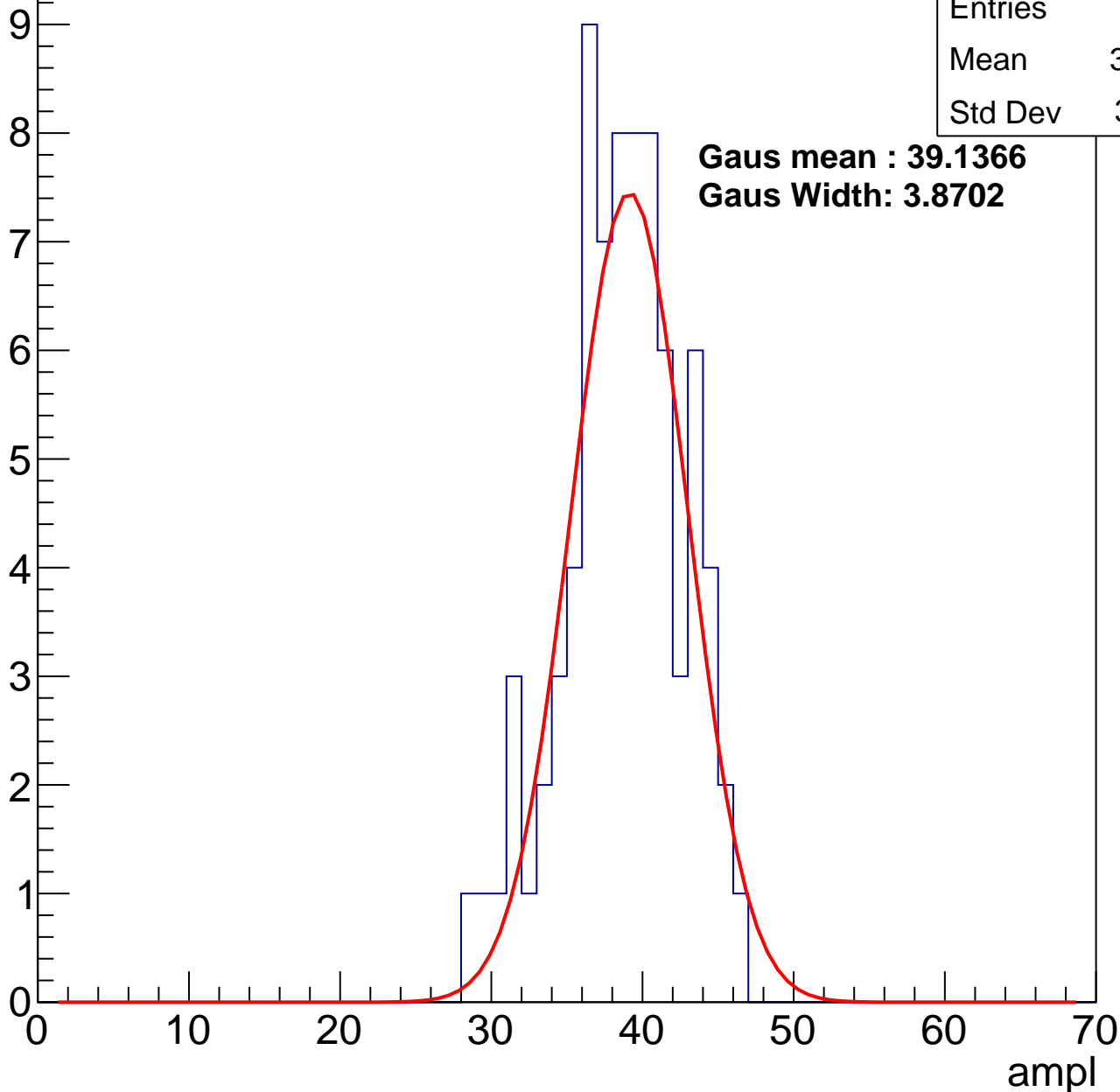
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	38.22
Std Dev	3.921

Gaus mean : 39.1366

Gaus Width: 3.8702



B1L101S, U23-ch104, adc2

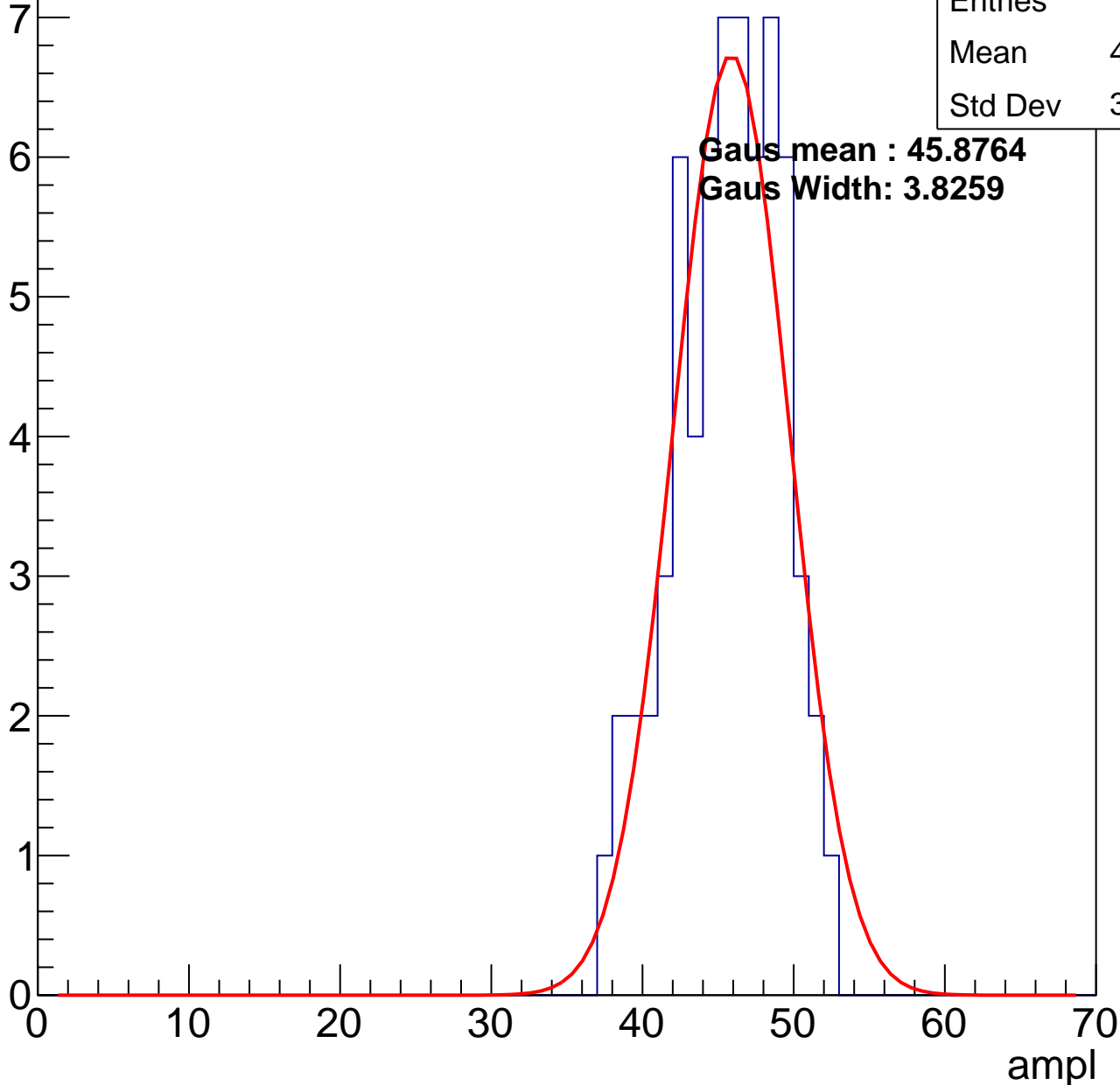
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.15
Std Dev	3.492

Gaus mean : 45.8764

Gaus Width: 3.8259

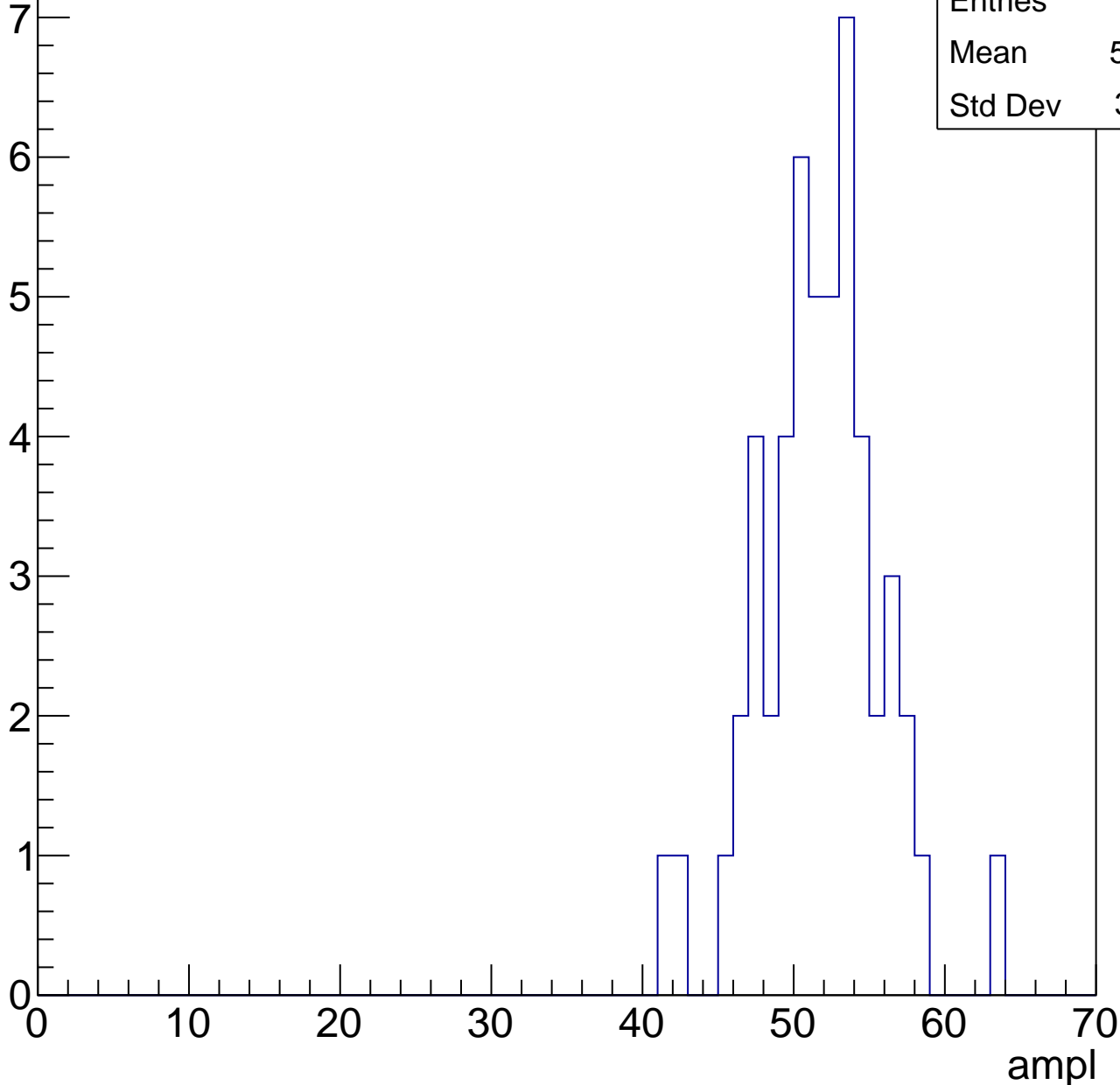


B1L101S, U23-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	51.27
Std Dev	3.981

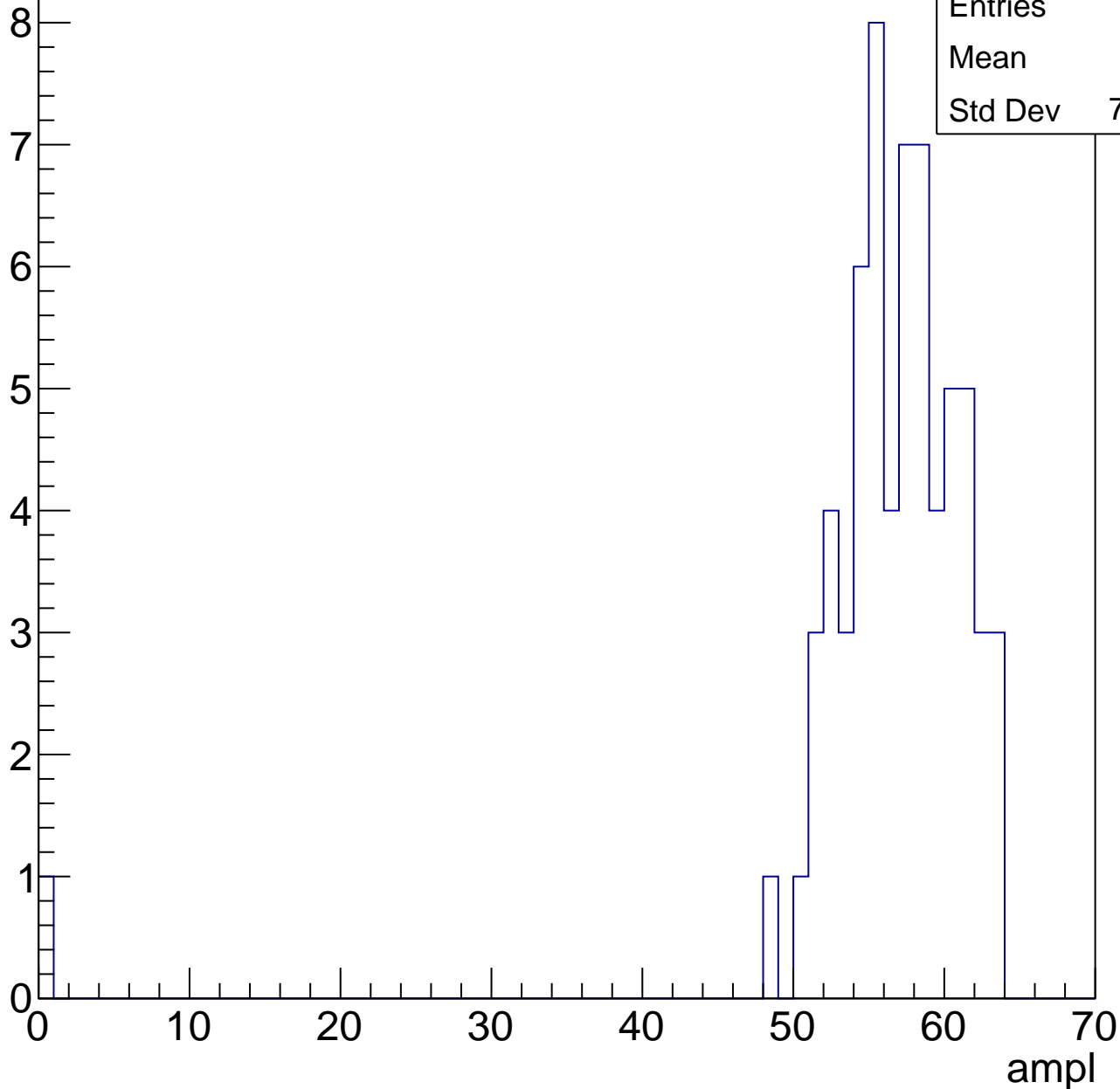


B1L101S, U23-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.8
Std Dev	7.808

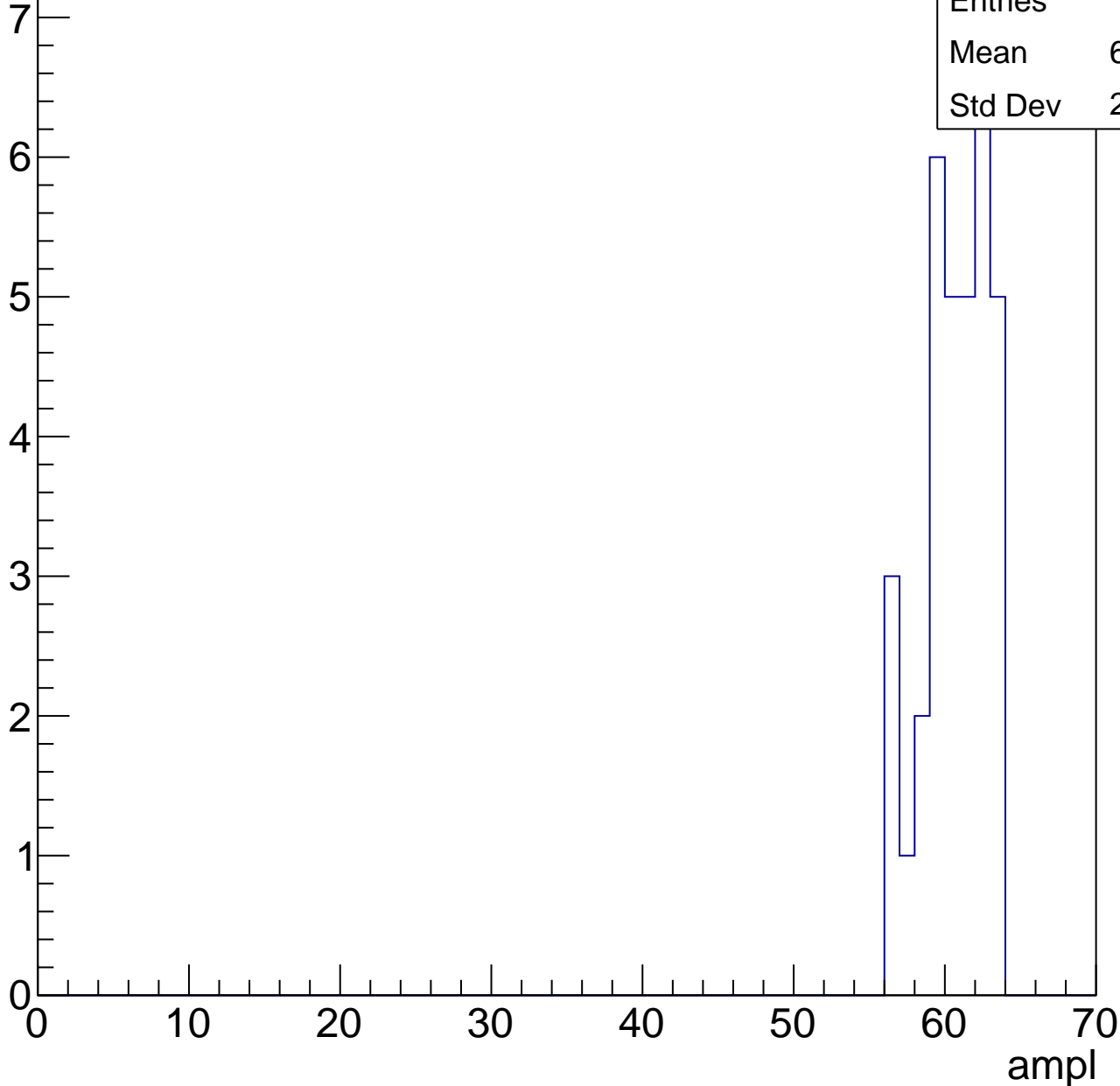


B1L101S, U23-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	60.26
Std Dev	2.077



B1L101S, U23-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch105, adc0

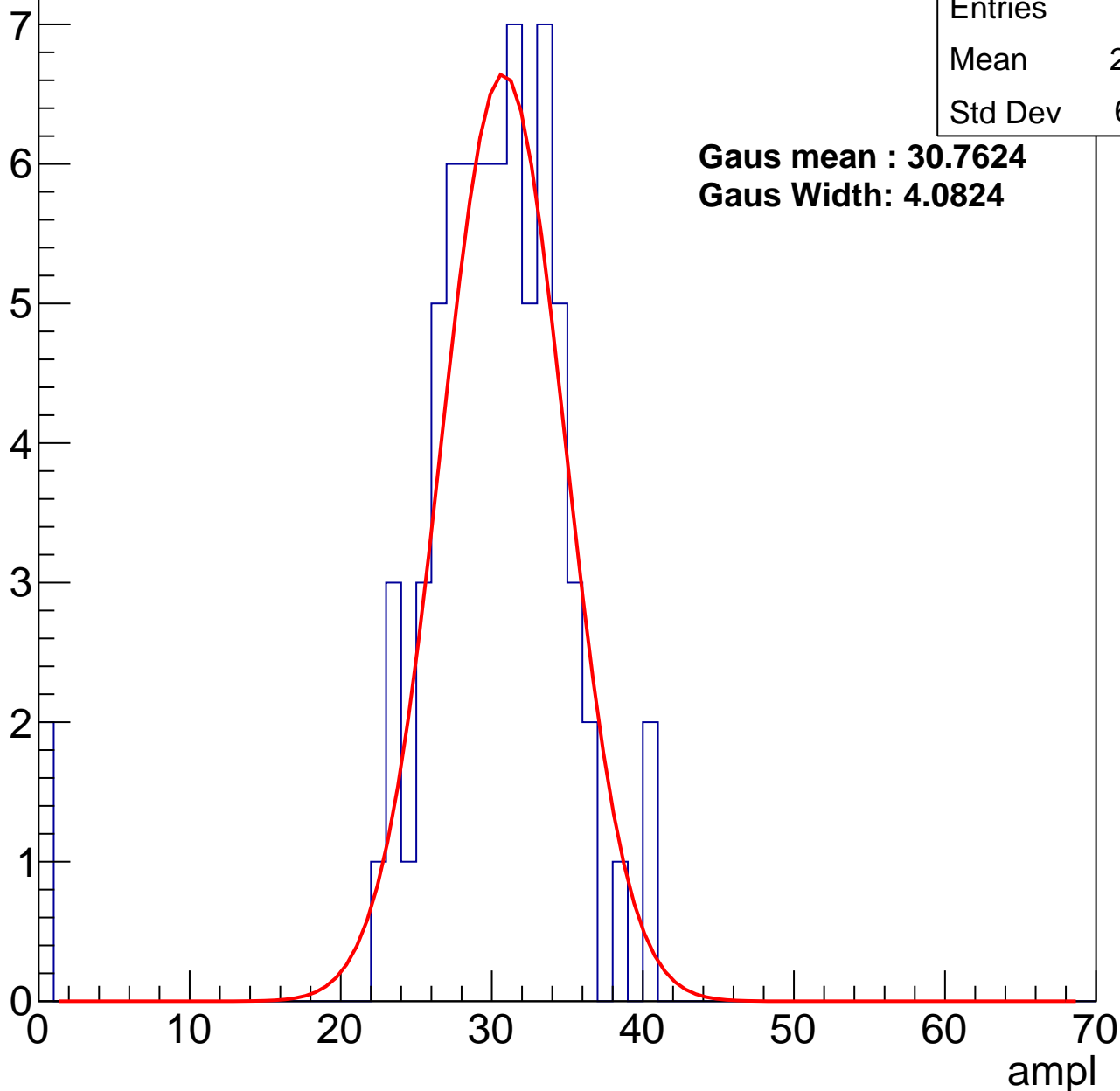
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.25
Std Dev	6.311

Gaus mean : 30.7624

Gaus Width: 4.0824



B1L101S, U23-ch105, adc1

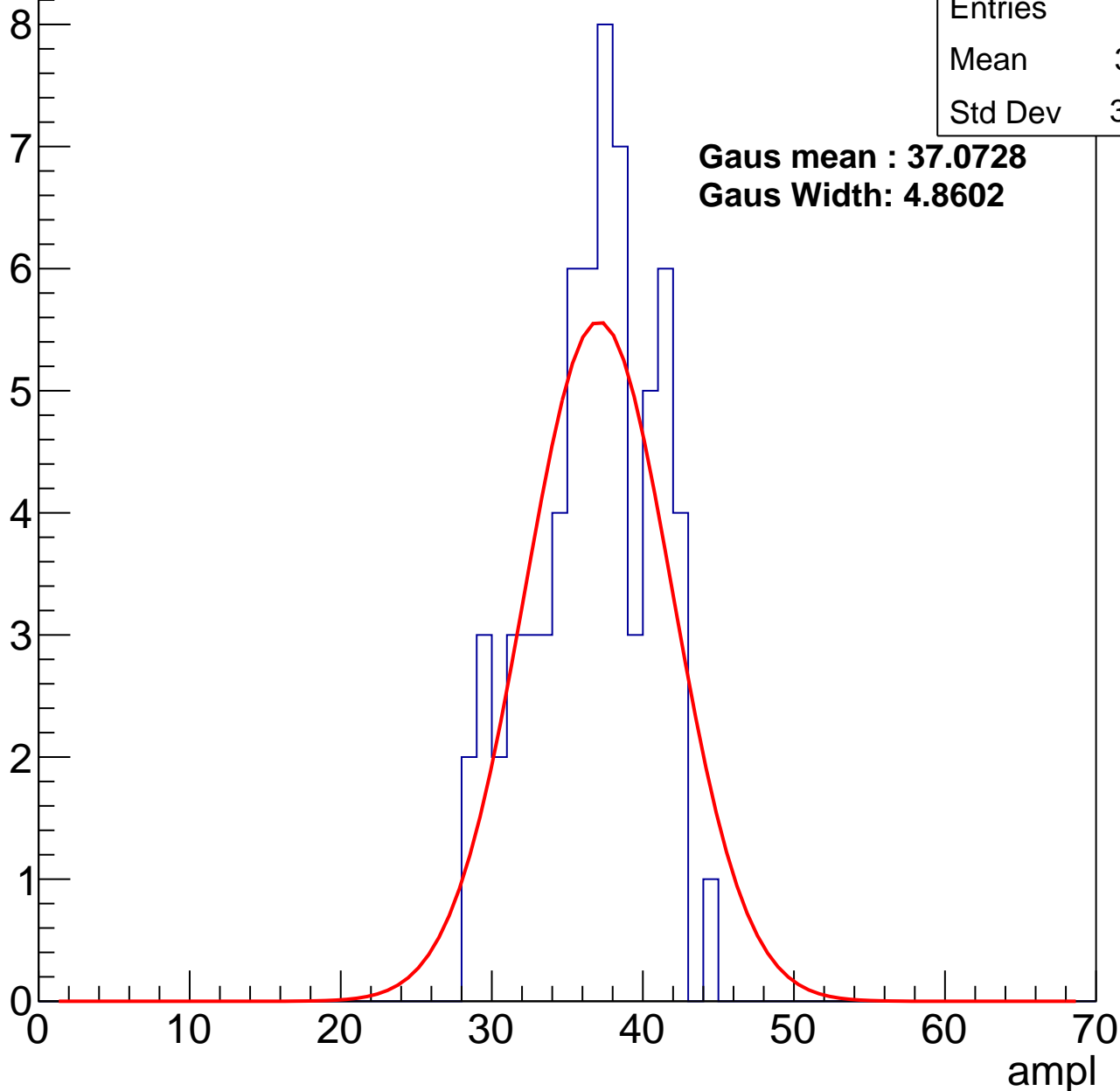
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.21
Std Dev	3.925

Gaus mean : 37.0728

Gaus Width: 4.8602



B1L101S, U23-ch105, adc2

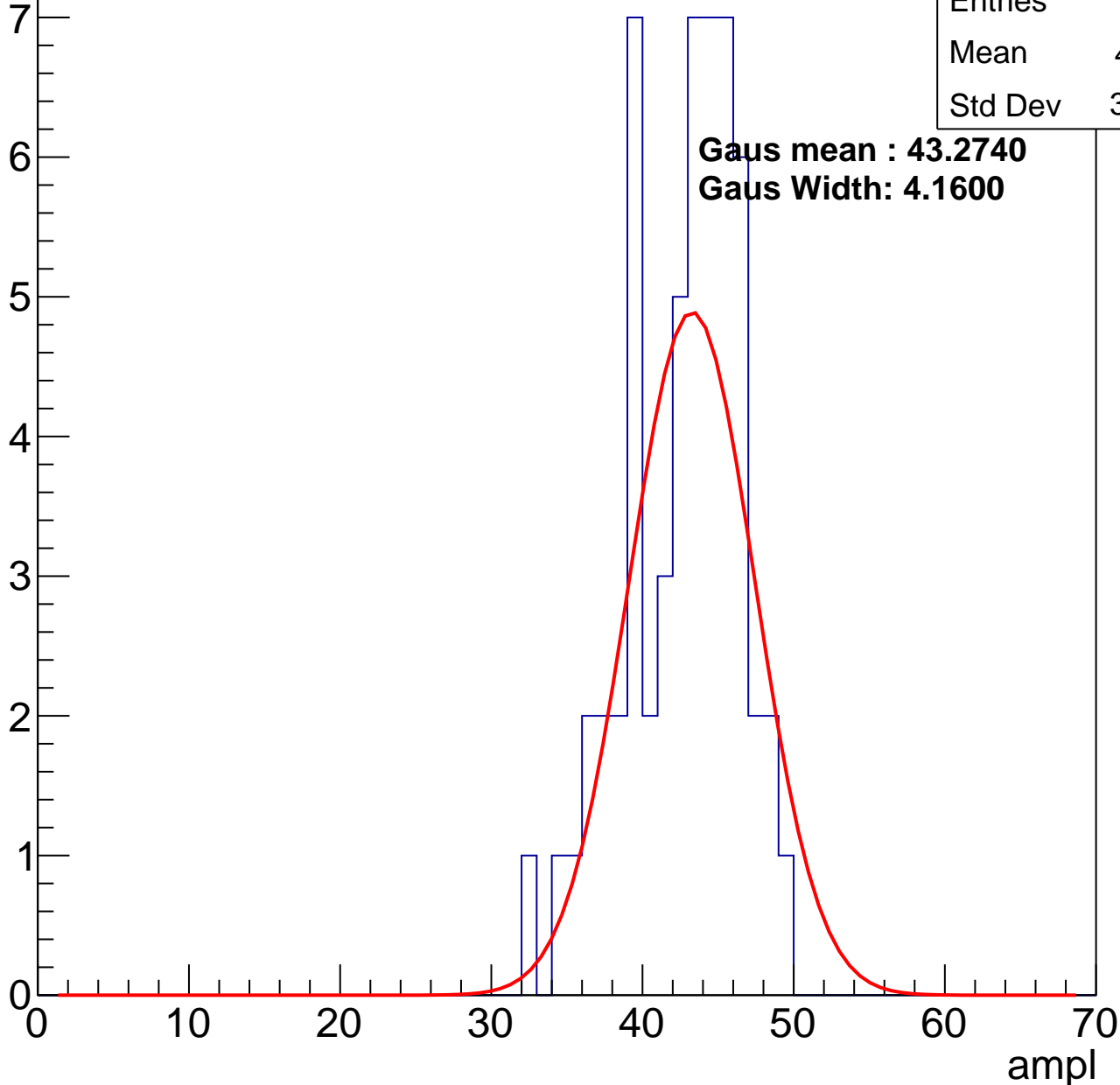
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.21
Std Dev	3.722

Gaus mean : 43.2740

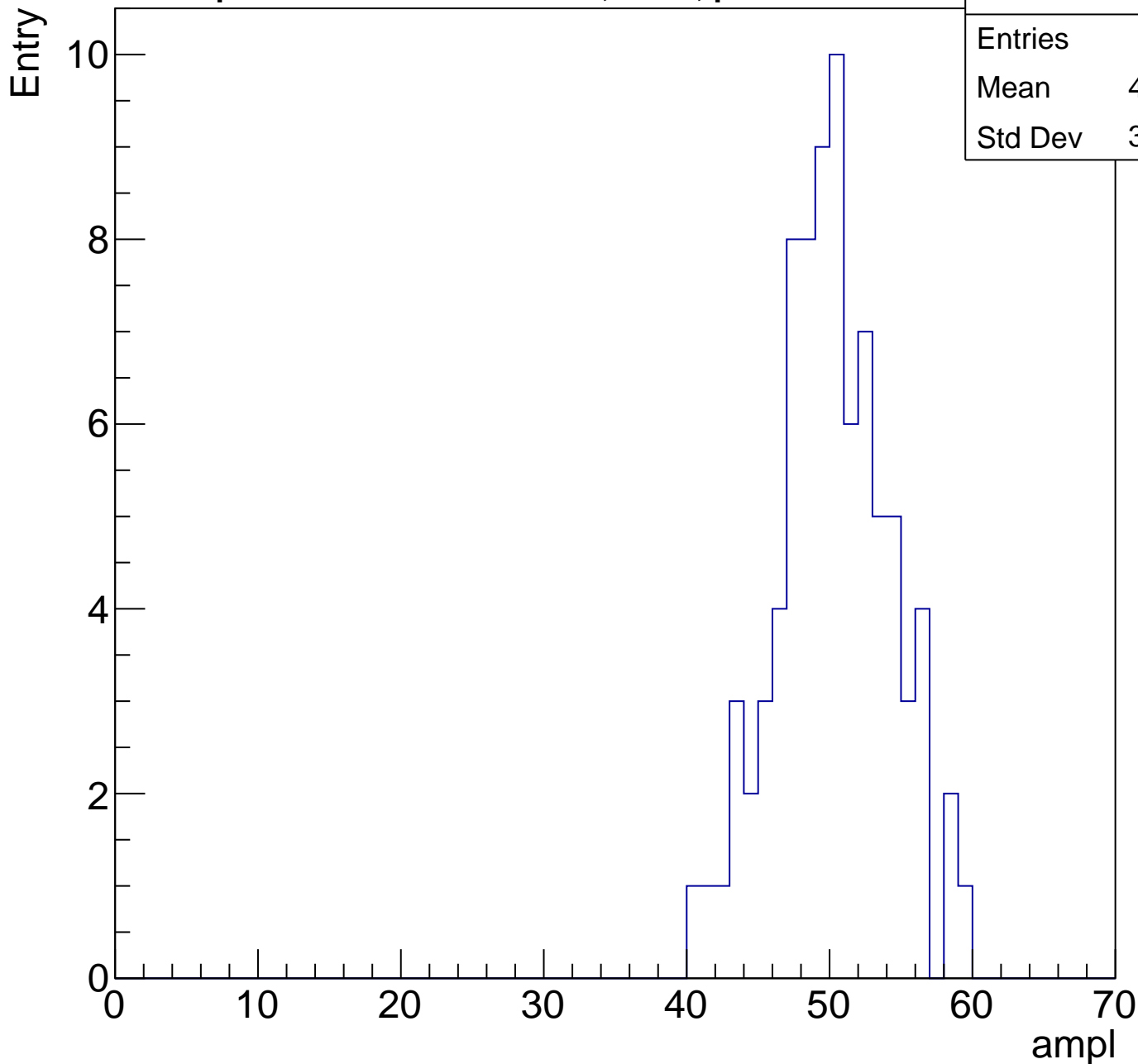
Gaus Width: 4.1600



B1L101S, U23-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	49.75
Std Dev	3.954

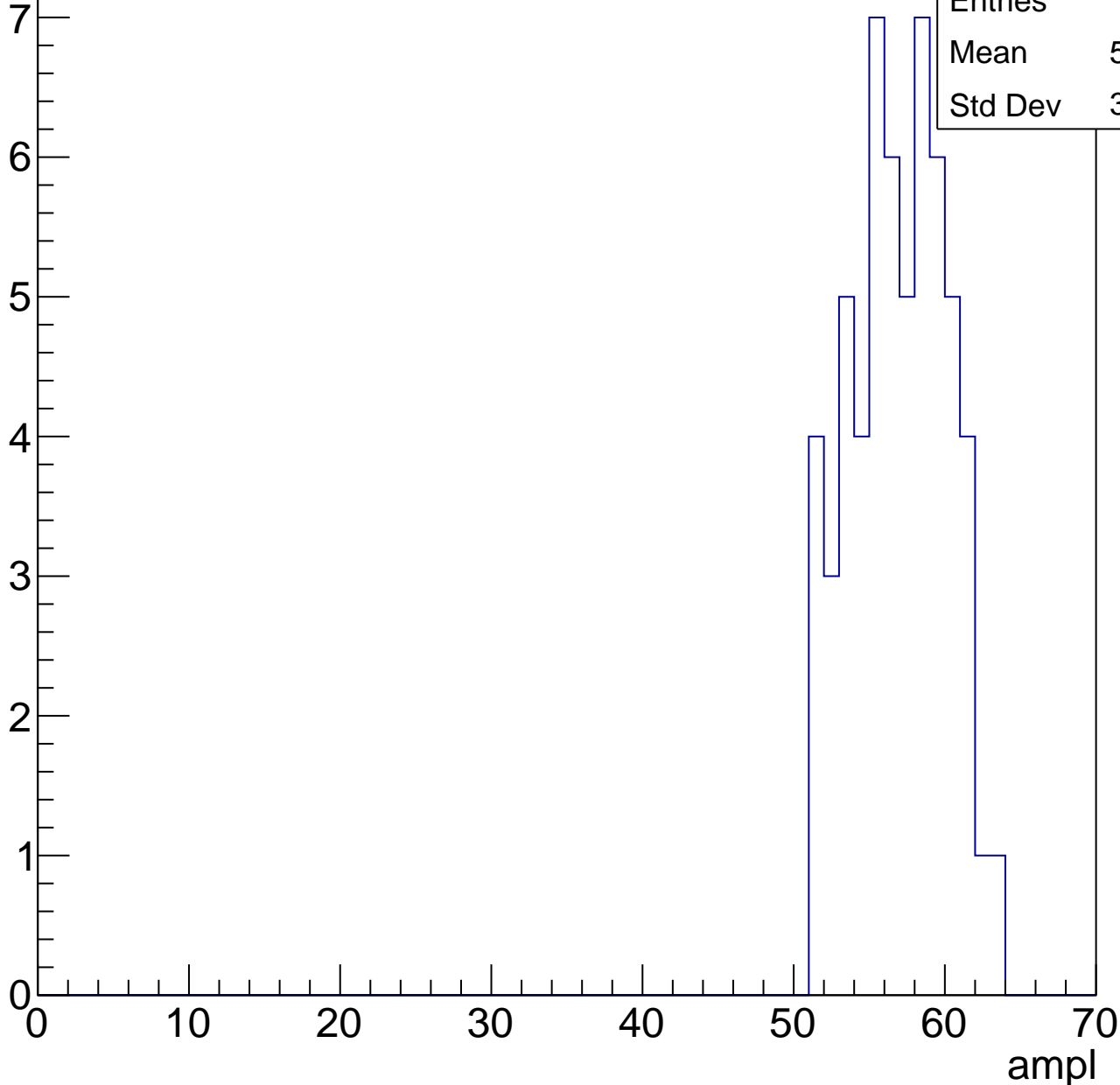


B1L101S, U23-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.48
Std Dev	3.092

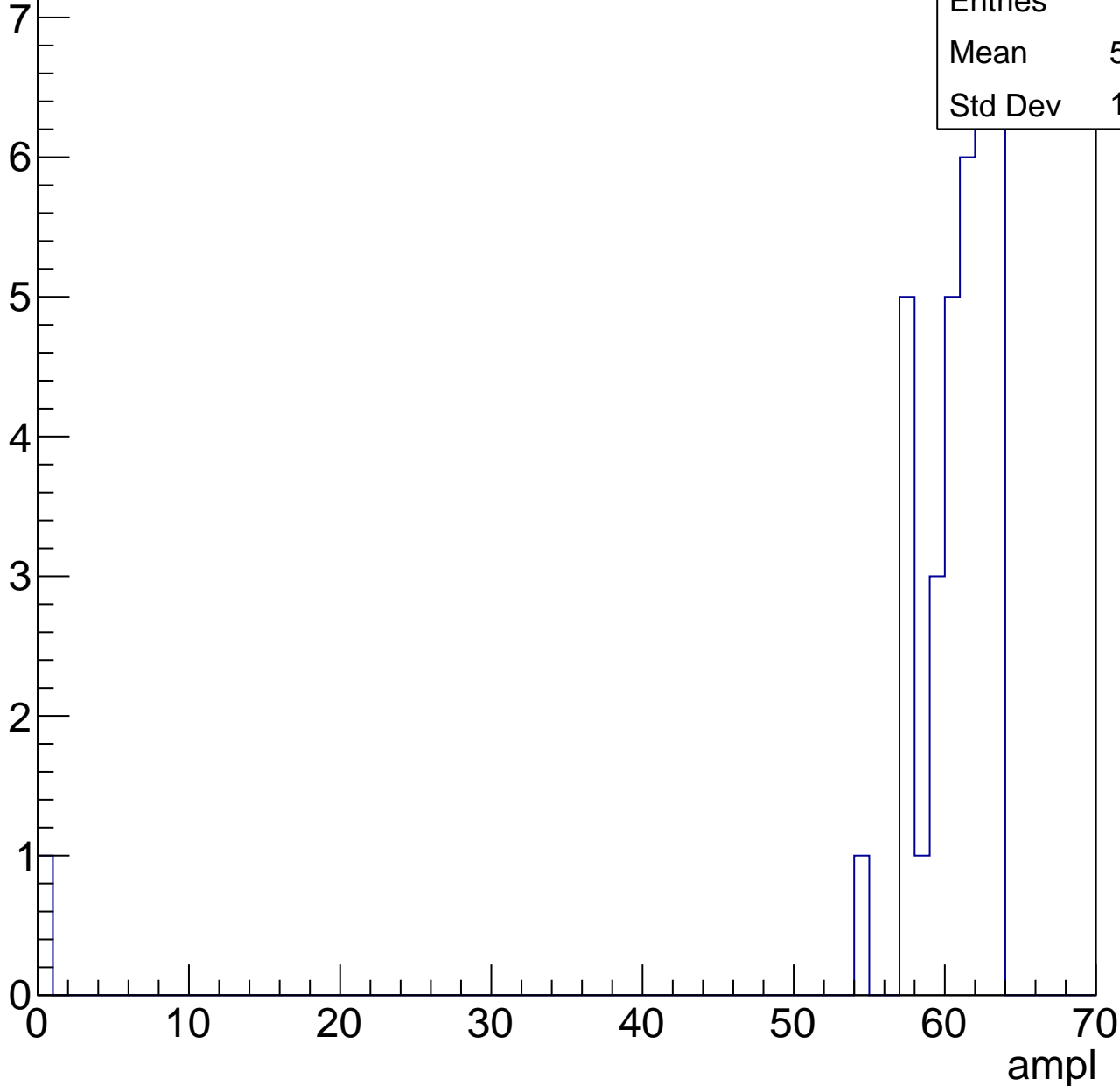


B1L101S, U23-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.75
Std Dev	10.18



B1L101S, U23-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.5
Std Dev	0.5

0 10 20 30 40 50 60 70

ampl

B1L101S, U23-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch106, adc0

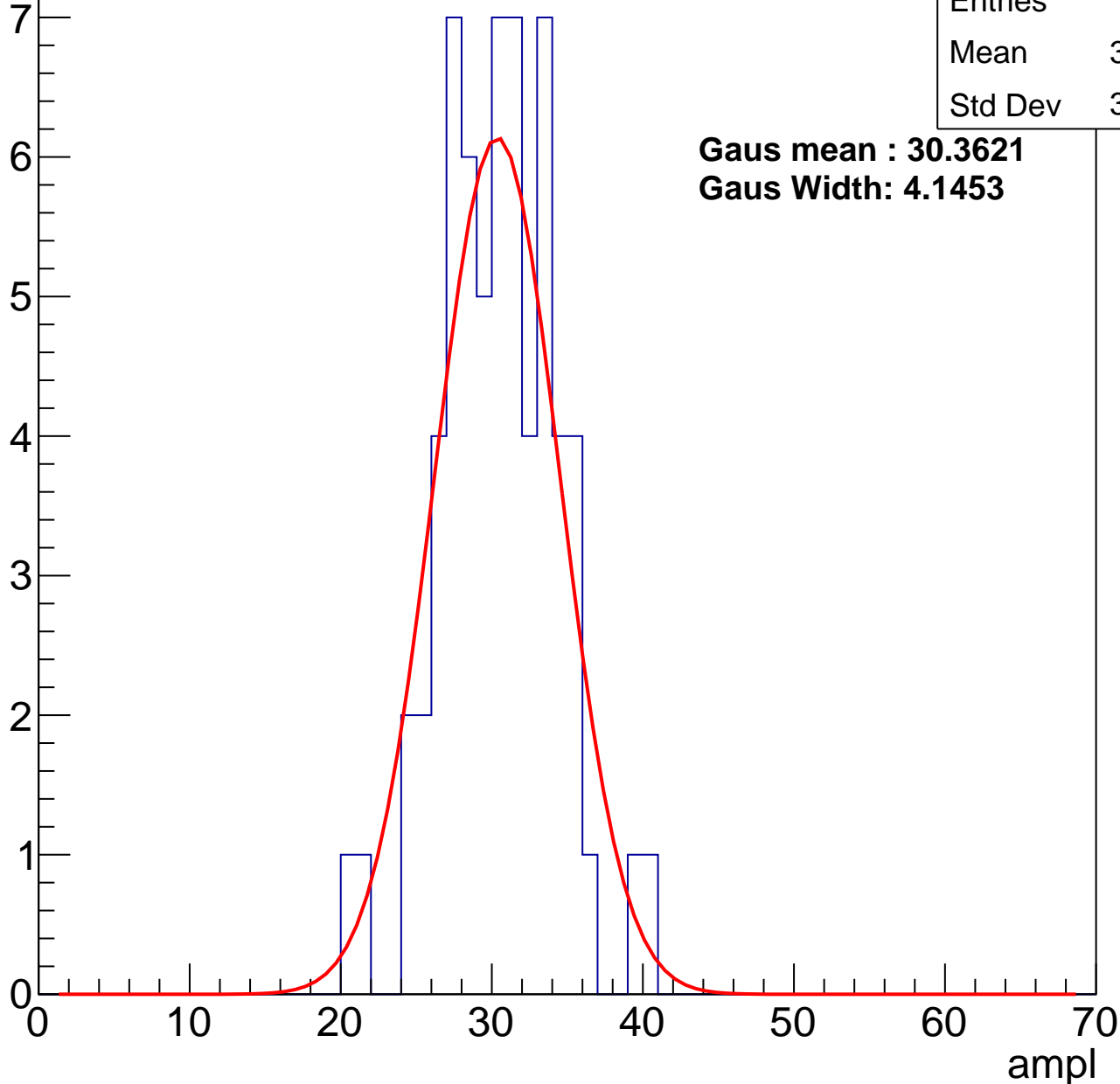
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.03
Std Dev	3.804

Gaus mean : 30.3621

Gaus Width: 4.1453



B1L101S, U23-ch106, adc1

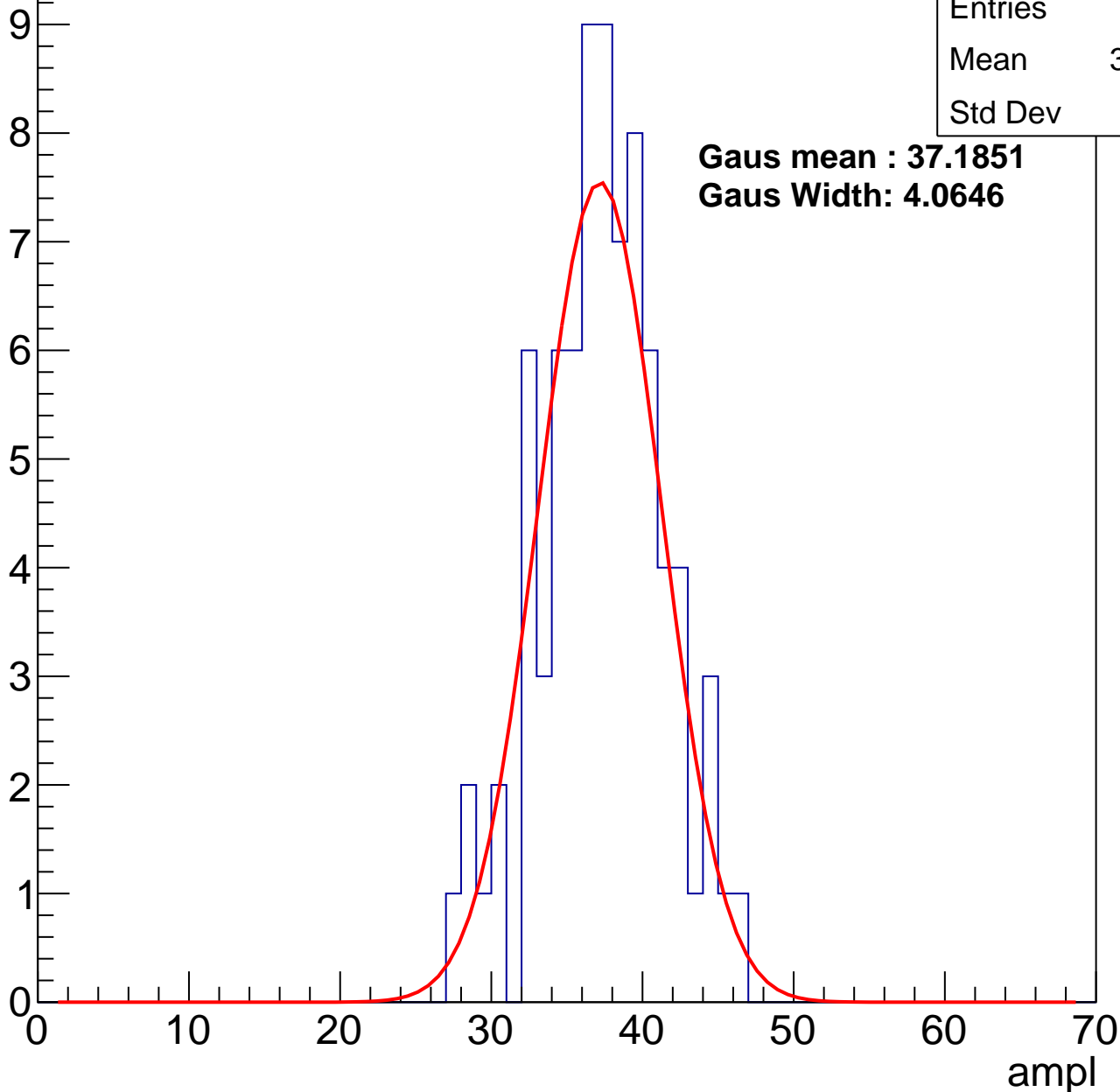
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.88
Std Dev	4.02

Gaus mean : 37.1851

Gaus Width: 4.0646



B1L101S, U23-ch106, adc2

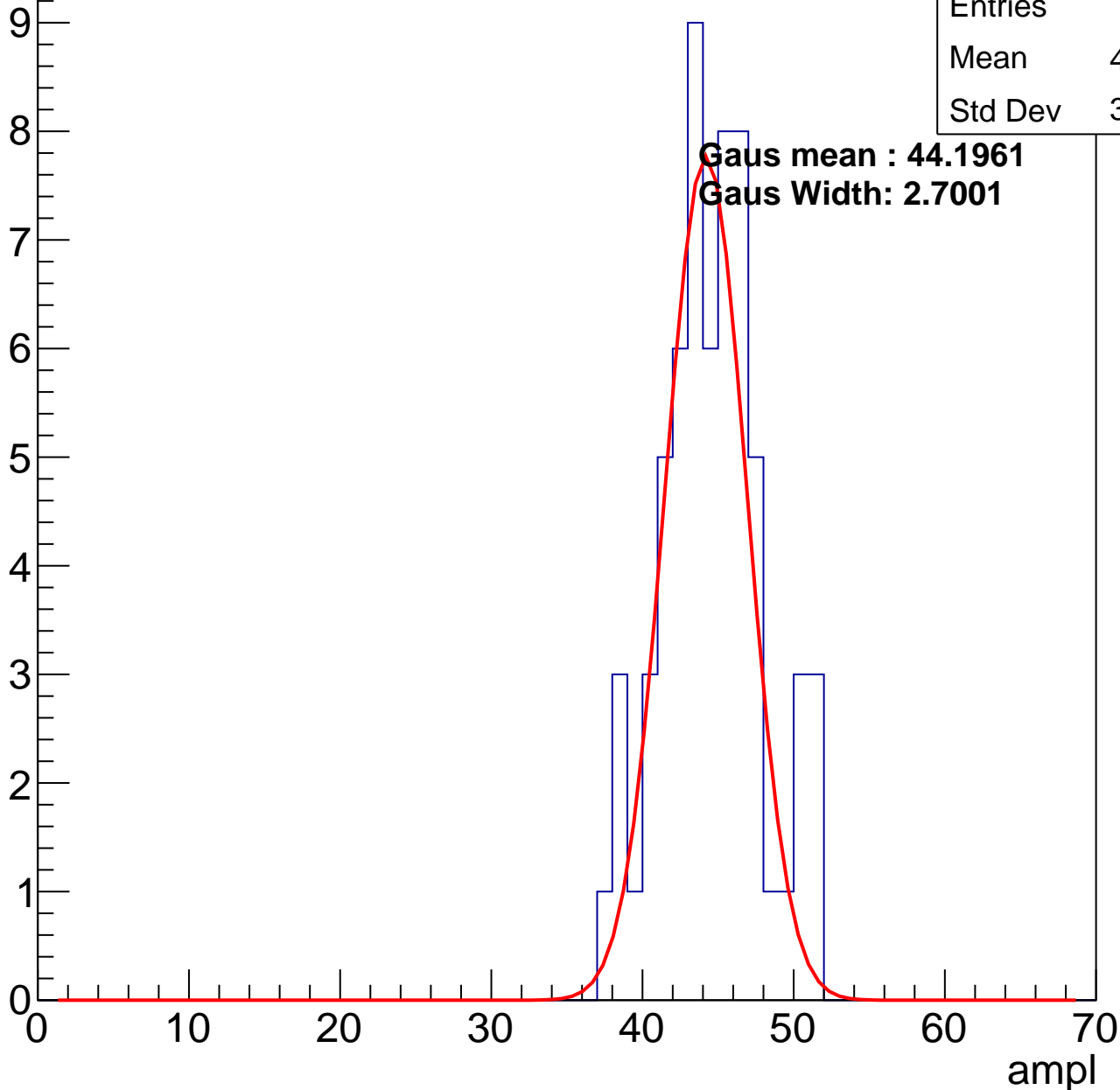
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.14
Std Dev	3.304

Gaus mean : 44.161

Gaus Width: 2.7001

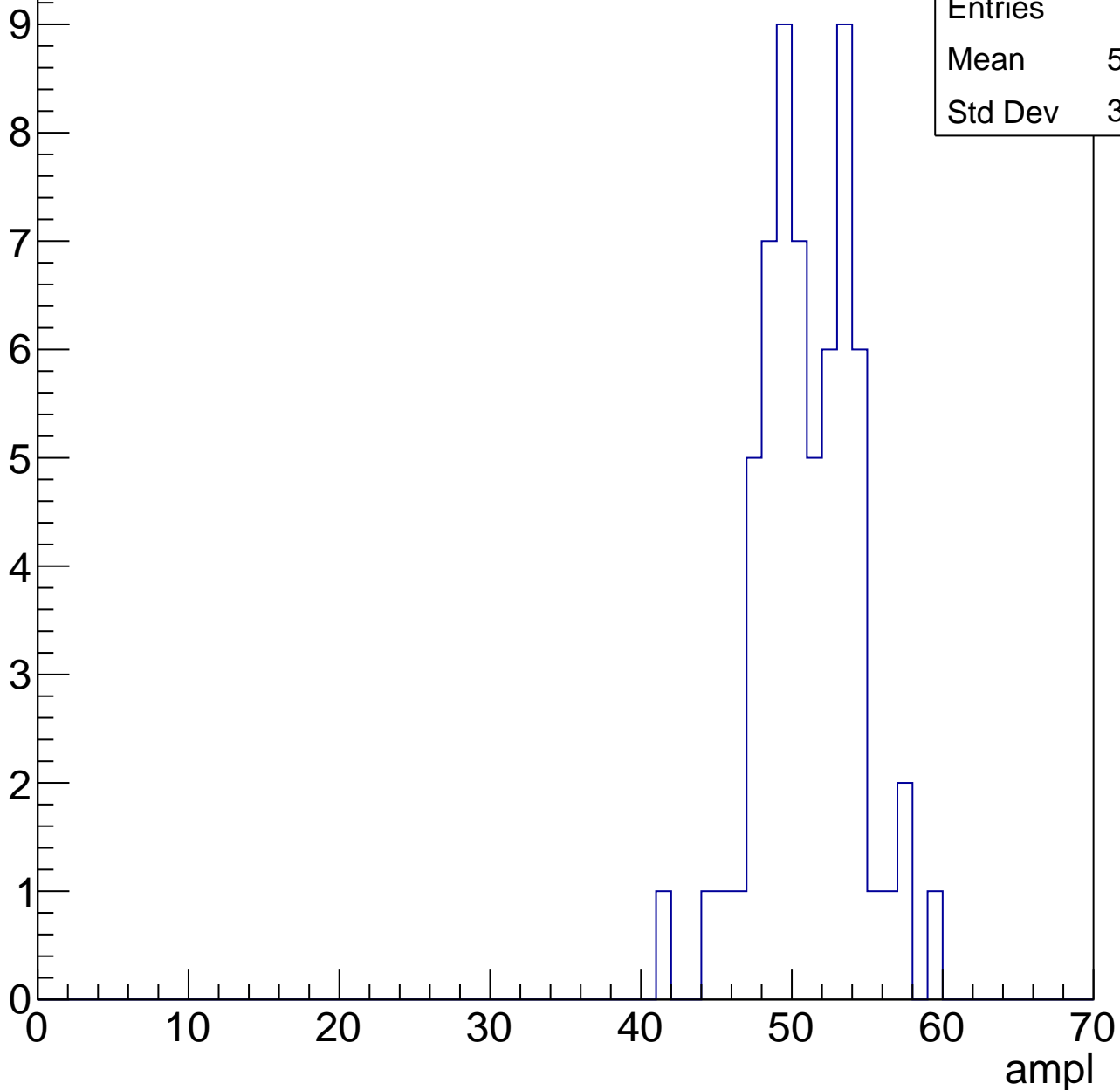


B1L101S, U23-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.63
Std Dev	3.238

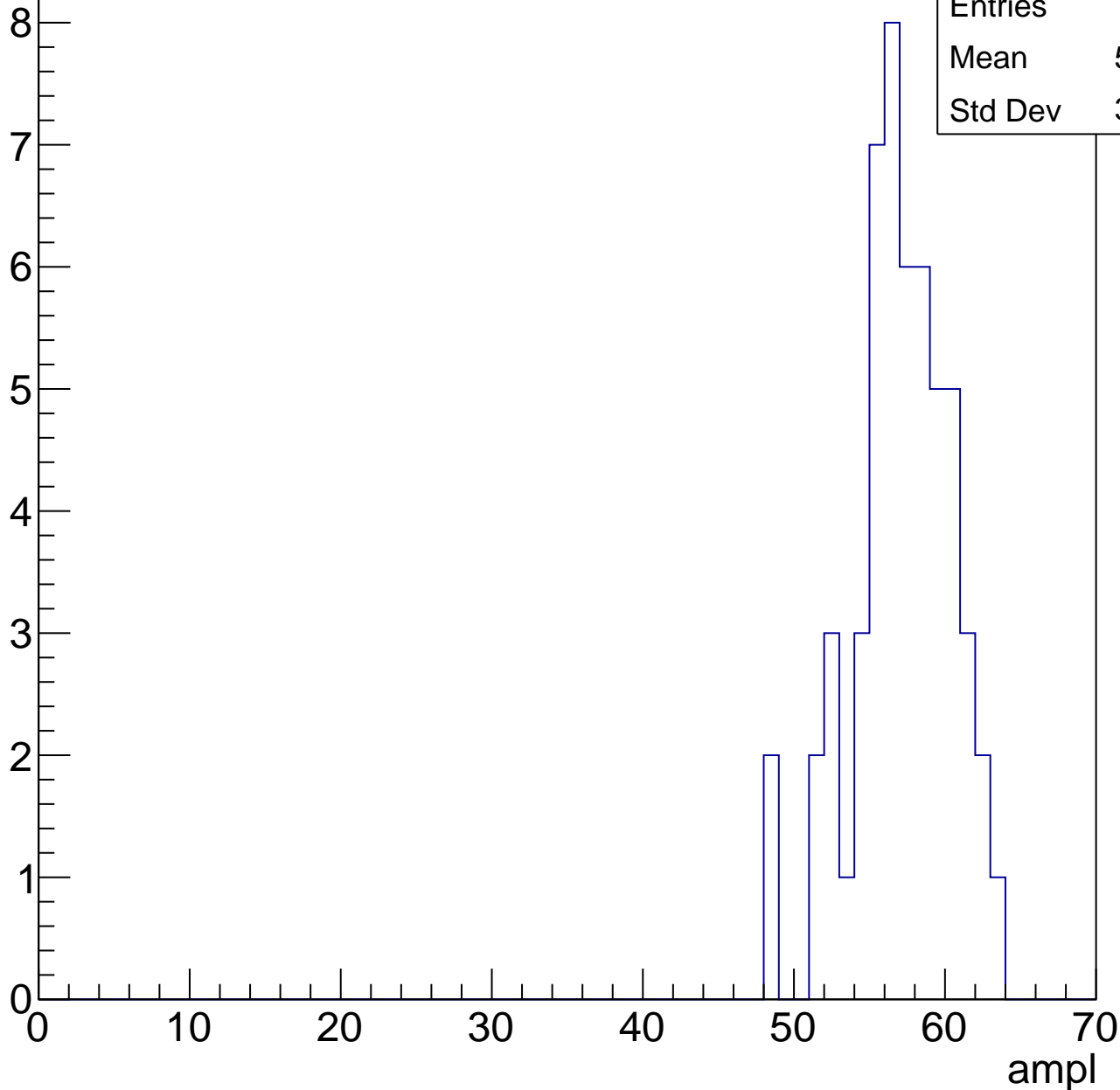


B1L101S, U23-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.61
Std Dev	3.291

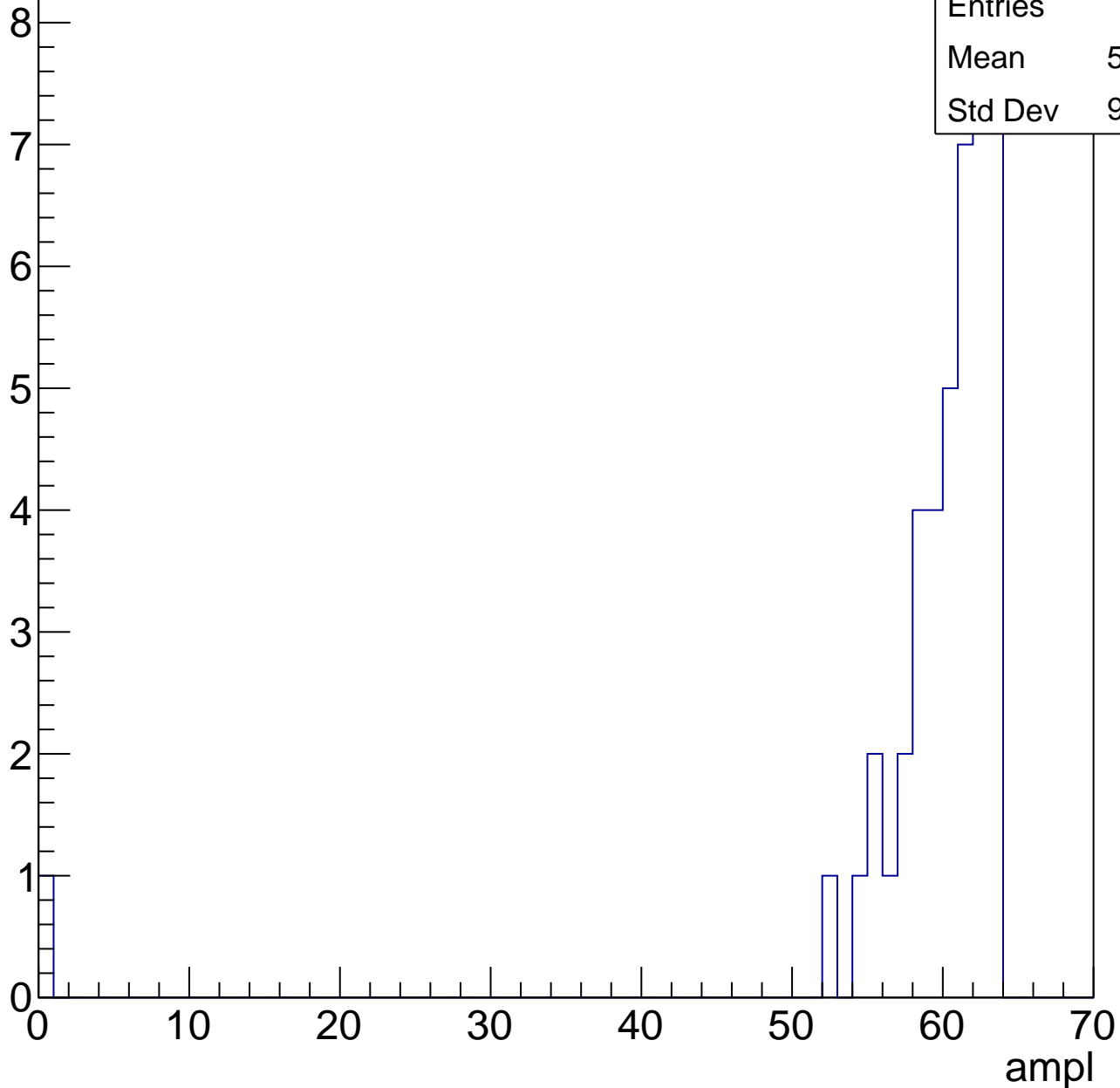


B1L101S, U23-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.66
Std Dev	9.337



B1L101S, U23-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U23-ch107, adc0

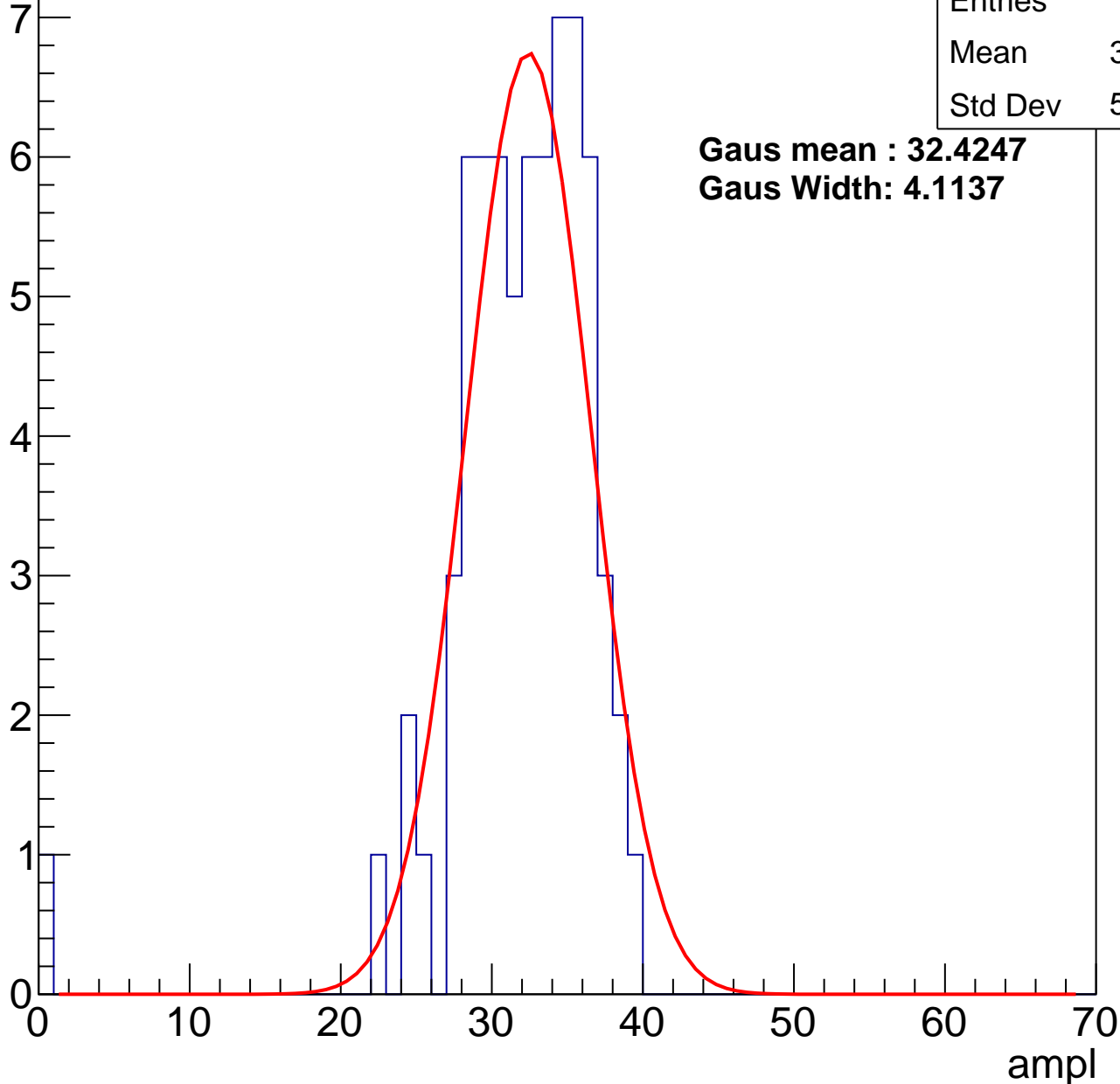
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	31.42
Std Dev	5.276

Gaus mean : 32.4247

Gaus Width: 4.1137



B1L101S, U23-ch107, adc1

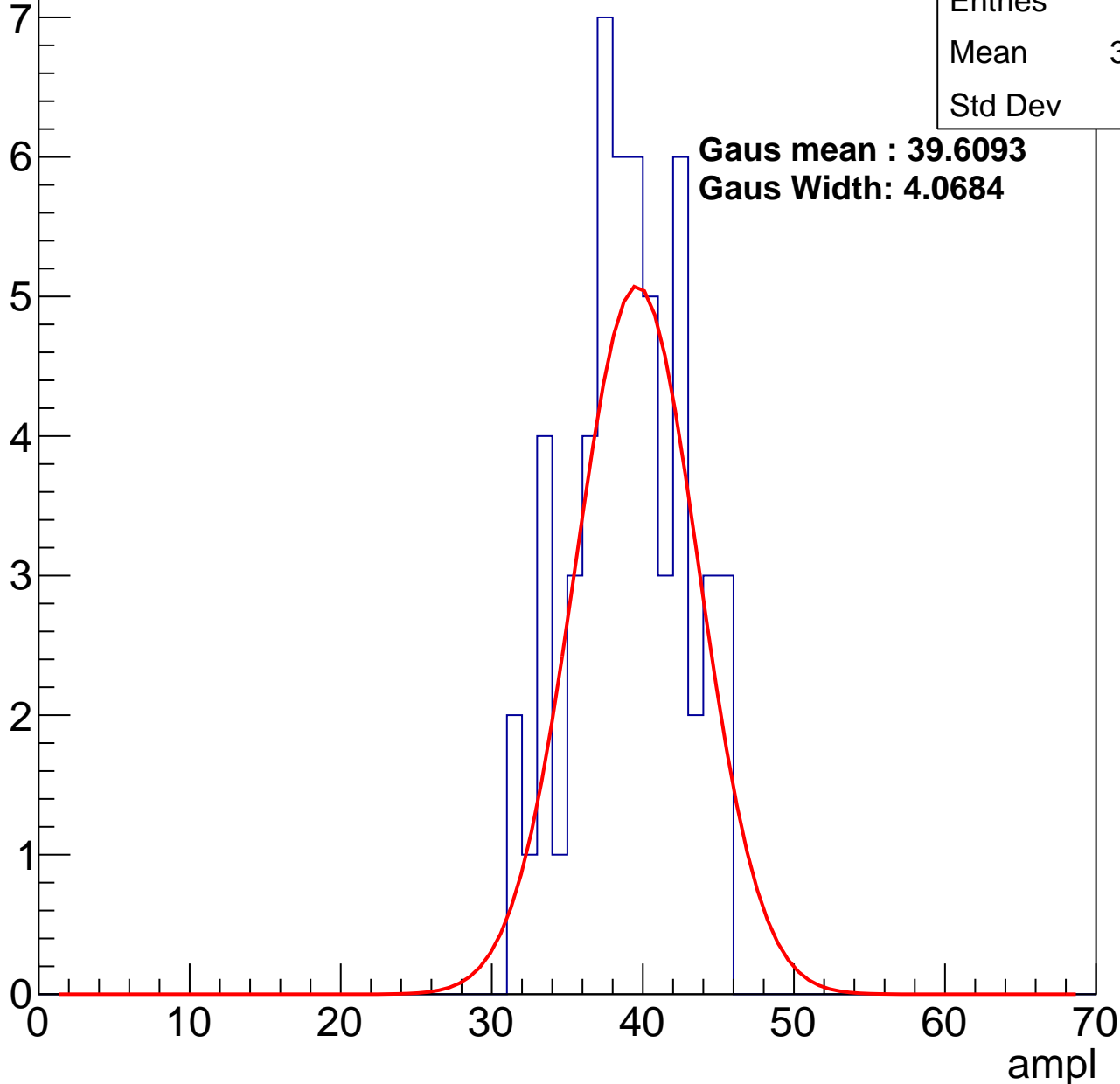
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	38.54
Std Dev	3.63

Gaus mean : 39.6093

Gaus Width: 4.0684



B1L101S, U23-ch107, adc2

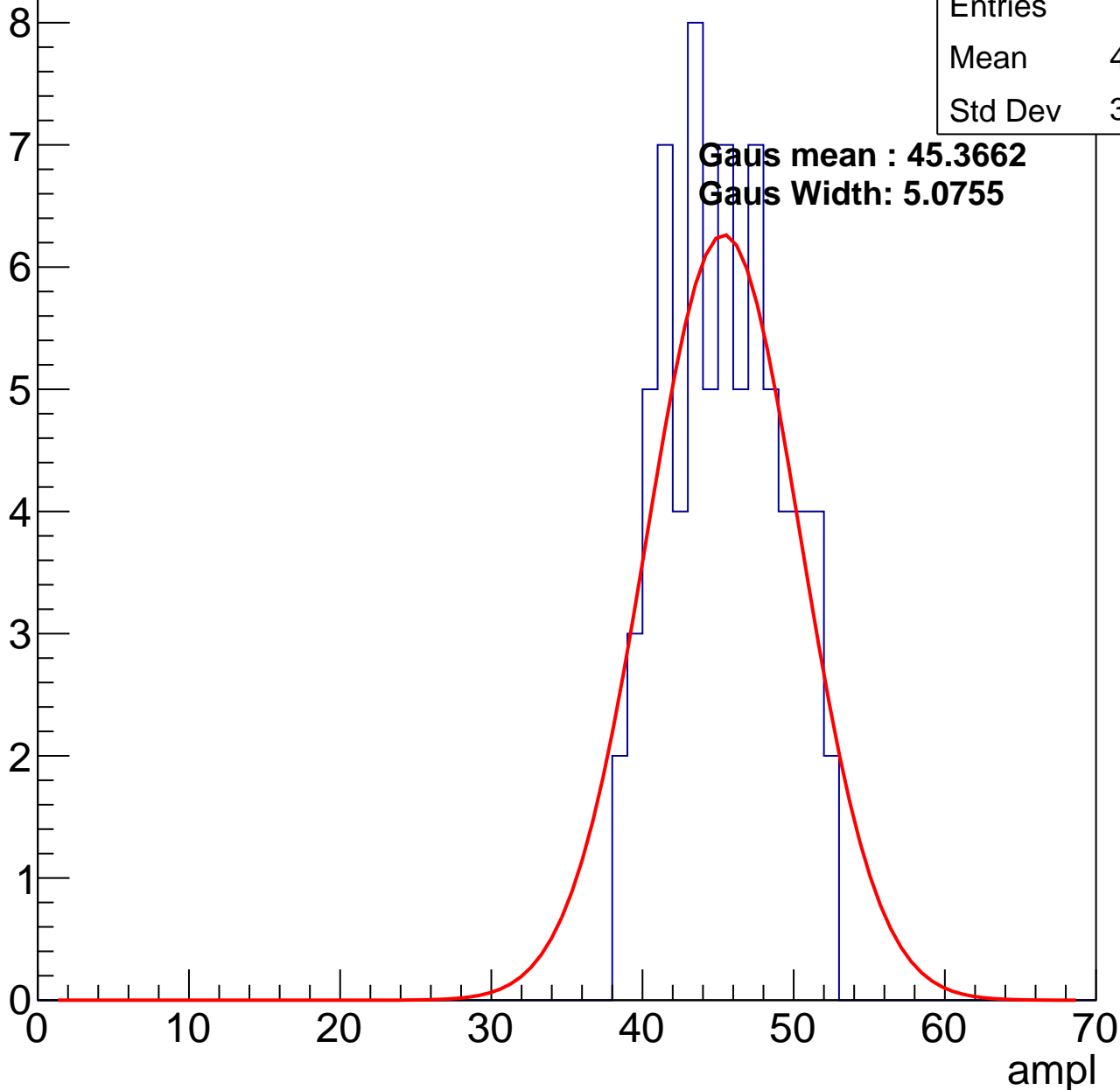
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	44.86
Std Dev	3.724

Gaus mean : 45.3662

Gaus Width: 5.0755

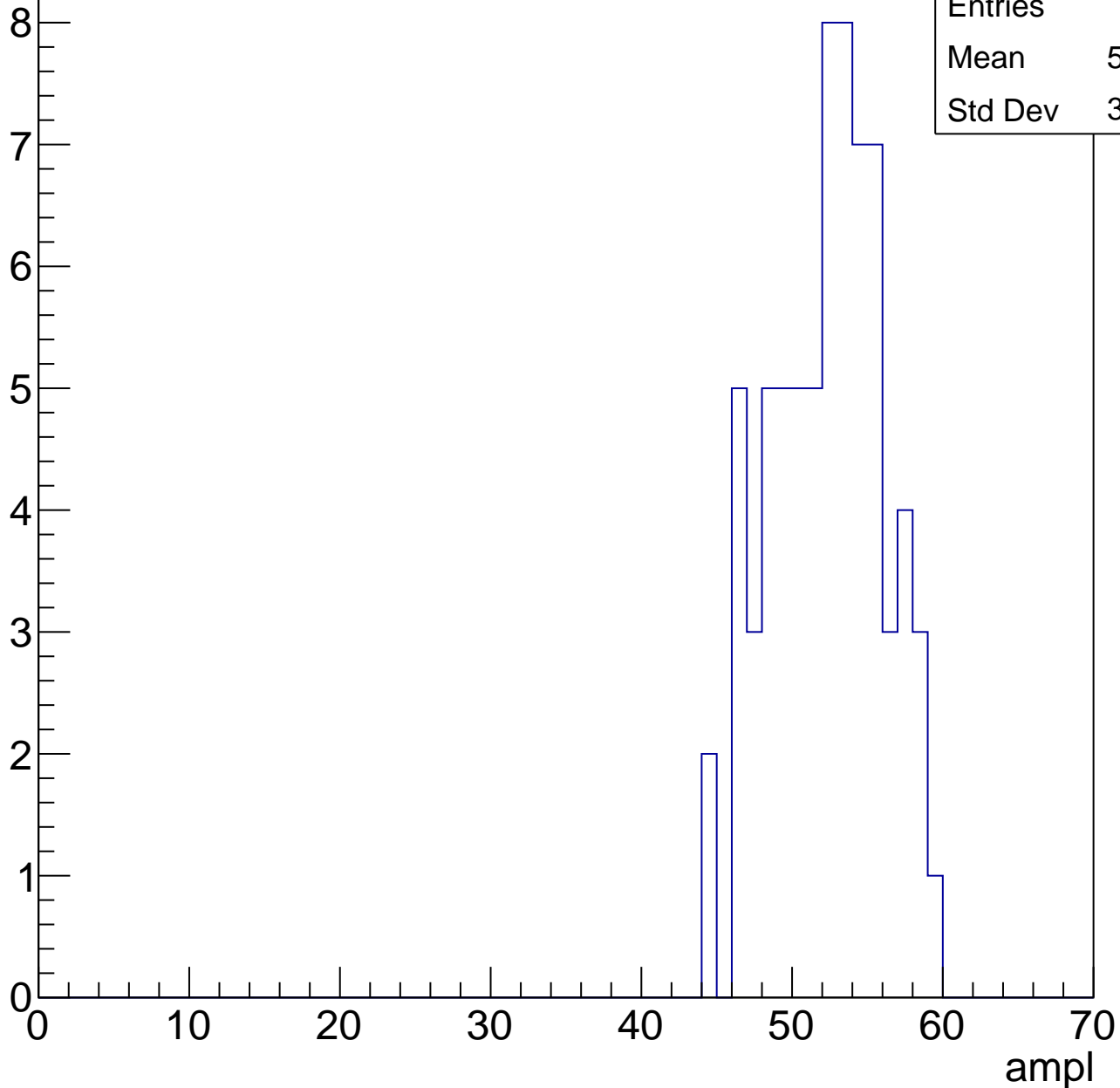


B1L101S, U23-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.85
Std Dev	3.629

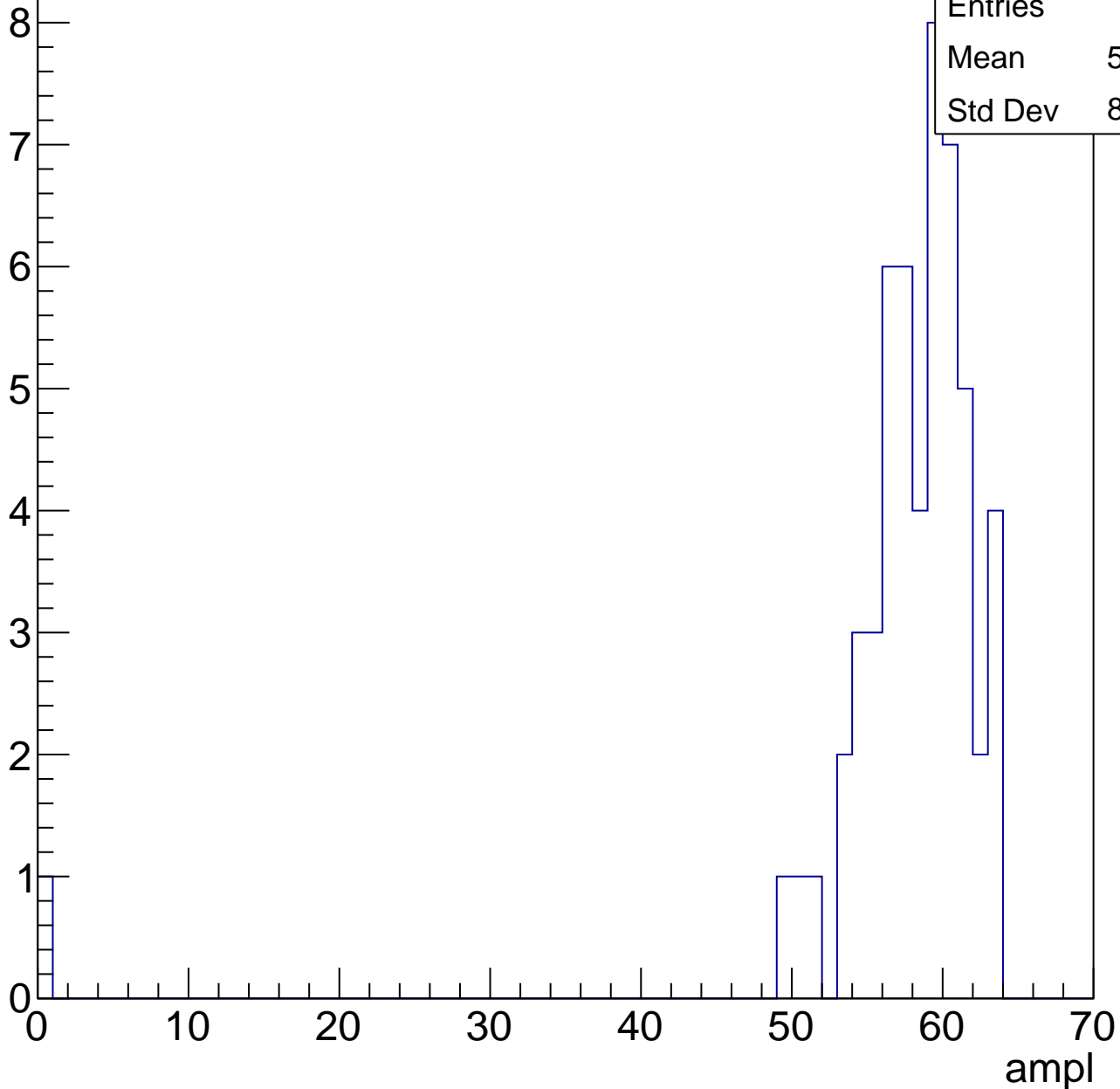


B1L101S, U23-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.78
Std Dev	8.439

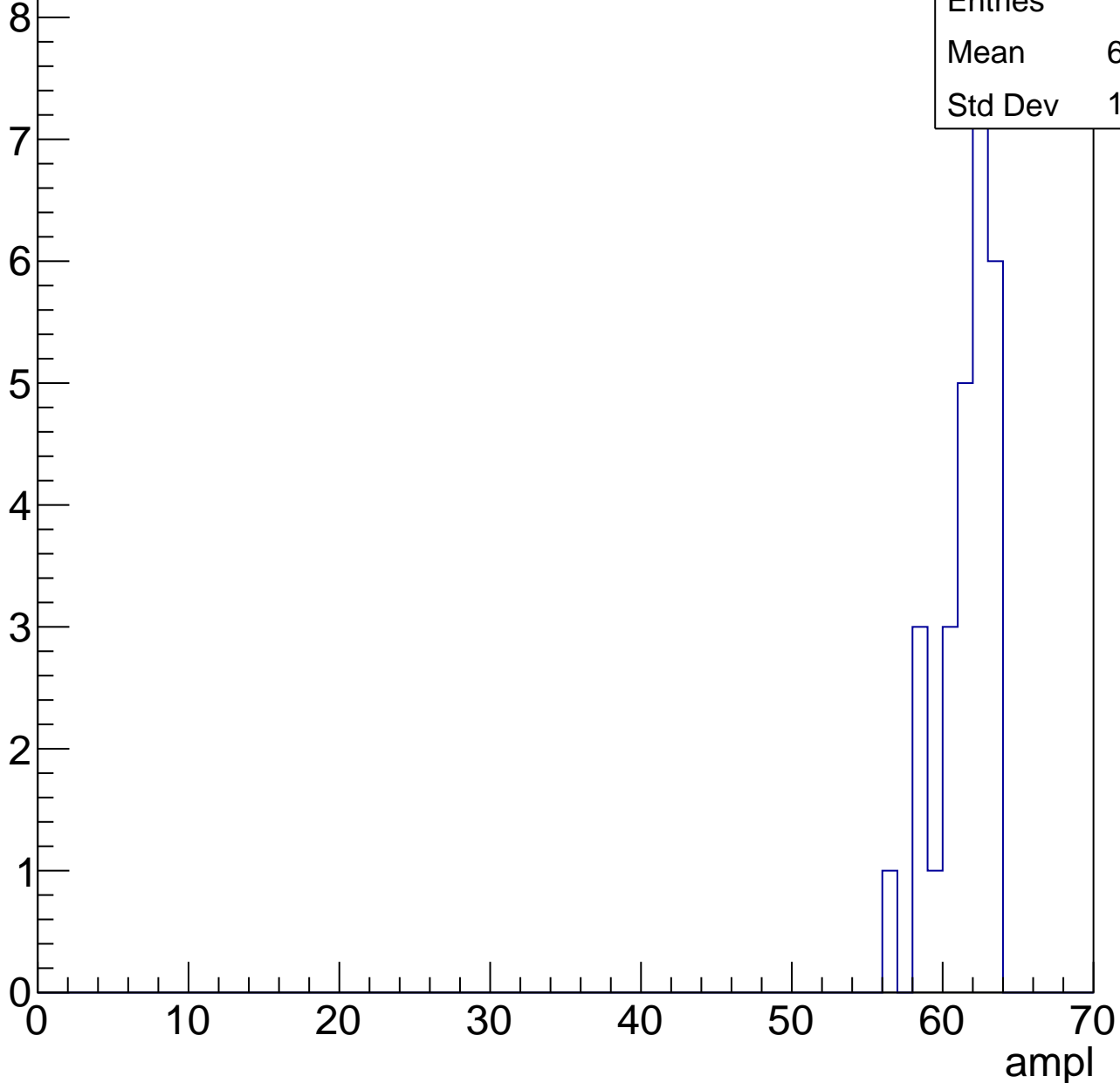


B1L101S, U23-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	61.04
Std Dev	1.835



B1L101S, U23-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch108, adc0

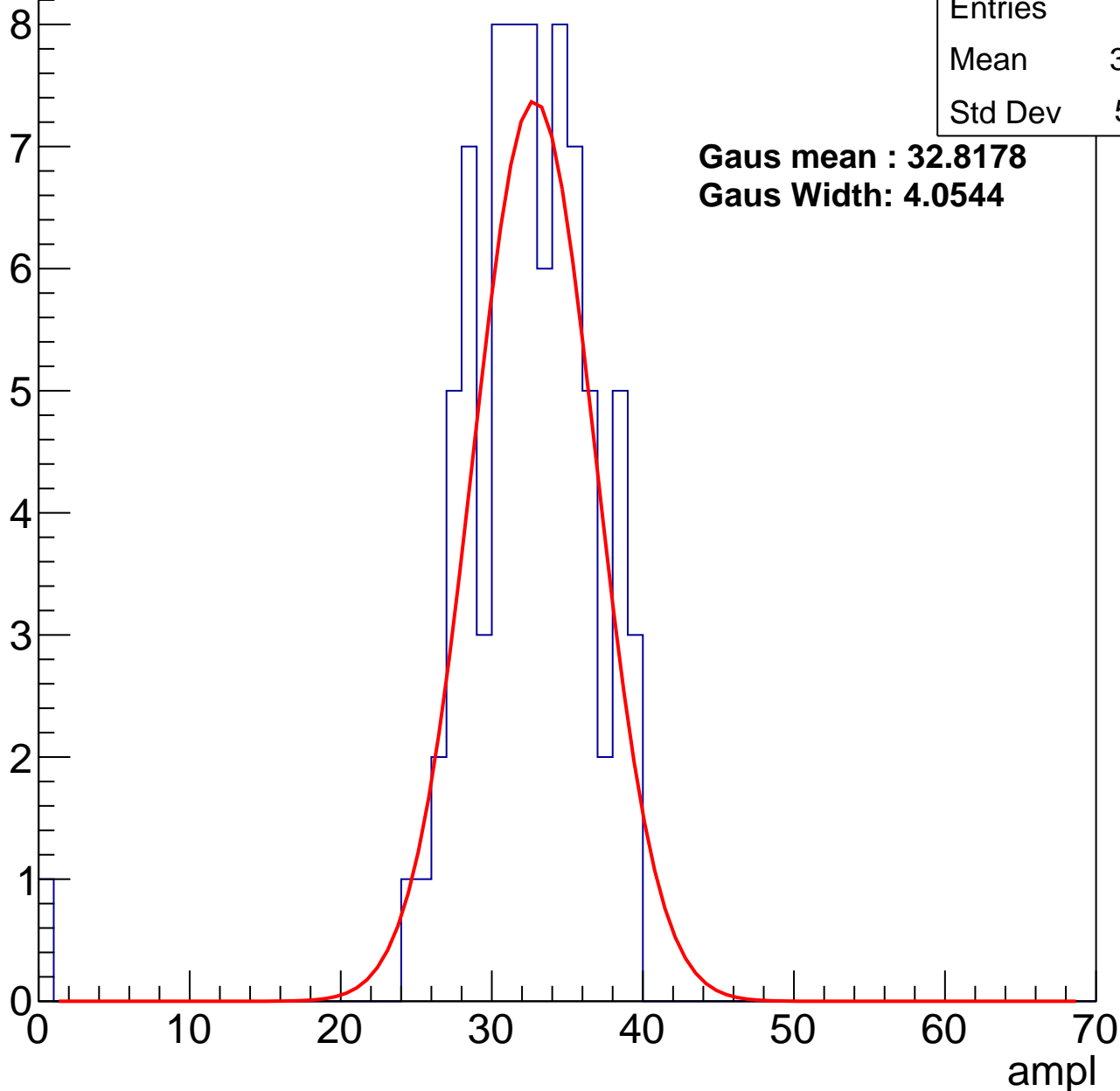
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	31.74
Std Dev	5.081

Gaus mean : 32.8178

Gaus Width: 4.0544



B1L101S, U23-ch108, adc1

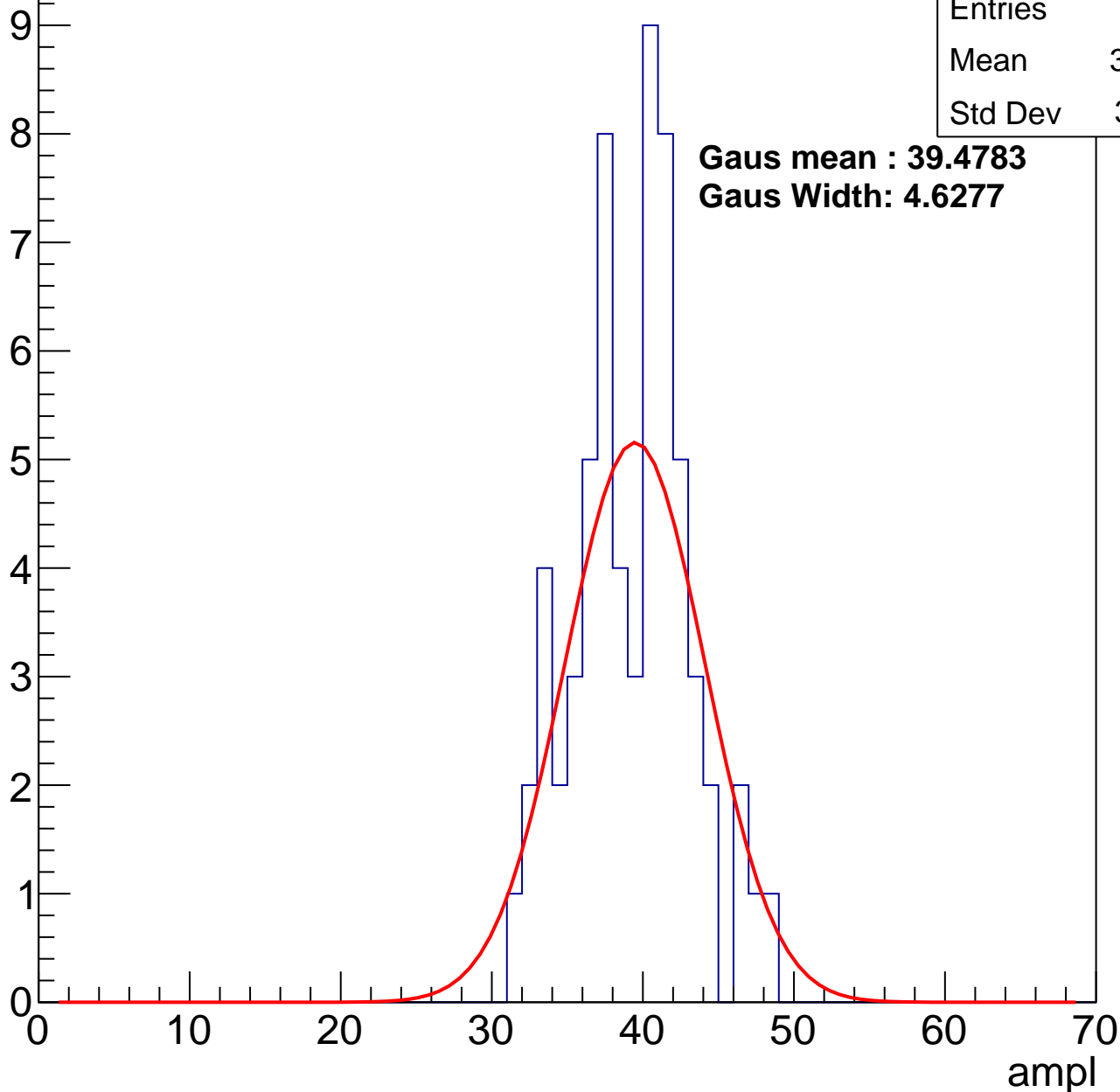
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.84
Std Dev	3.801

Gaus mean : 39.4783

Gaus Width: 4.6277



B1L101S, U23-ch108, adc2

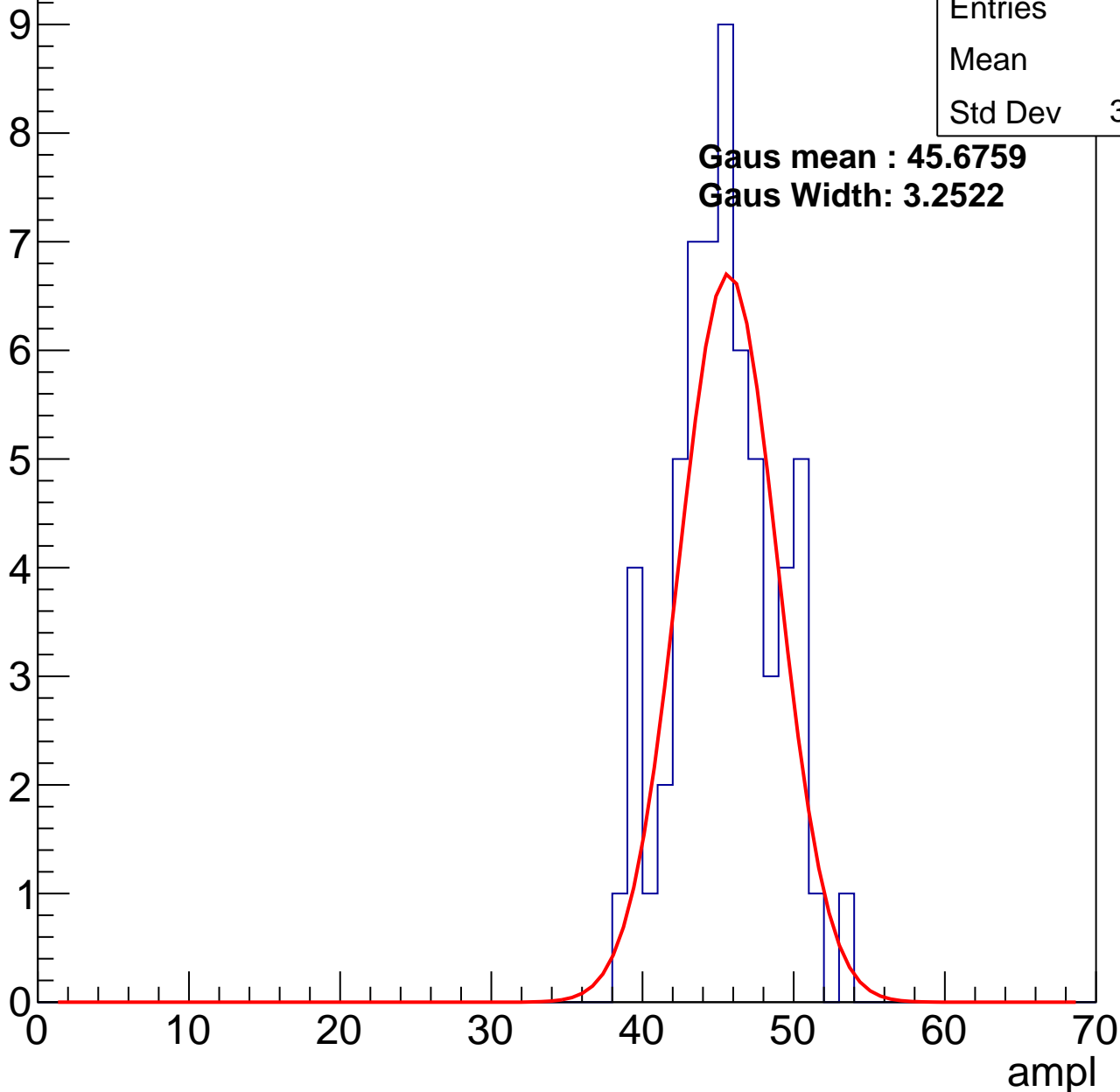
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	45
Std Dev	3.319

Gaus mean : 45.6759

Gaus Width: 3.2522



B1L101S, U23-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

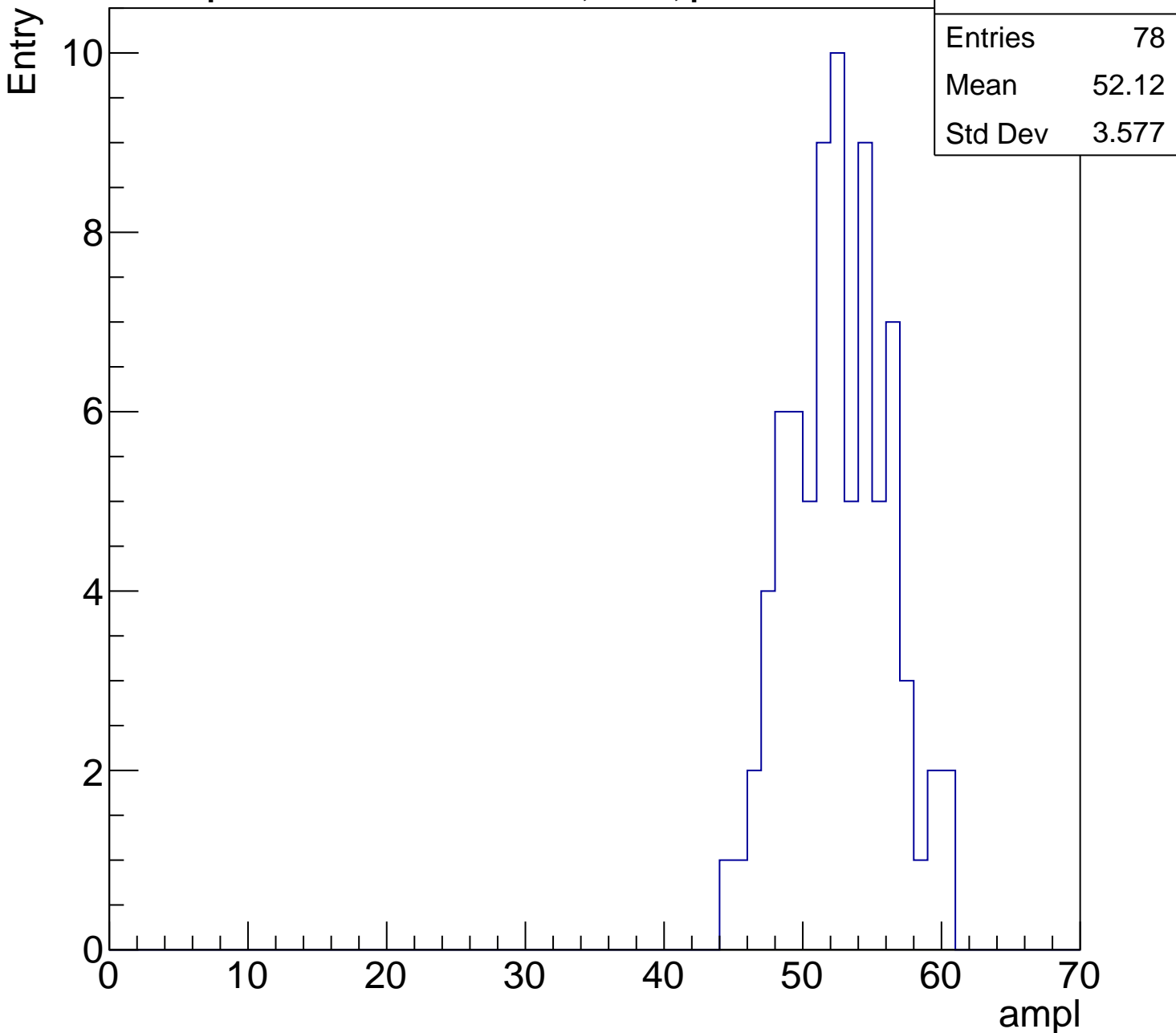
Entries	78
Mean	52.12
Std Dev	3.577

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

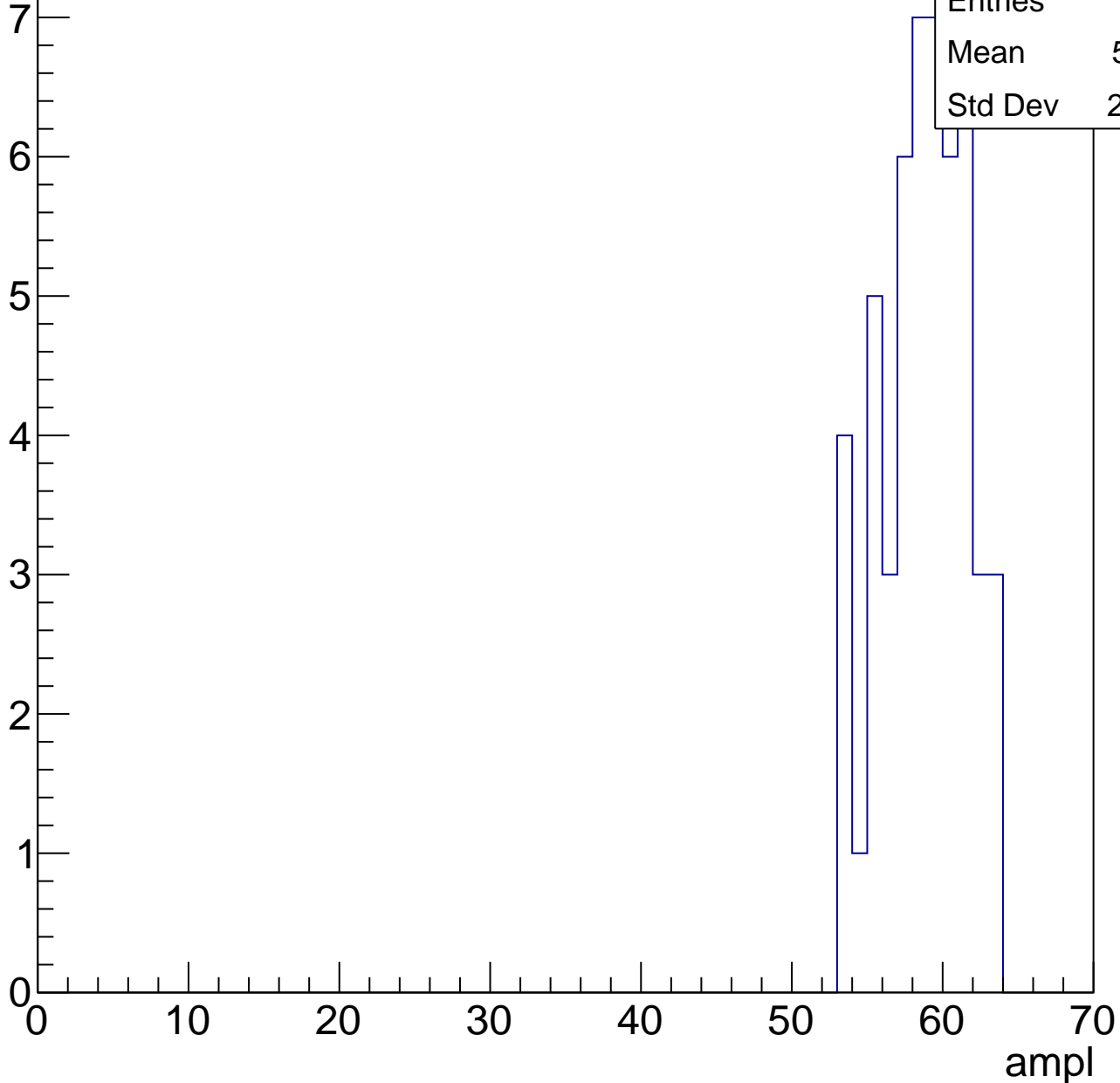


B1L101S, U23-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	58.31
Std Dev	2.742

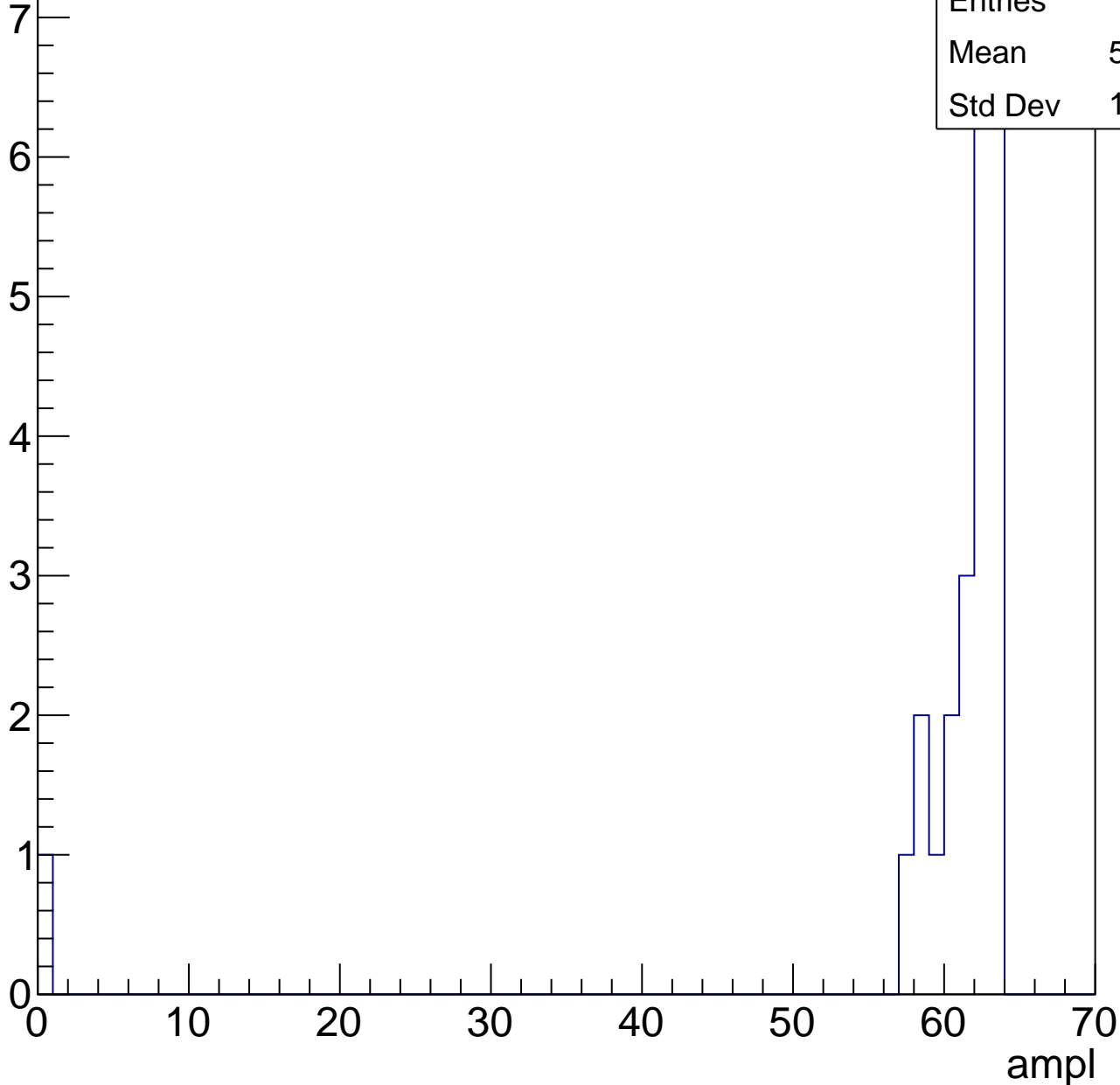


B1L101S, U23-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	58.75
Std Dev	12.37



B1L101S, U23-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch109, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	32.06
Std Dev	6.531

Gaus mean : 32.9329

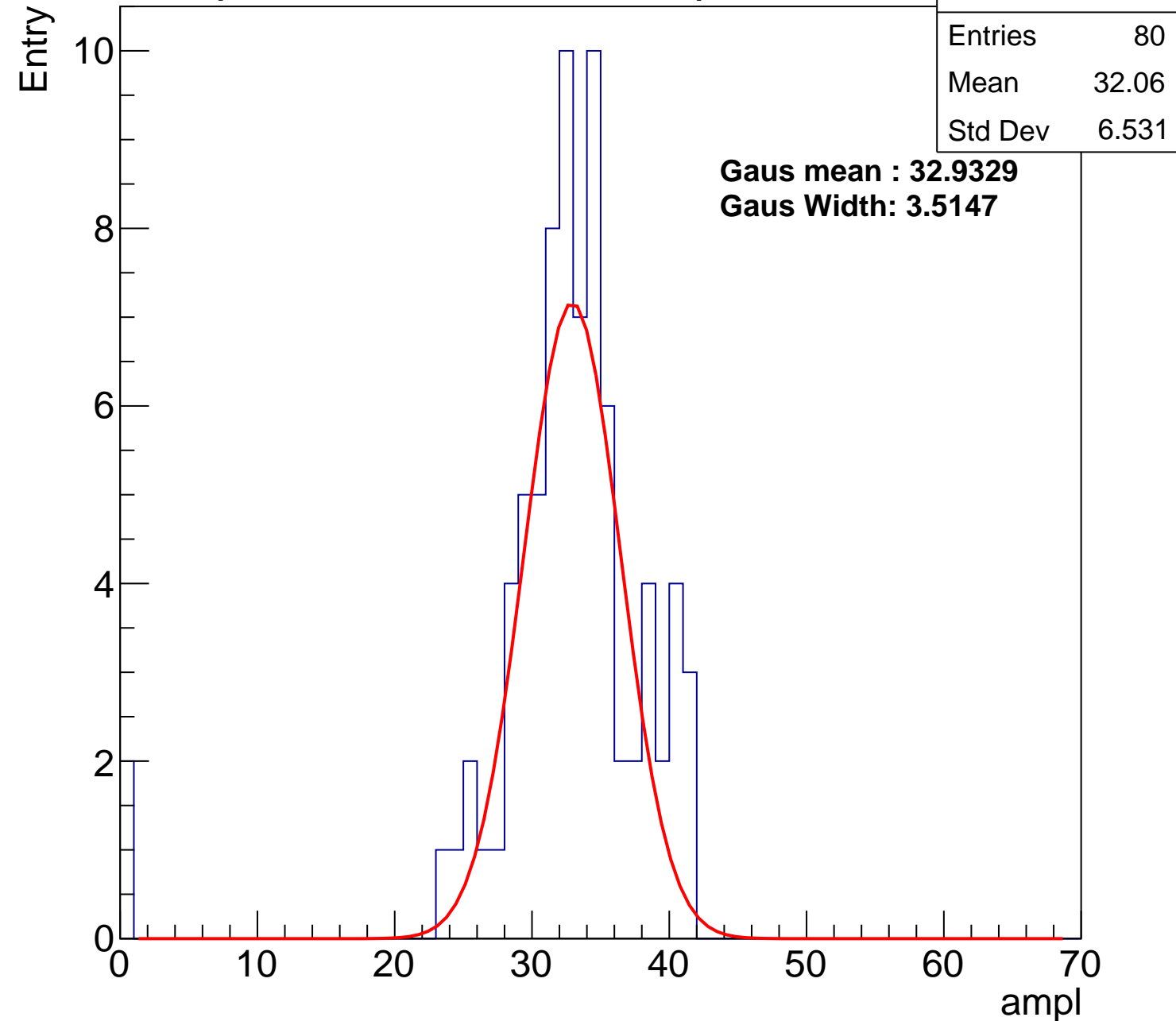
Gaus Width: 3.5147

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch109, adc1

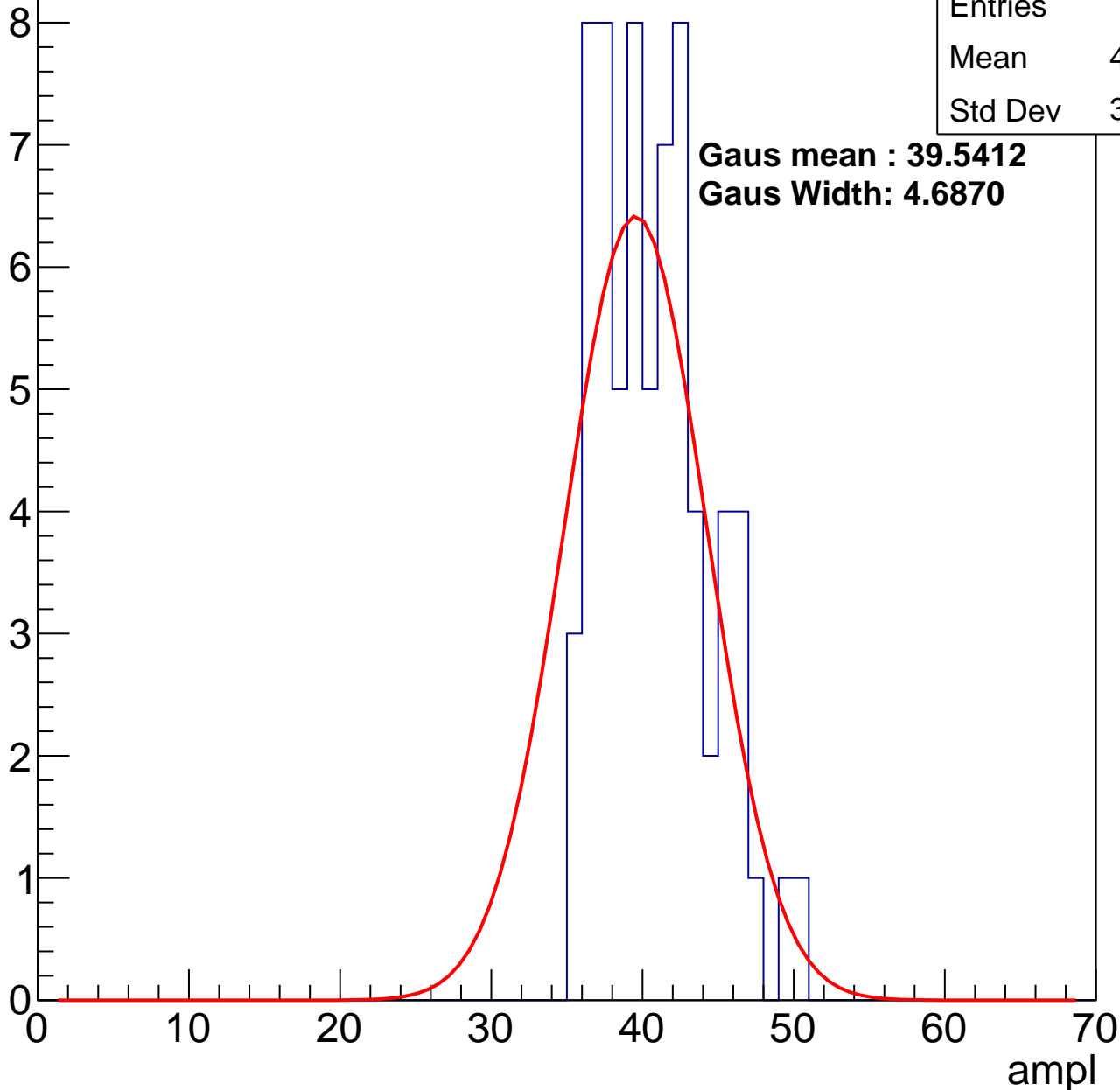
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	40.35
Std Dev	3.562

Gaus mean : 39.5412

Gaus Width: 4.6870



B1L101S, U23-ch109, adc2

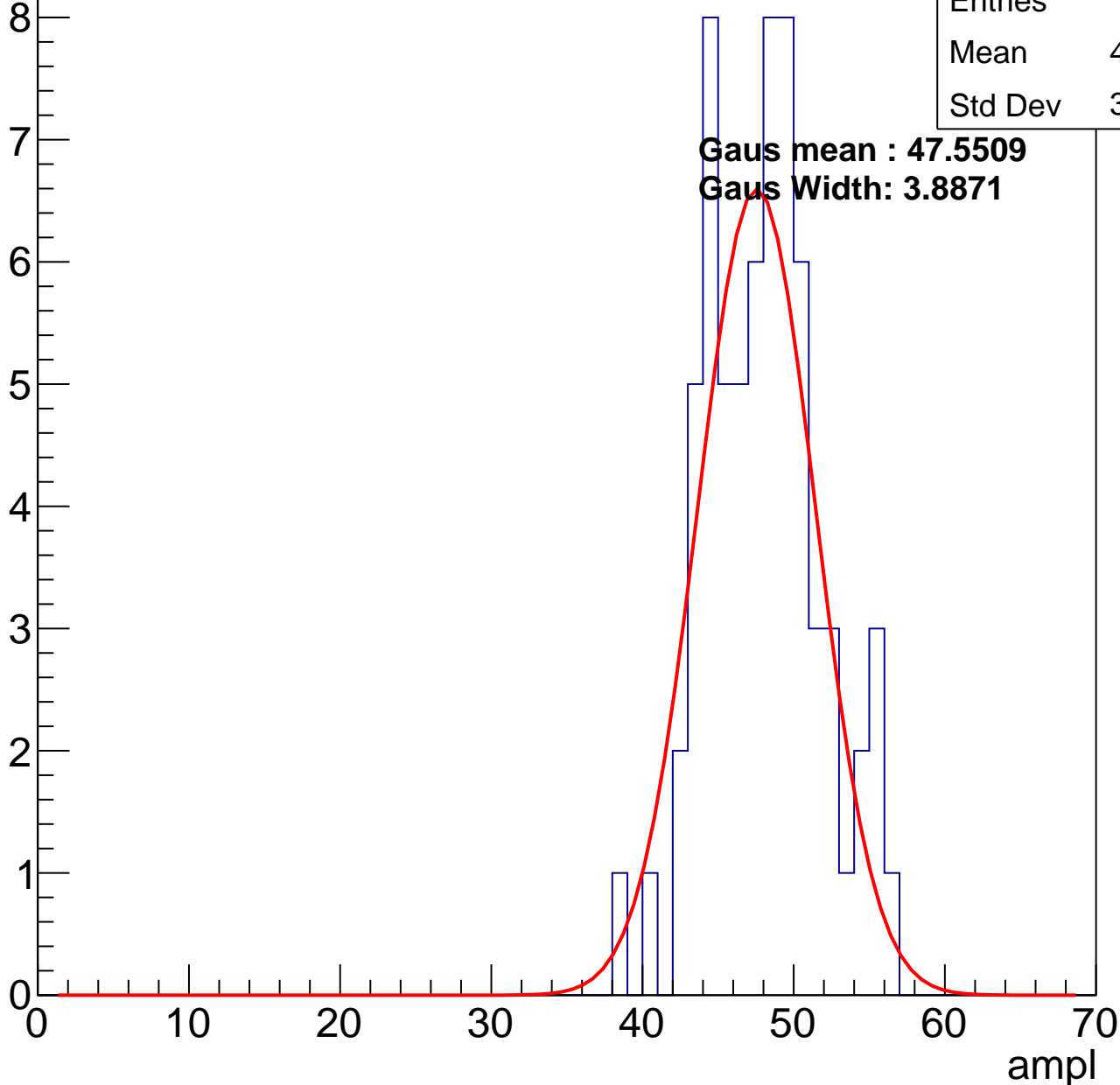
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	47.54
Std Dev	3.759

Gaus mean : 47.5509

Gaus Width: 3.8871

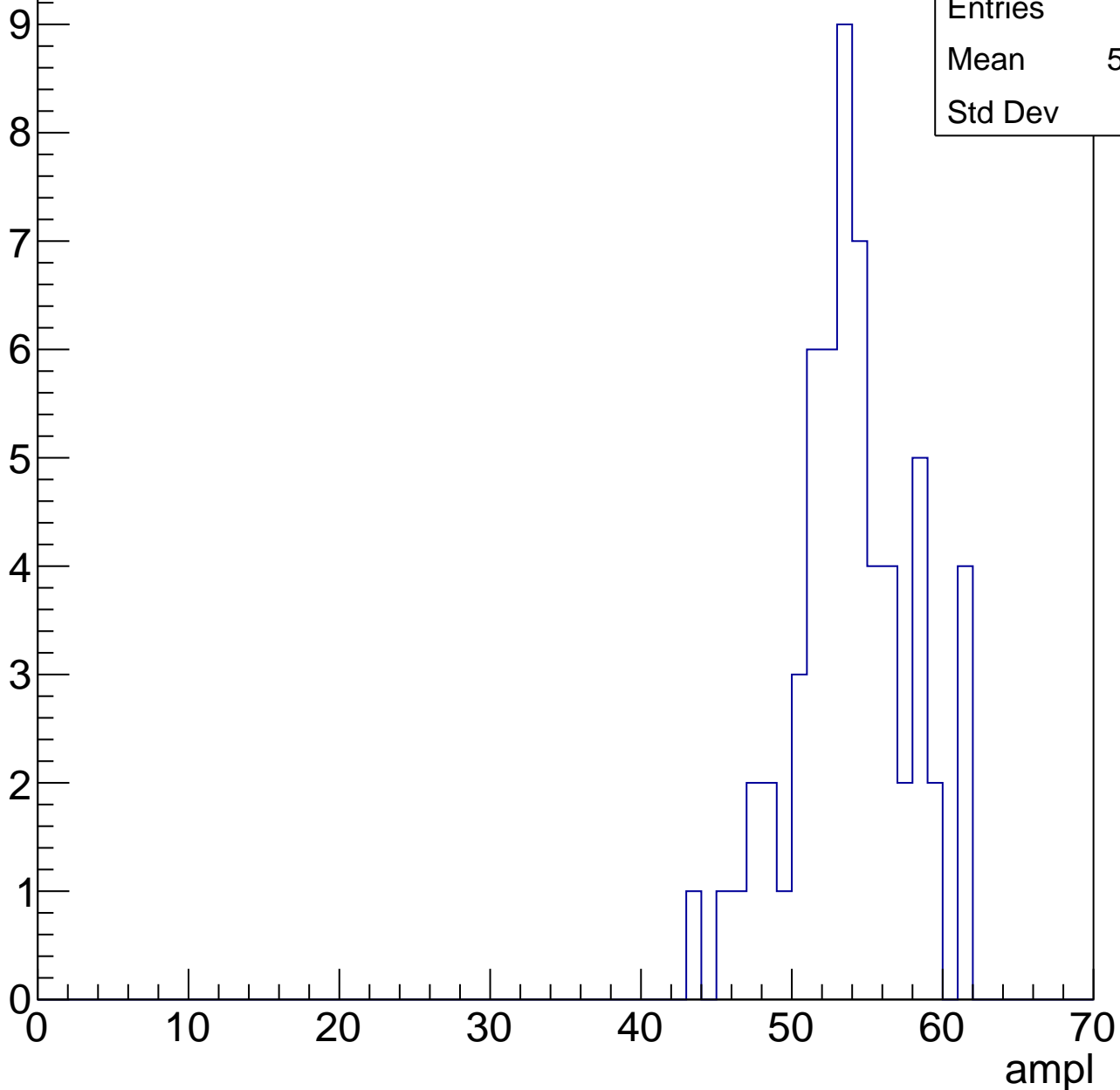


B1L101S, U23-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

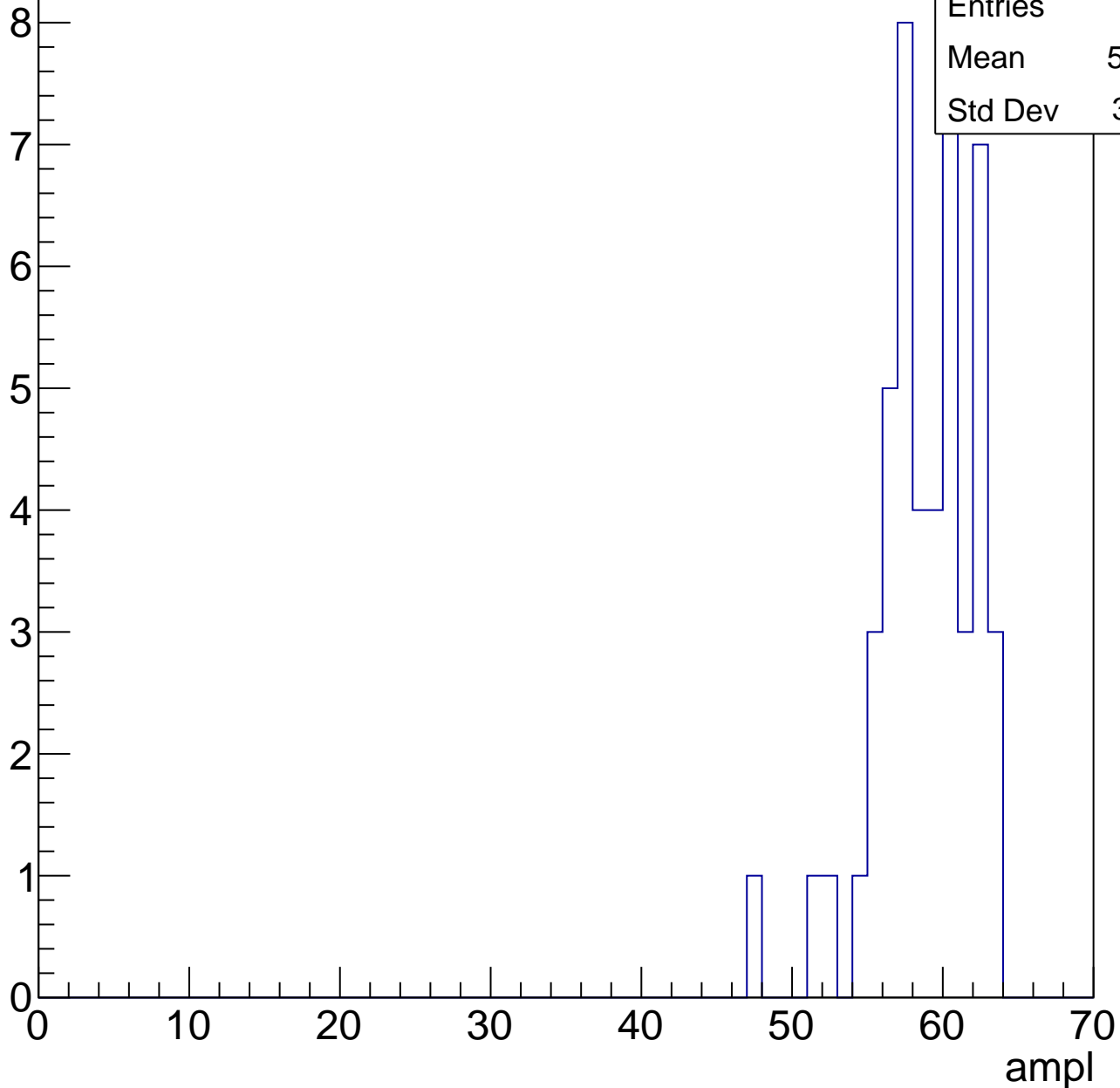
Entries	60
Mean	53.43
Std Dev	3.96



B1L101S, U23-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

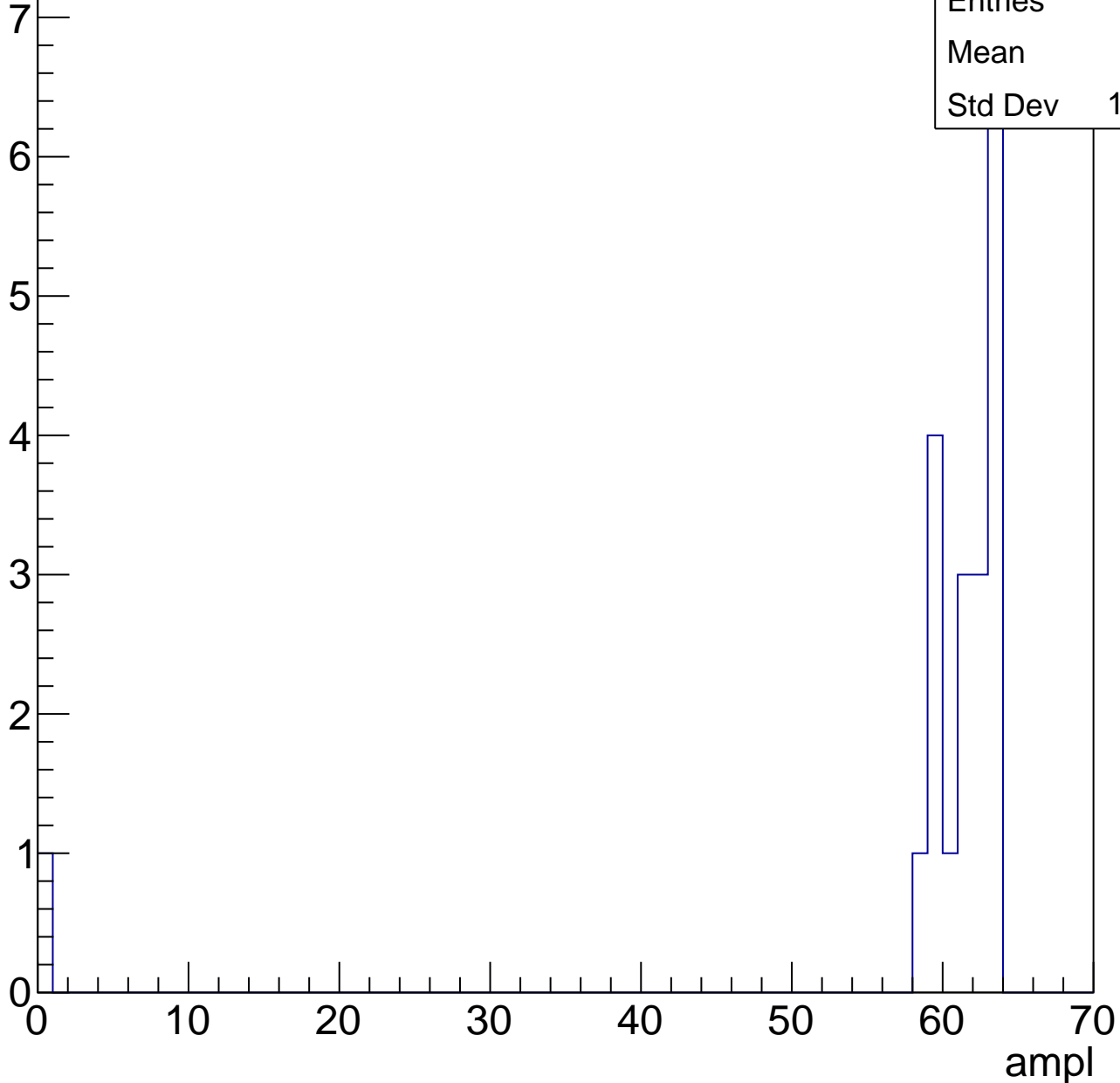


B1L101S, U23-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	58.2
Std Dev	13.46



B1L101S, U23-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U23-ch110, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	28.86
Std Dev	6.058

Gaus mean : 30.6049

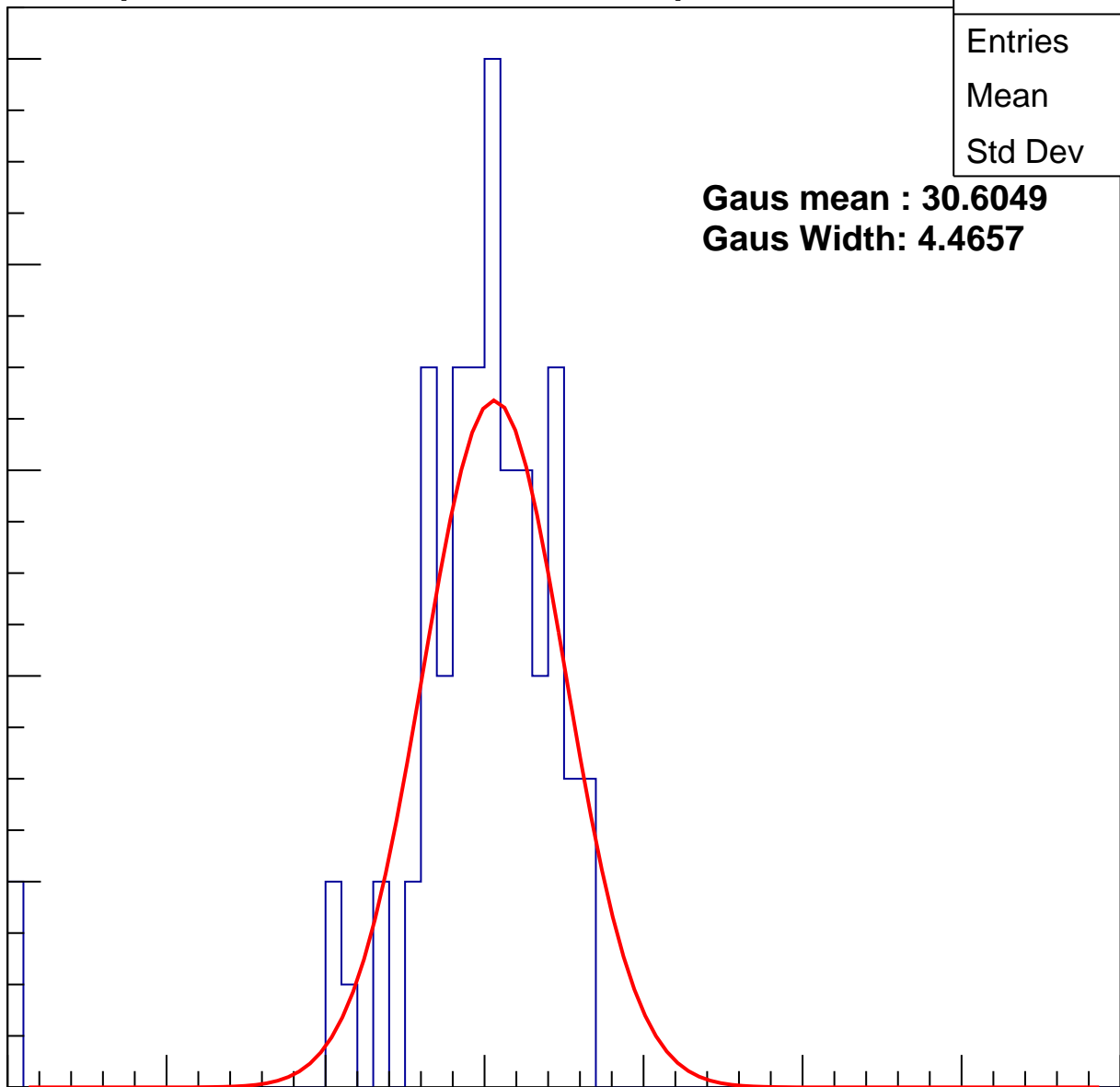
Gaus Width: 4.4657

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch110, adc1

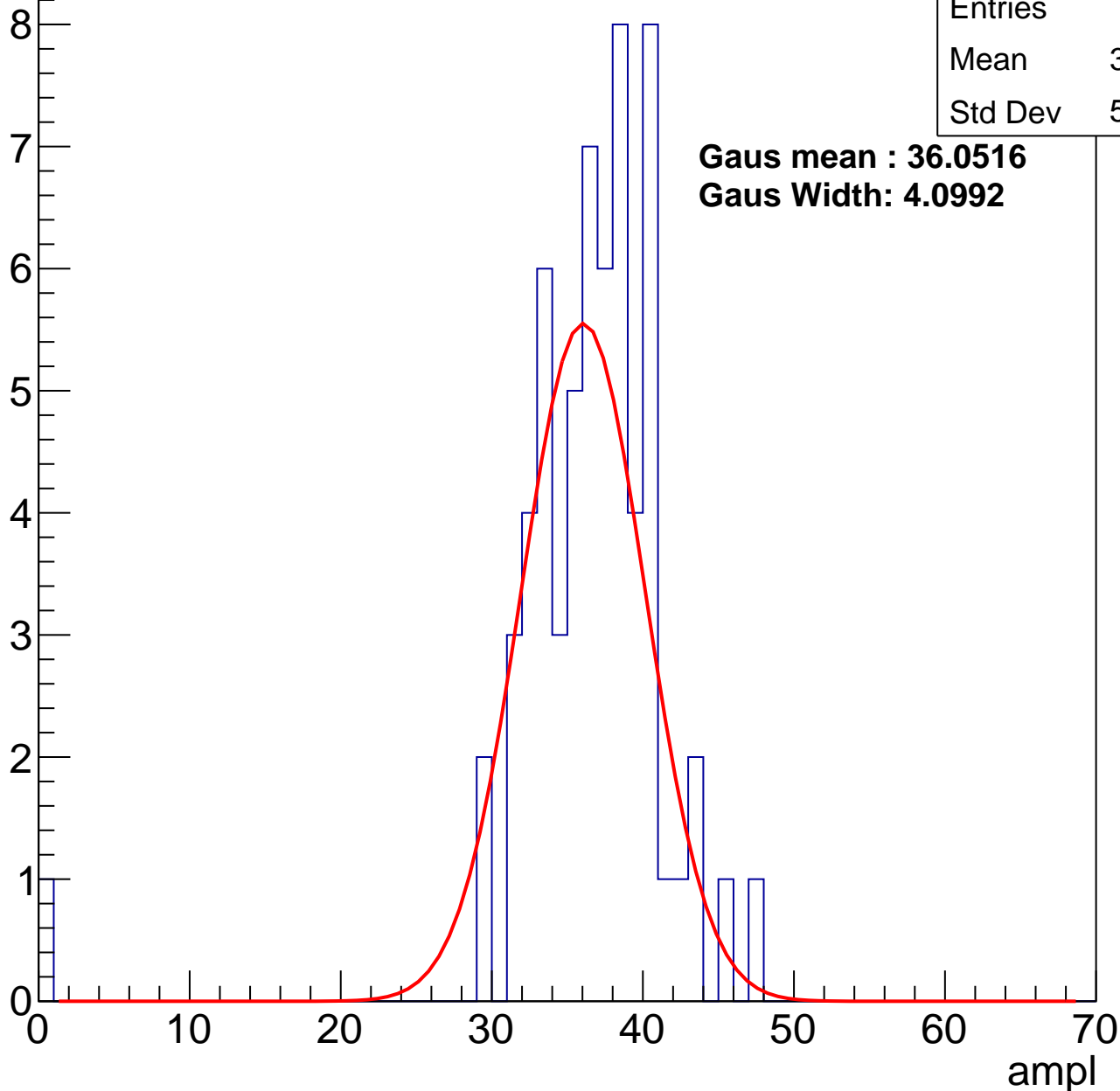
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.02
Std Dev	5.865

Gaus mean : 36.0516

Gaus Width: 4.0992



B1L101S, U23-ch110, adc2

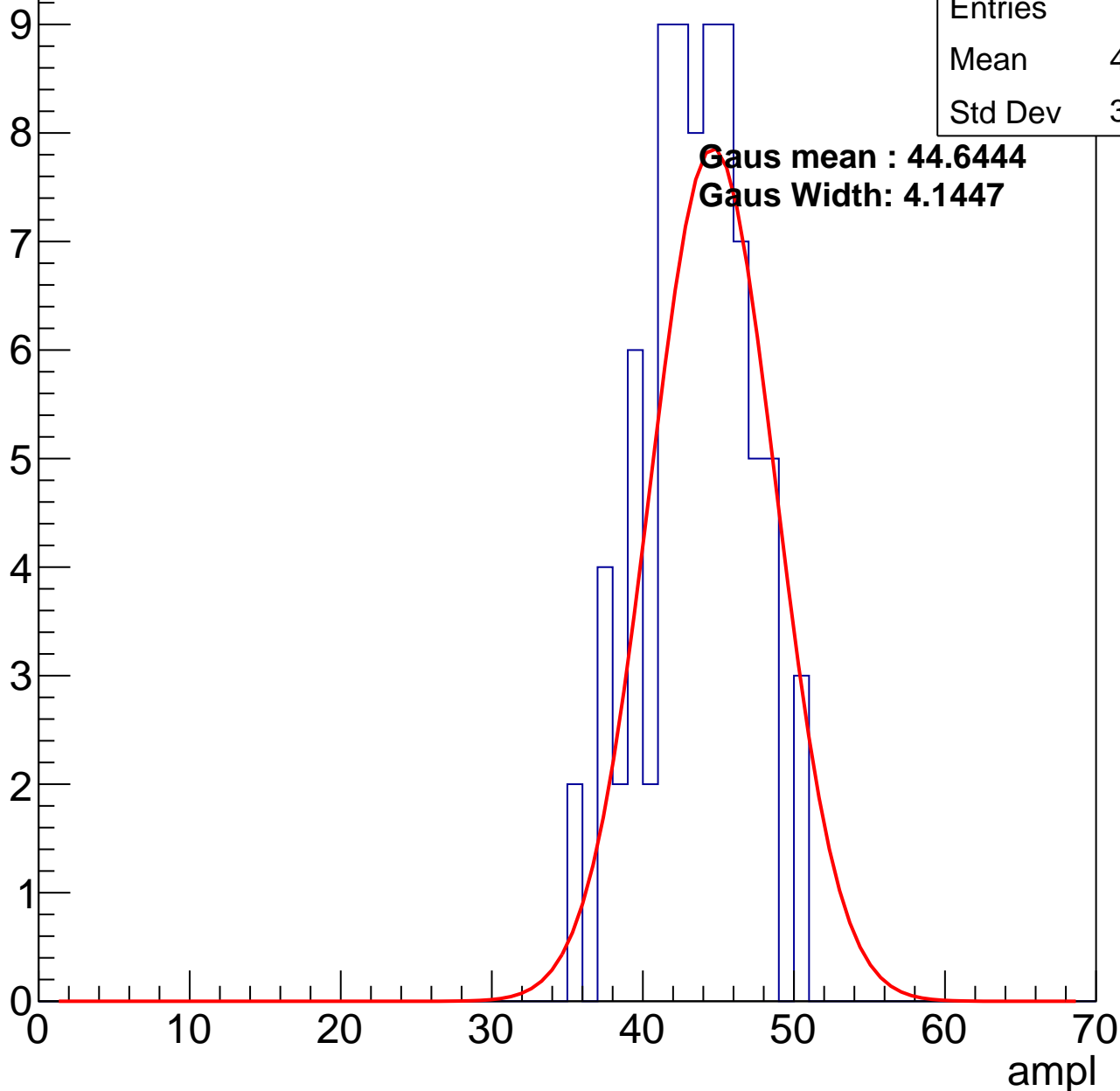
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	43.09
Std Dev	3.429

Gaus mean : 44.6444

Gaus Width: 4.1447

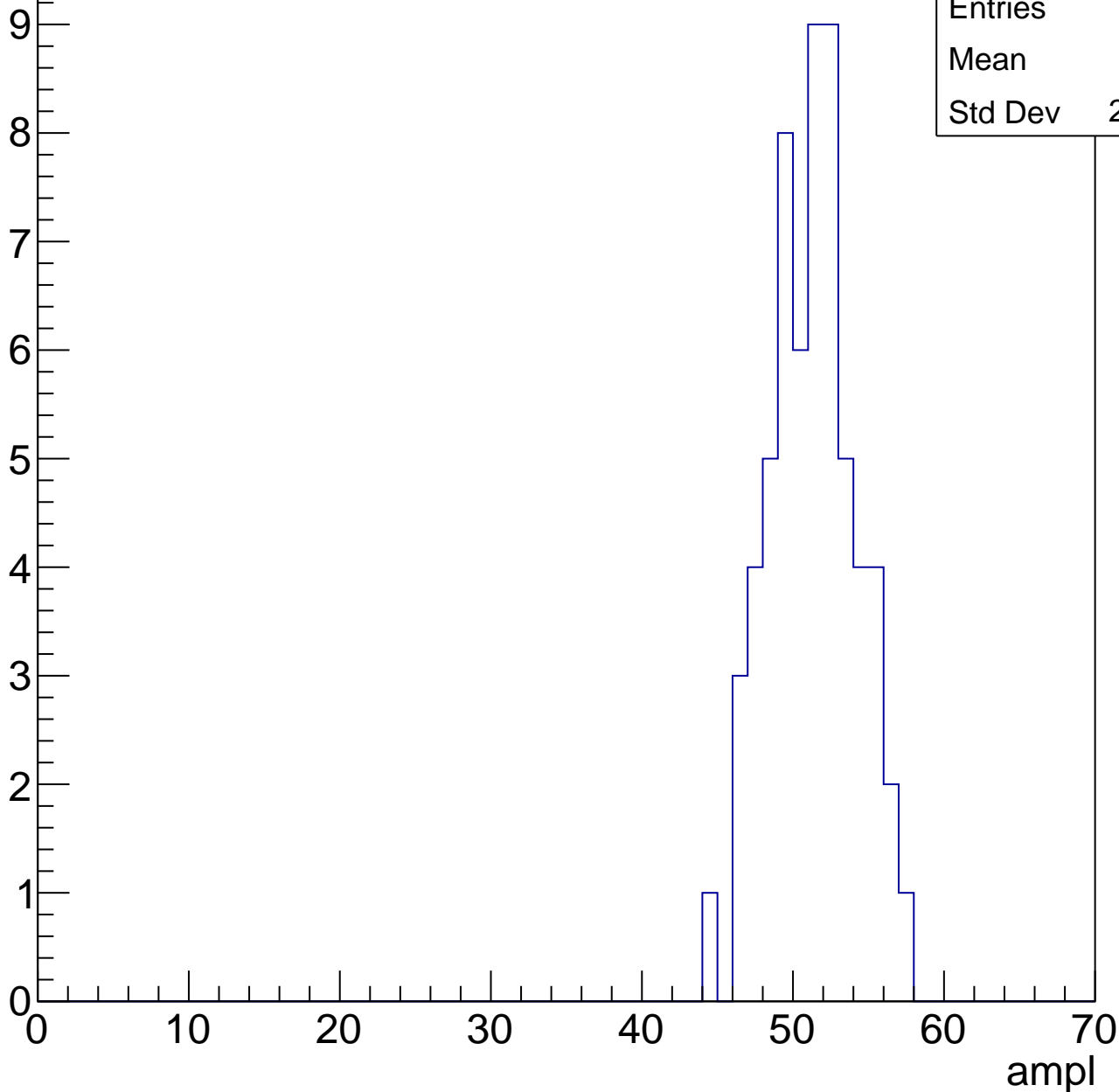


B1L101S, U23-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

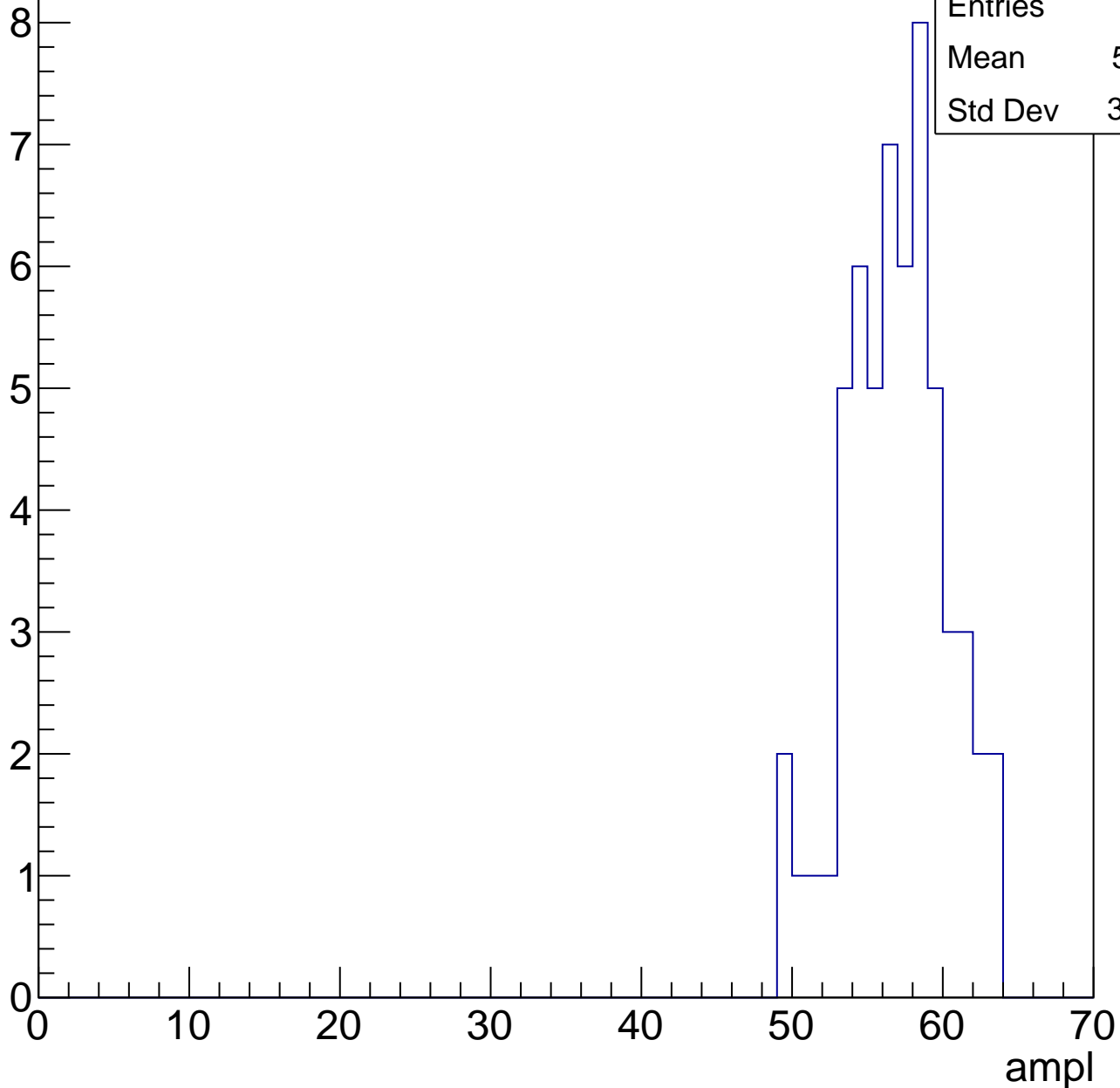
Entries	61
Mean	50.8
Std Dev	2.816



B1L101S, U23-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



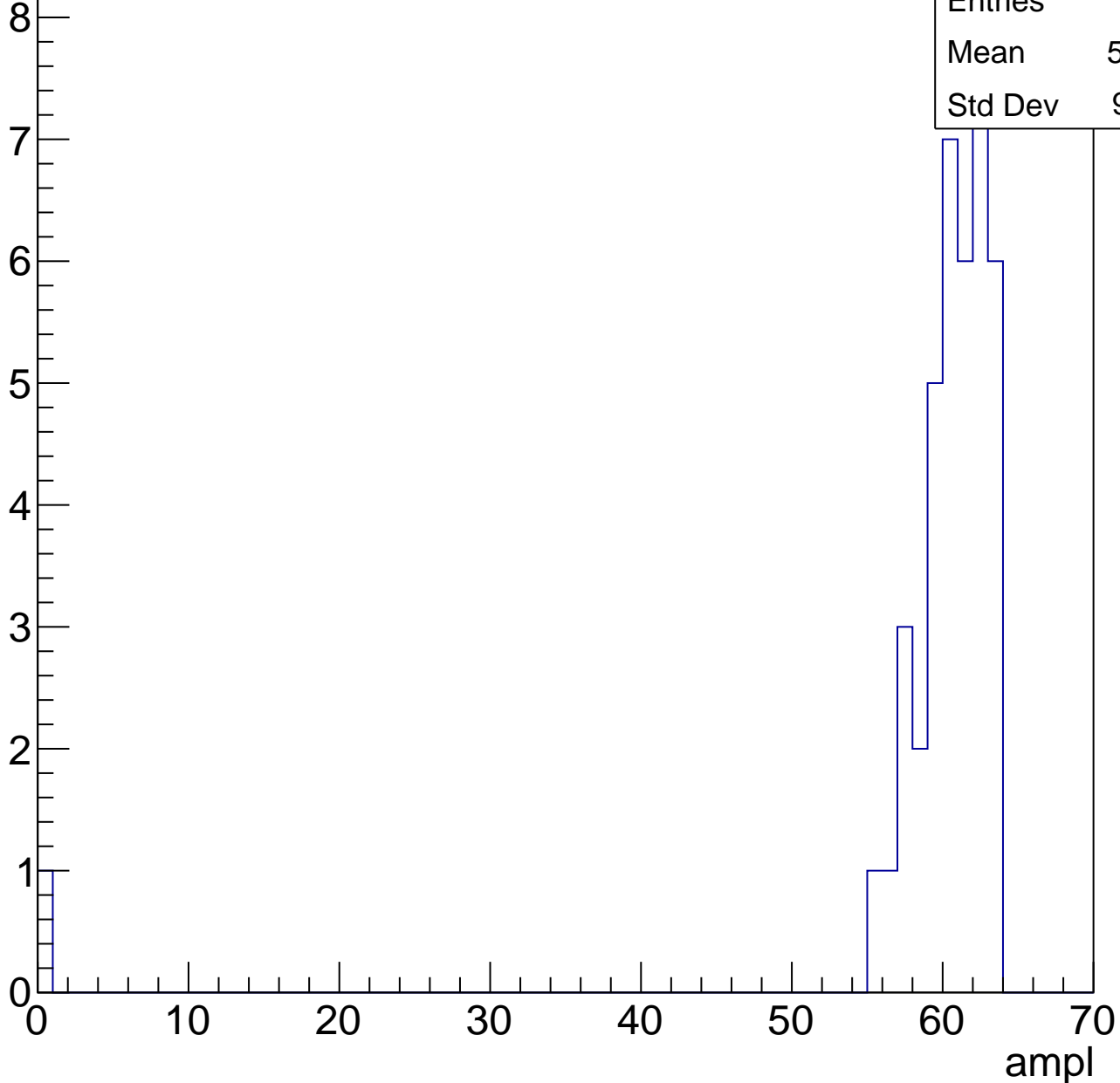
Entries	57
Mean	56.51
Std Dev	3.272

B1L101S, U23-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

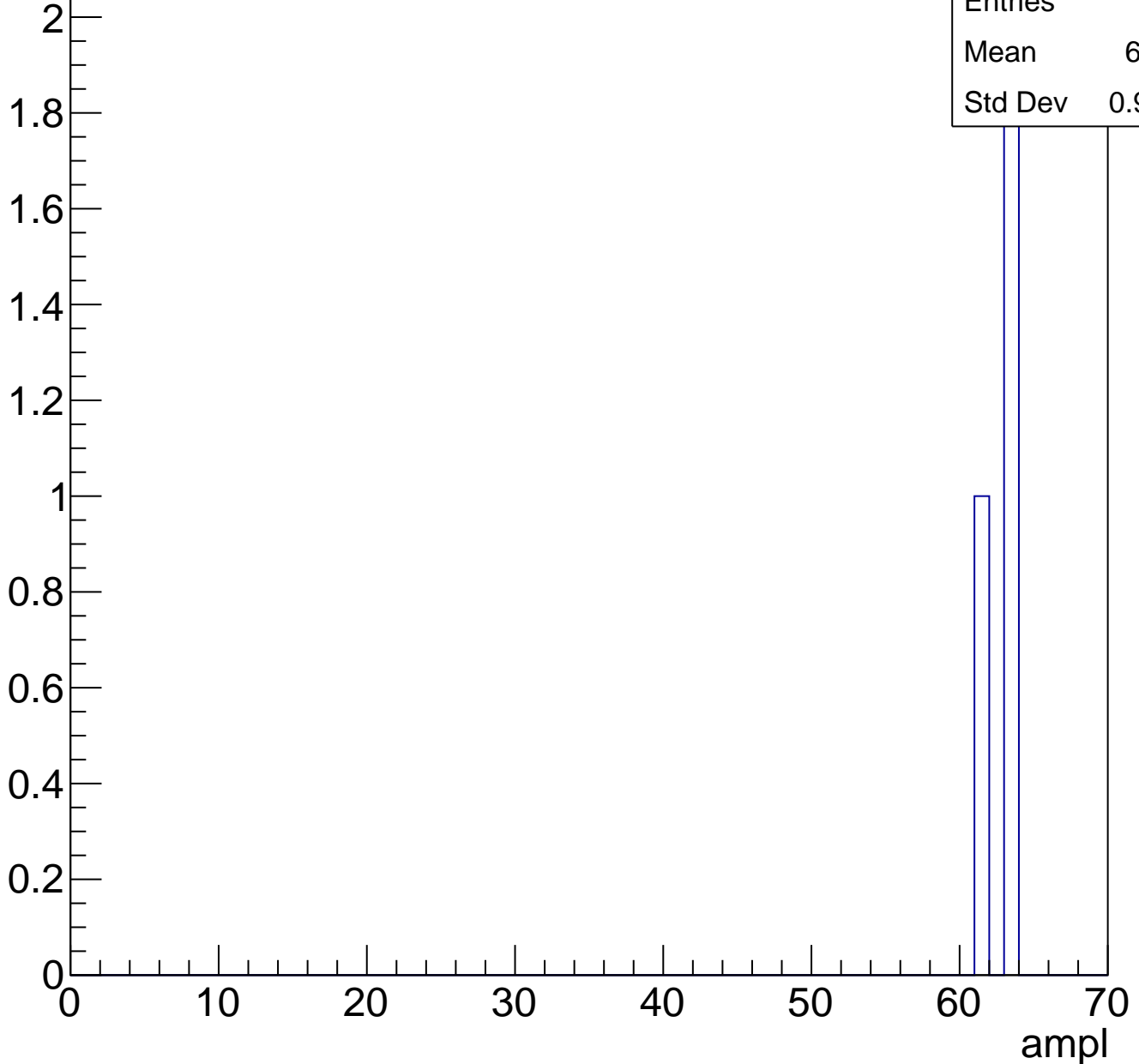
Entries	40
Mean	58.83
Std Dev	9.641



B1L101S, U23-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch111, adc0

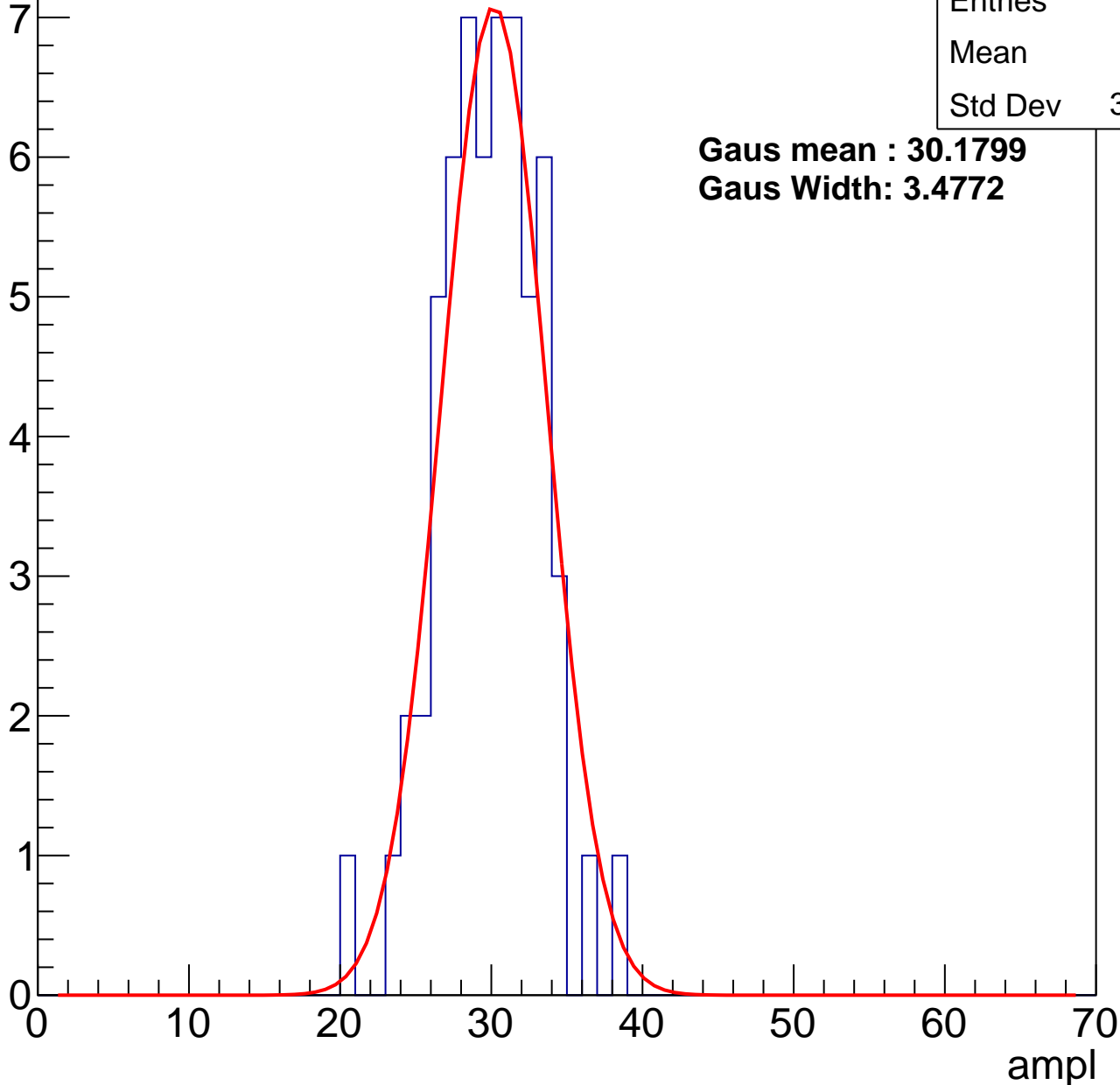
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.4
Std Dev	3.282

Gaus mean : 30.1799

Gaus Width: 3.4772



B1L101S, U23-ch111, adc1

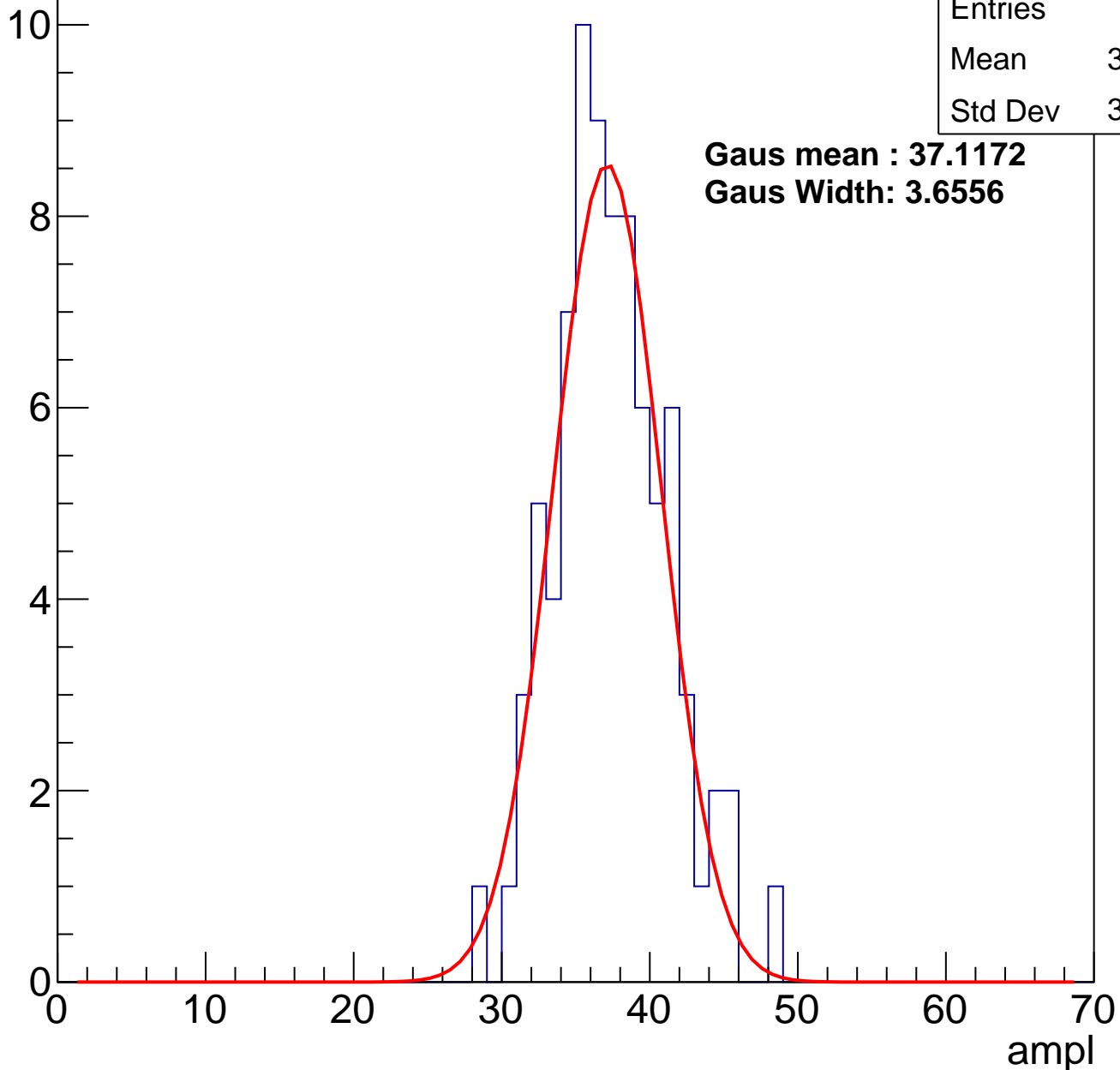
calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	36.95
Std Dev	3.777

Gaus mean : 37.1172

Gaus Width: 3.6556

Entry



B1L101S, U23-ch111, adc2

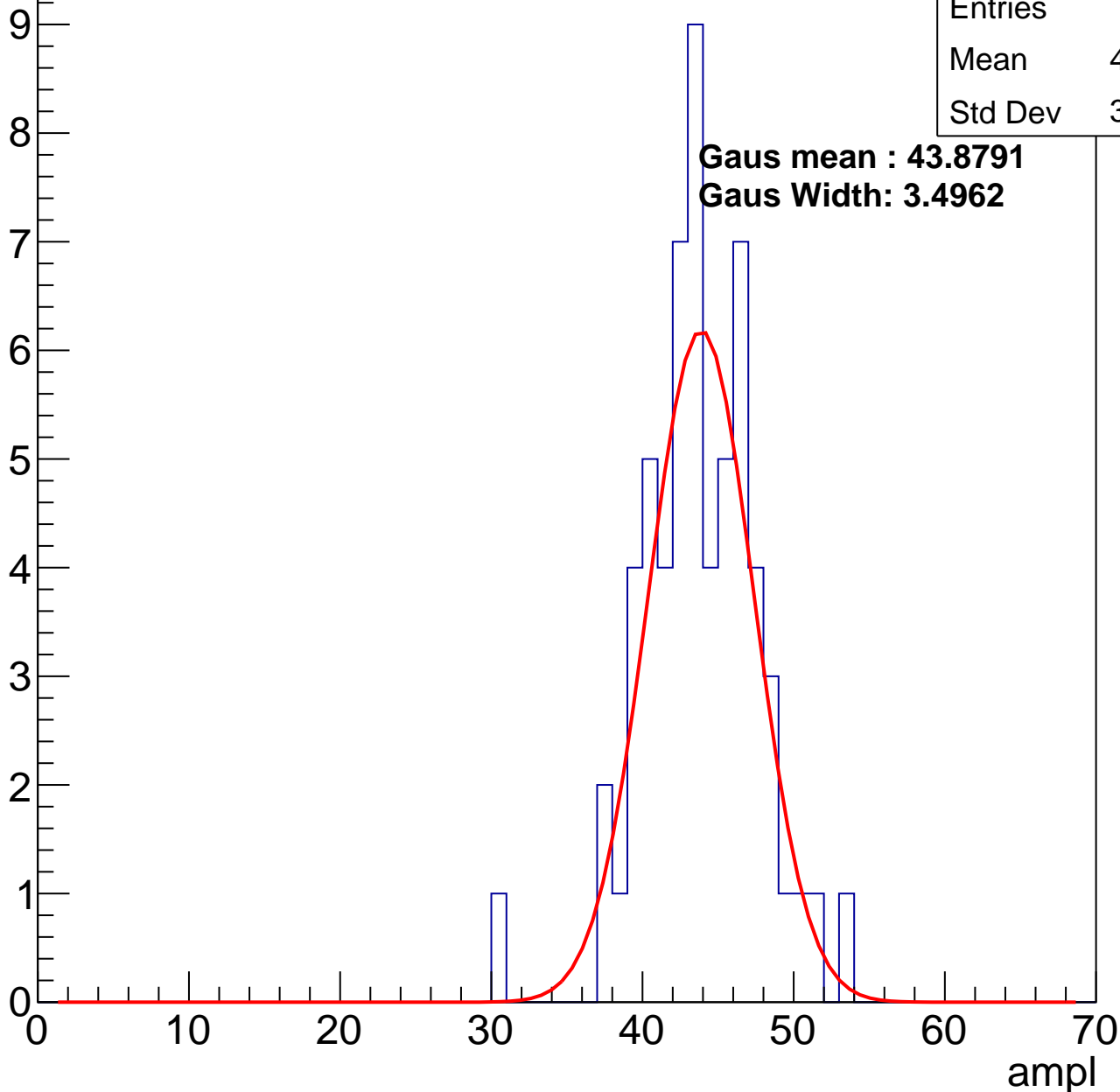
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.35
Std Dev	3.798

Gaus mean : 43.8791

Gaus Width: 3.4962

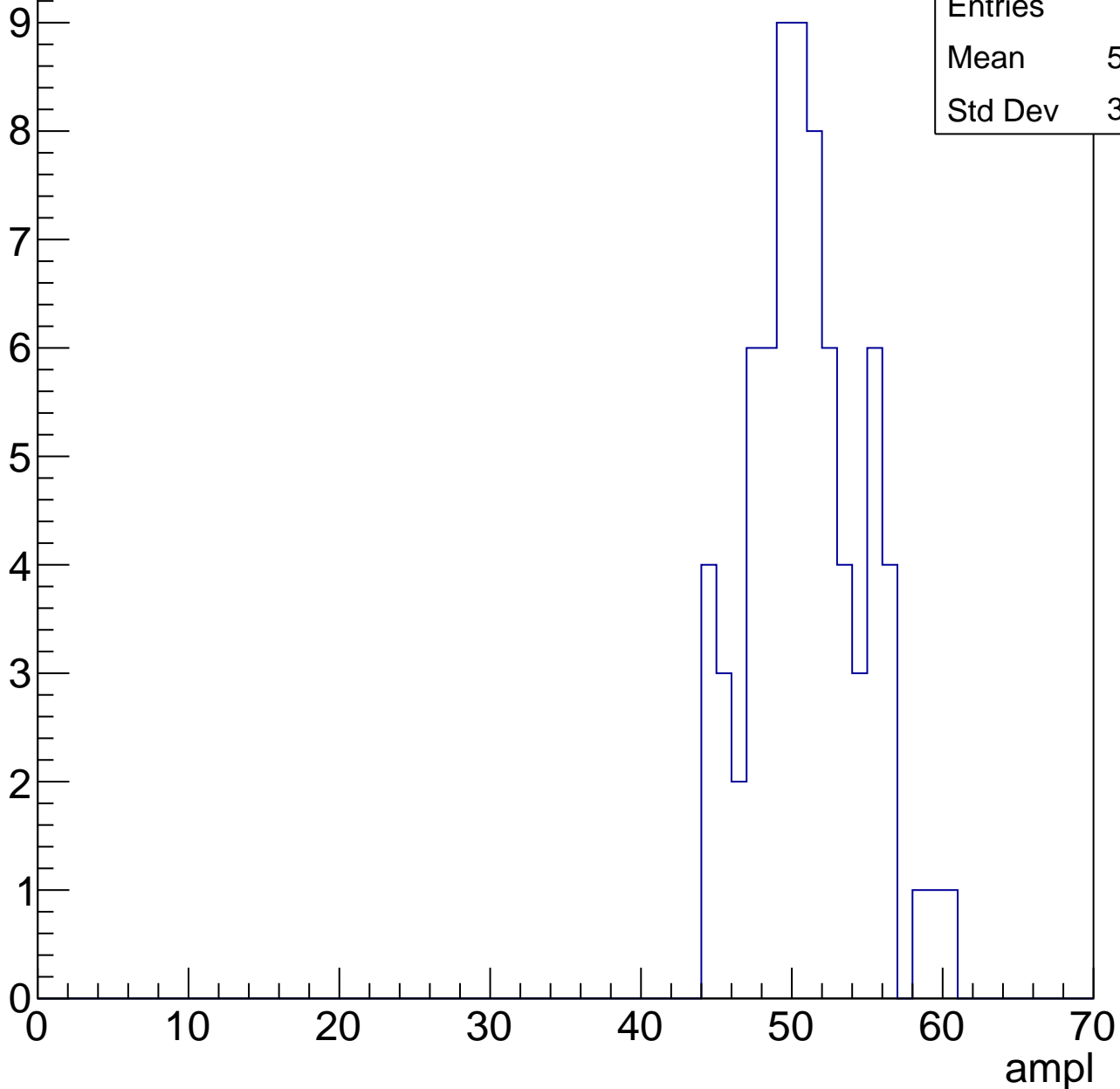


B1L101S, U23-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	50.53
Std Dev	3.649

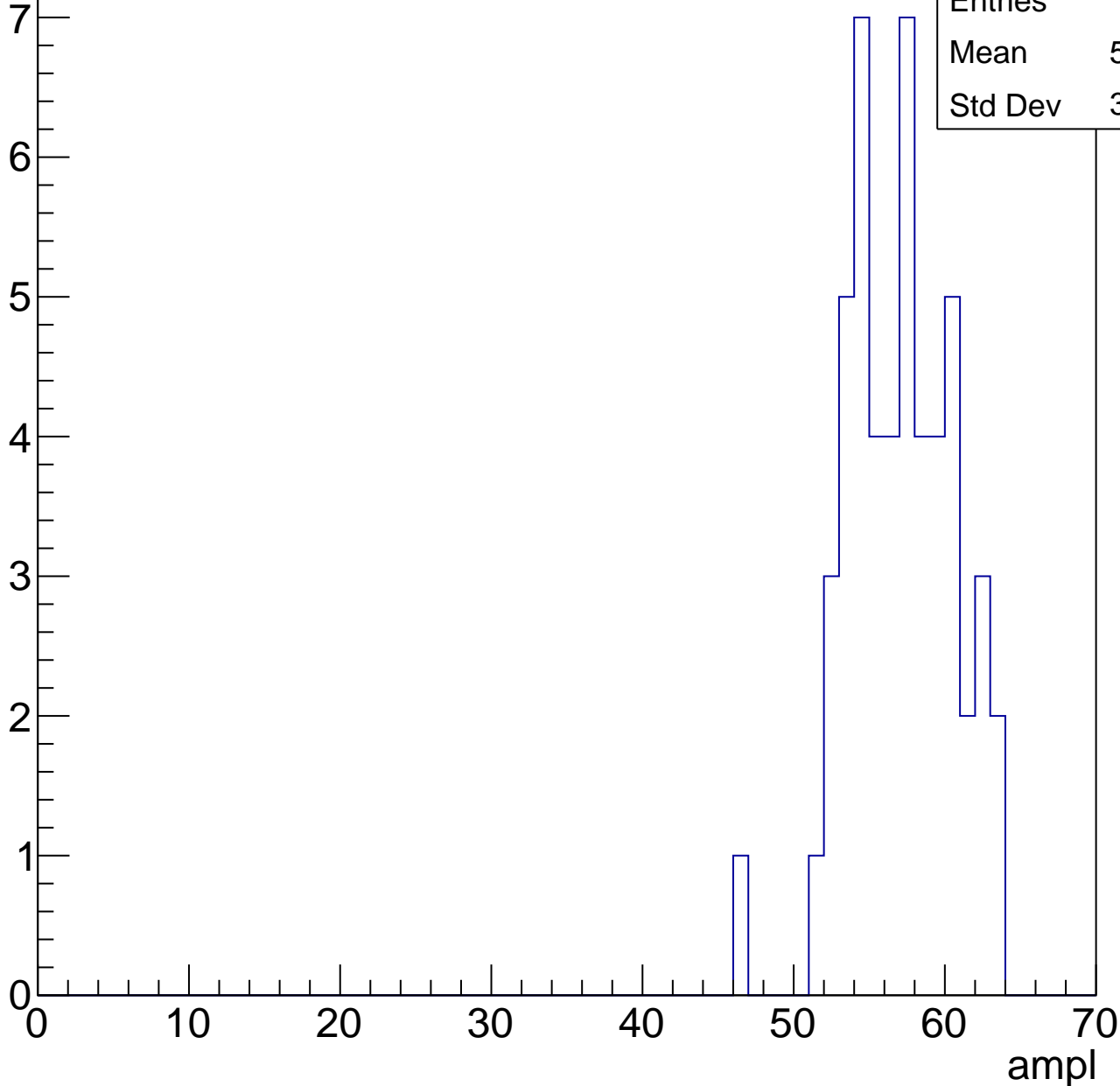


B1L101S, U23-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.56
Std Dev	3.477

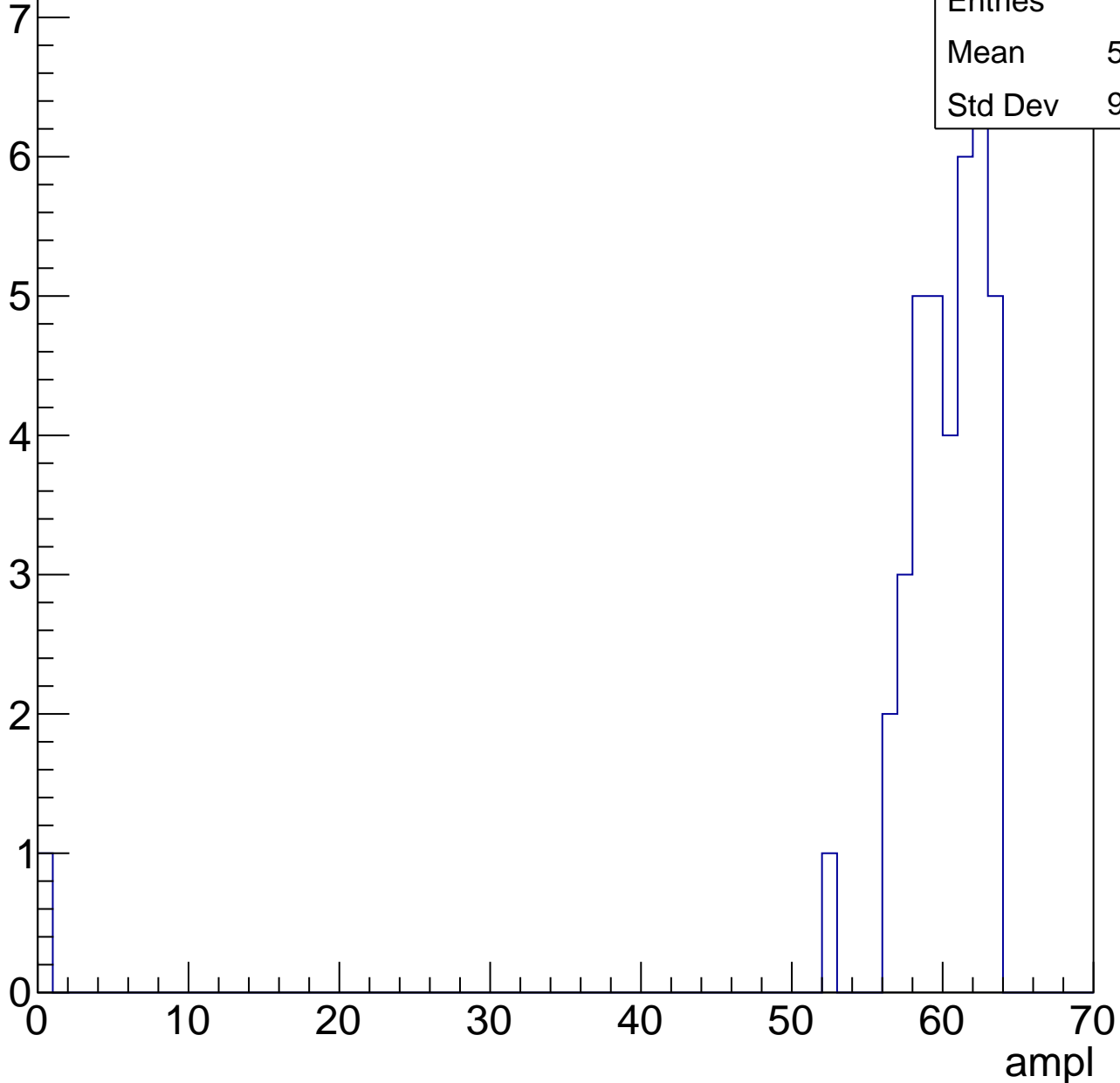


B1L101S, U23-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.33
Std Dev	9.765



B1L101S, U23-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U23-ch112, adc0

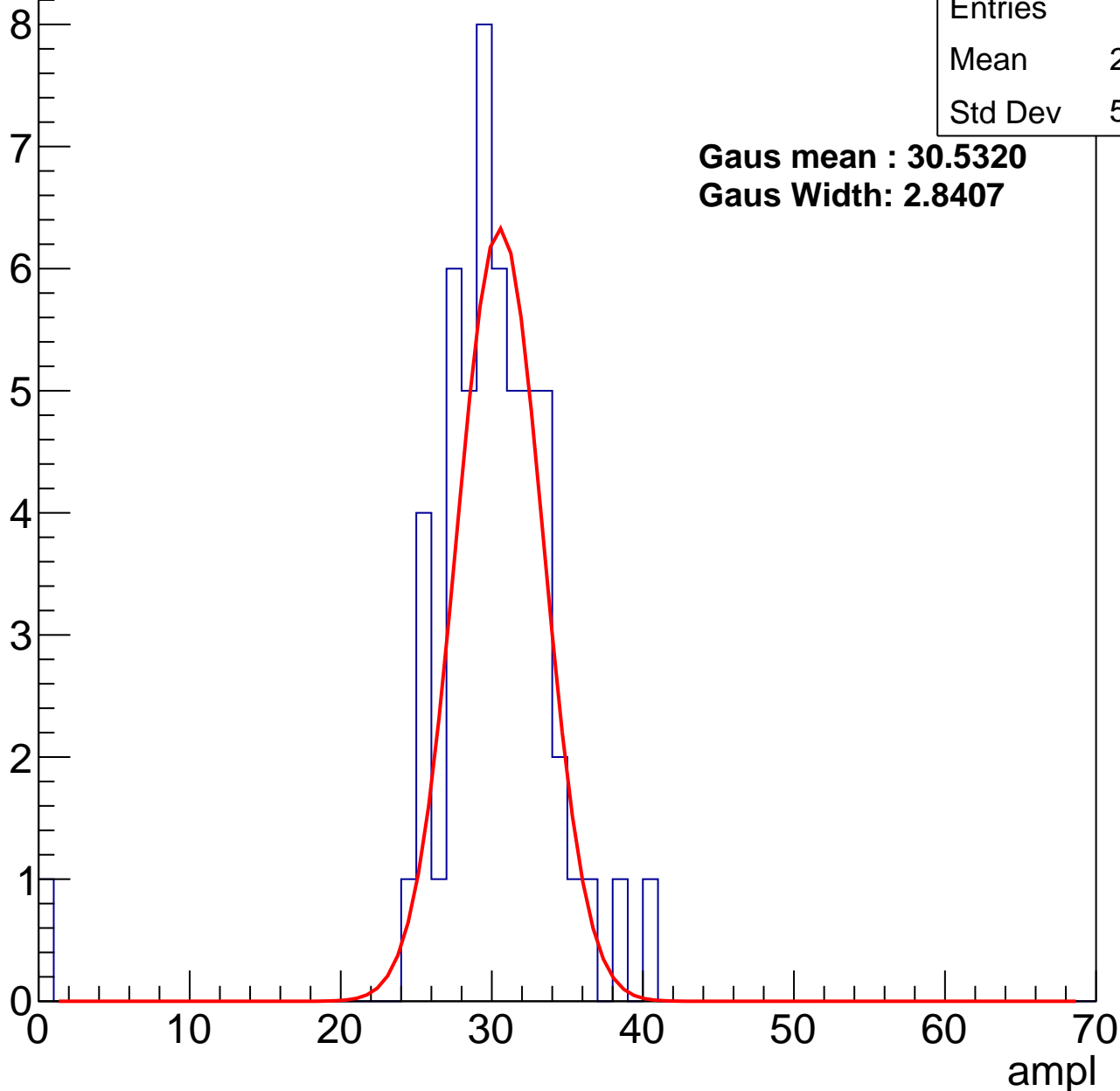
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.45
Std Dev	5.222

Gaus mean : 30.5320

Gaus Width: 2.8407



B1L101S, U23-ch112, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.36
Std Dev	3.769

Gaus mean : 36.2021

Gaus Width: 3.6881

Entry

10
8
6
4
2
0

0

10

20

30

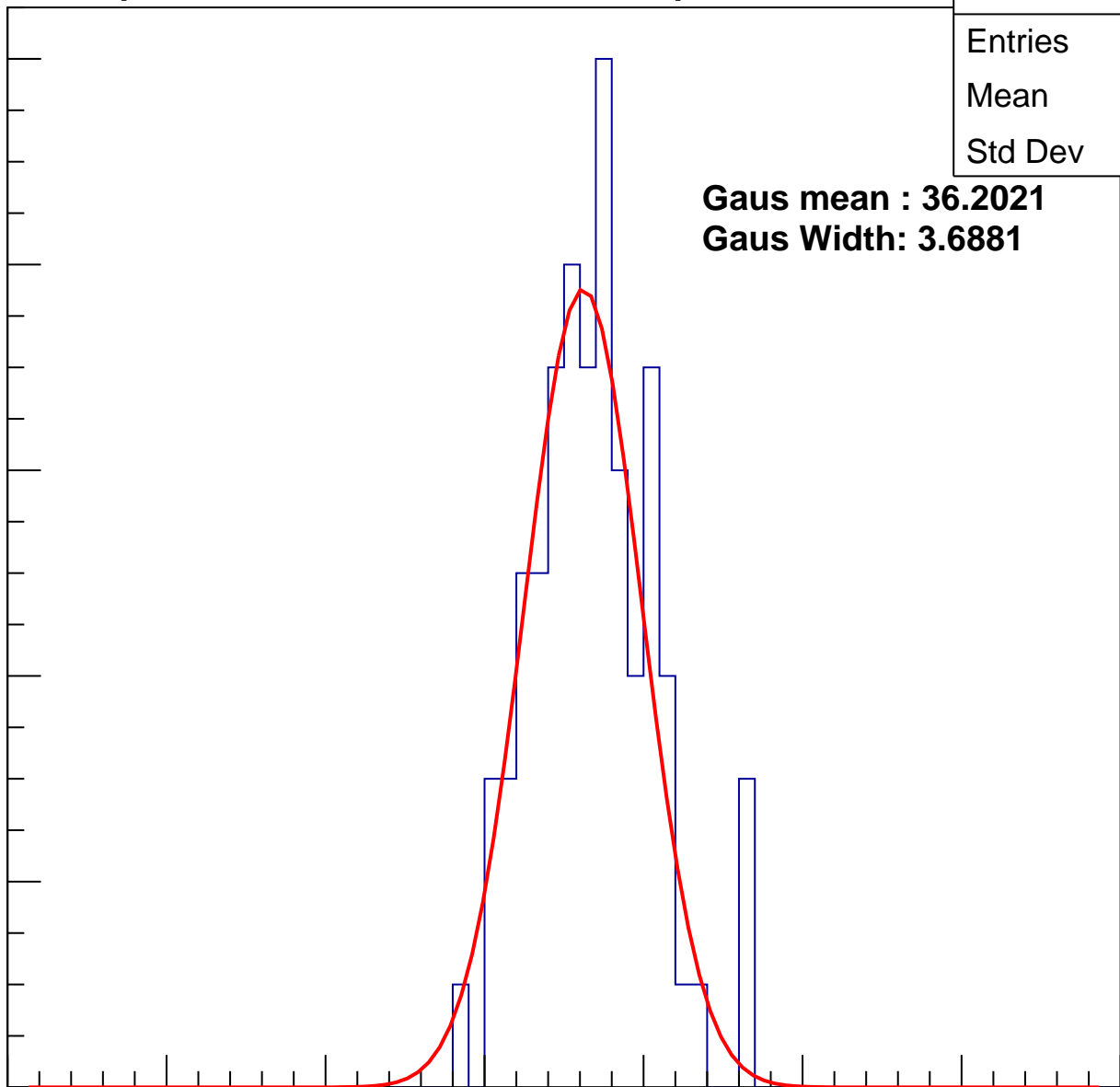
40

50

60

70

ampl



B1L101S, U23-ch112, adc2

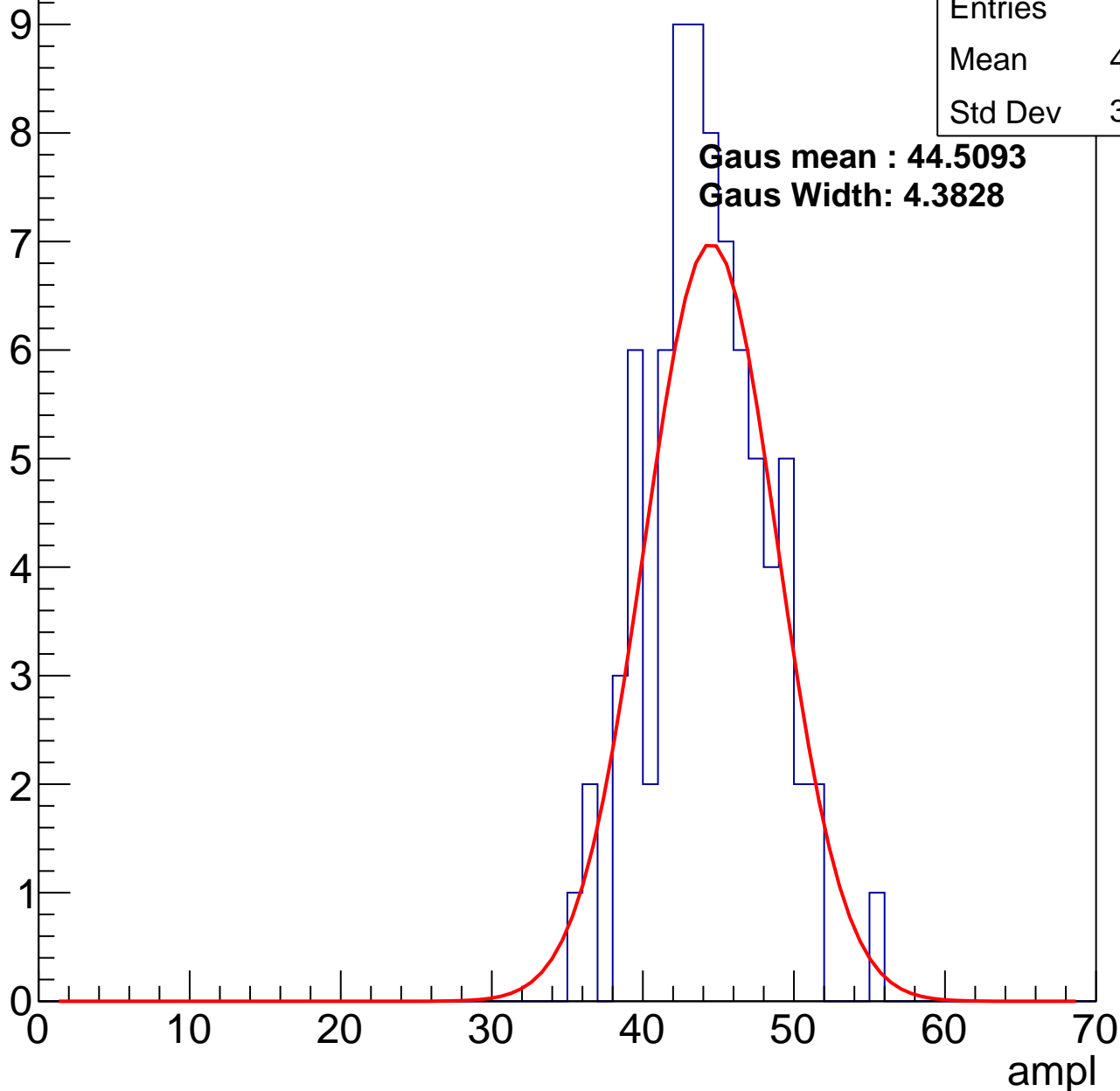
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.82
Std Dev	3.842

Gaus mean : 44.5093

Gaus Width: 4.3828

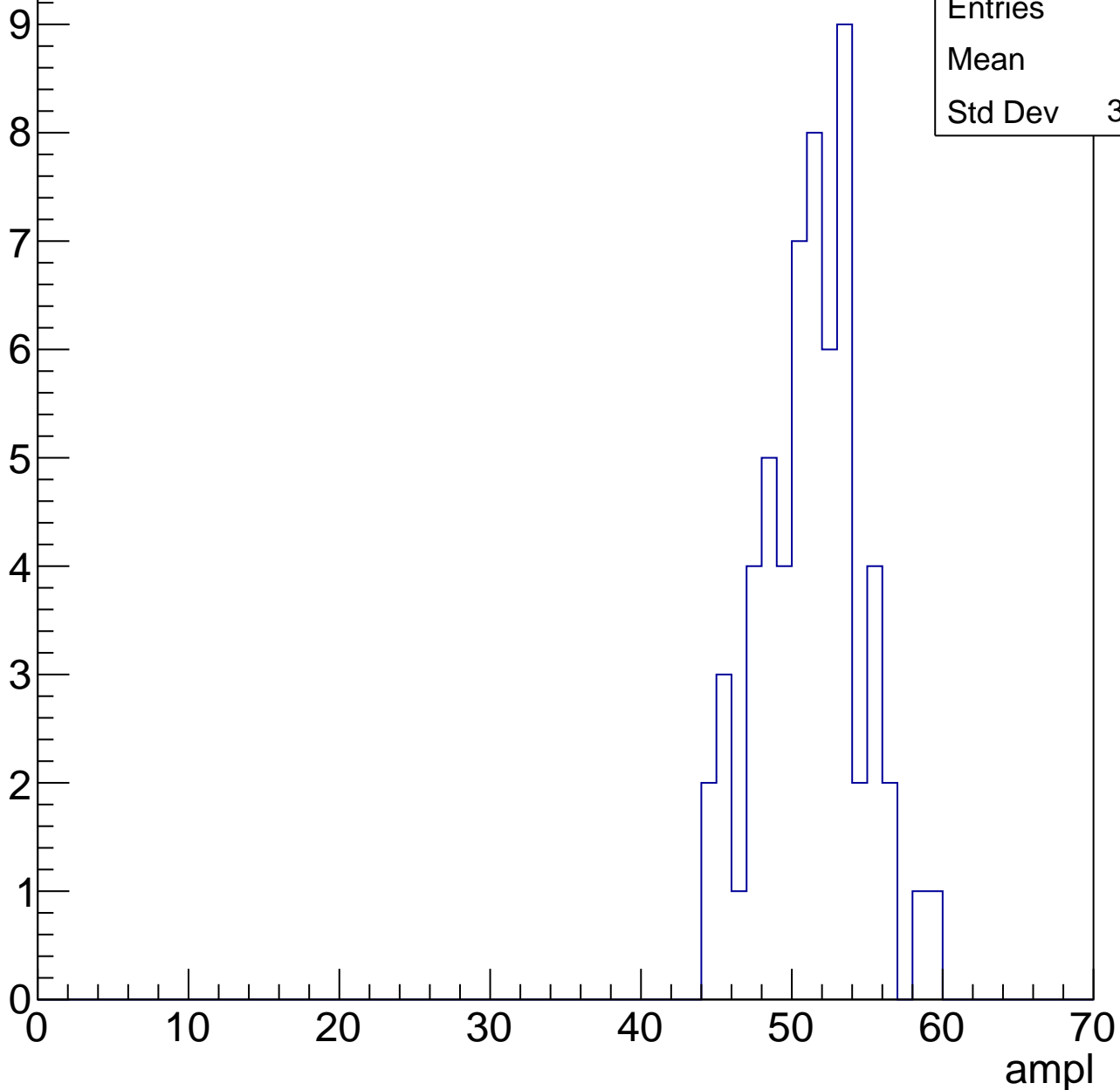


B1L101S, U23-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.8
Std Dev	3.313

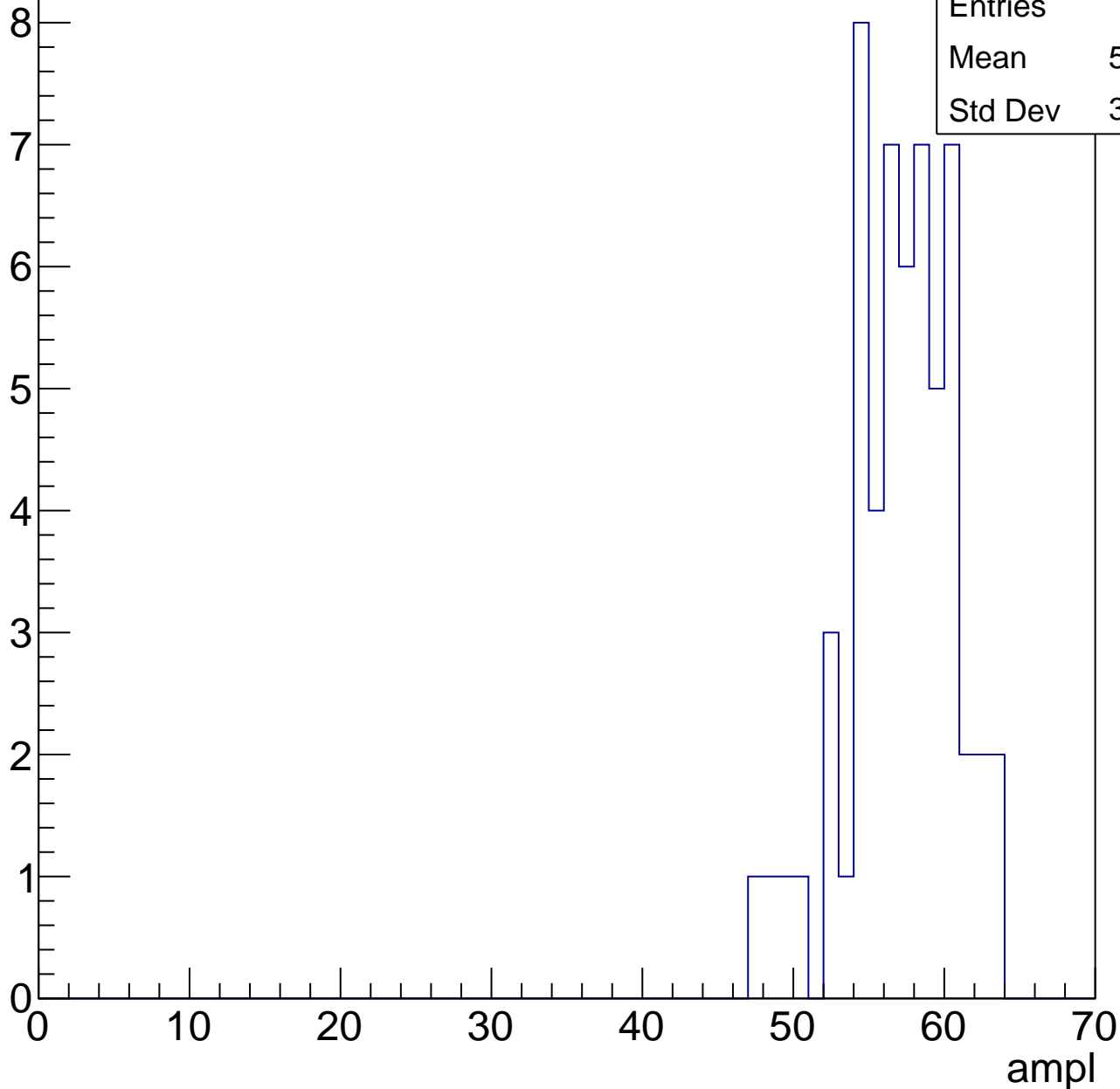


B1L101S, U23-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.59
Std Dev	3.509

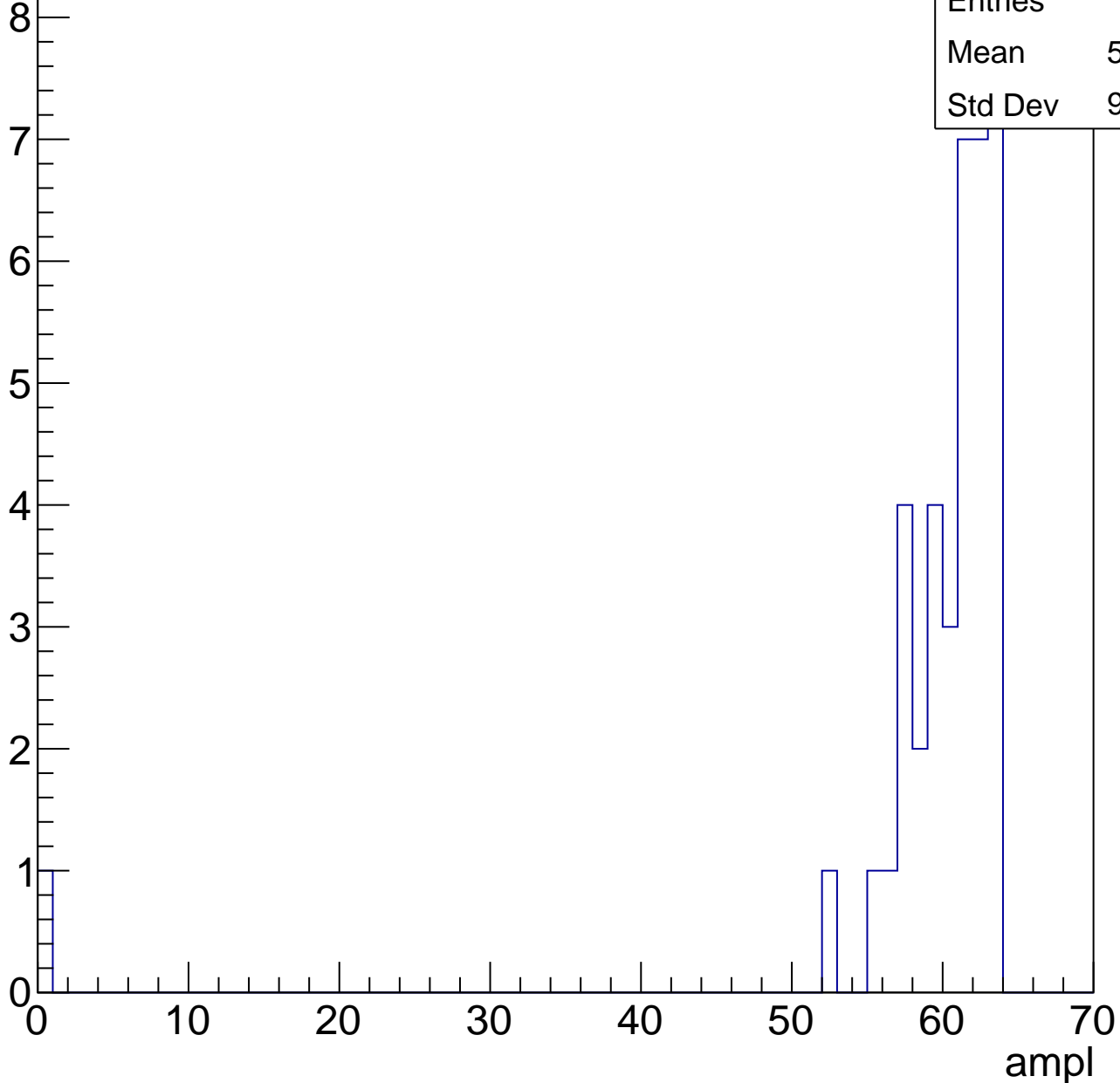


B1L101S, U23-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

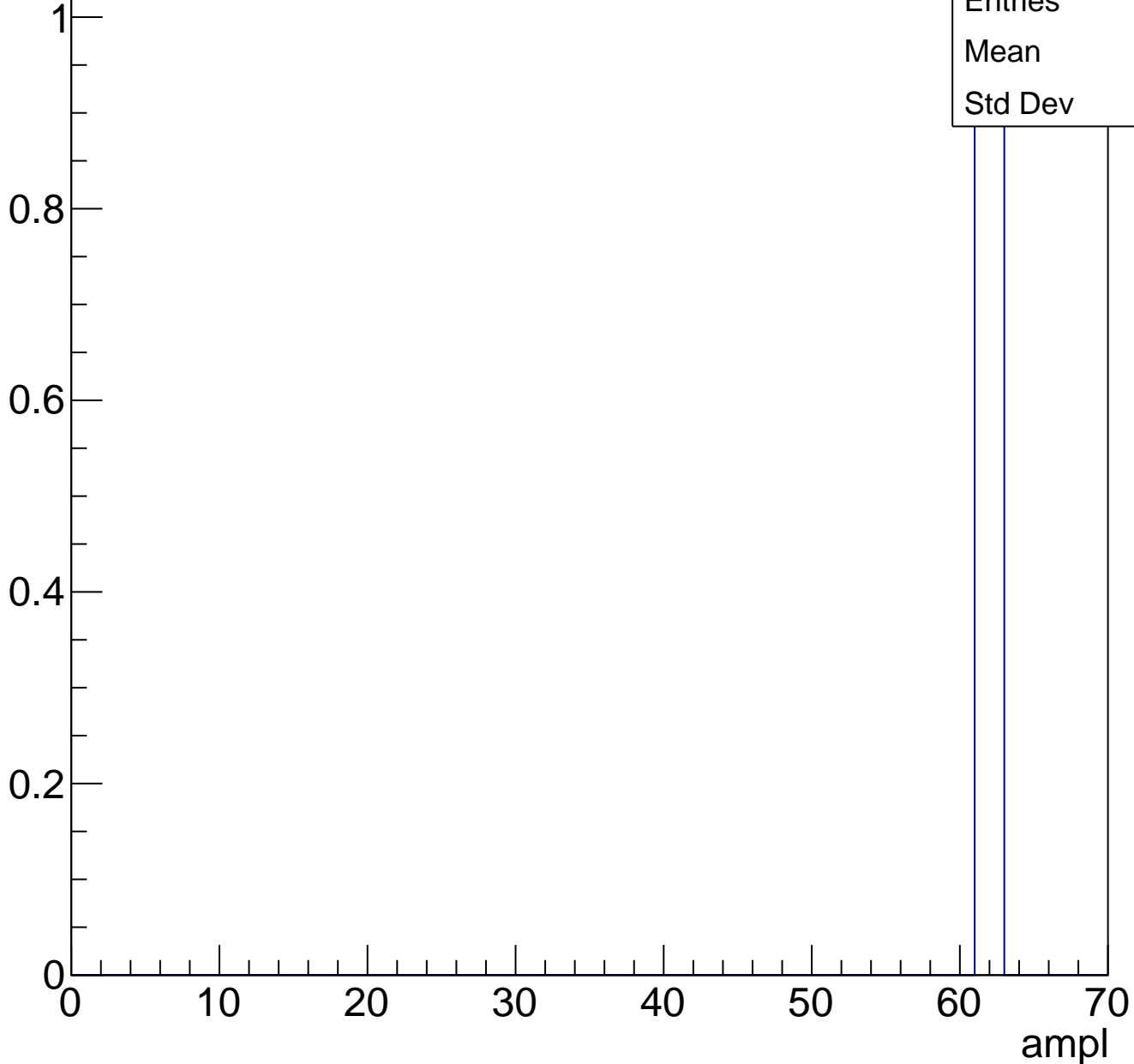
Entries	39
Mean	58.67
Std Dev	9.859



B1L101S, U23-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch113, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	28.49
Std Dev	5.635

Gaus mean : 29.7266

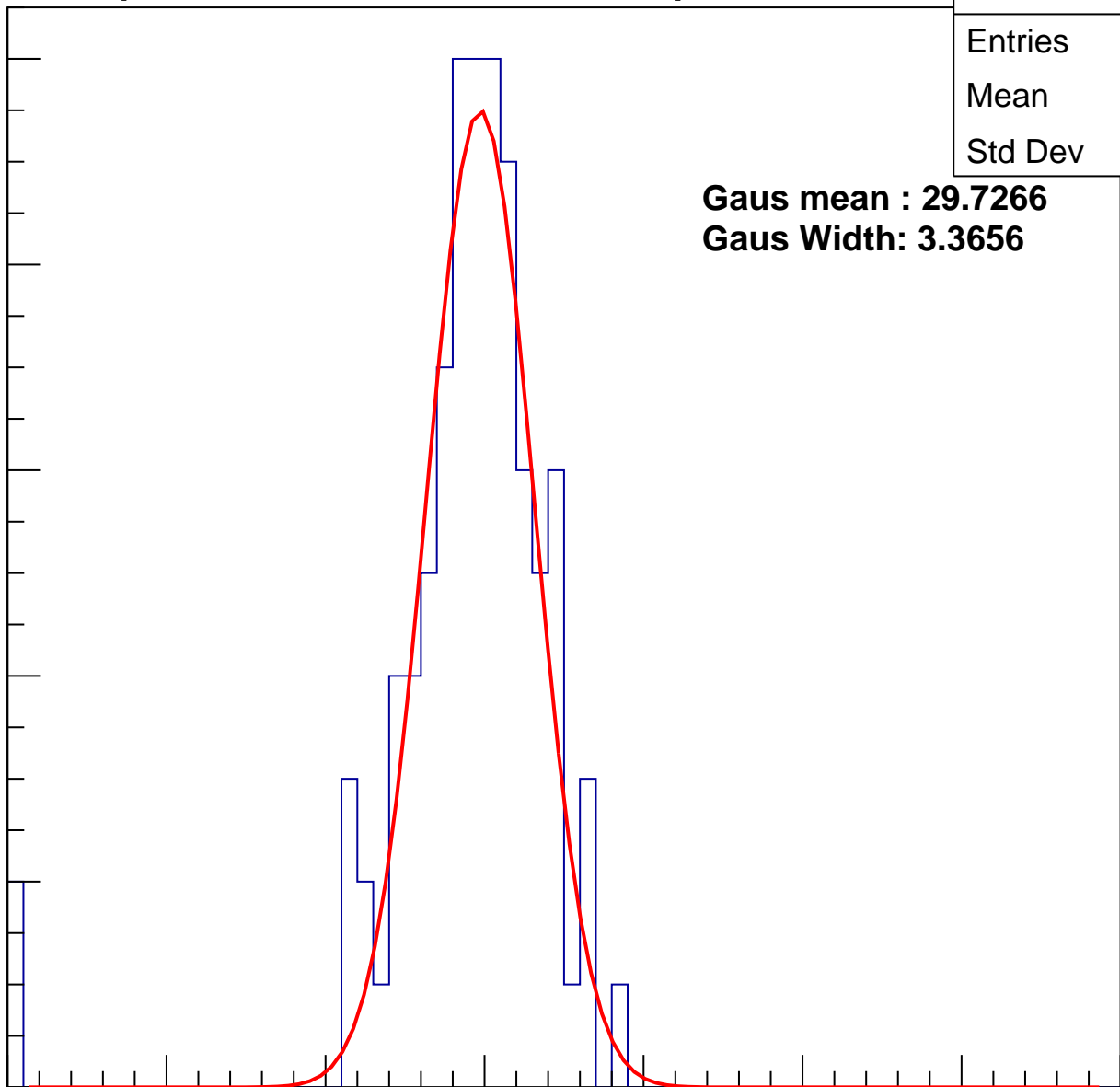
Gaus Width: 3.3656

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch113, adc1

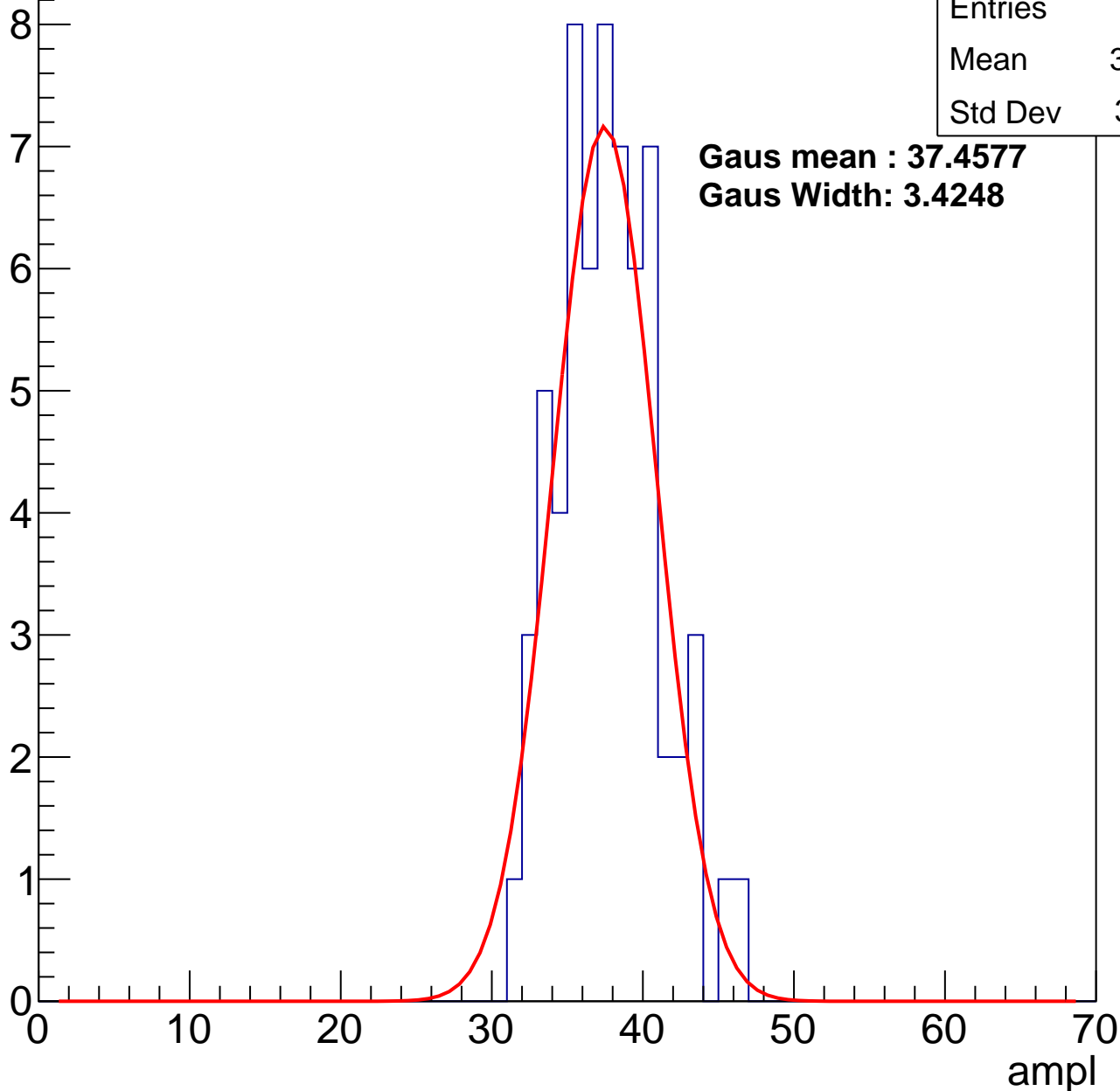
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.28
Std Dev	3.281

Gaus mean : 37.4577

Gaus Width: 3.4248



B1L101S, U23-ch113, adc2

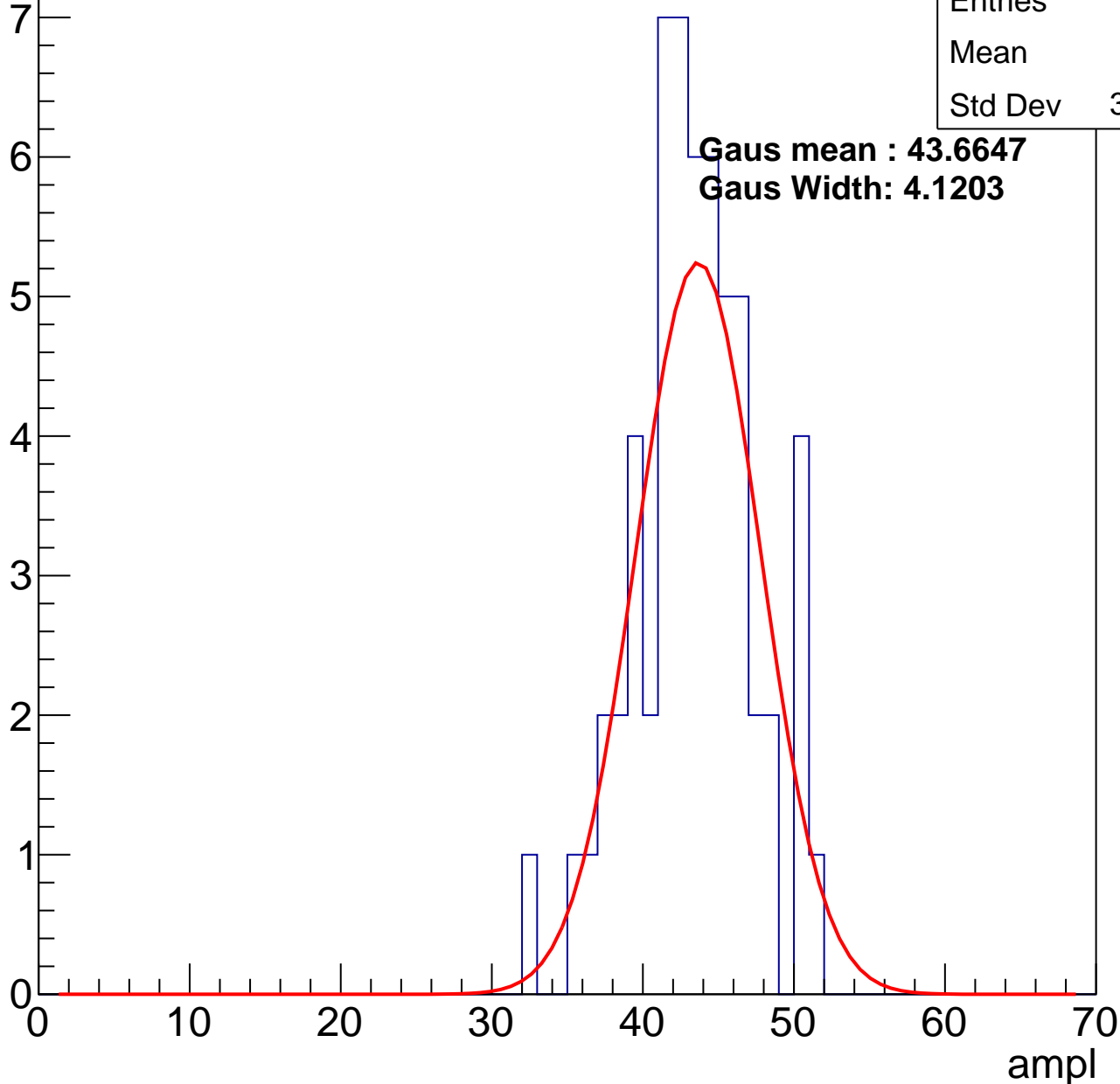
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.9
Std Dev	3.907

Gaus mean : 43.6647

Gaus Width: 4.1203

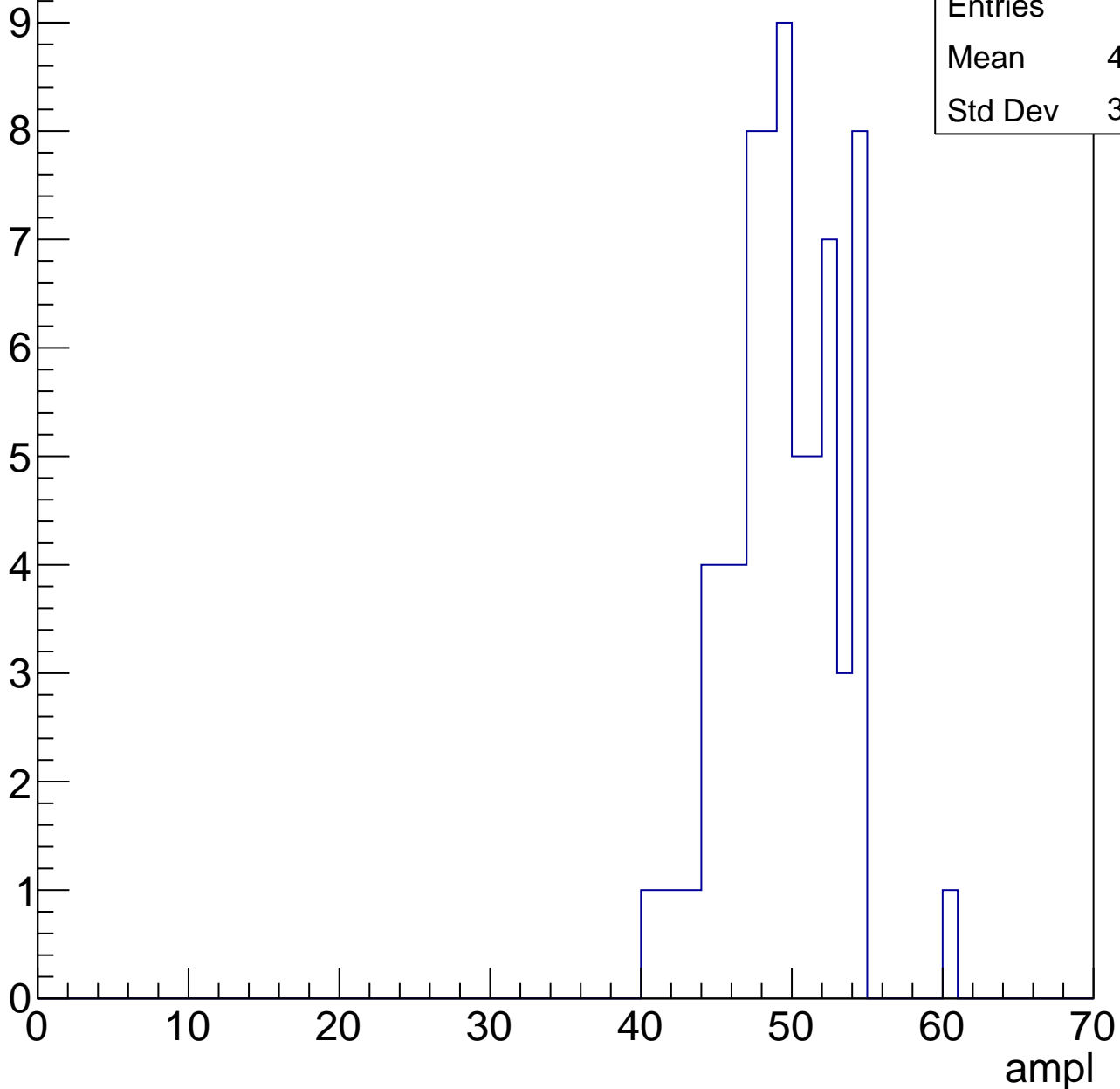


B1L101S, U23-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	48.96
Std Dev	3.639

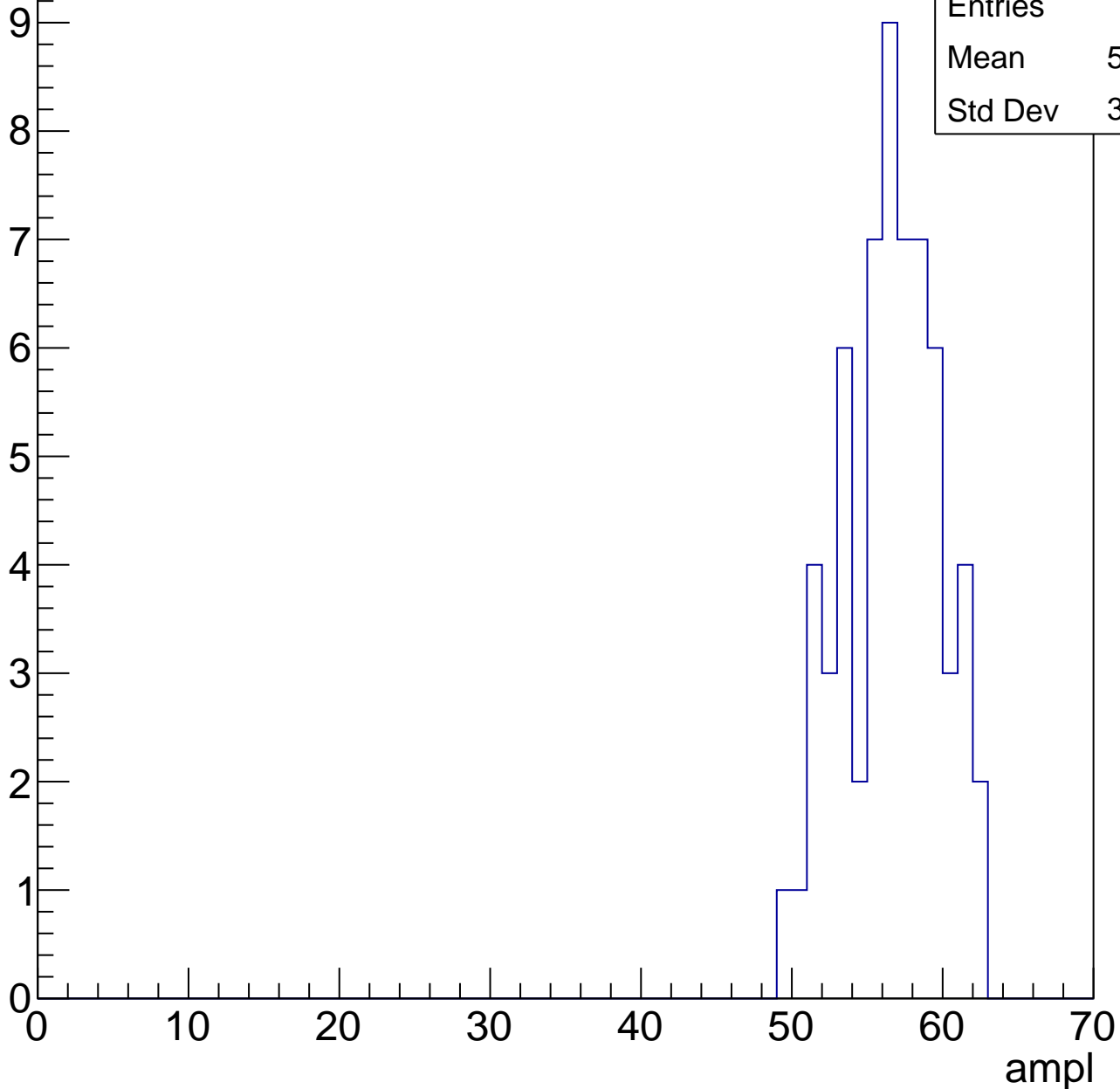


B1L101S, U23-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.15
Std Dev	3.136

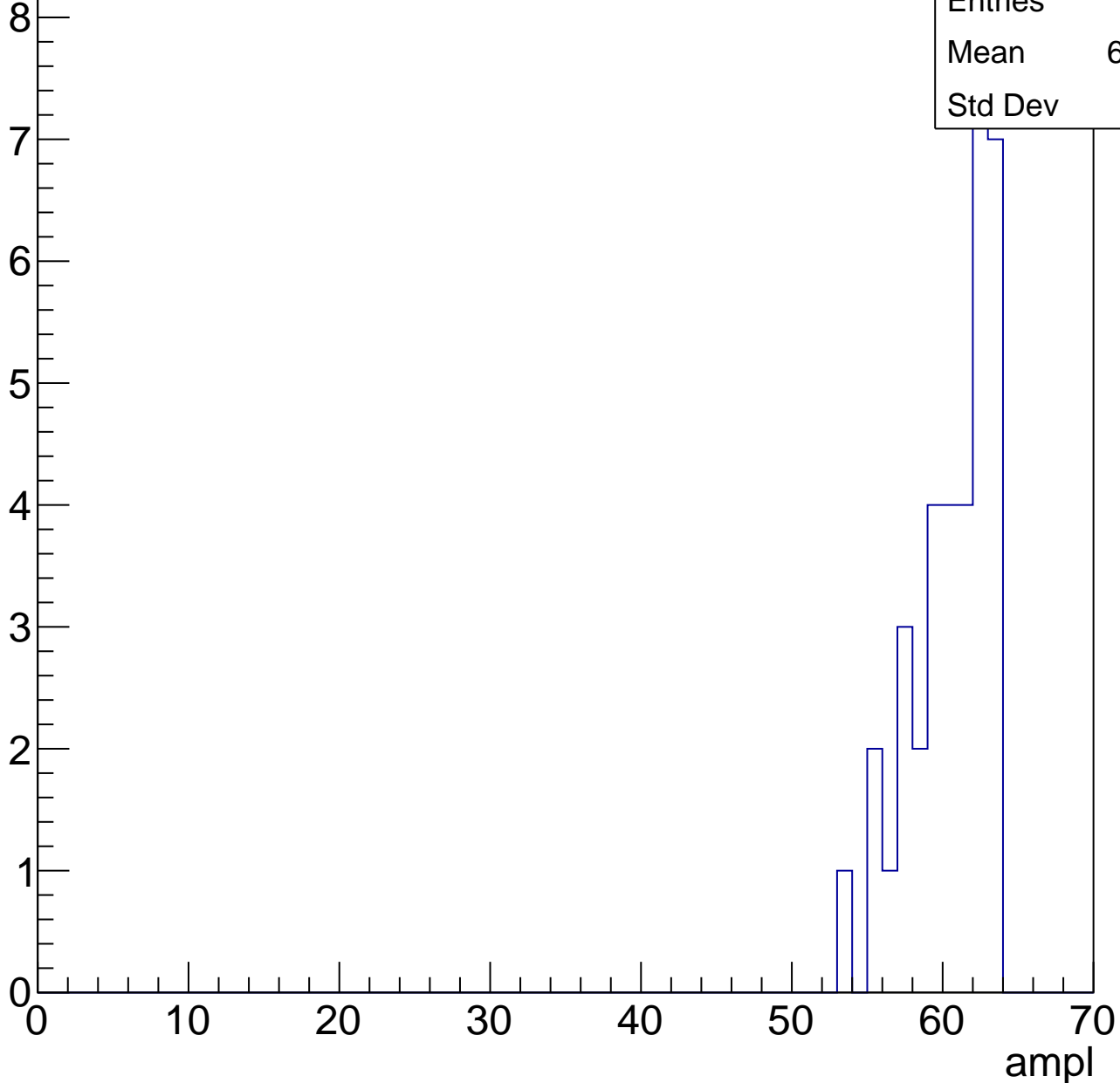


B1L101S, U23-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

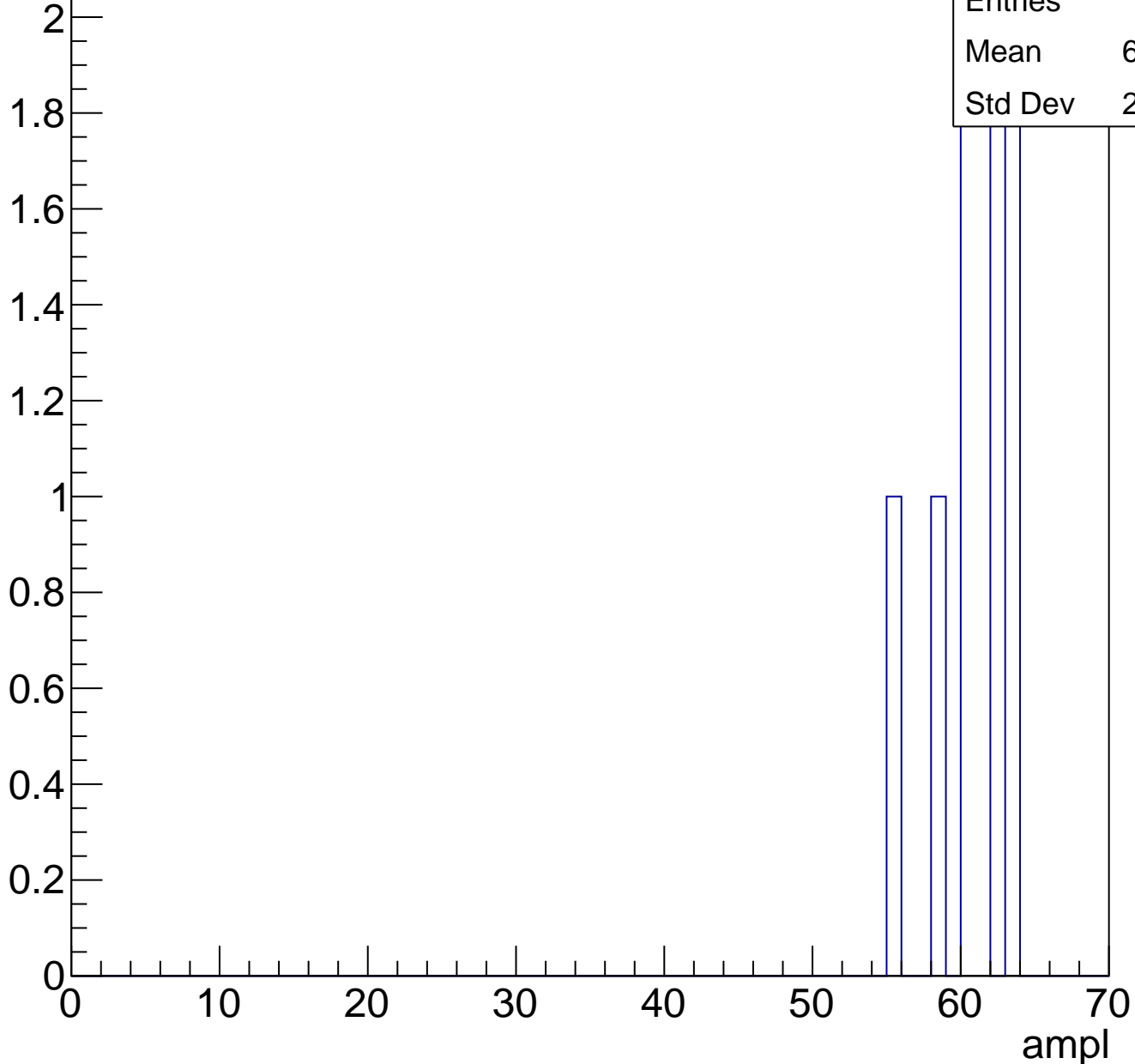
Entries	36
Mean	60.08
Std Dev	2.65



B1L101S, U23-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	60.12
Std Dev	2.472

B1L101S, U23-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U23-ch114, adc0

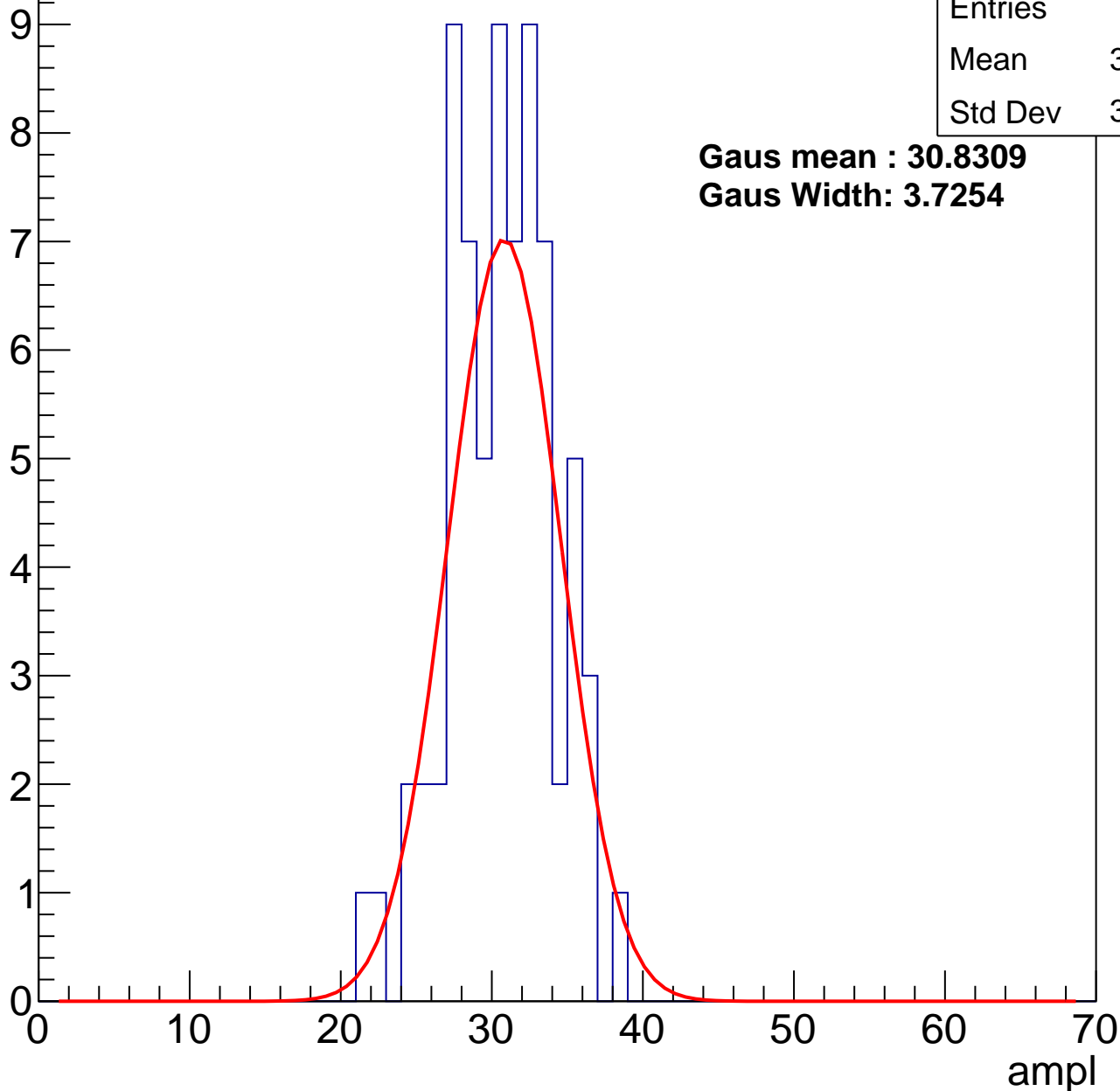
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.17
Std Dev	3.428

Gaus mean : 30.8309

Gaus Width: 3.7254



B1L101S, U23-ch114, adc1

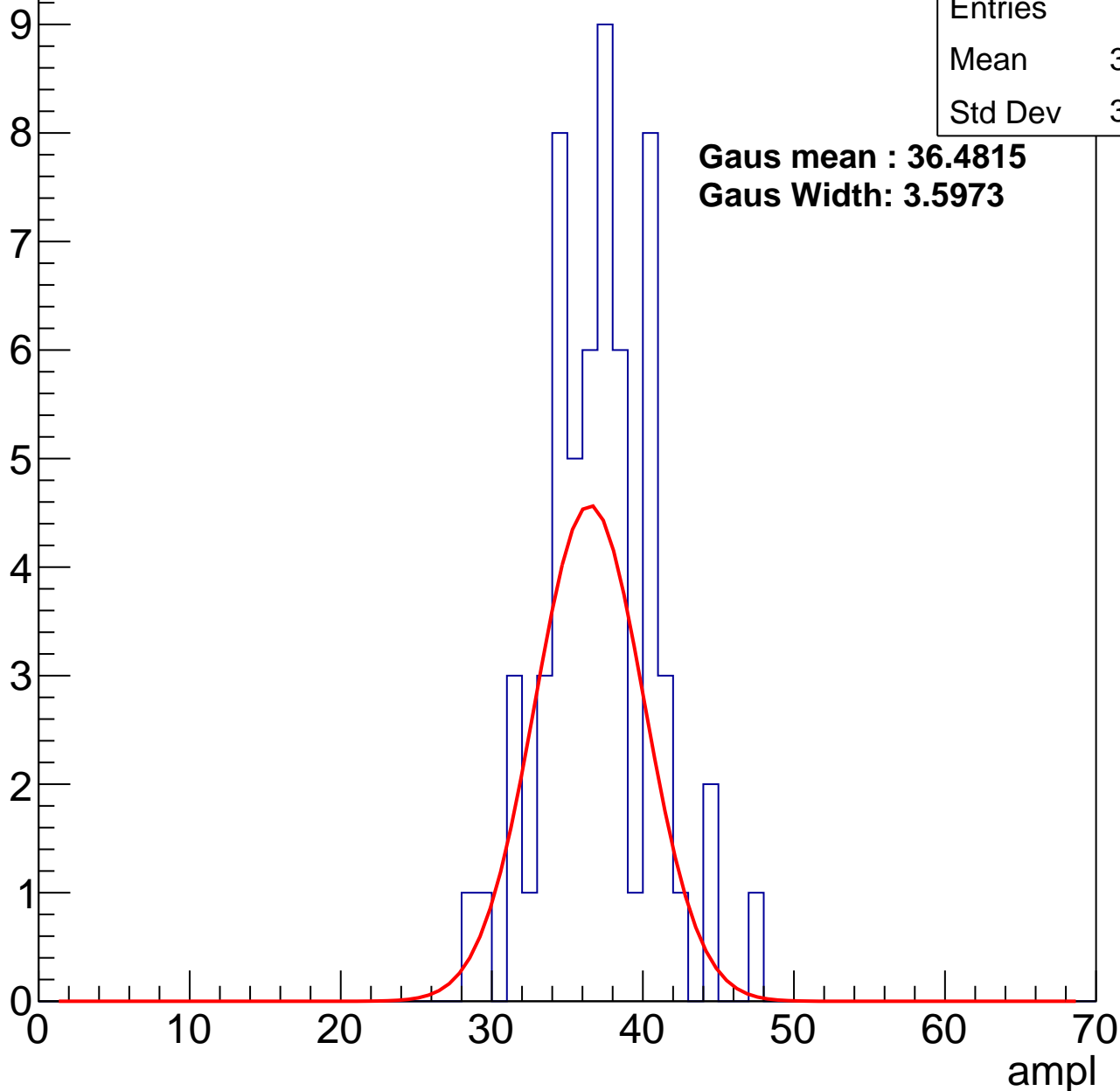
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	36.68
Std Dev	3.615

Gaus mean : 36.4815

Gaus Width: 3.5973



B1L101S, U23-ch114, adc2

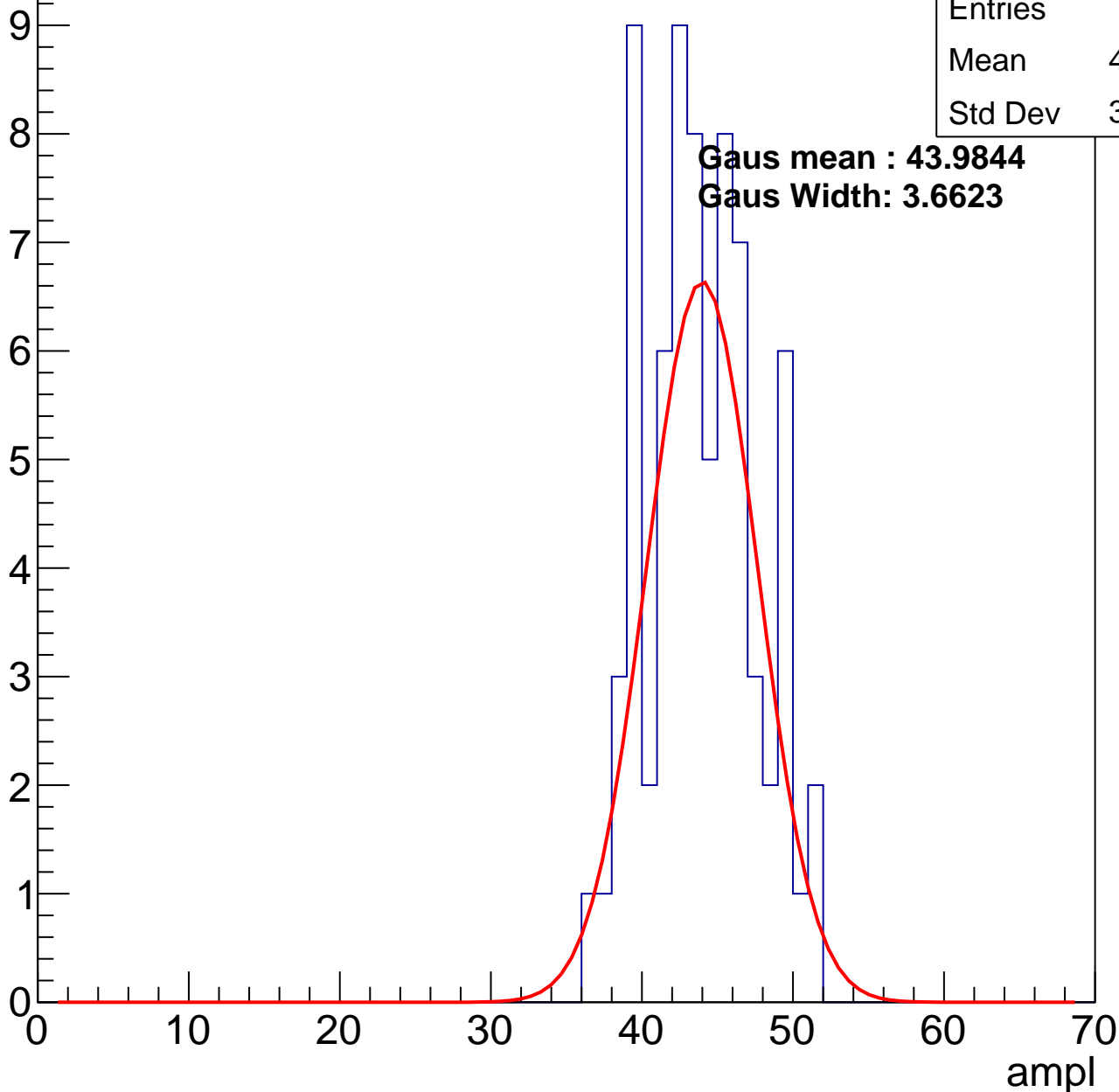
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.44
Std Dev	3.573

Gaus mean : 43.9844

Gaus Width: 3.6623

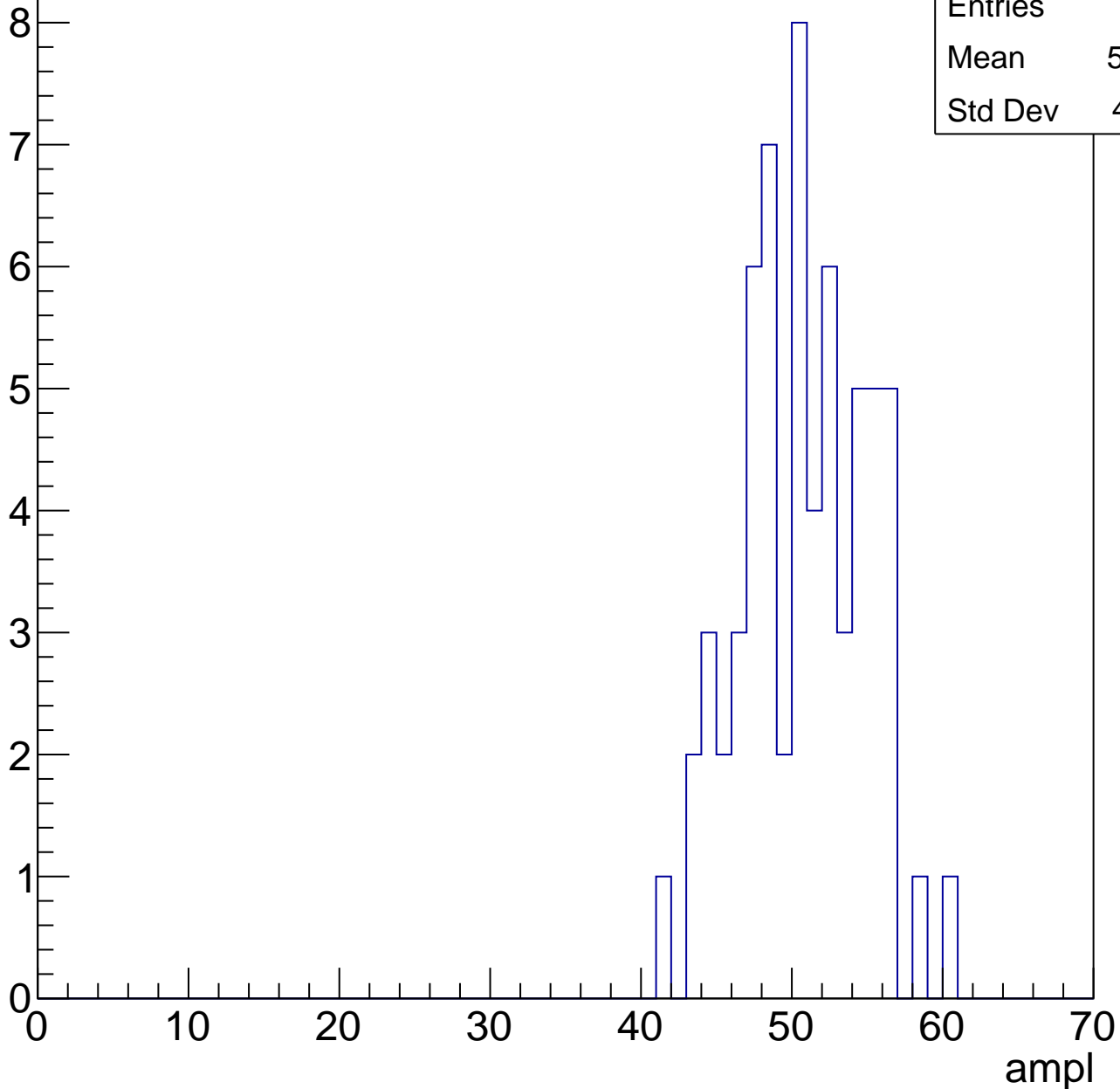


B1L101S, U23-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.33
Std Dev	4.081

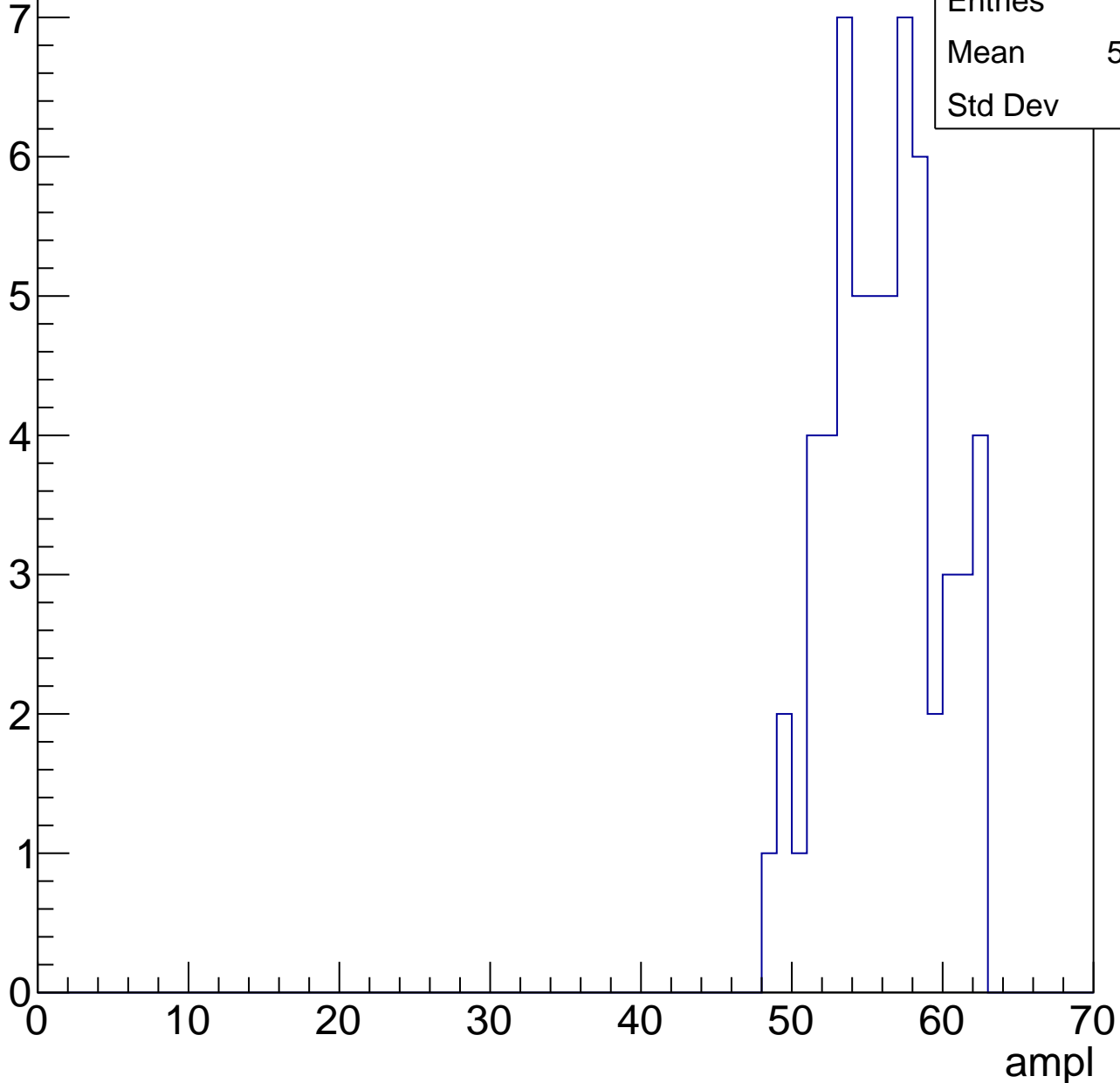


B1L101S, U23-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.59
Std Dev	3.58



B1L101S, U23-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

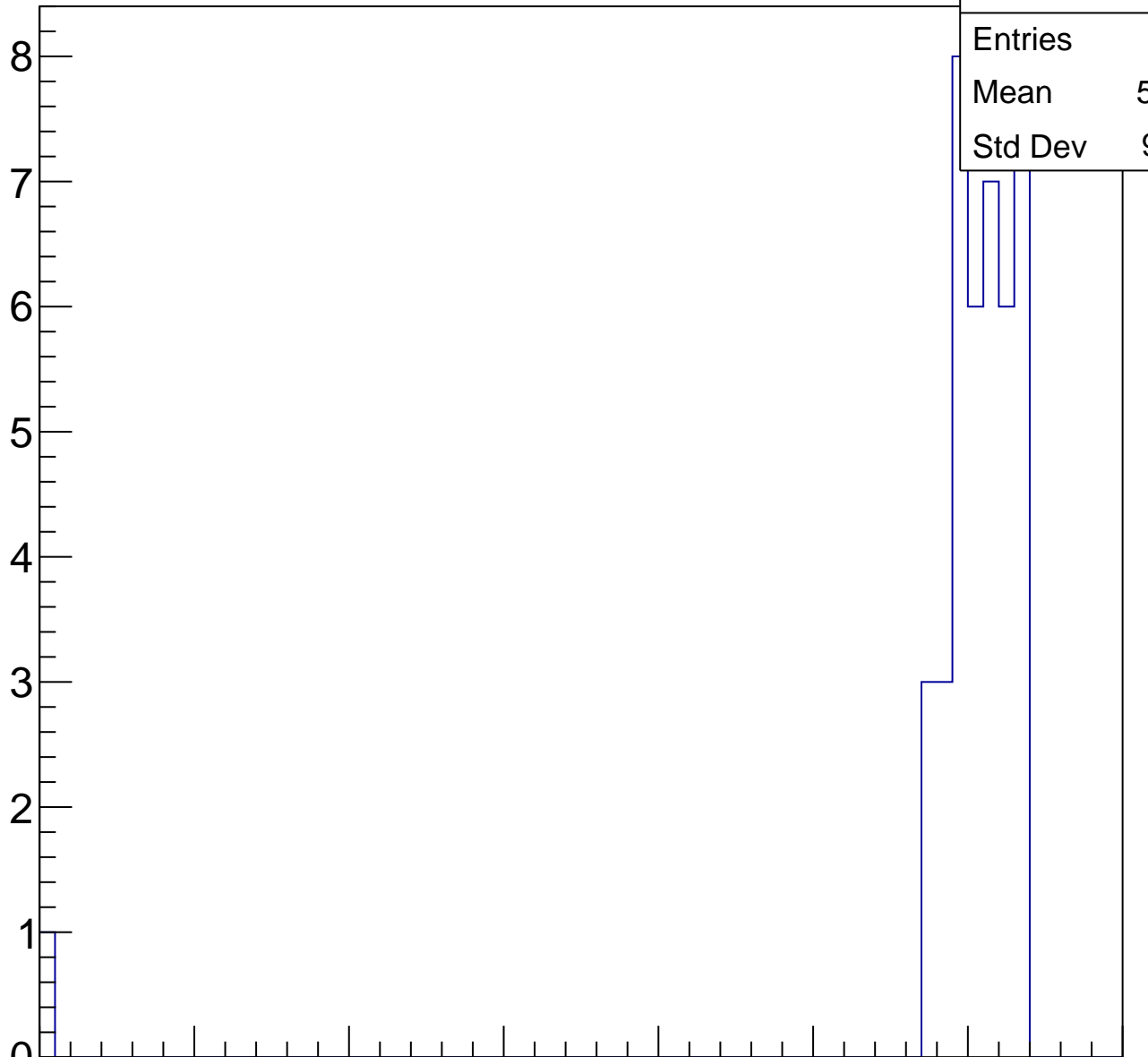
Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.05
Std Dev	9.401

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B1L101S, U23-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch115, adc0

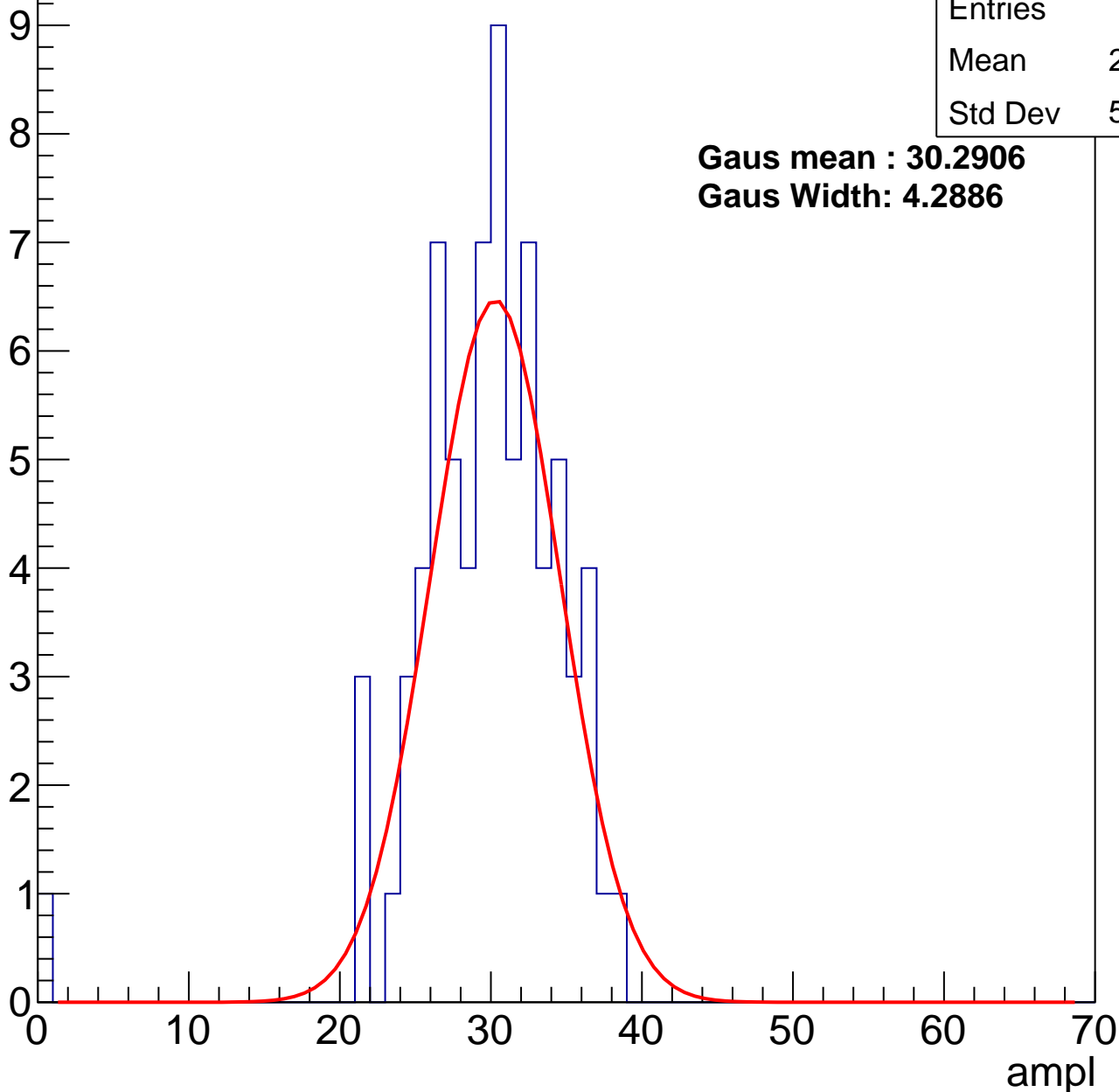
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.26
Std Dev	5.225

Gaus mean : 30.2906

Gaus Width: 4.2886



B1L101S, U23-ch115, adc1

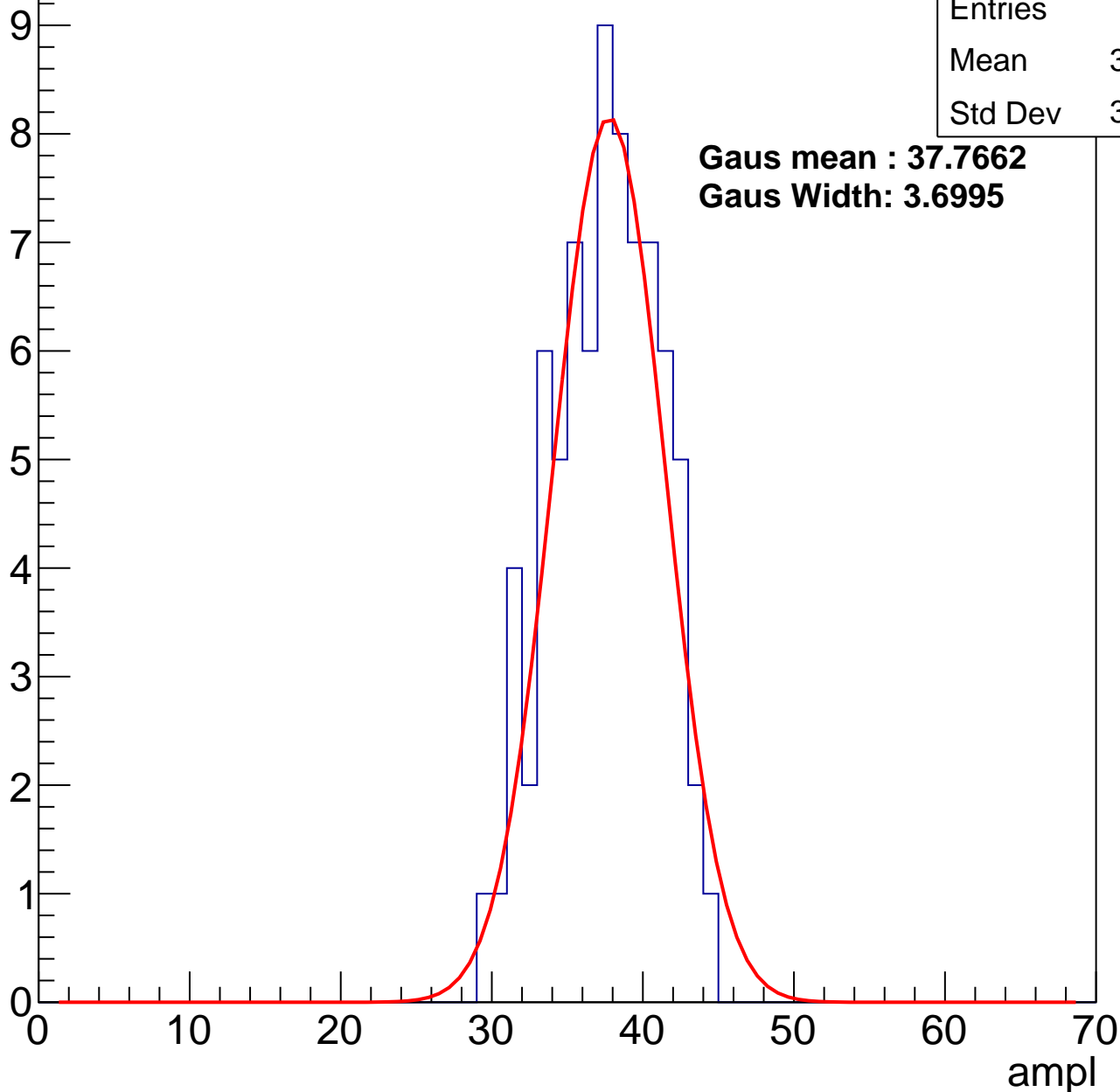
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.04
Std Dev	3.462

Gaus mean : 37.7662

Gaus Width: 3.6995



B1L101S, U23-ch115, adc2

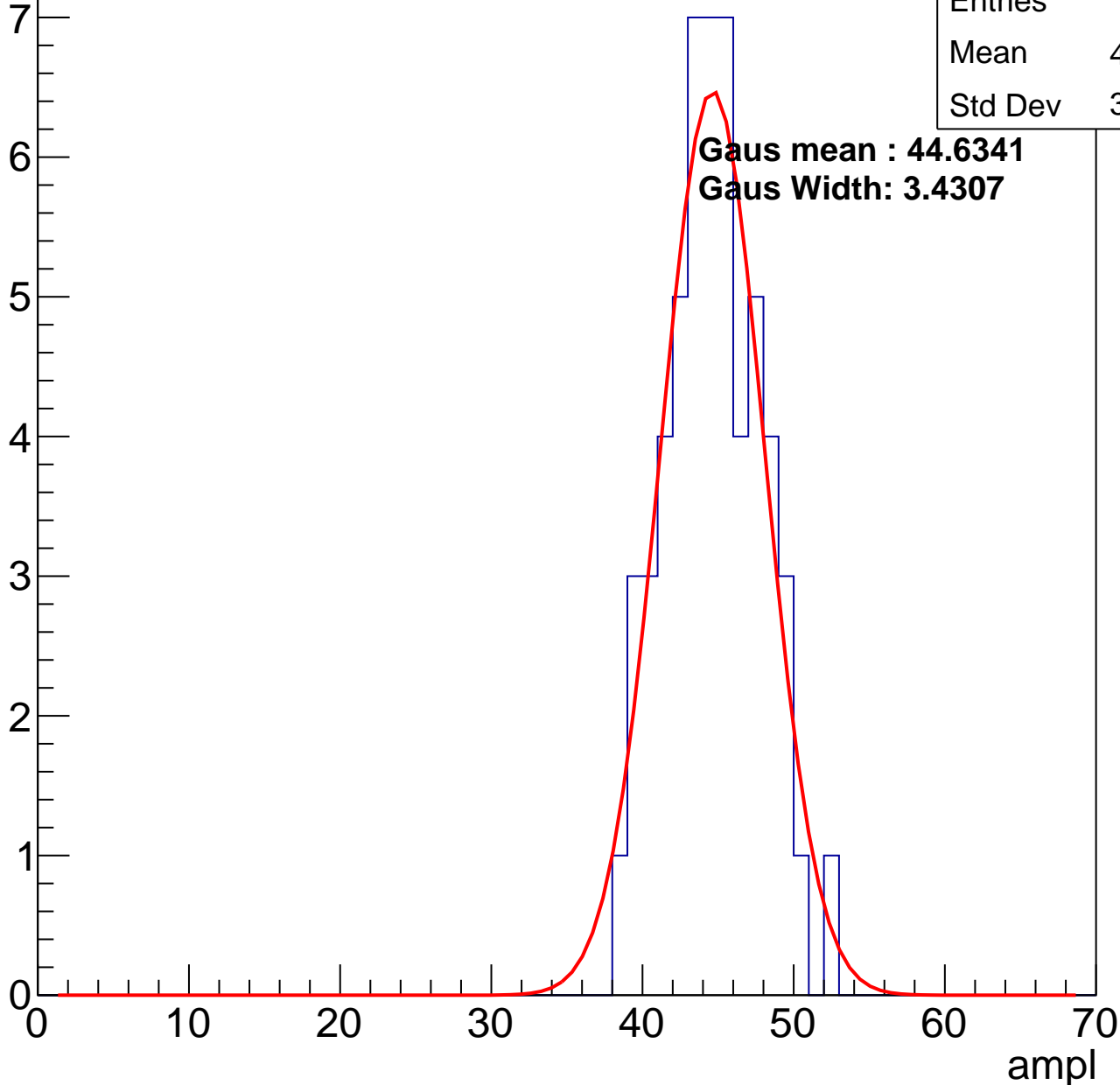
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	44.24
Std Dev	3.092

Gaus mean : 44.6341

Gaus Width: 3.4307

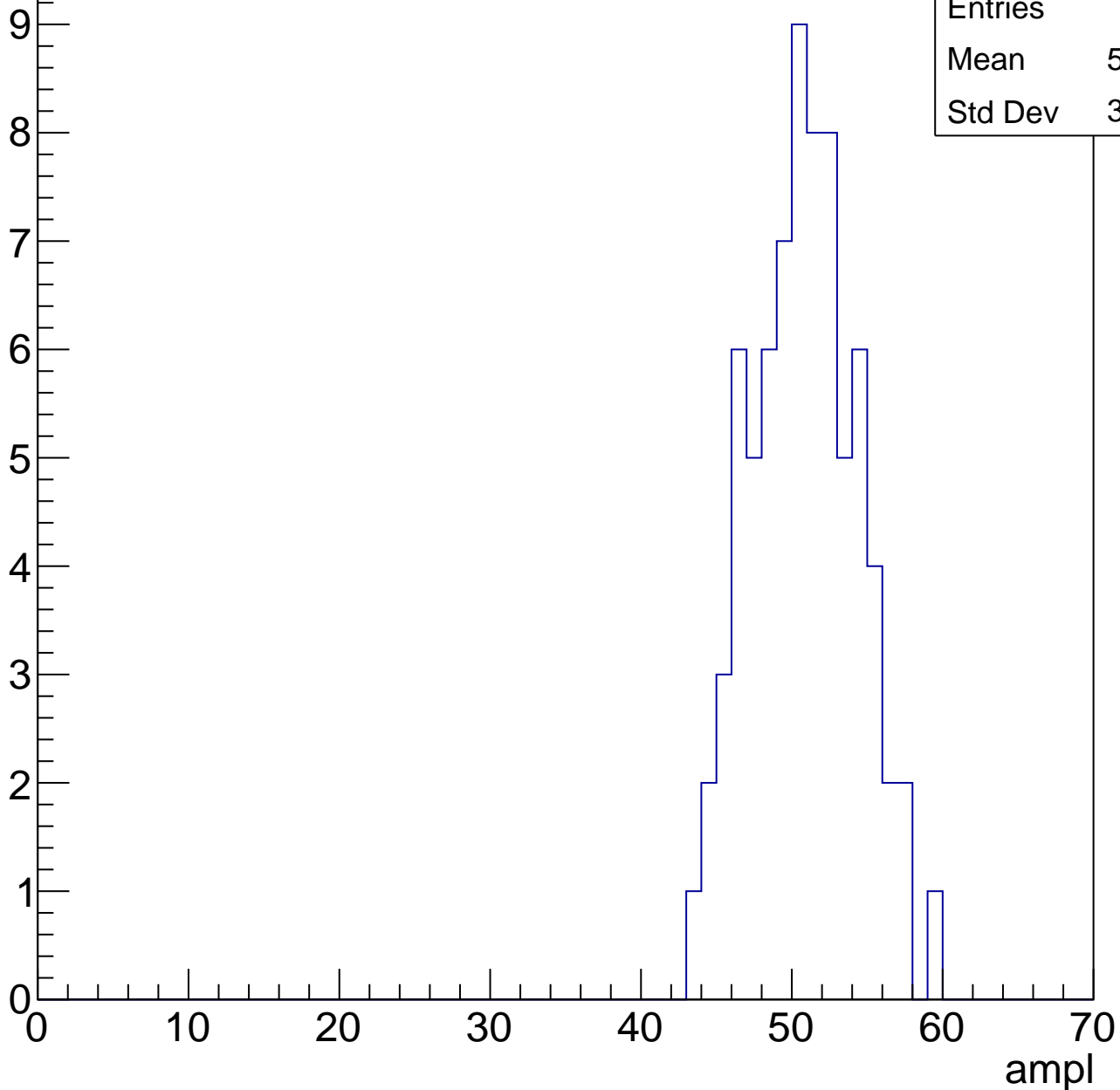


B1L101S, U23-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

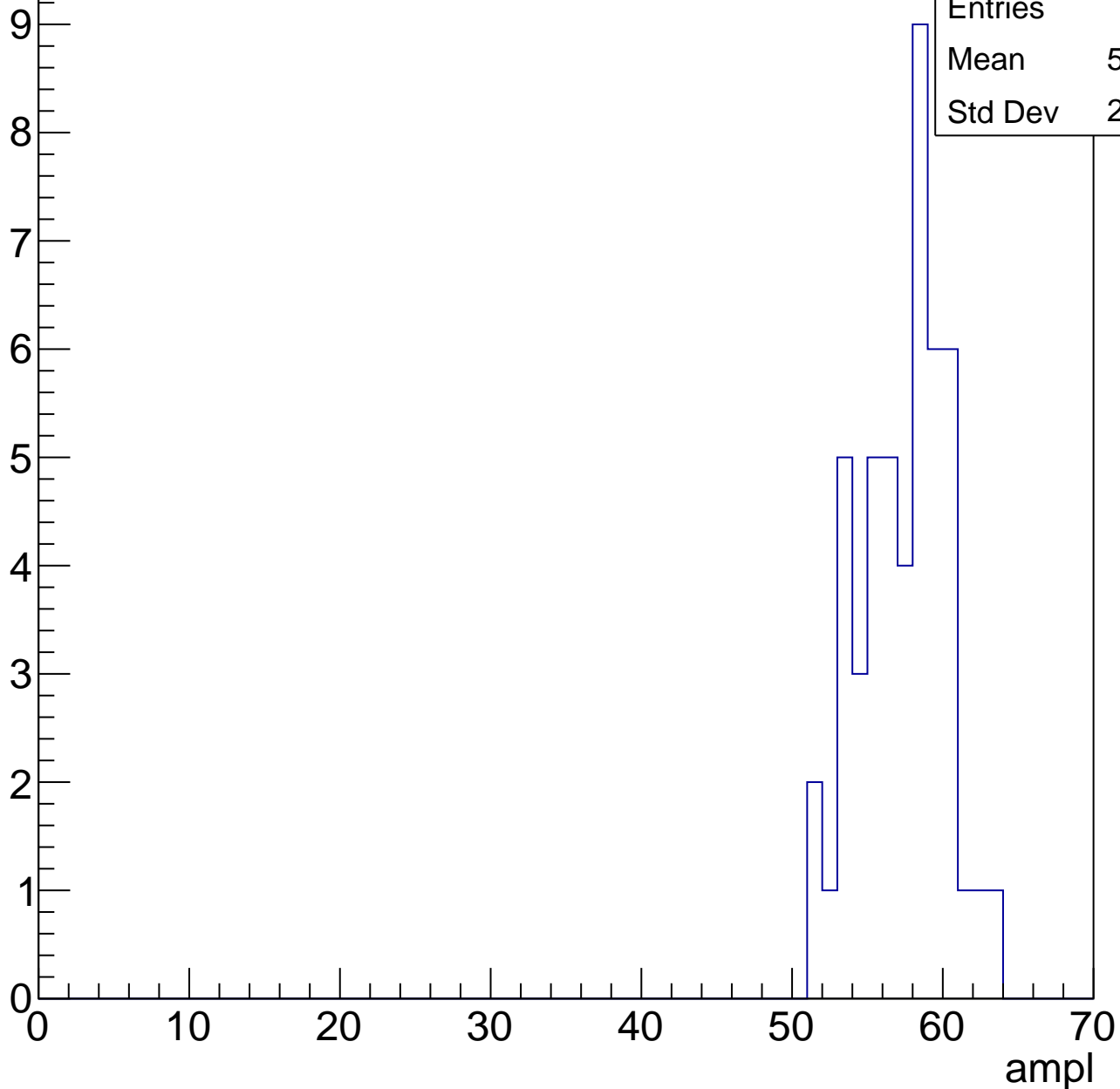
Entries	75
Mean	50.35
Std Dev	3.447



B1L101S, U23-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

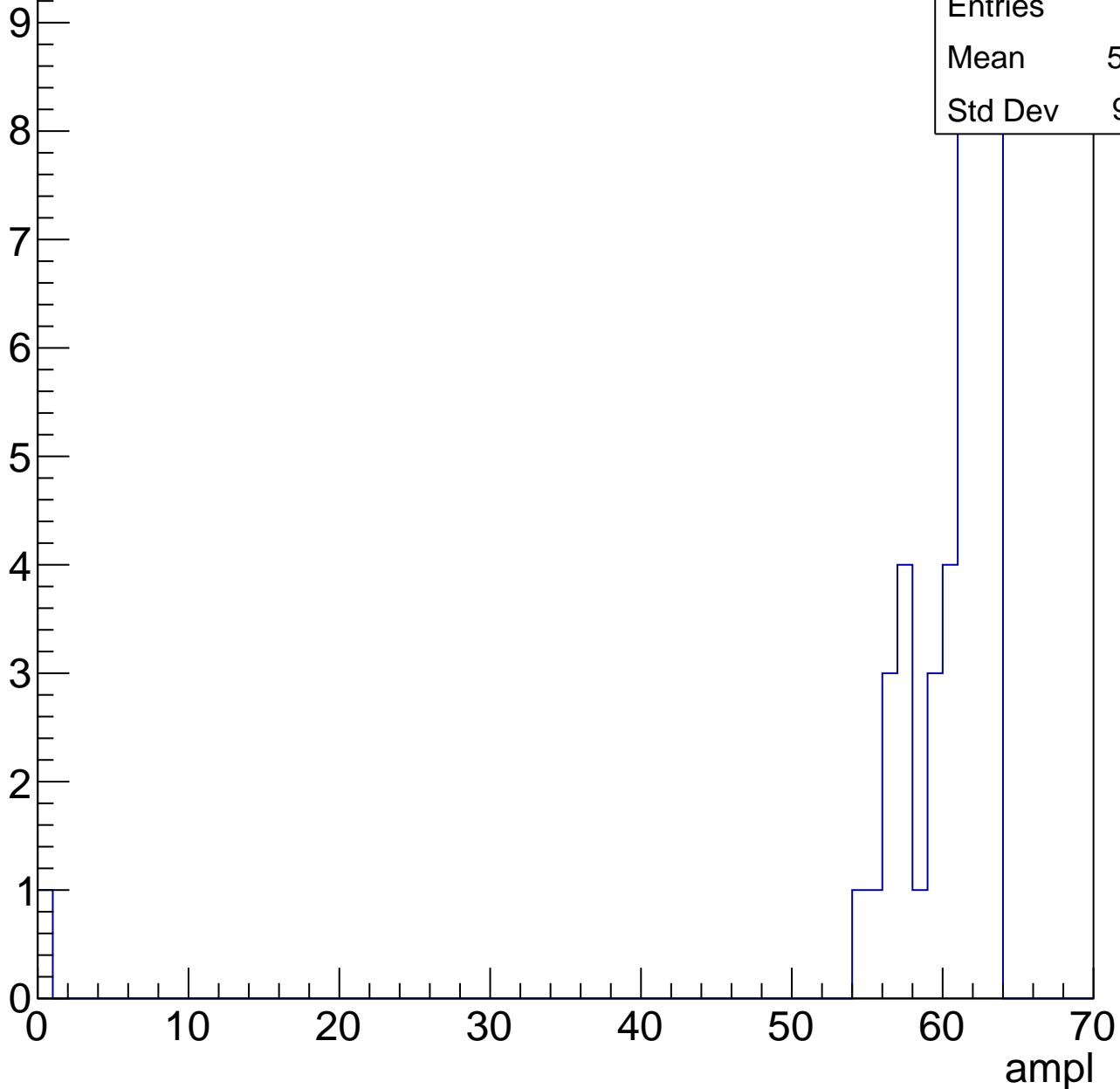


B1L101S, U23-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.89
Std Dev	9.311



B1L101S, U23-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U23-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch116, adc0

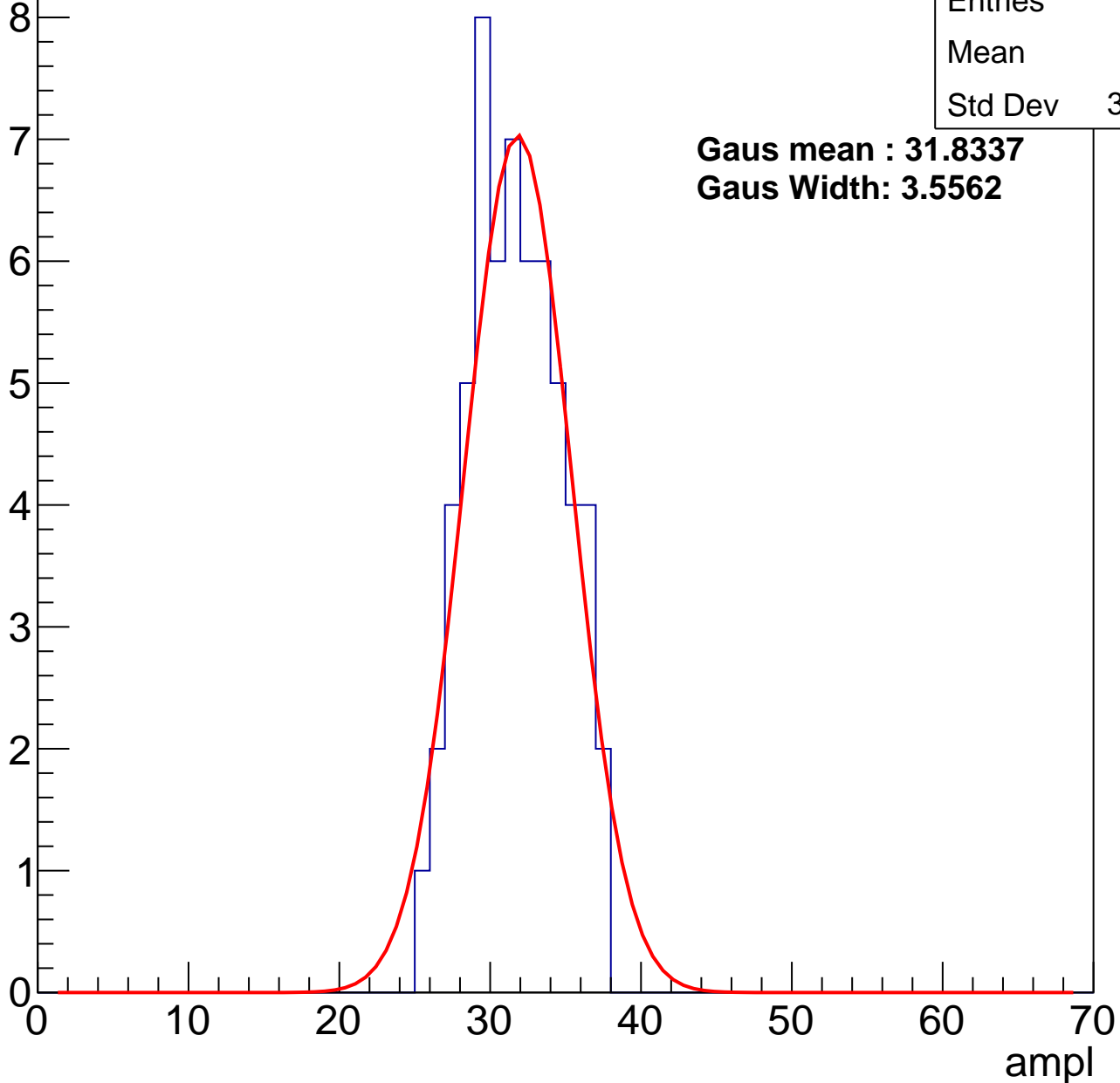
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	31.2
Std Dev	3.004

Gaus mean : 31.8337

Gaus Width: 3.5562



B1L101S, U23-ch116, adc1

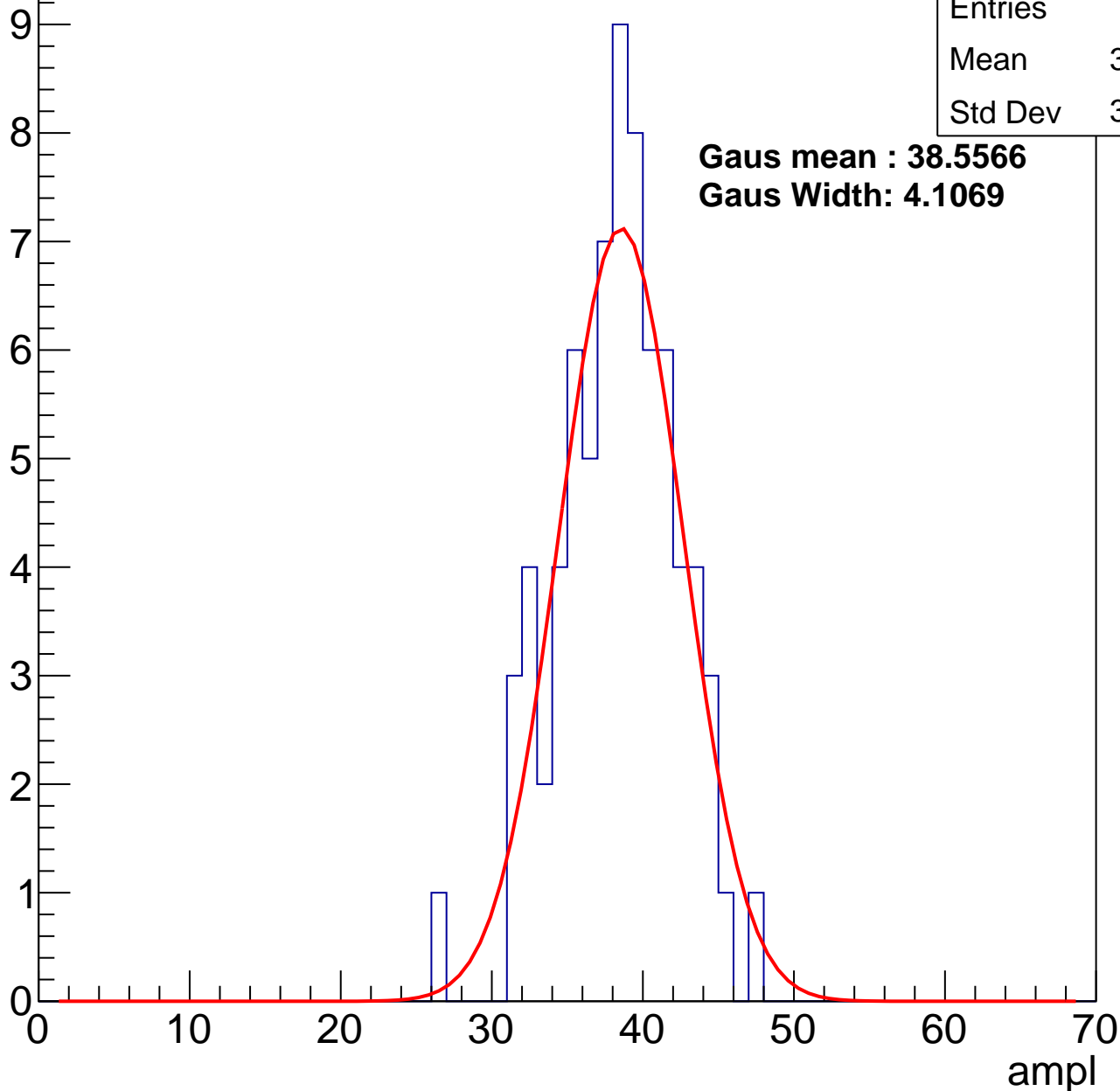
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.86
Std Dev	3.885

Gaus mean : 38.5566

Gaus Width: 4.1069



B1L101S, U23-ch116, adc2

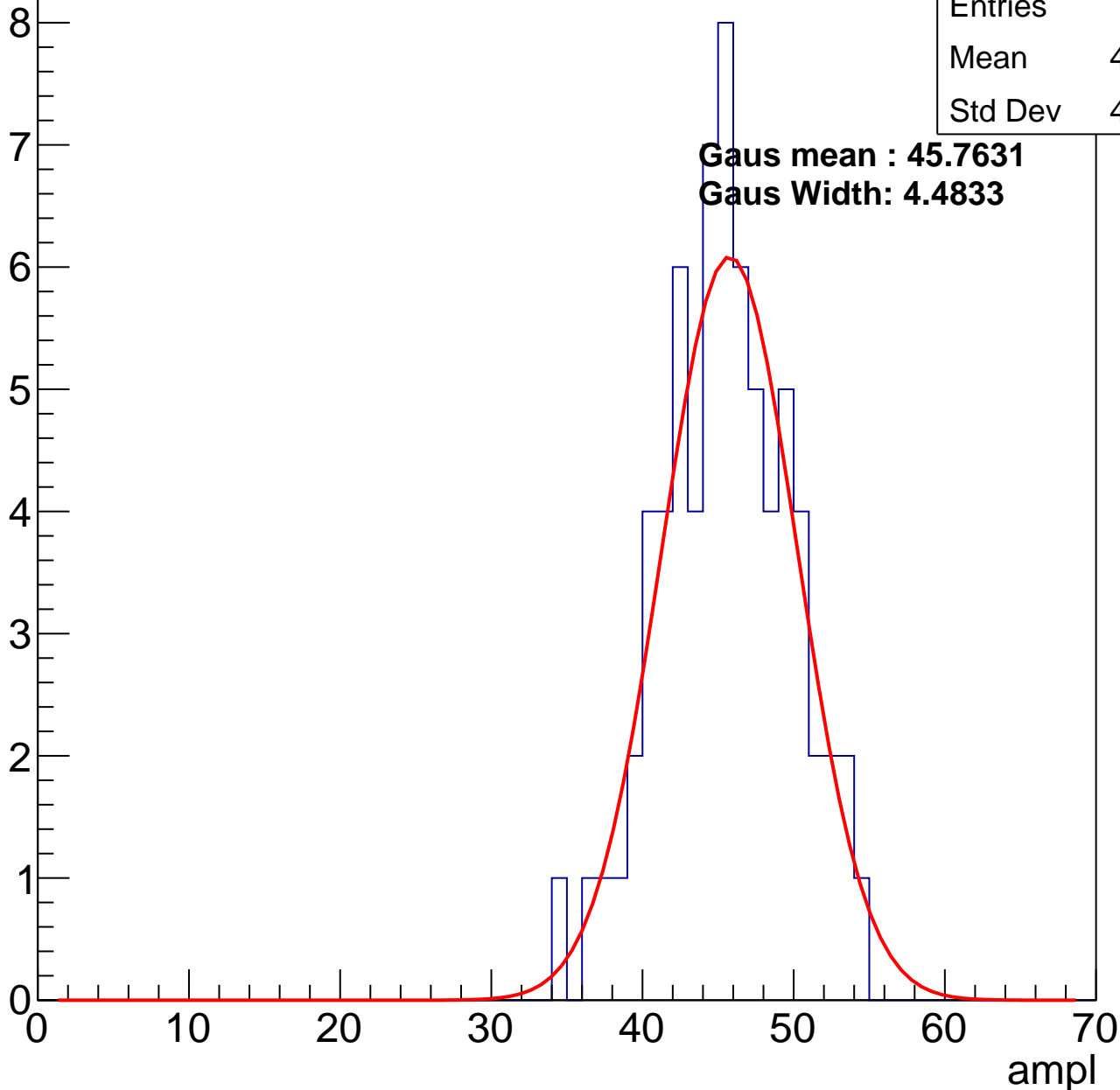
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	45.04
Std Dev	4.224

Gaus mean : 45.7631

Gaus Width: 4.4833

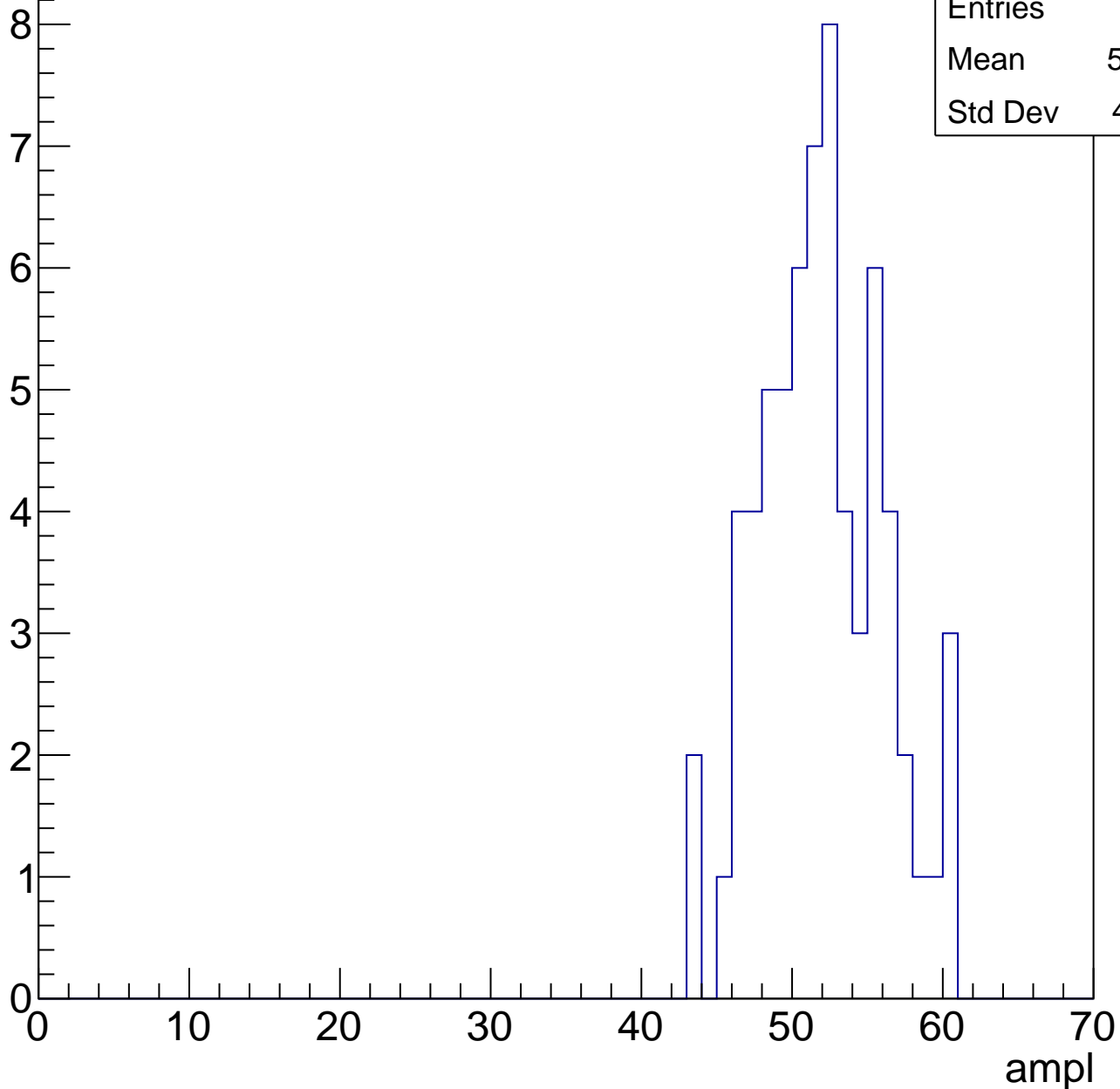


B1L101S, U23-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	51.52
Std Dev	4.001

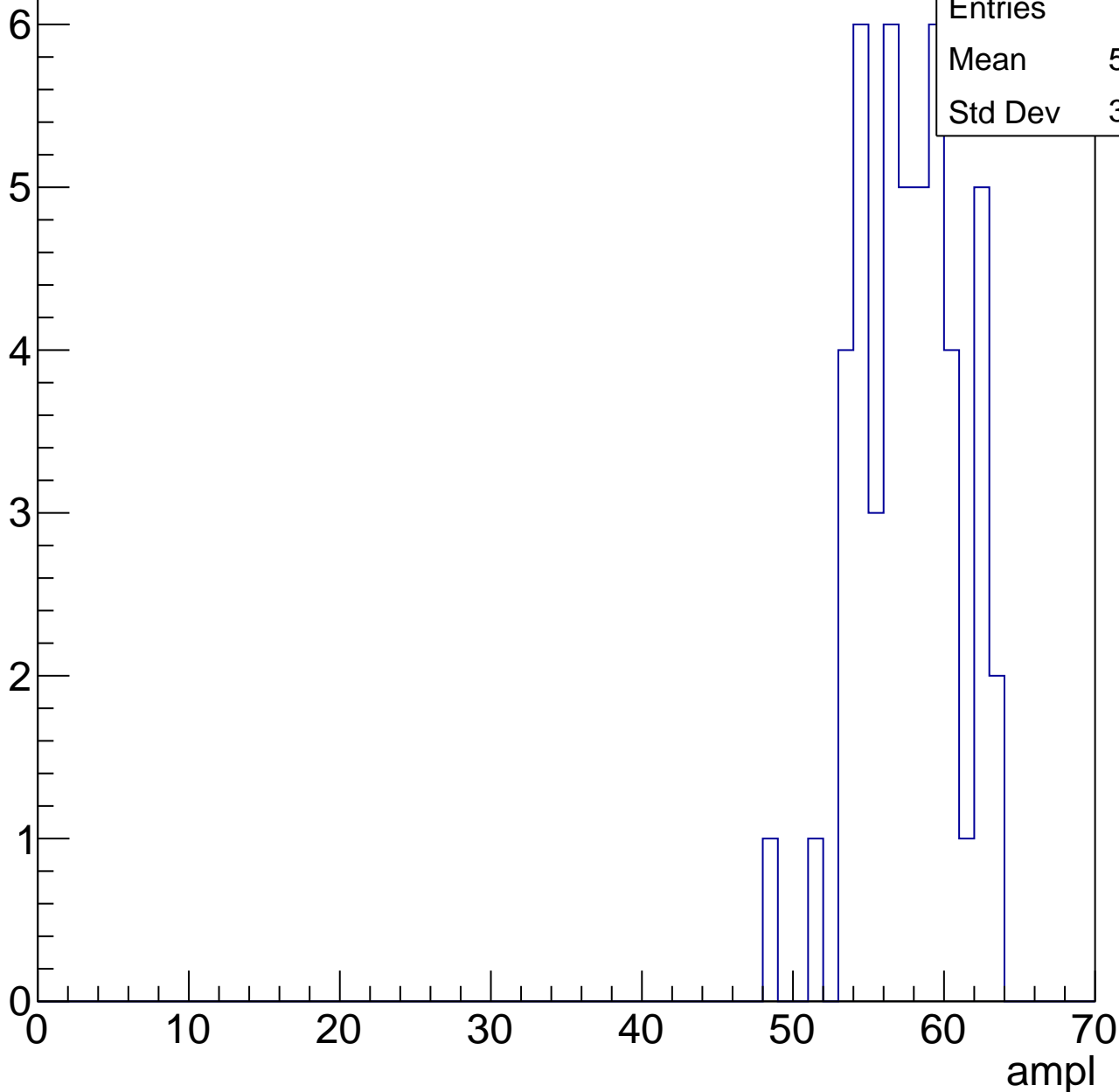


B1L101S, U23-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.18
Std Dev	3.287

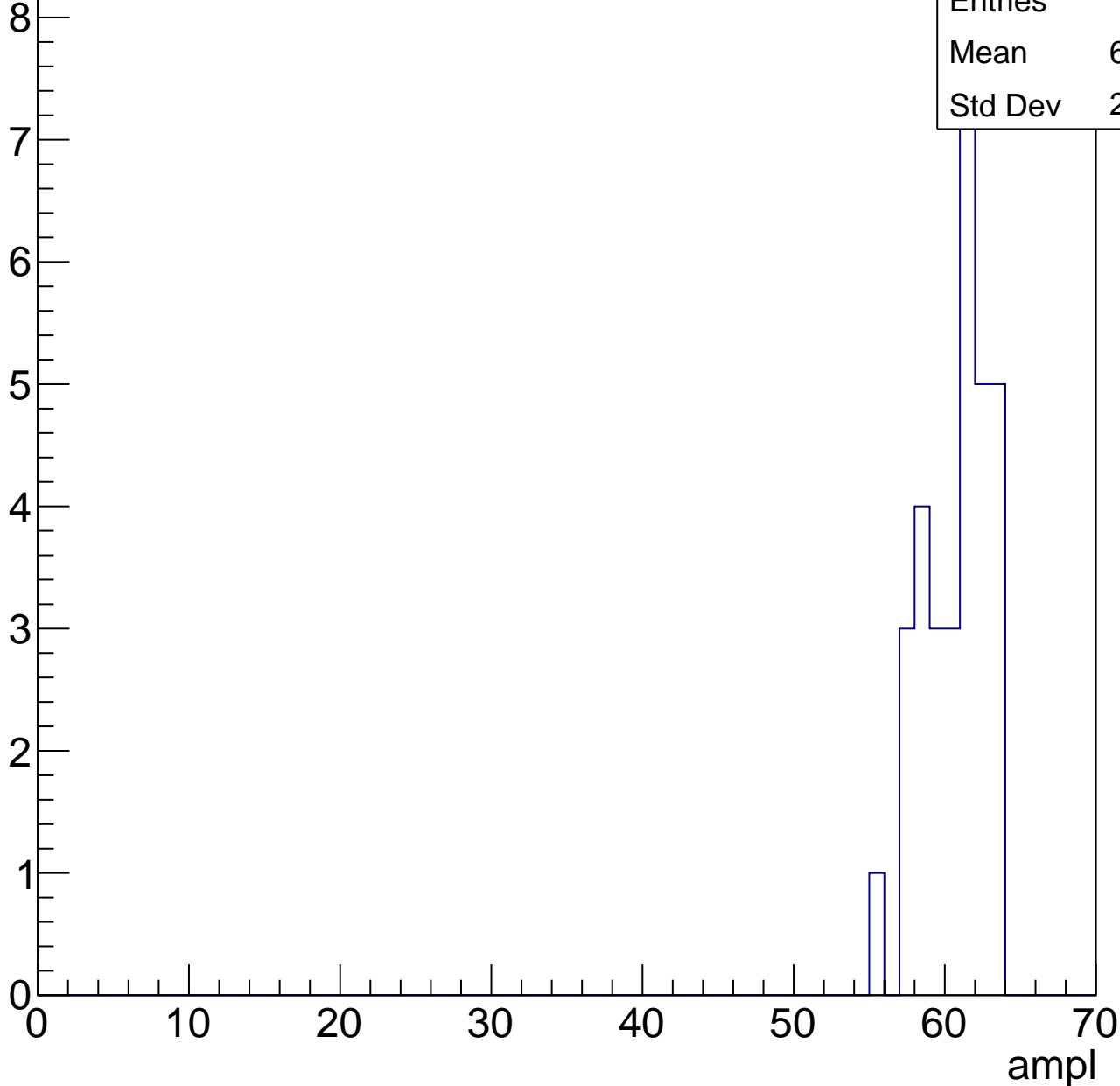


B1L101S, U23-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

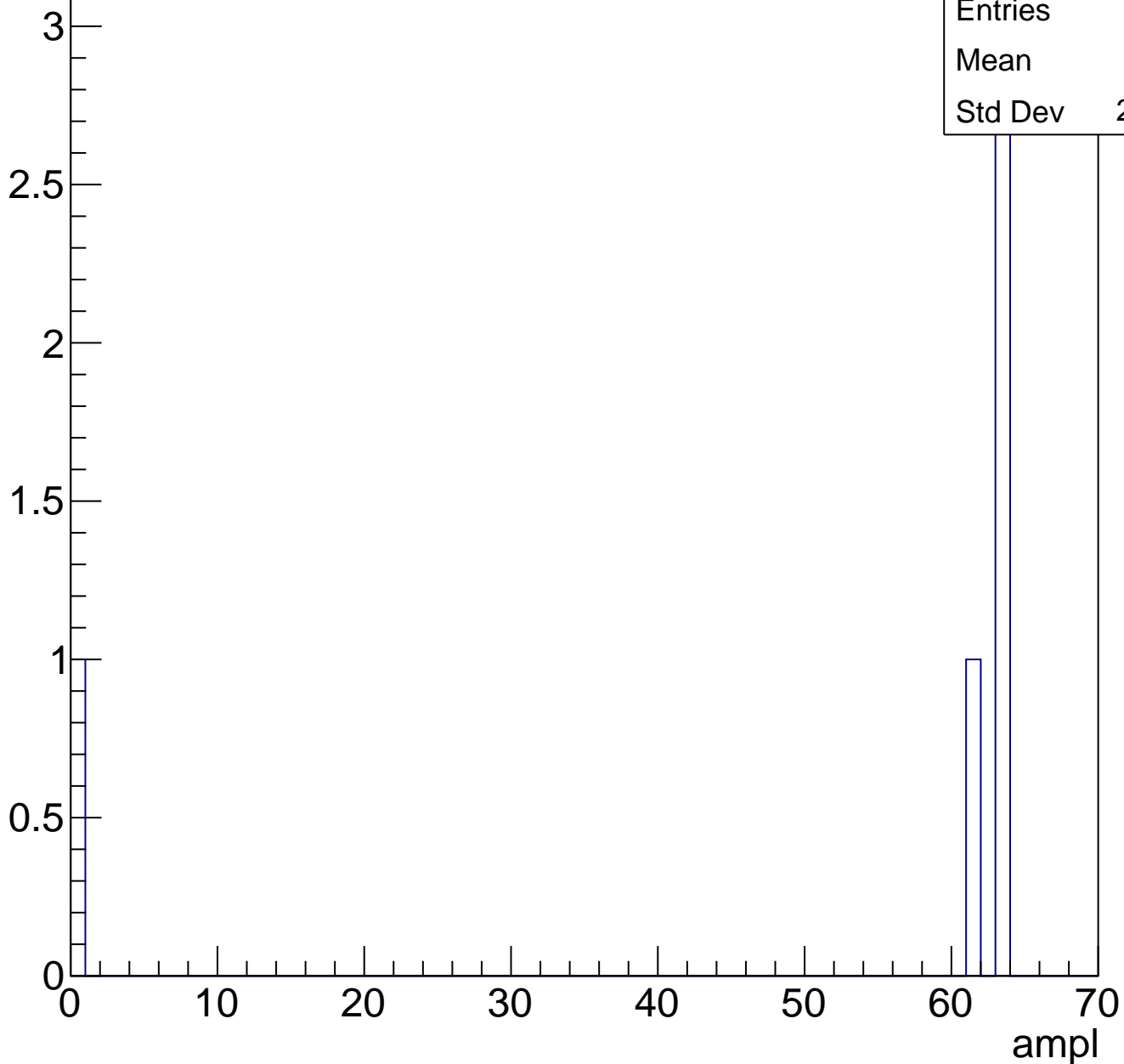
Entries	32
Mean	60.25
Std Dev	2.107



B1L101S, U23-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch117, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	82
Mean	30.39
Std Dev	4.97

Gaus mean : 31.3908

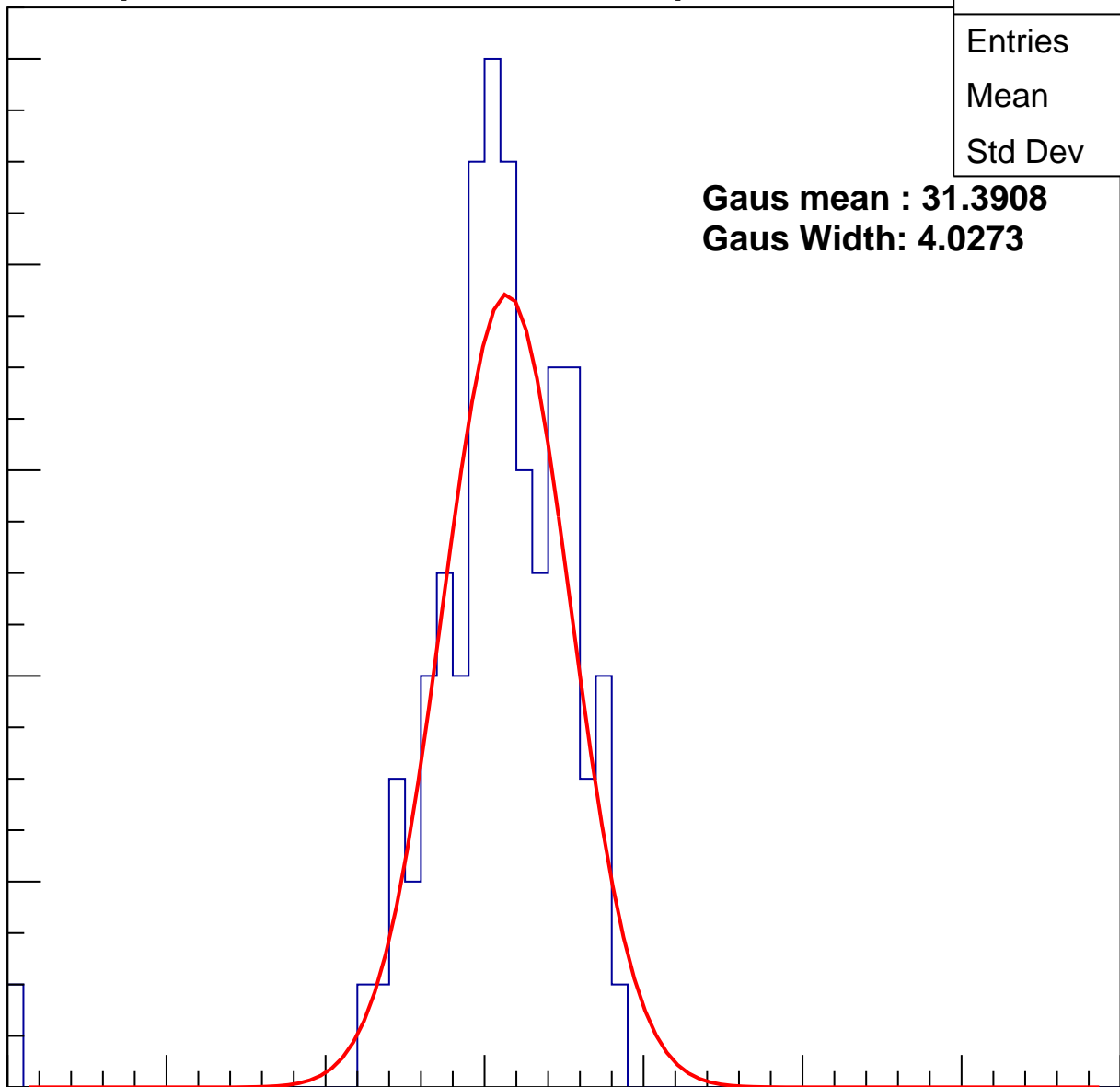
Gaus Width: 4.0273

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch117, adc1

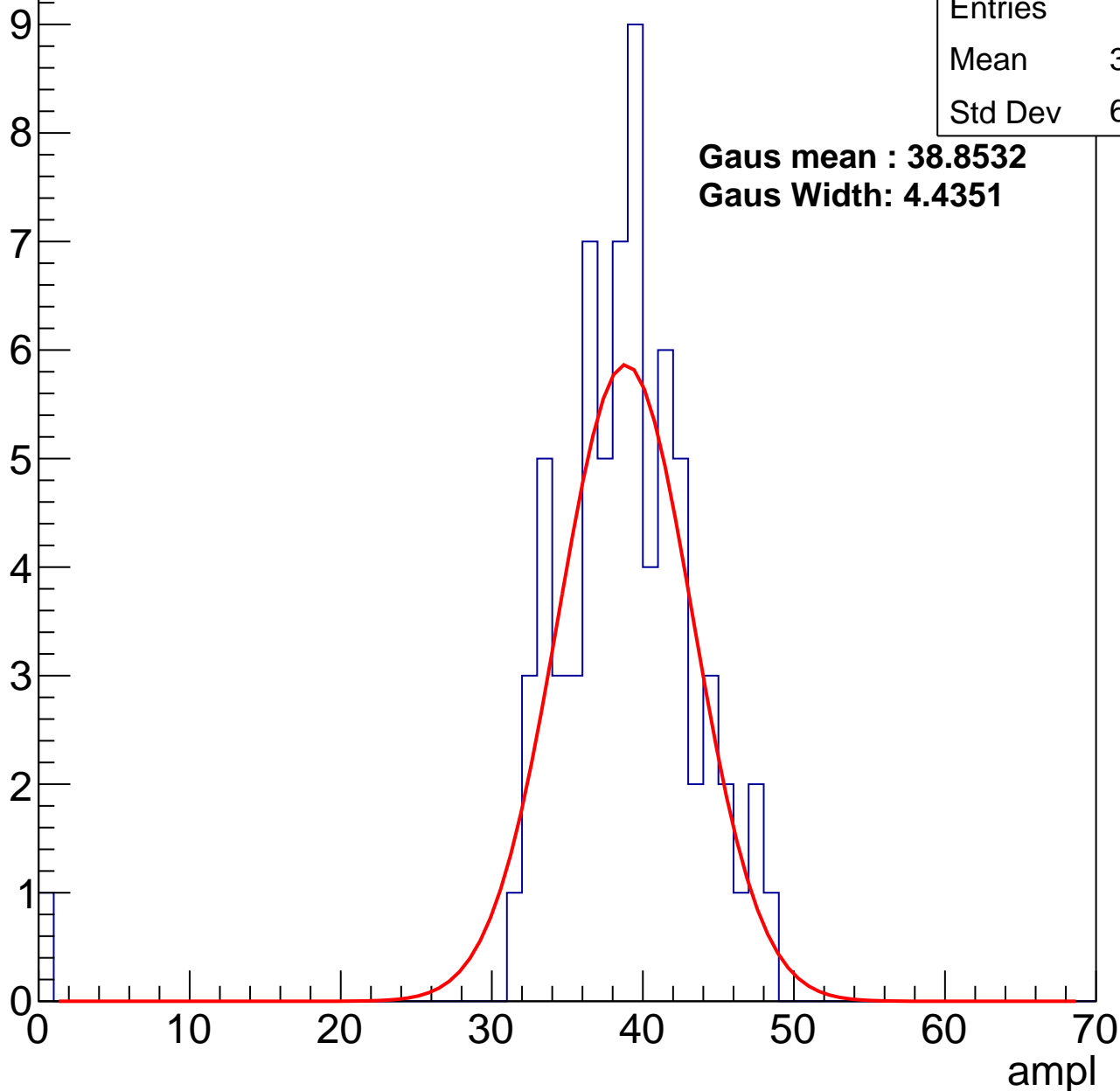
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	38.07
Std Dev	6.065

Gaus mean : 38.8532

Gaus Width: 4.4351



B1L101S, U23-ch117, adc2

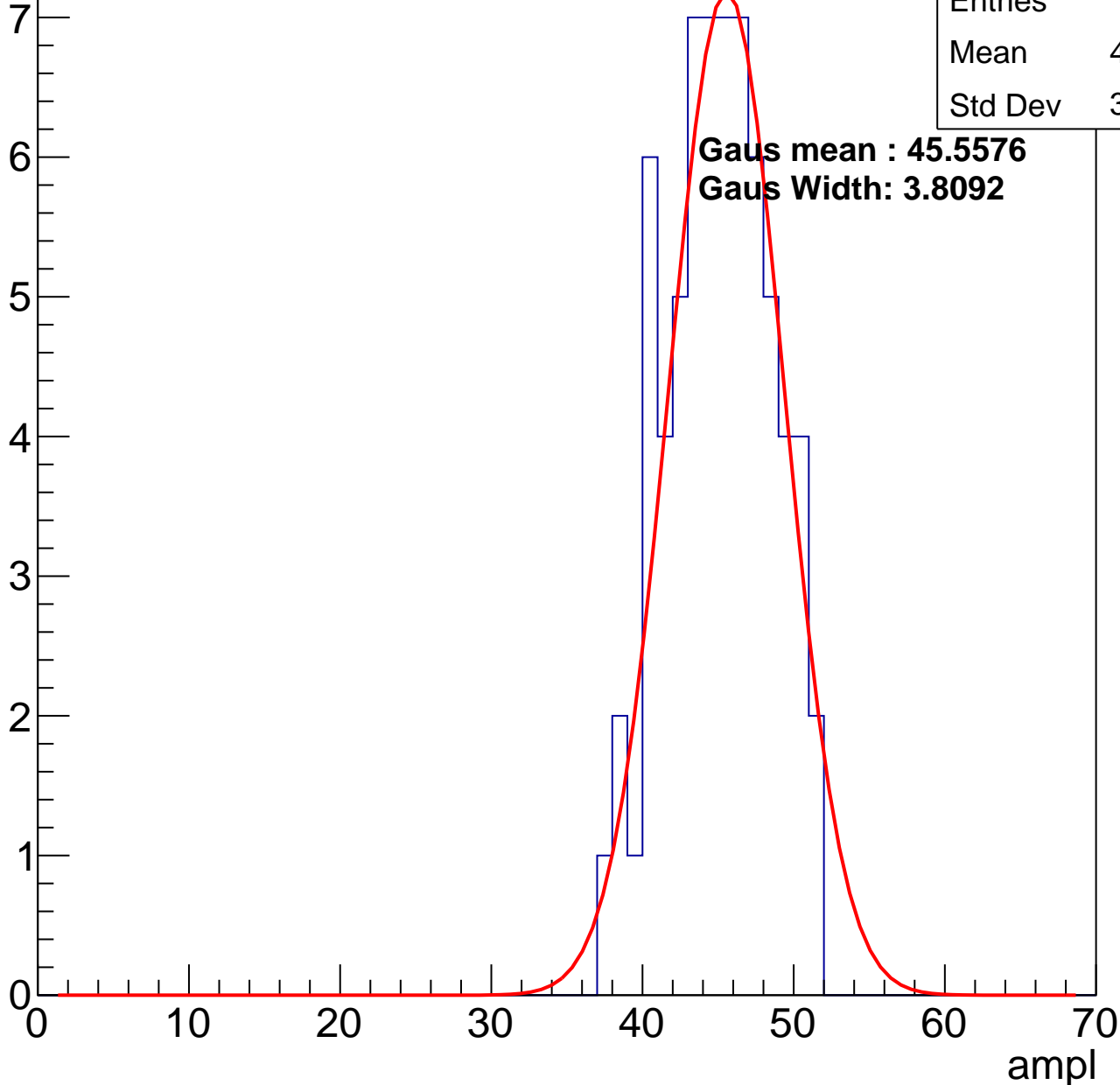
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.59
Std Dev	3.414

Gaus mean : 45.5576

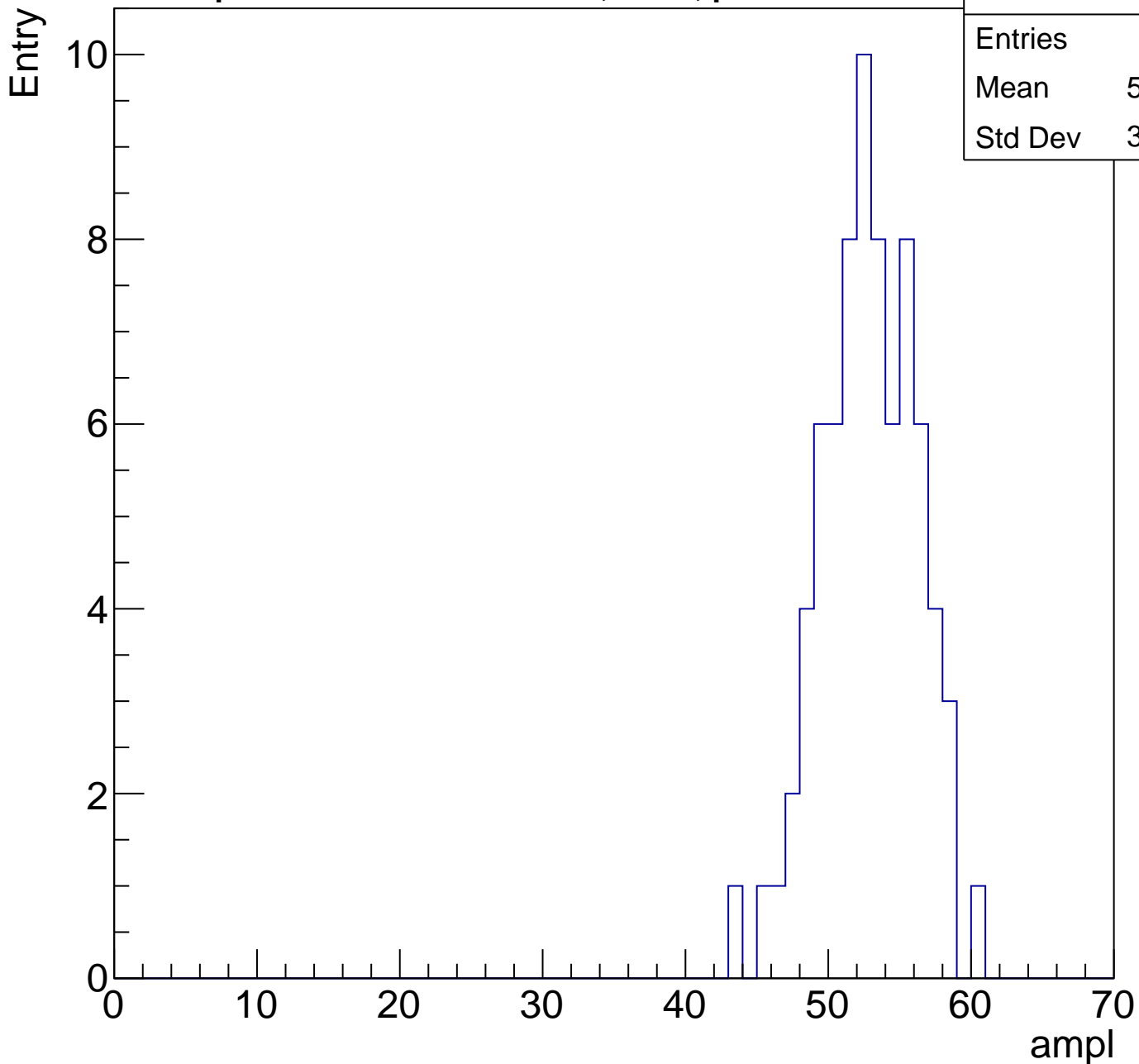
Gaus Width: 3.8092



B1L101S, U23-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	52.37
Std Dev	3.322

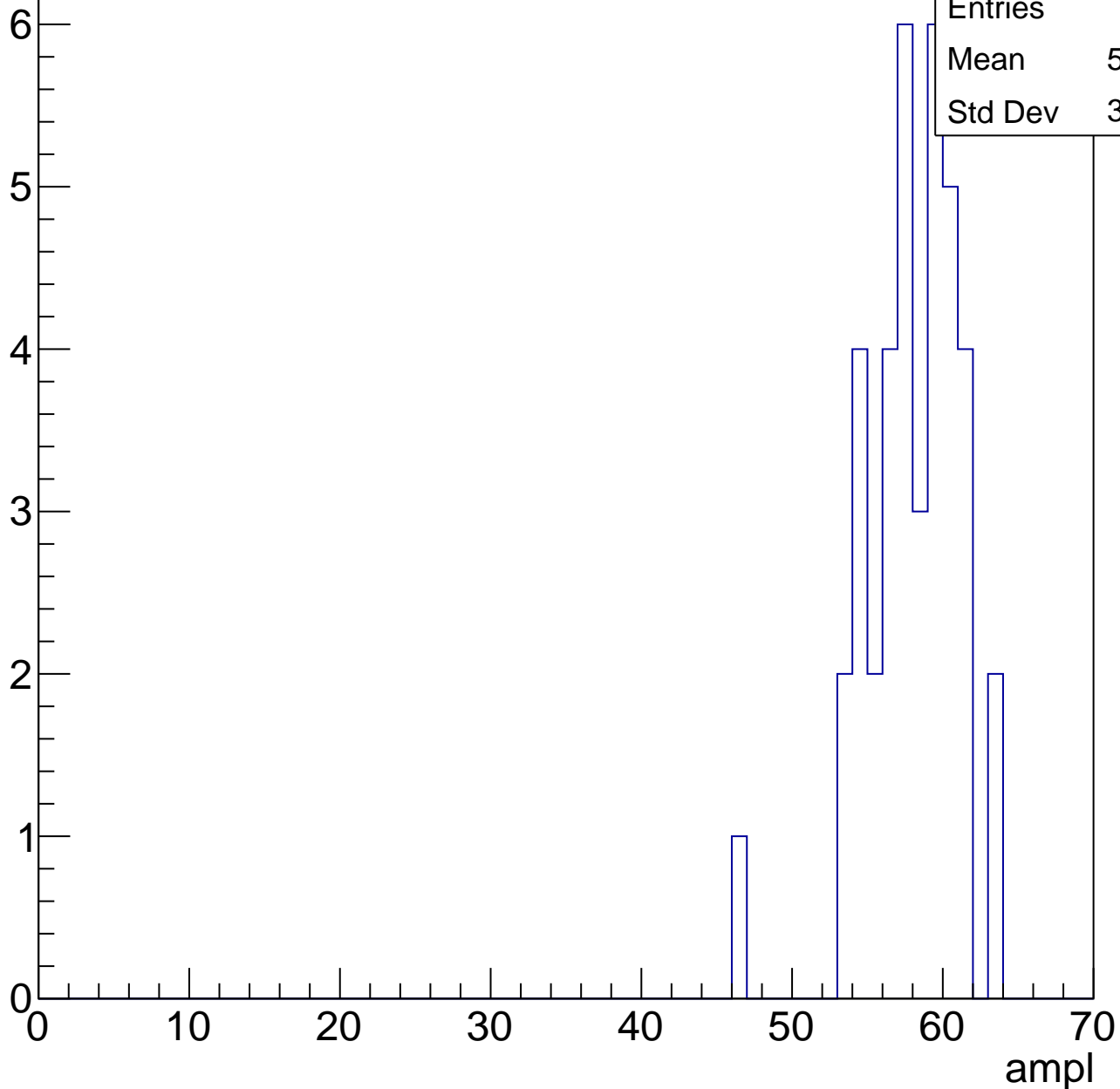


B1L101S, U23-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	57.49
Std Dev	3.202

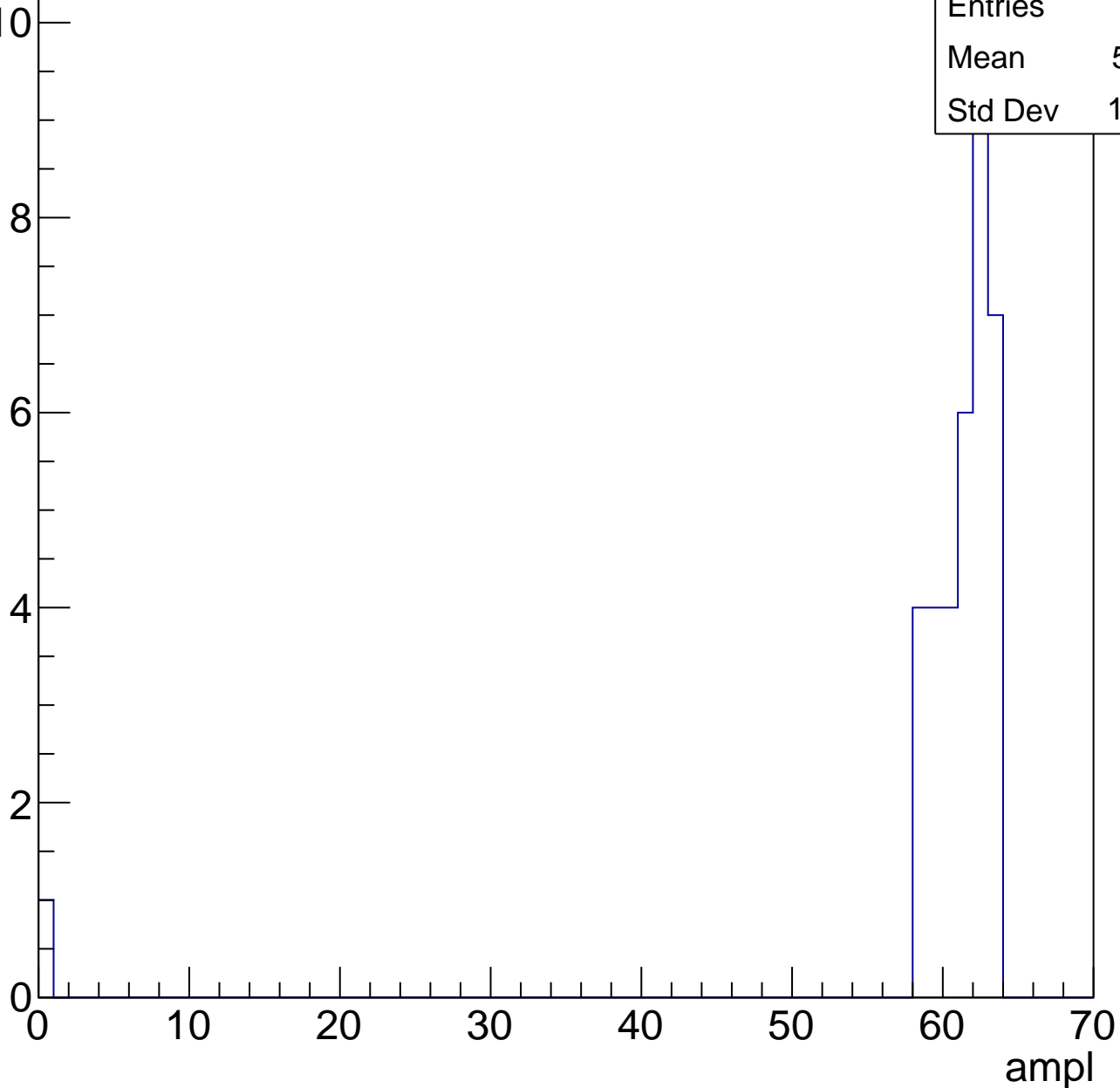


B1L101S, U23-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	59.31
Std Dev	10.15



B1L101S, U23-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	63
Std Dev	0

B1L101S, U23-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch118, adc0

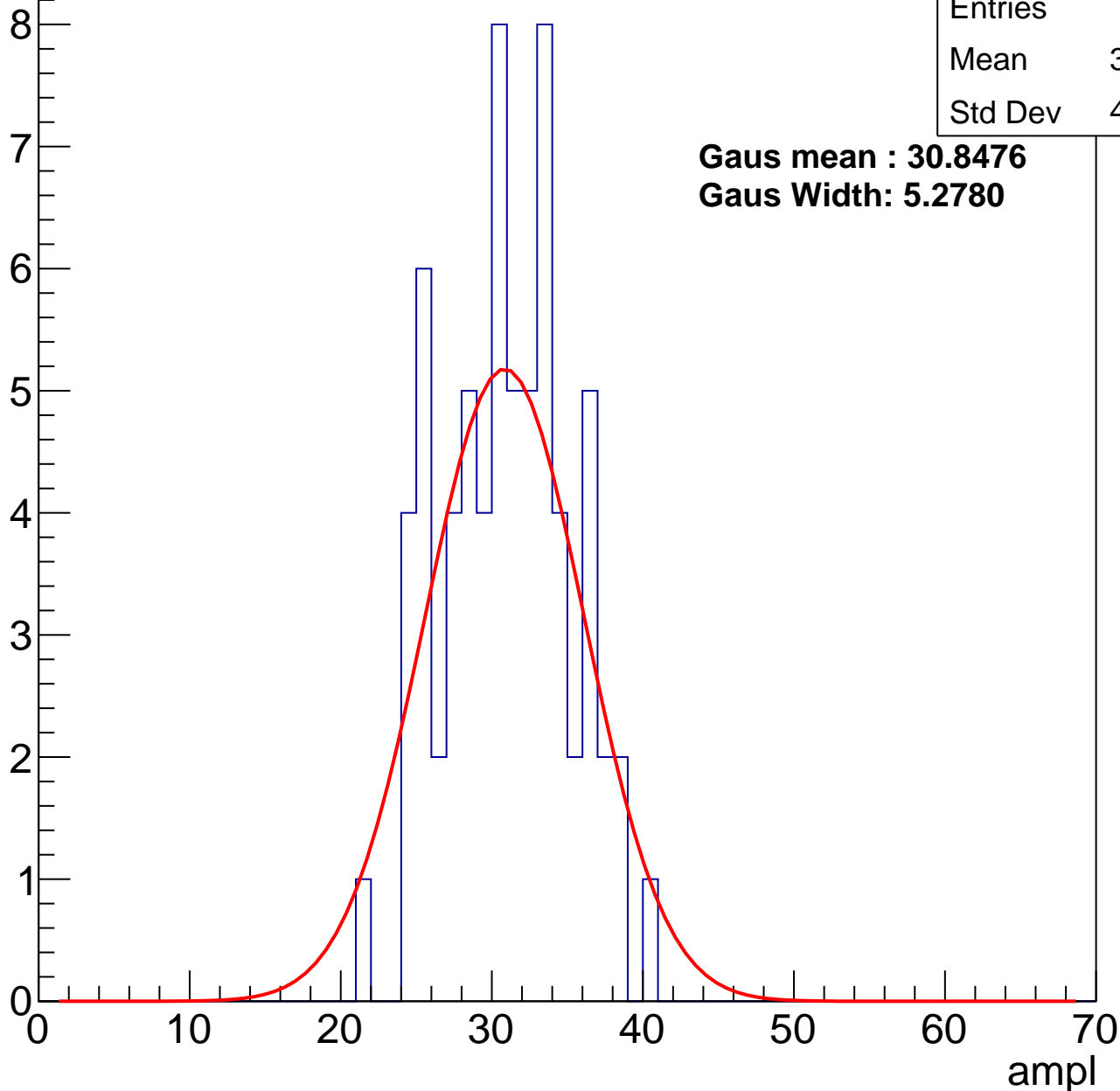
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.56
Std Dev	4.135

Gaus mean : 30.8476

Gaus Width: 5.2780



B1L101S, U23-ch118, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	95
Mean	38.4
Std Dev	4.084

Gaus mean : 39.2338

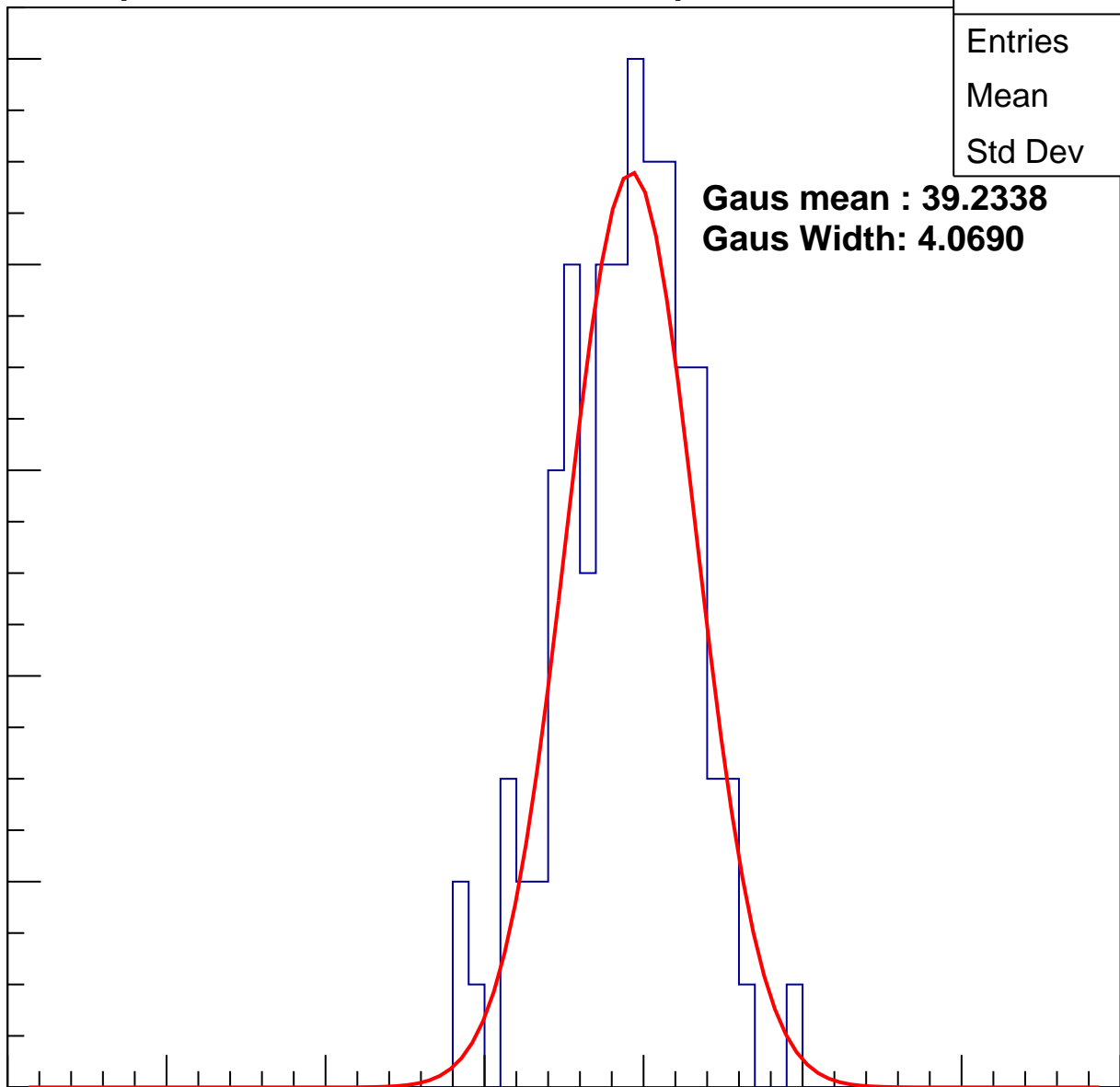
Gaus Width: 4.0690

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U23-ch118, adc2

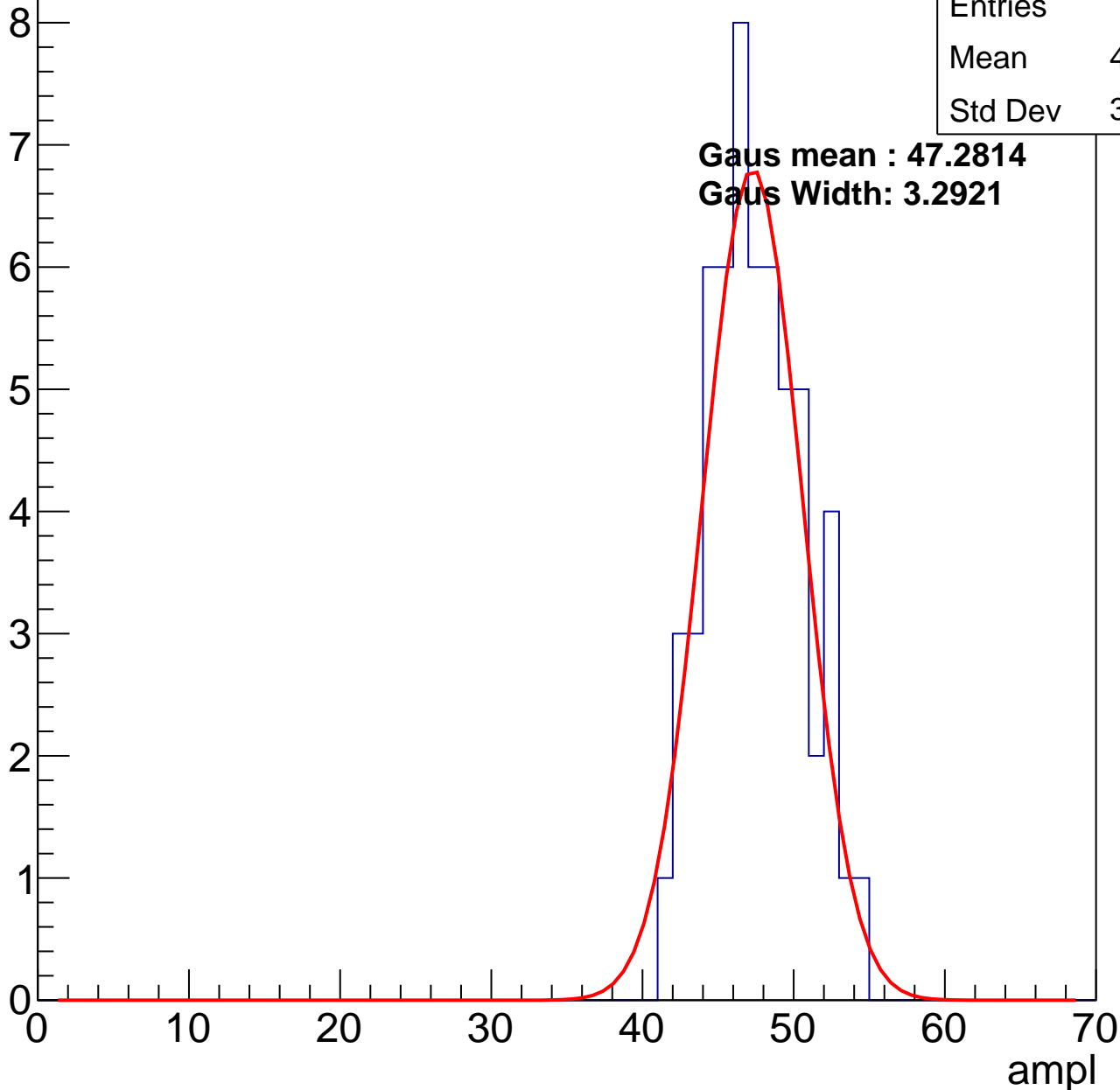
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	47.02
Std Dev	3.058

Gaus mean : 47.2814

Gaus Width: 3.2921

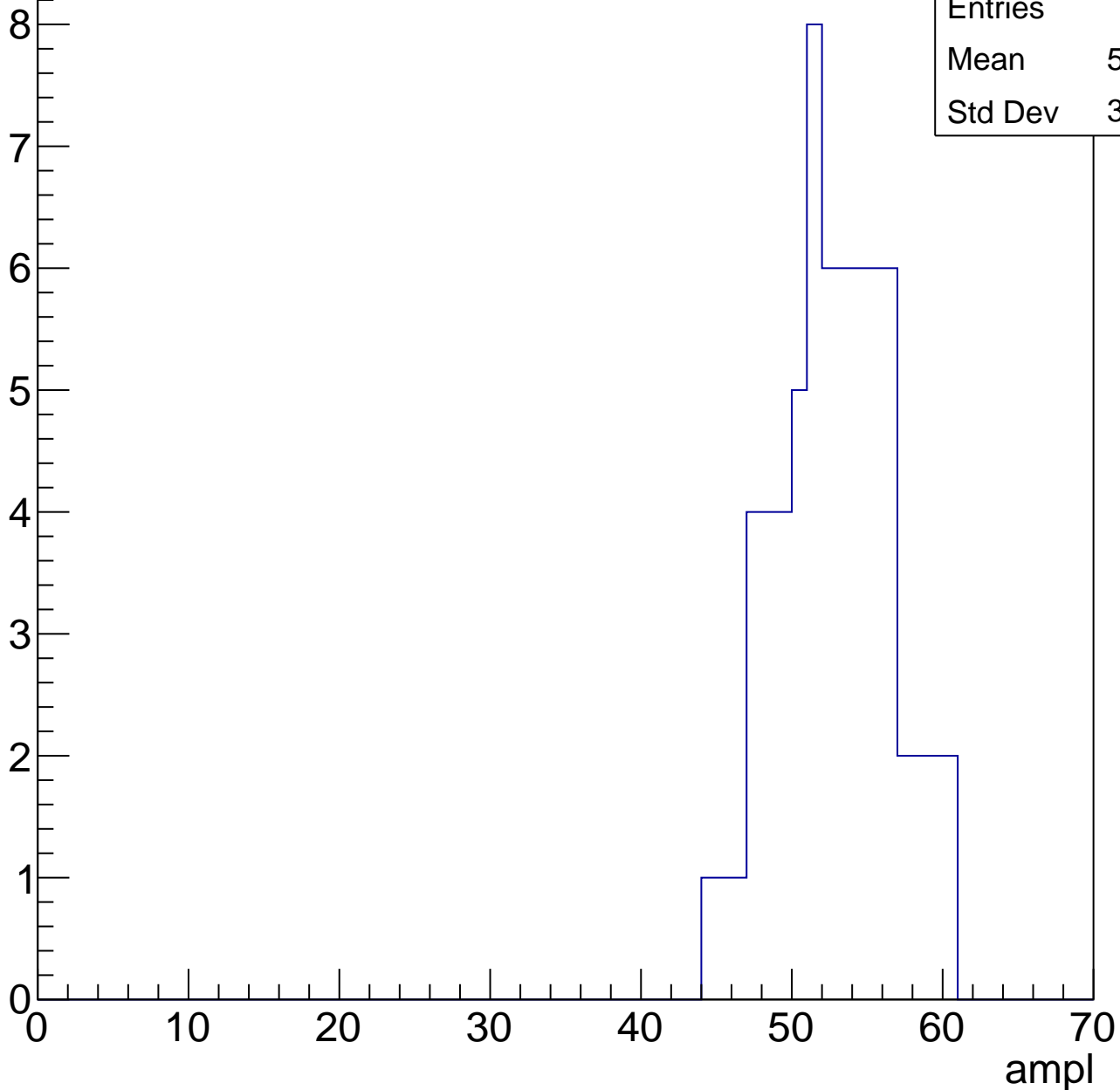


B1L101S, U23-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	52.38
Std Dev	3.684

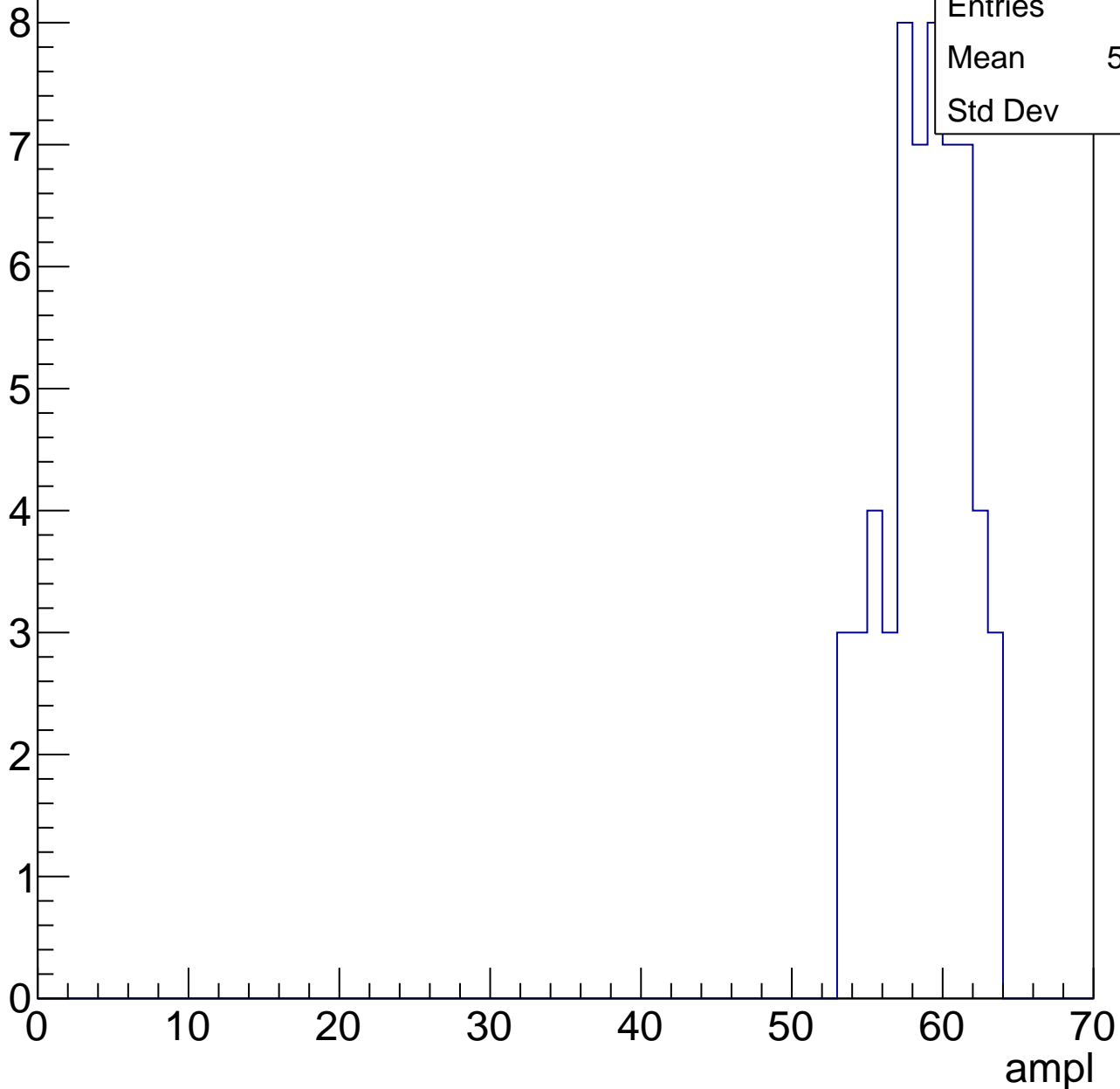


B1L101S, U23-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	58.37
Std Dev	2.68

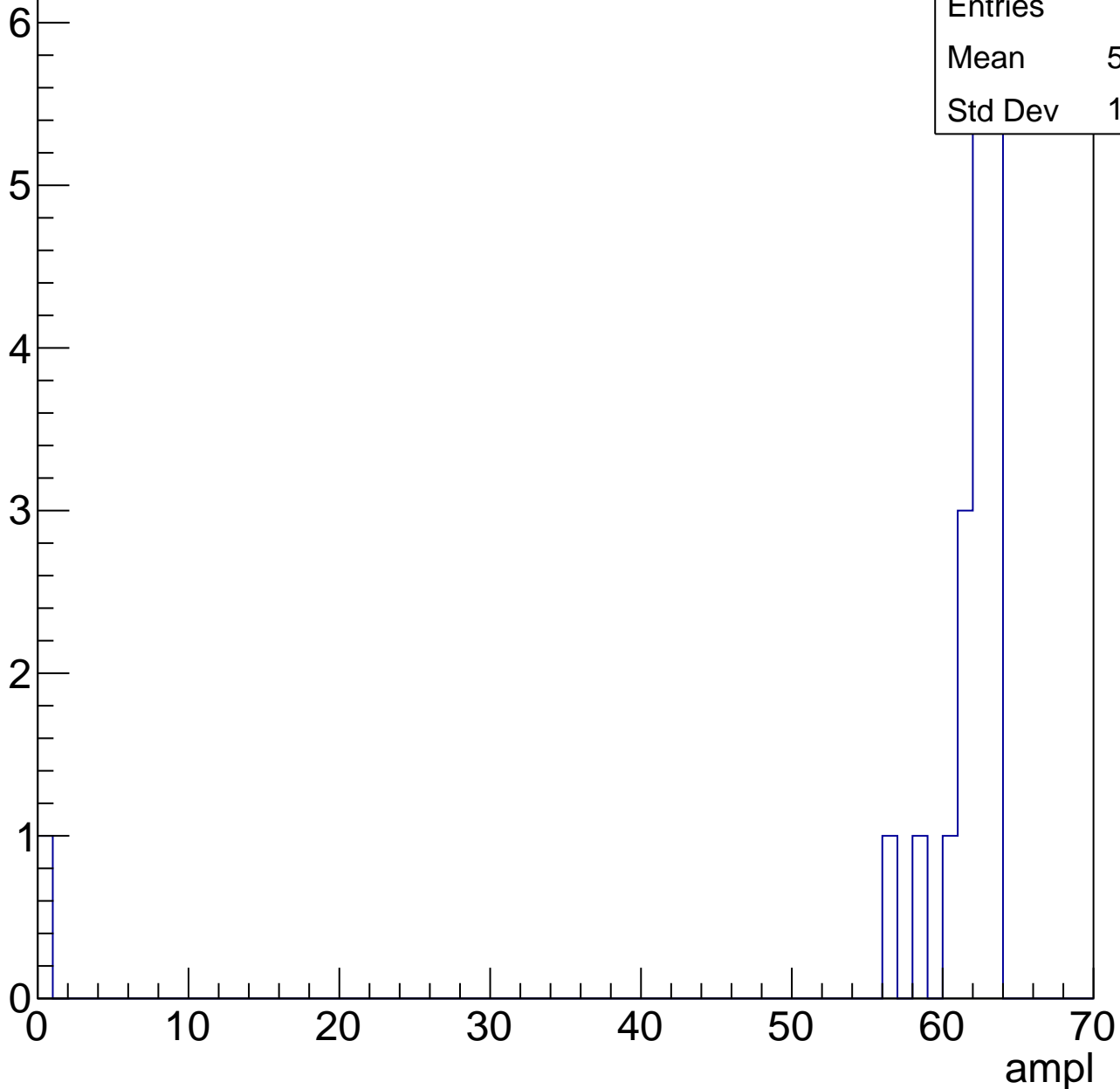


B1L101S, U23-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	58.26
Std Dev	13.85



B1L101S, U23-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U23-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch119, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	32.59
Std Dev	5.42

Gaus mean : 33.2496

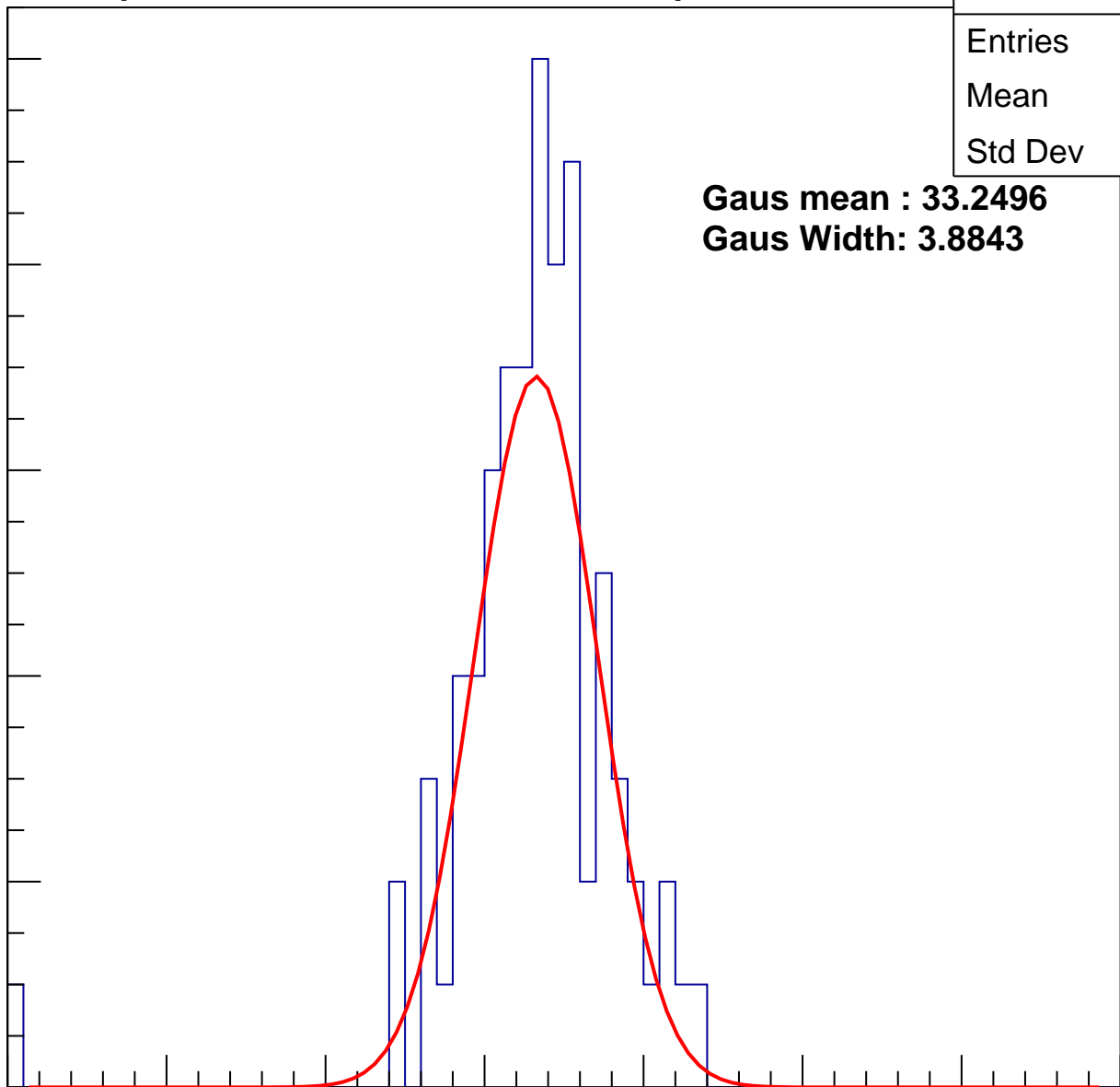
Gaus Width: 3.8843

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch119, adc1

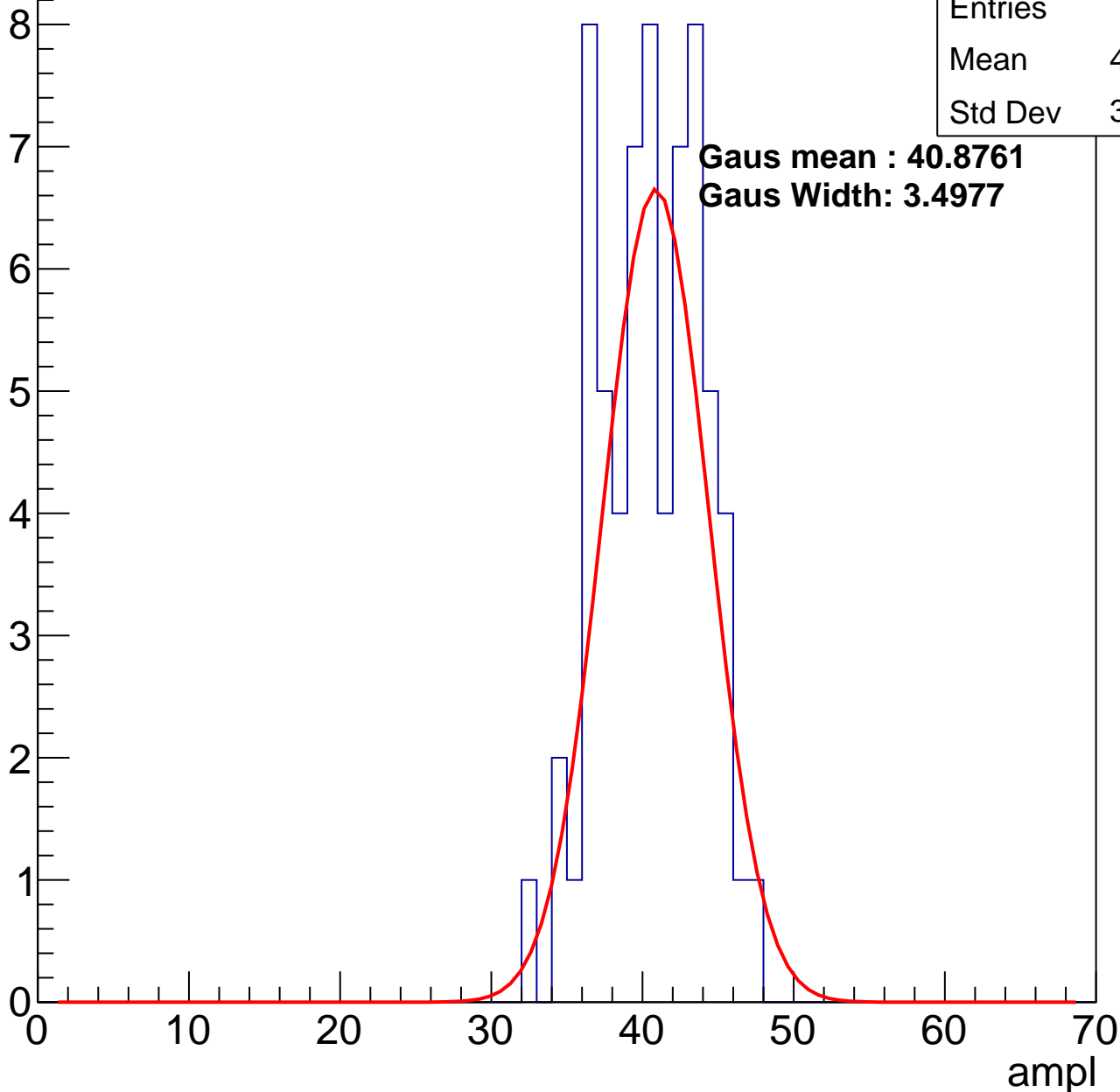
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	40.12
Std Dev	3.314

Gaus mean : 40.8761

Gaus Width: 3.4977



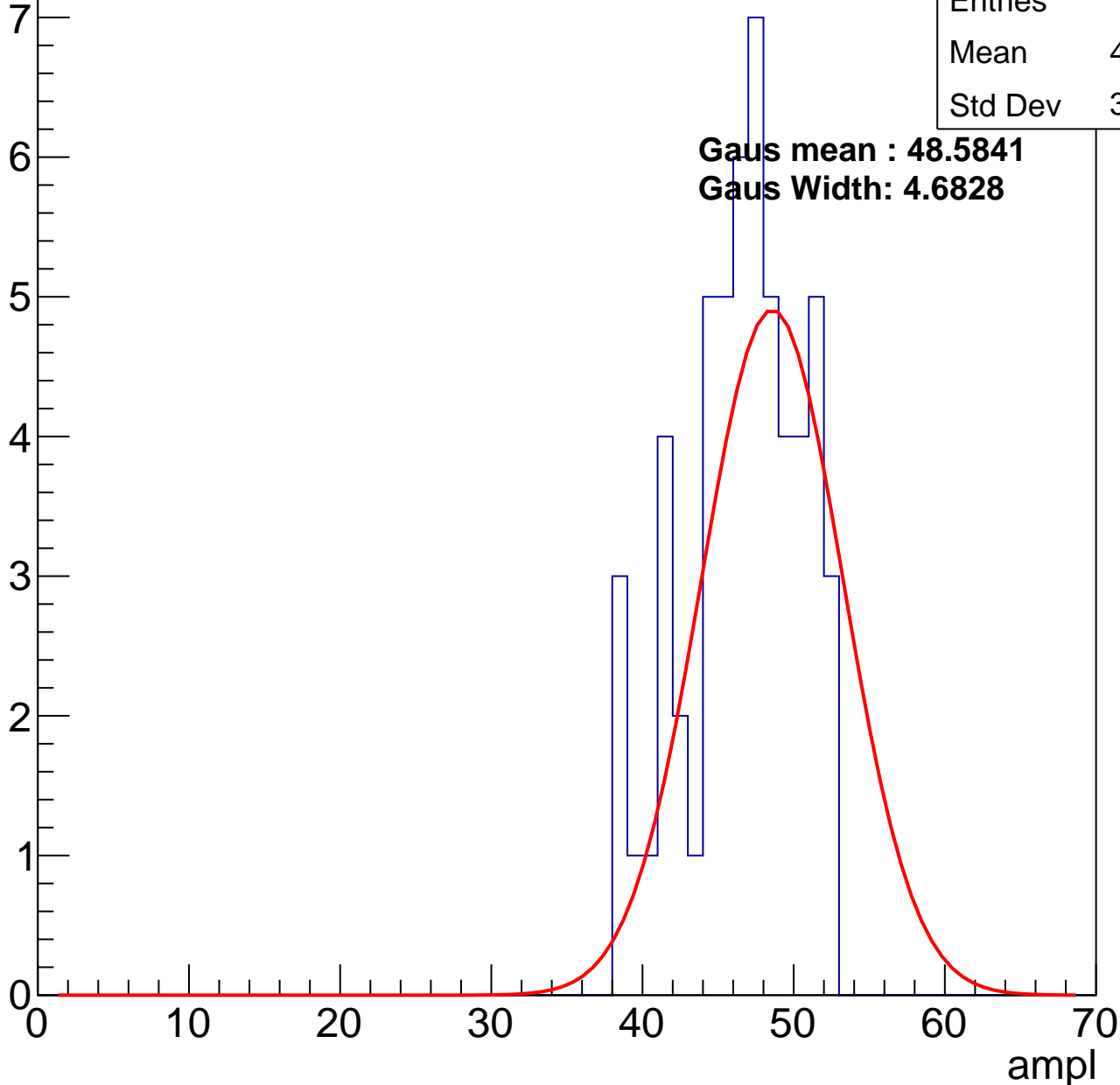
B1L101S, U23-ch119, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	46.09
Std Dev	3.786

Gaus mean : 48.5841
Gaus Width: 4.6828

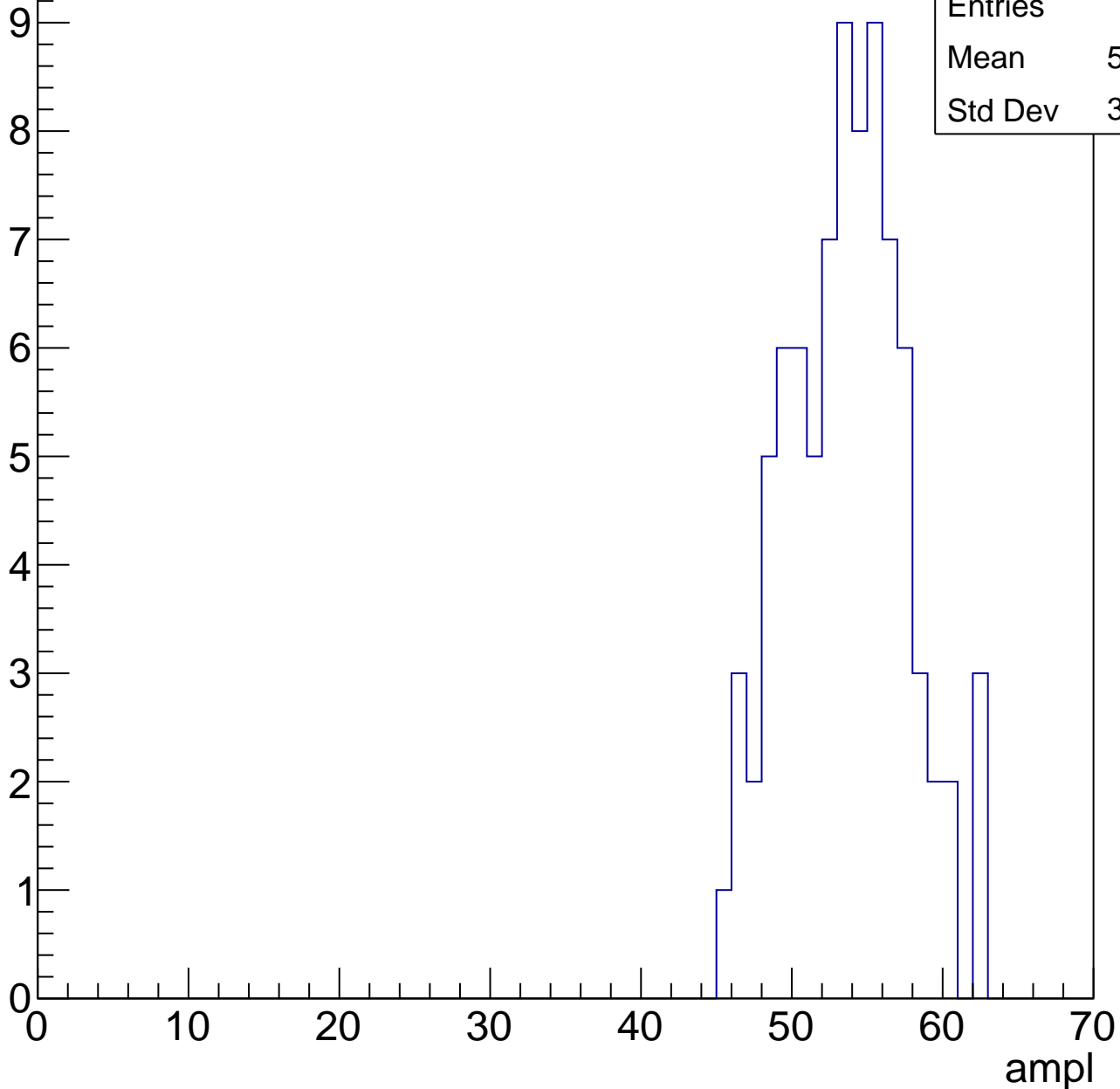


B1L101S, U23-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	53.17
Std Dev	3.894



B1L101S, U23-ch119, adc4

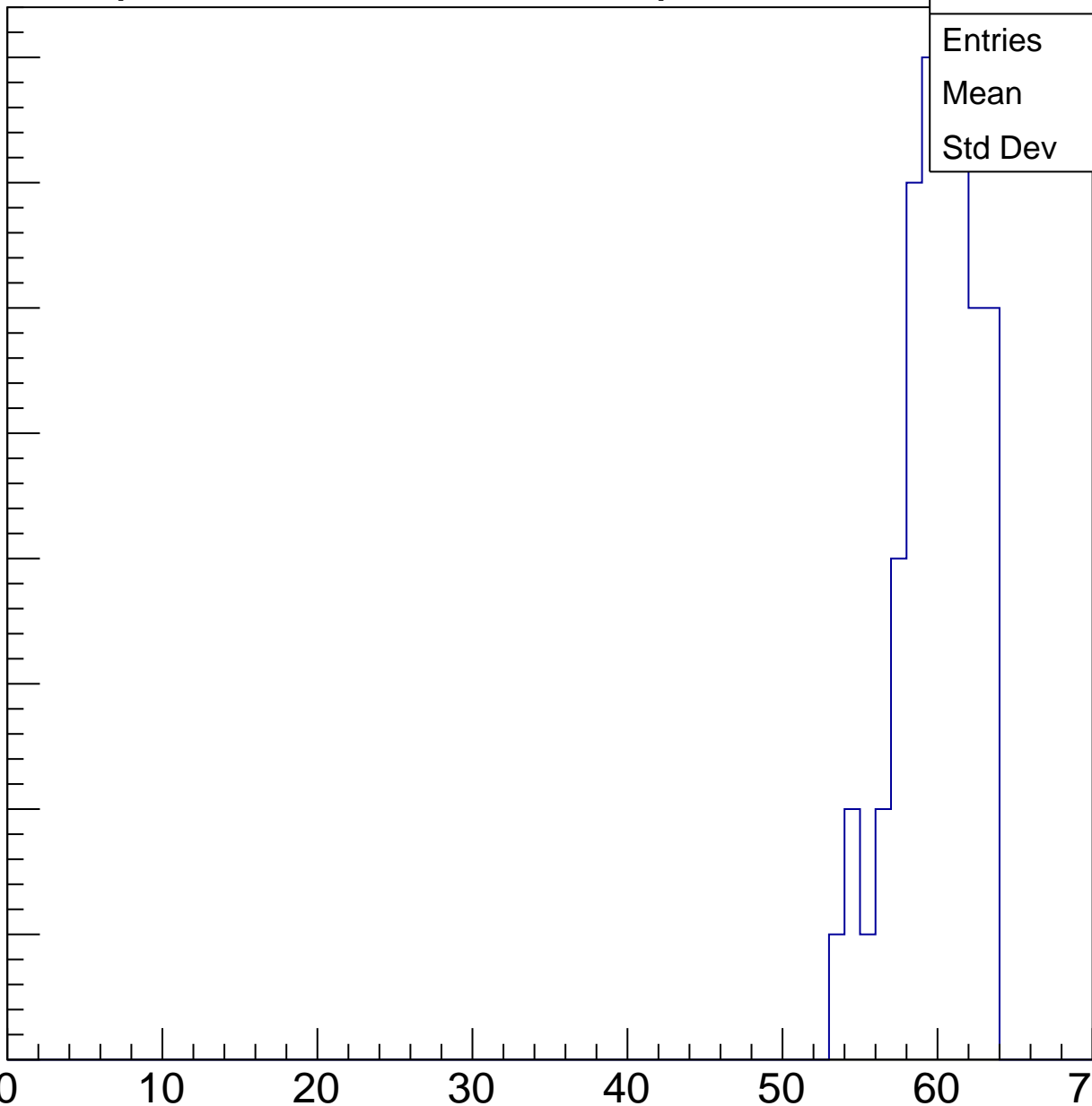
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.47
Std Dev	2.462

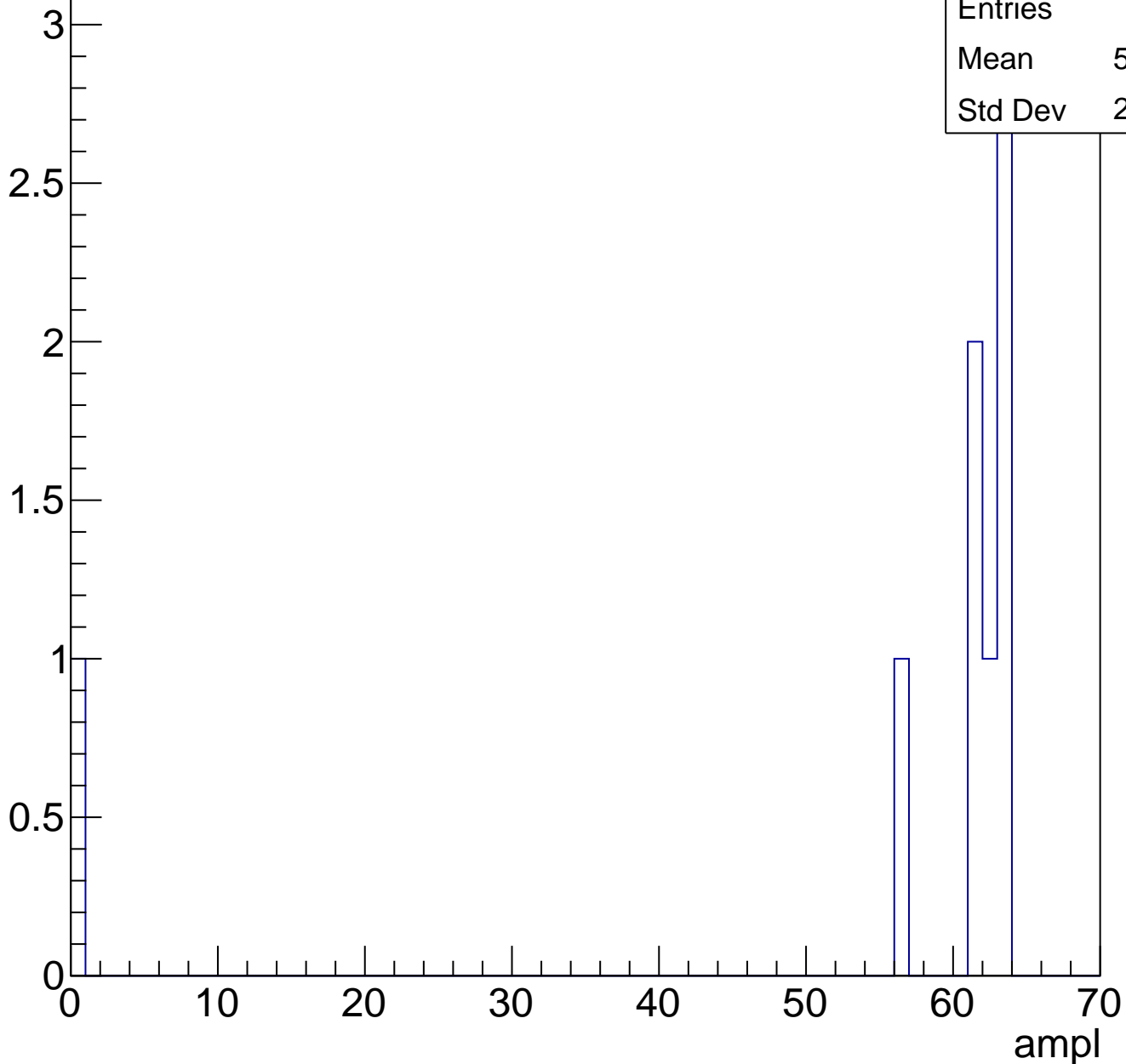
ampl



B1L101S, U23-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch120, adc0

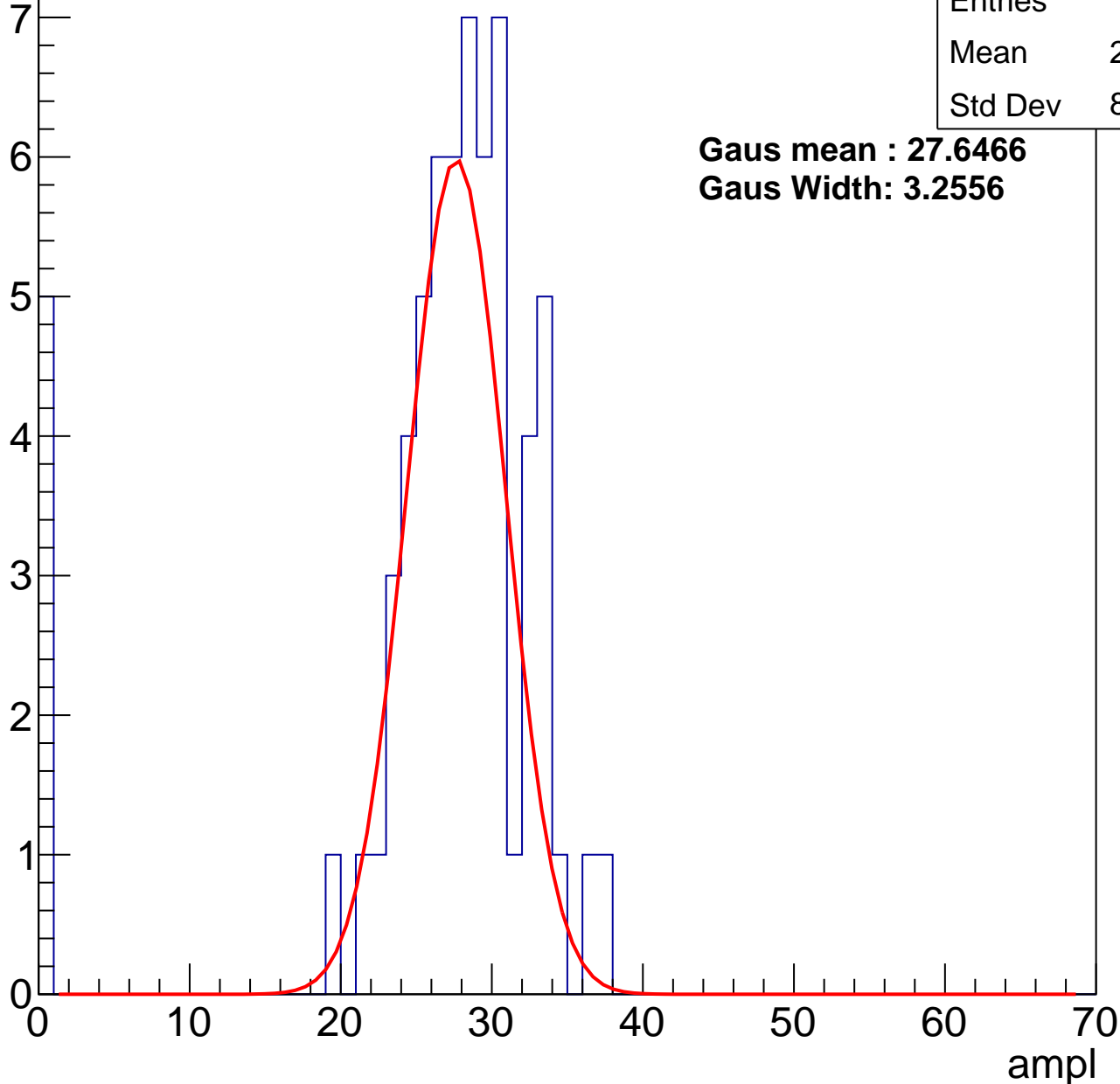
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	25.86
Std Dev	8.242

Gaus mean : 27.6466

Gaus Width: 3.2556



B1L101S, U23-ch120, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	98
Mean	35.8
Std Dev	3.992

Gaus mean : 36.0318

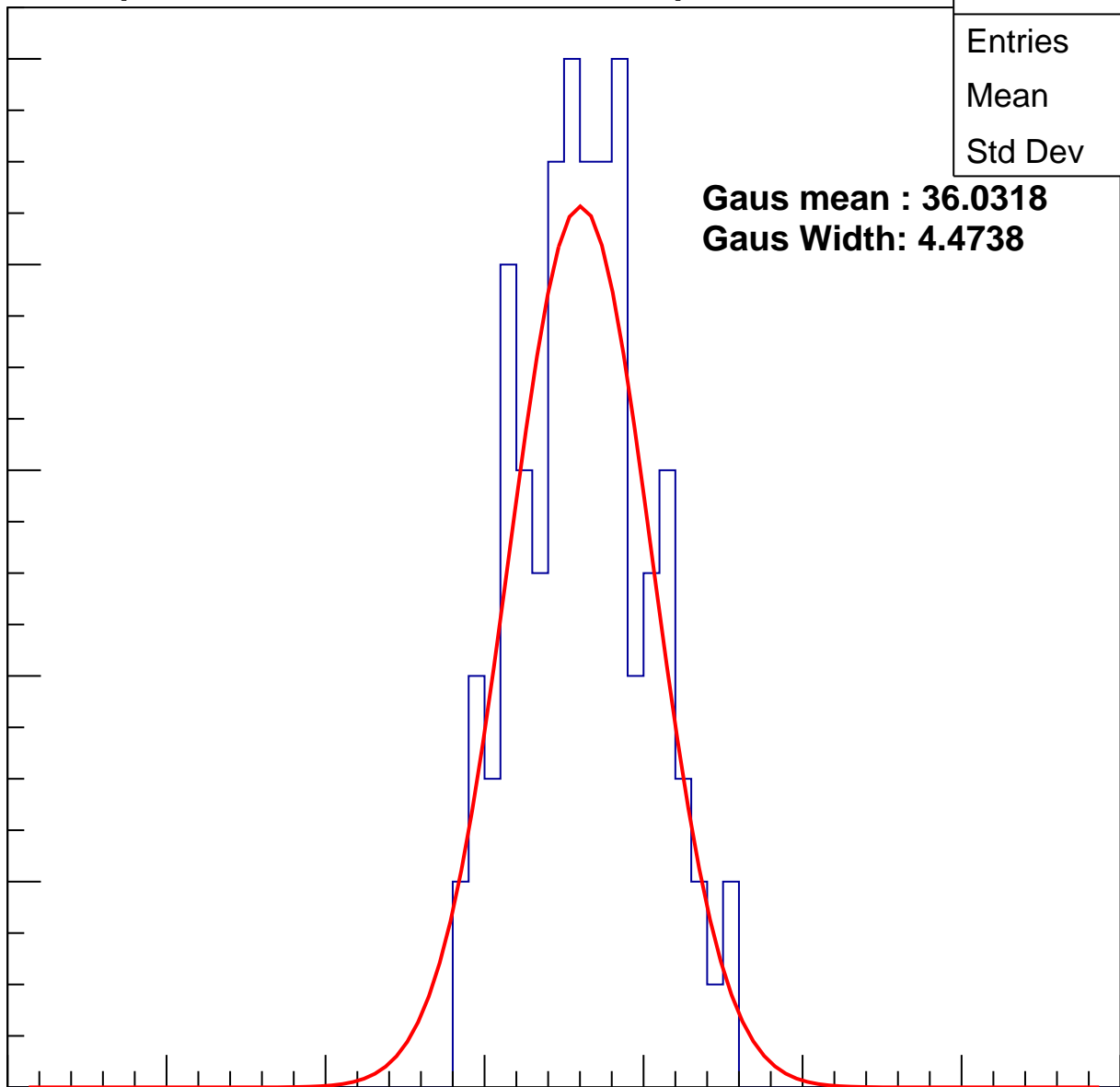
Gaus Width: 4.4738

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U23-ch120, adc2

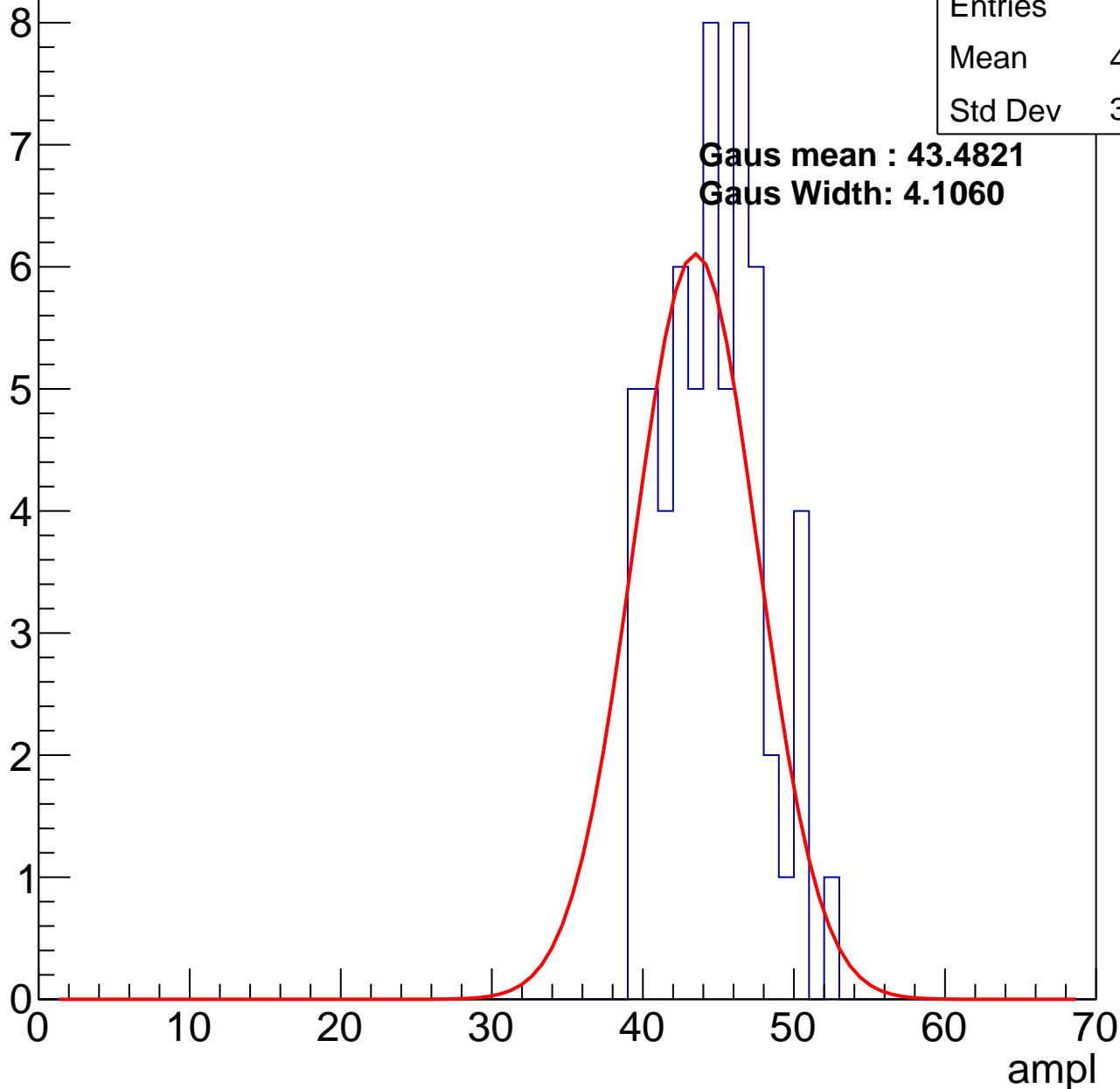
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.17
Std Dev	3.226

Gaus mean : 43.4821

Gaus Width: 4.1060

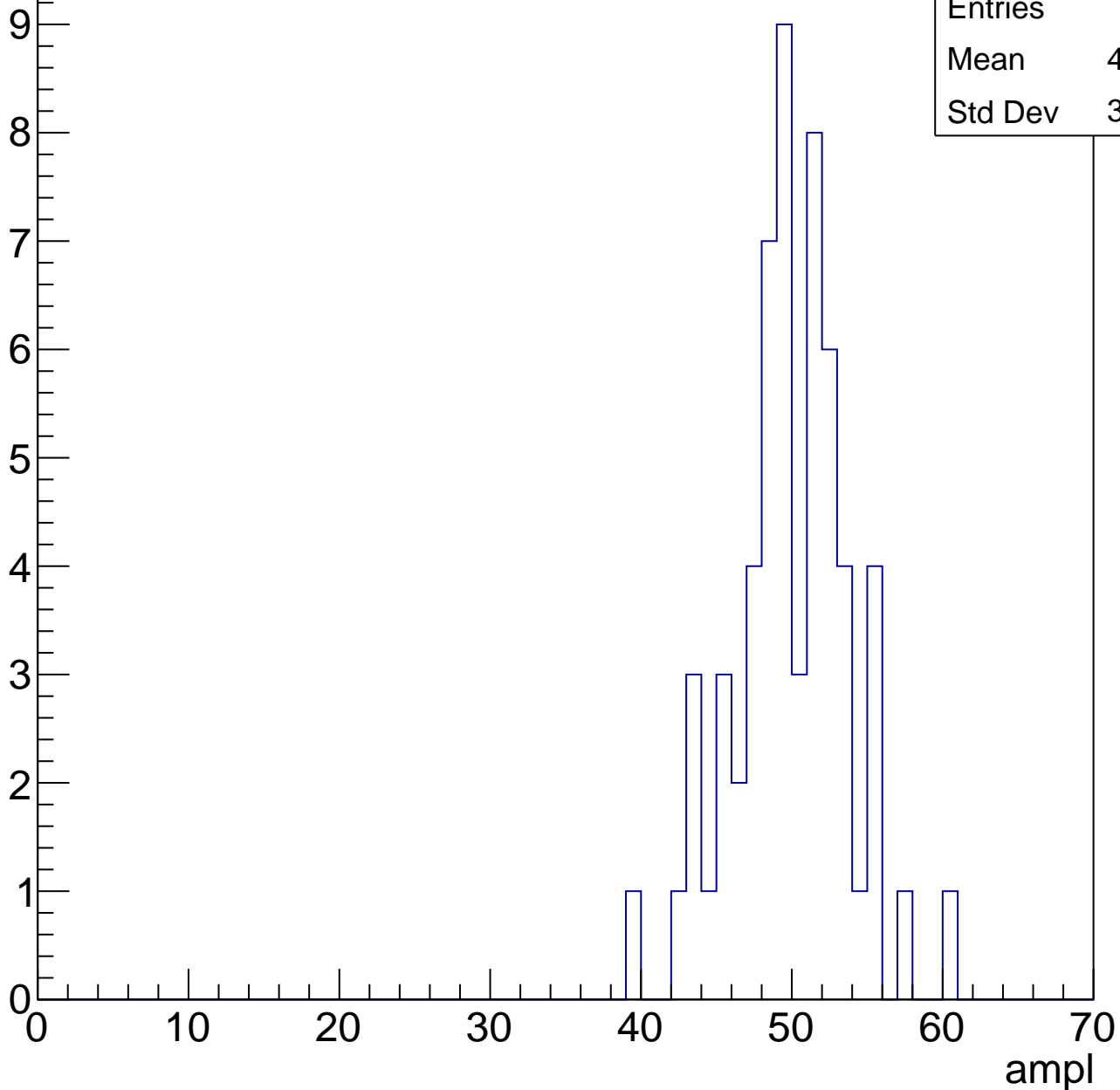


B1L101S, U23-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.47
Std Dev	3.828

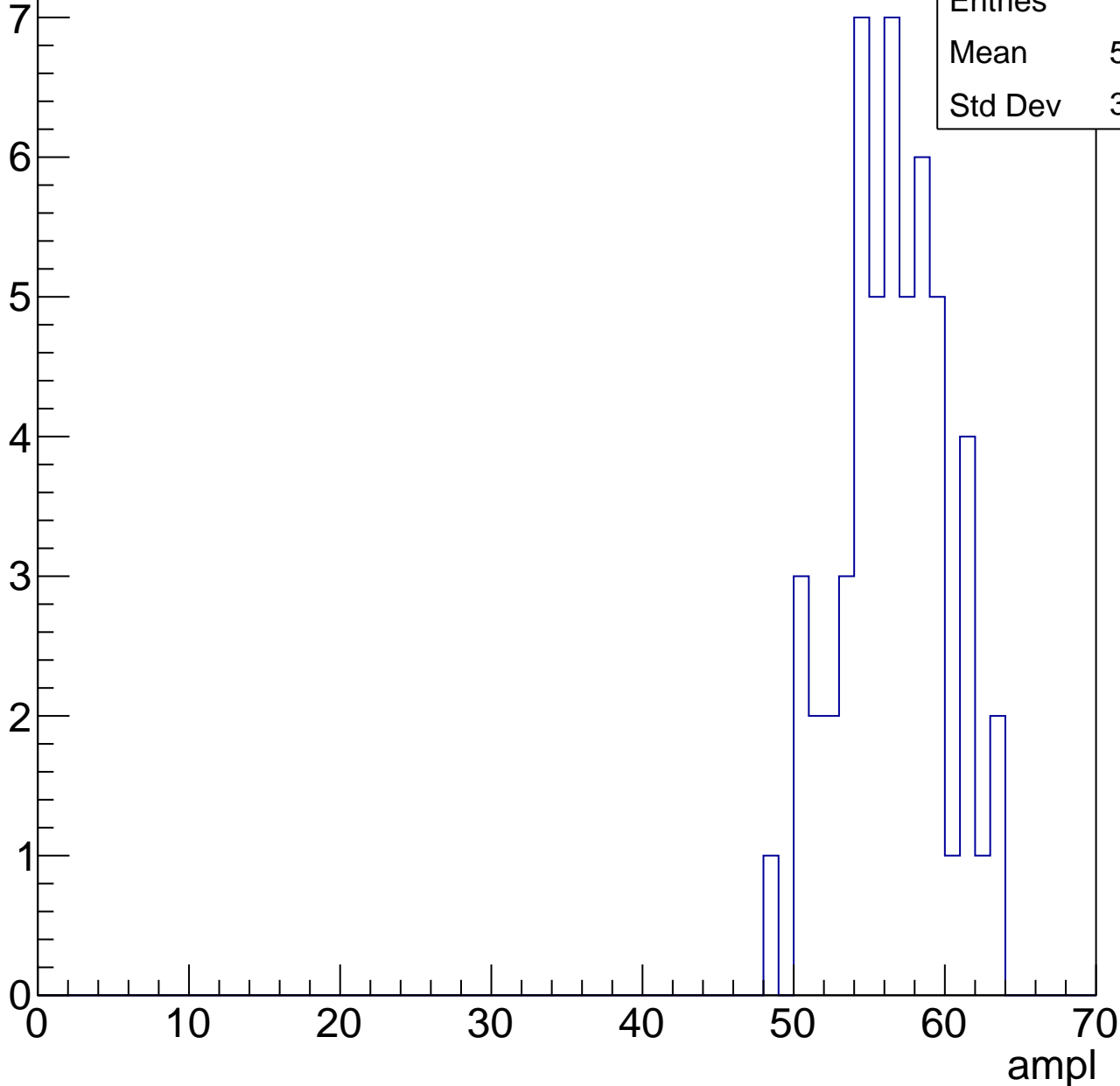


B1L101S, U23-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.07
Std Dev	3.436

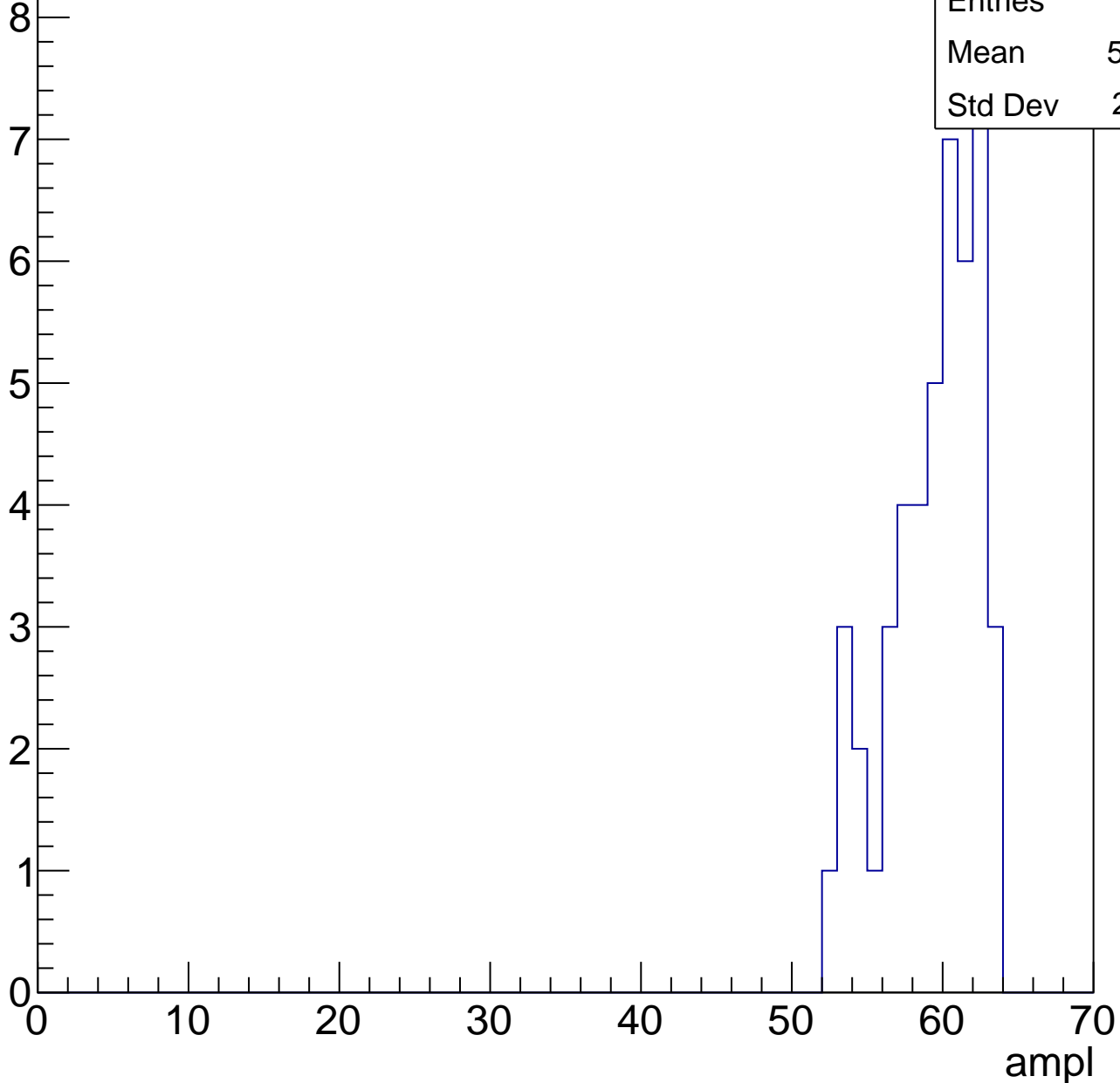


B1L101S, U23-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

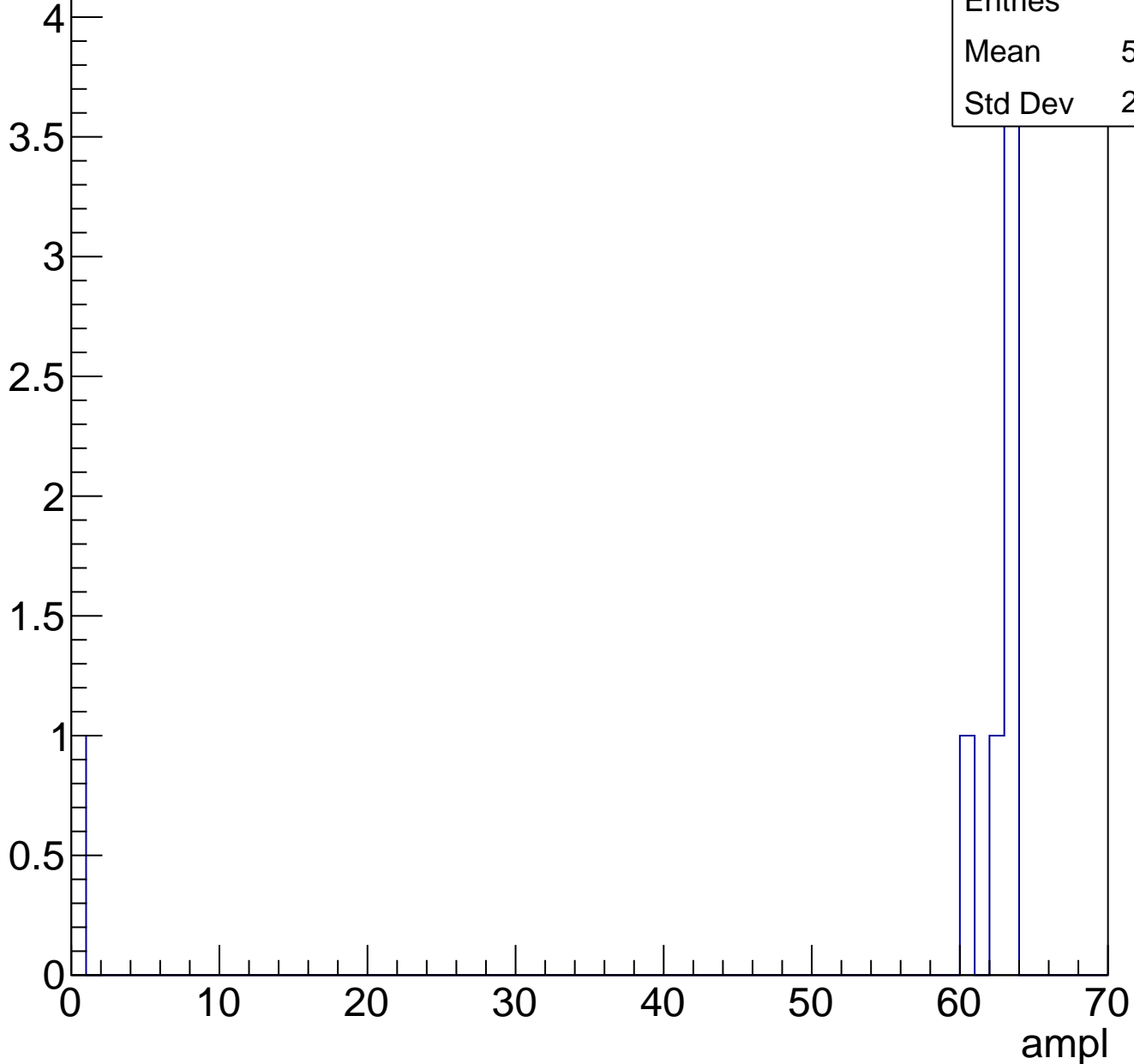
Entries	47
Mean	58.89
Std Dev	2.991



B1L101S, U23-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L101S, U23-ch121, adc0

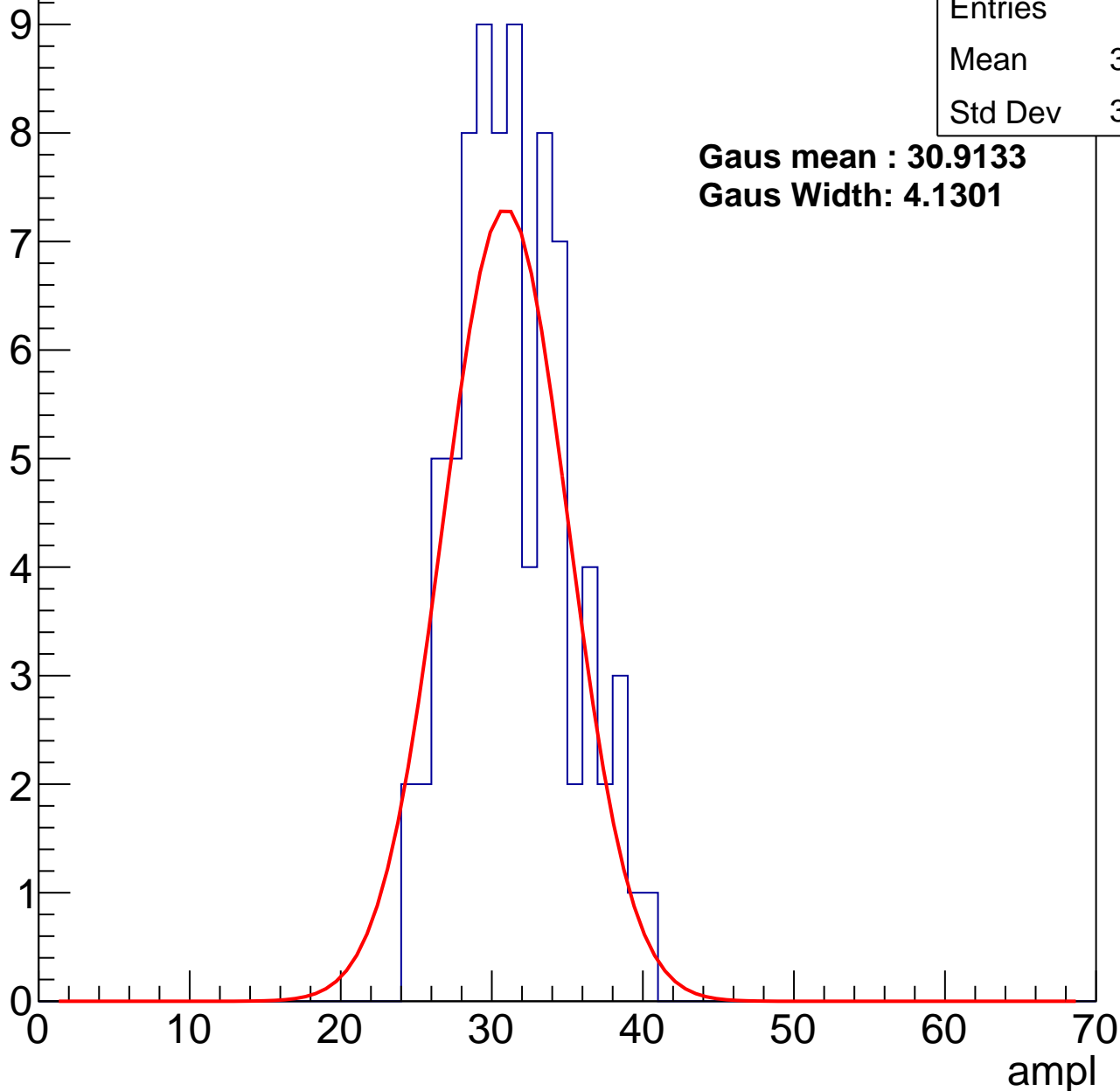
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.98
Std Dev	3.684

Gaus mean : 30.9133

Gaus Width: 4.1301



B1L101S, U23-ch121, adc1

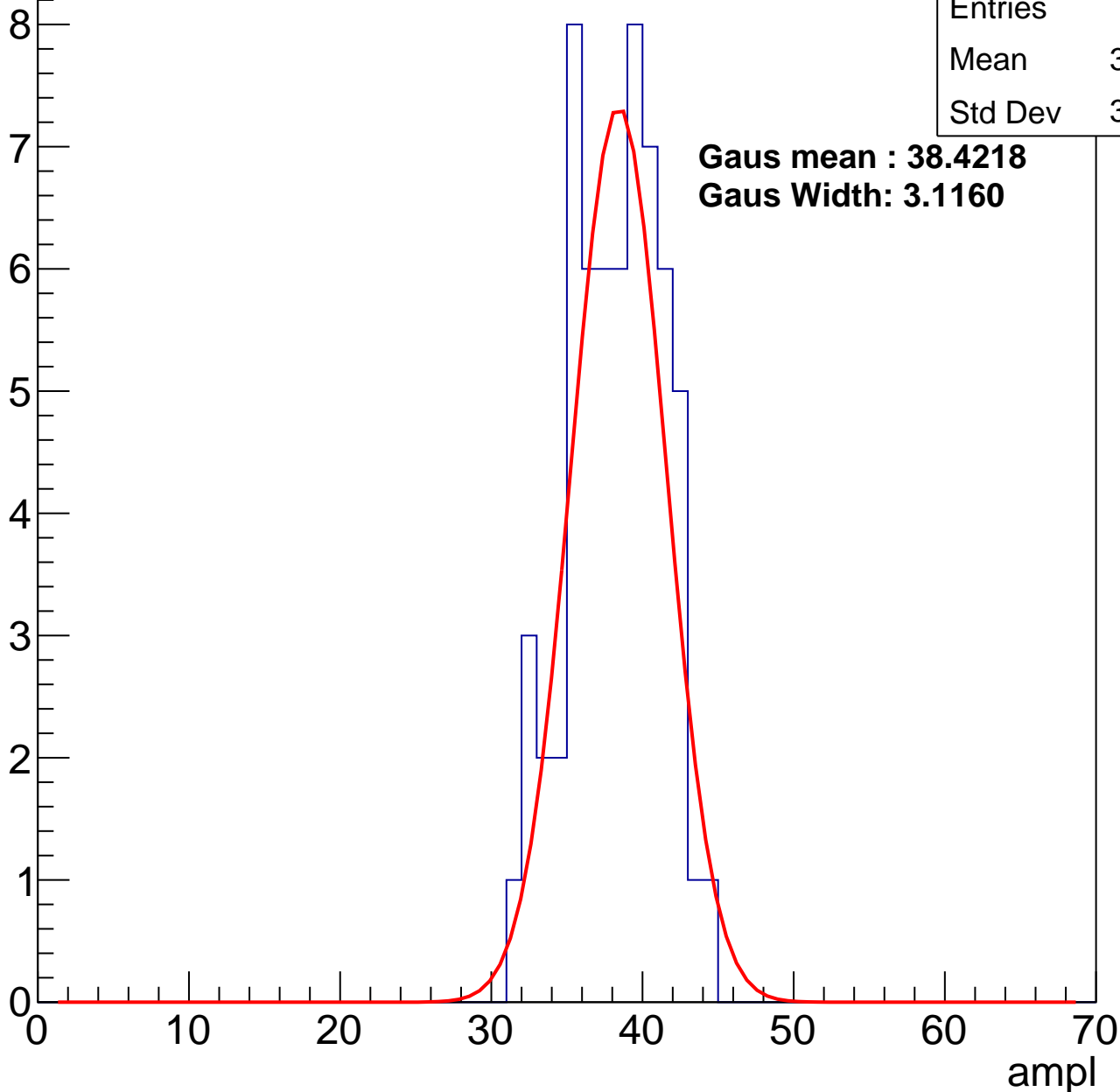
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.77
Std Dev	3.029

Gaus mean : 38.4218

Gaus Width: 3.1160



B1L101S, U23-ch121, adc2

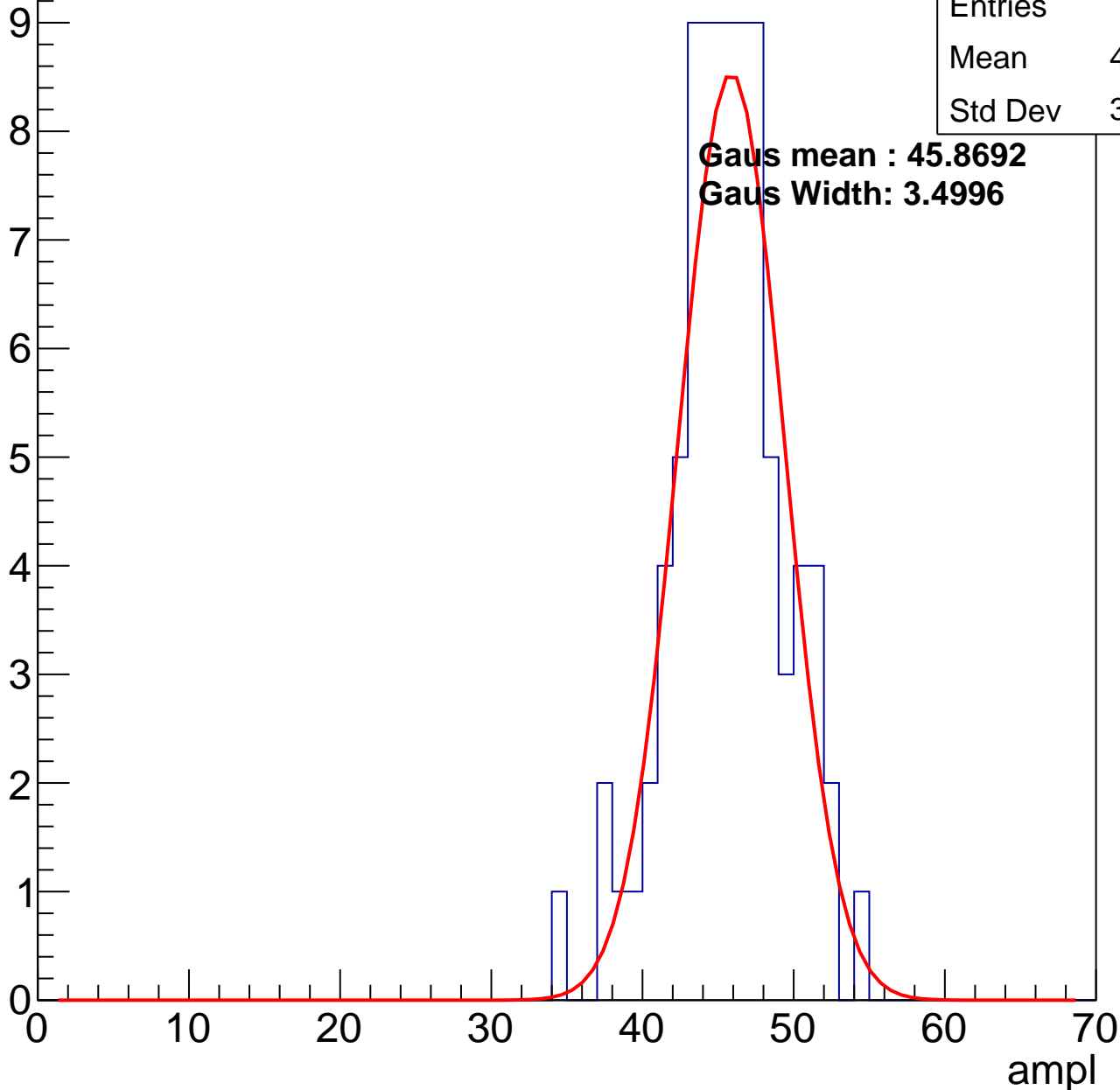
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	45.16
Std Dev	3.703

Gaus mean : 45.8692

Gaus Width: 3.4996

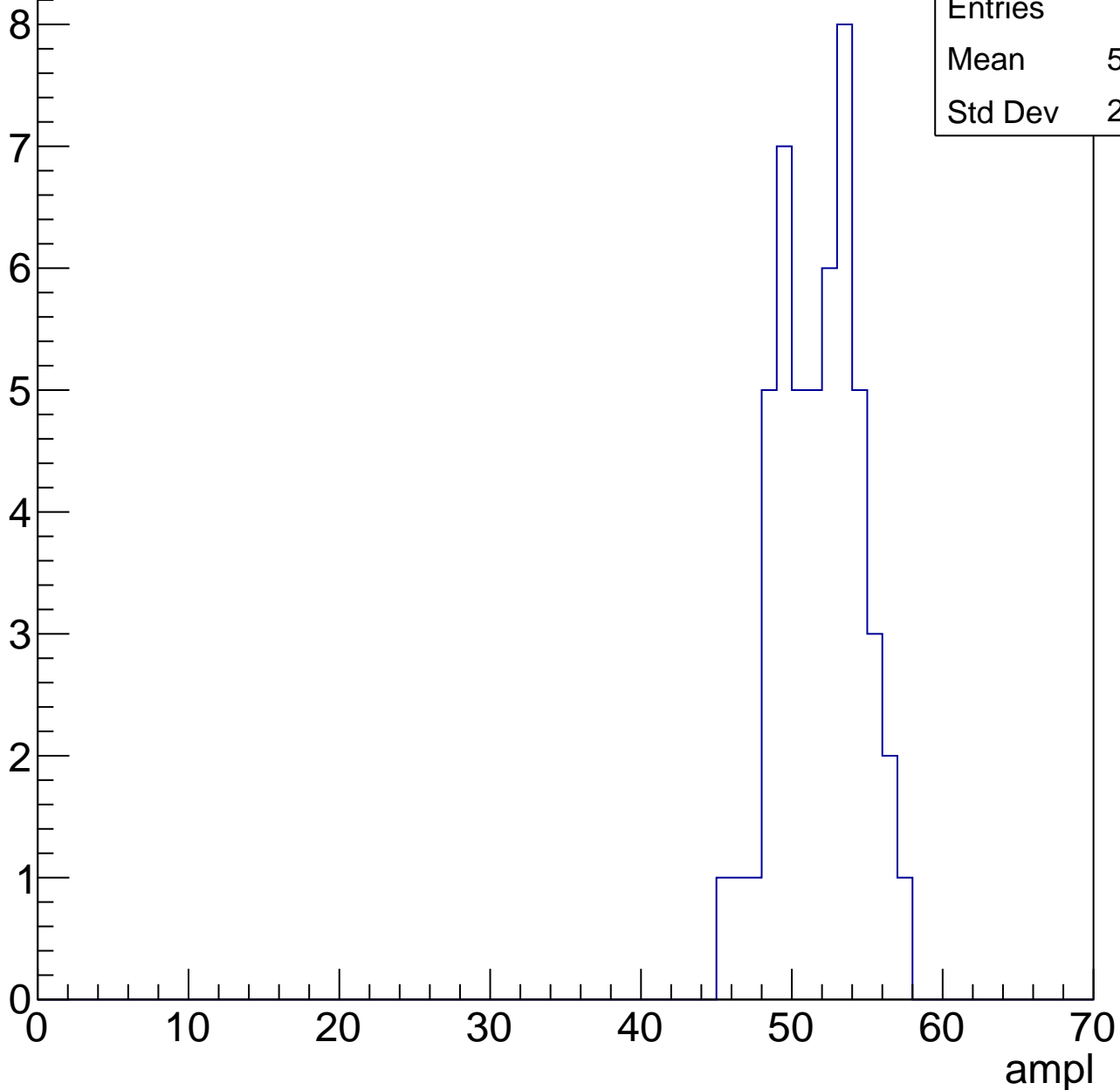


B1L101S, U23-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	51.32
Std Dev	2.709



B1L101S, U23-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	57.6
Std Dev	2.951

Entry

10

8

6

4

2

0

0

10

20

30

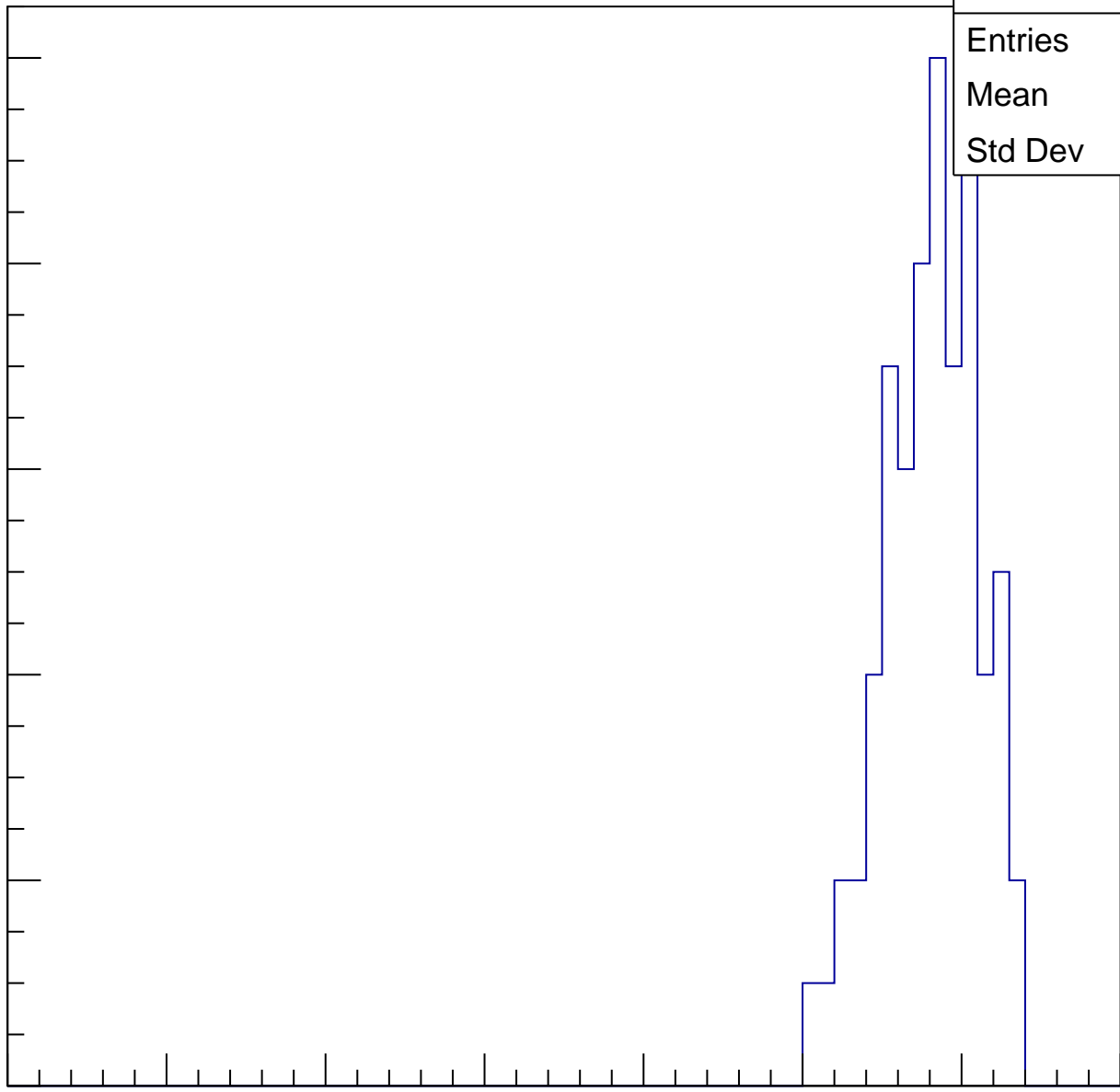
40

50

60

ampl

70

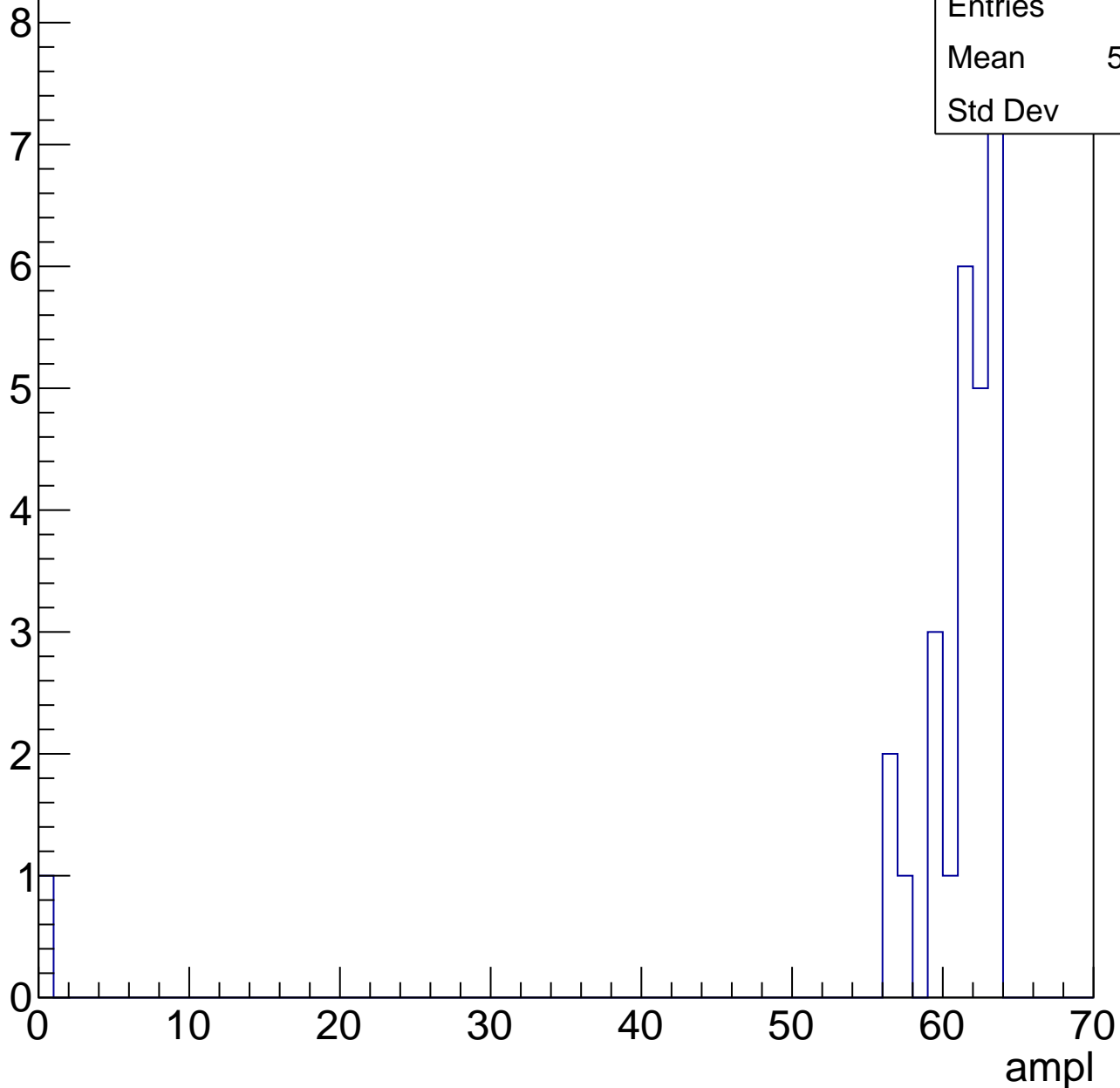


B1L101S, U23-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.74
Std Dev	11.7



B1L101S, U23-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

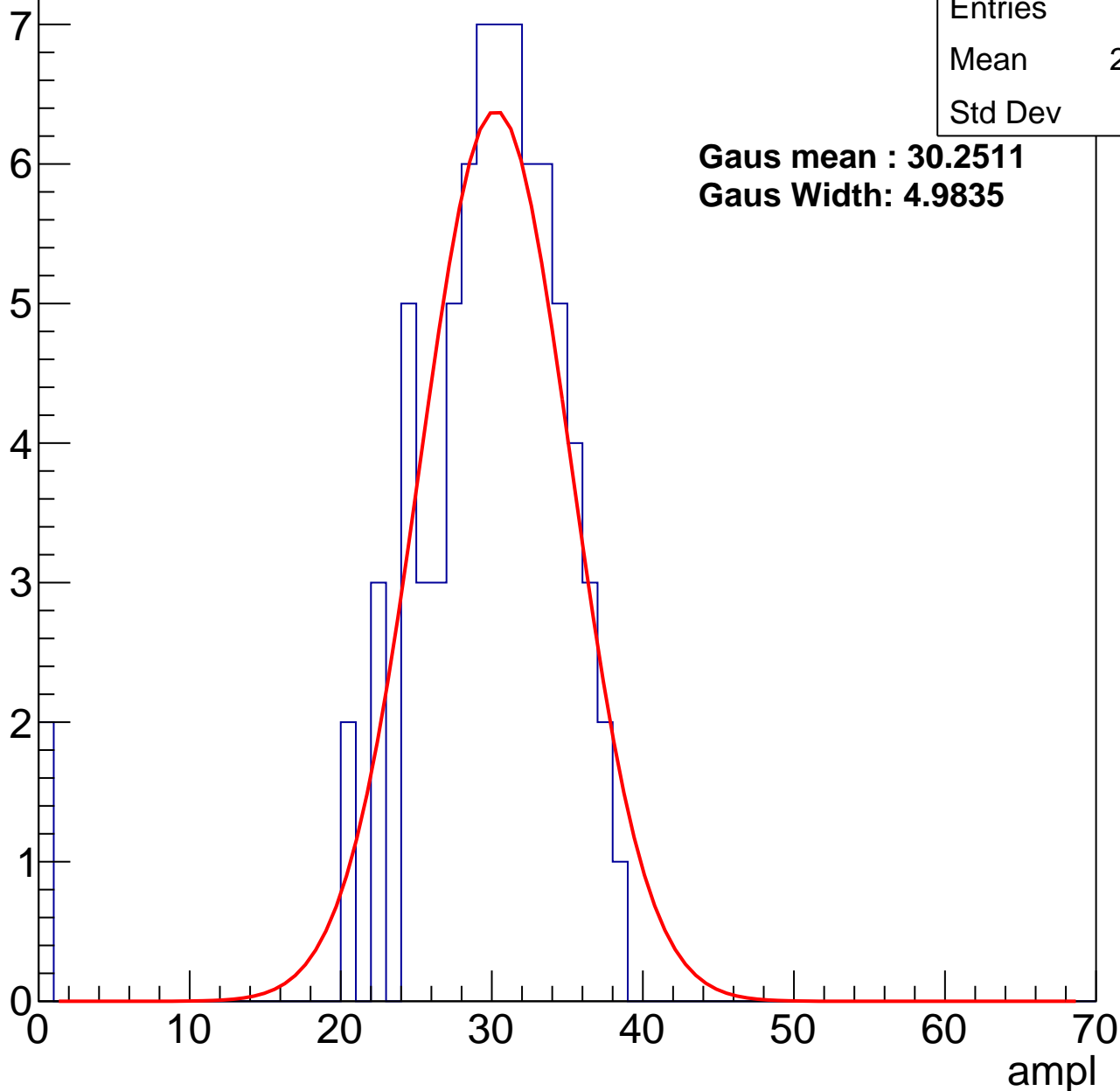
B1L101S, U23-ch122, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	28.99
Std Dev	6.27

Gaus mean : 30.2511
Gaus Width: 4.9835



B1L101S, U23-ch122, adc1

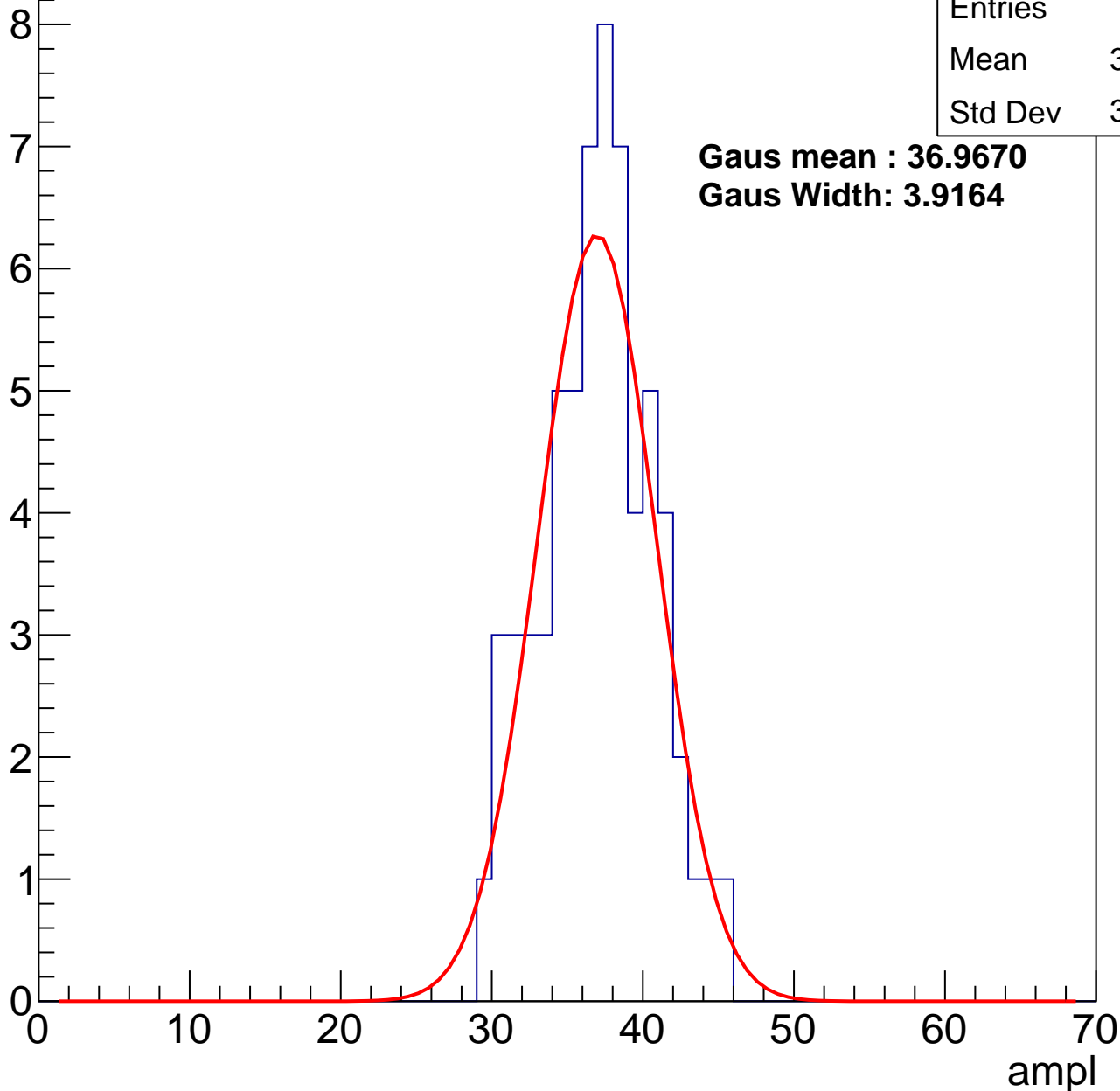
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.54
Std Dev	3.633

Gaus mean : 36.9670

Gaus Width: 3.9164



B1L101S, U23-ch122, adc2

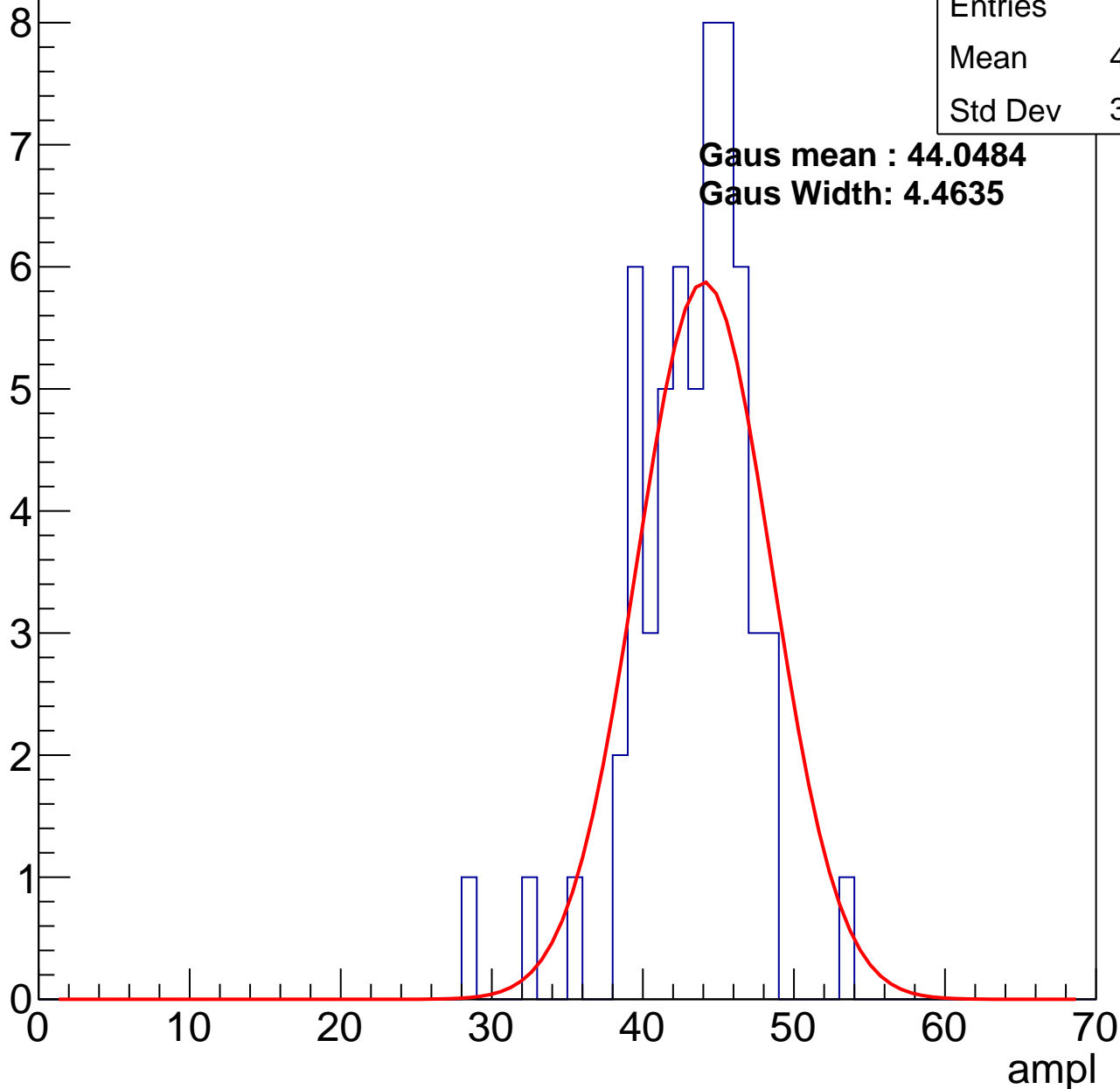
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	42.76
Std Dev	3.955

Gaus mean : 44.0484

Gaus Width: 4.4635

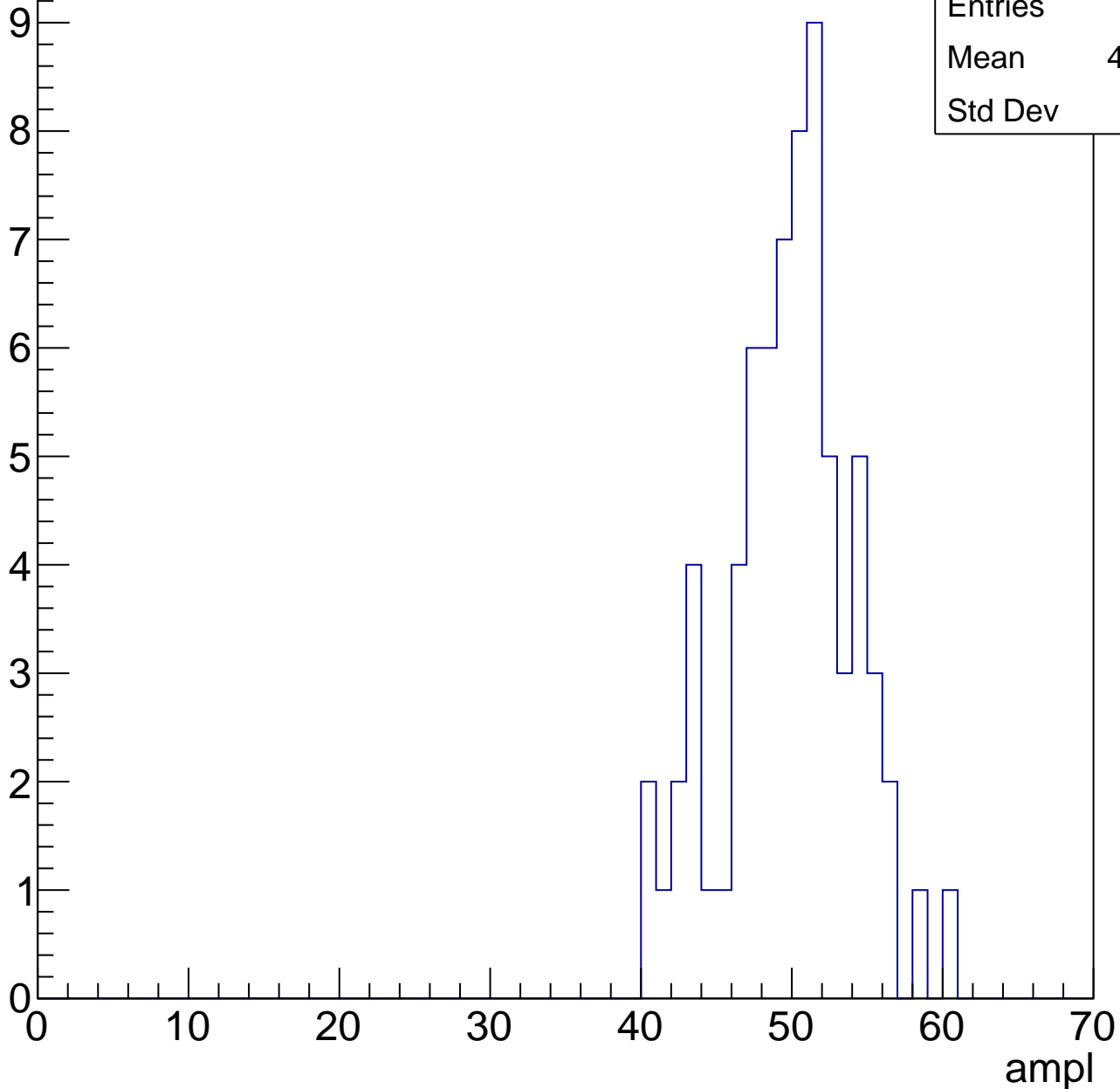


B1L101S, U23-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.38
Std Dev	4.17

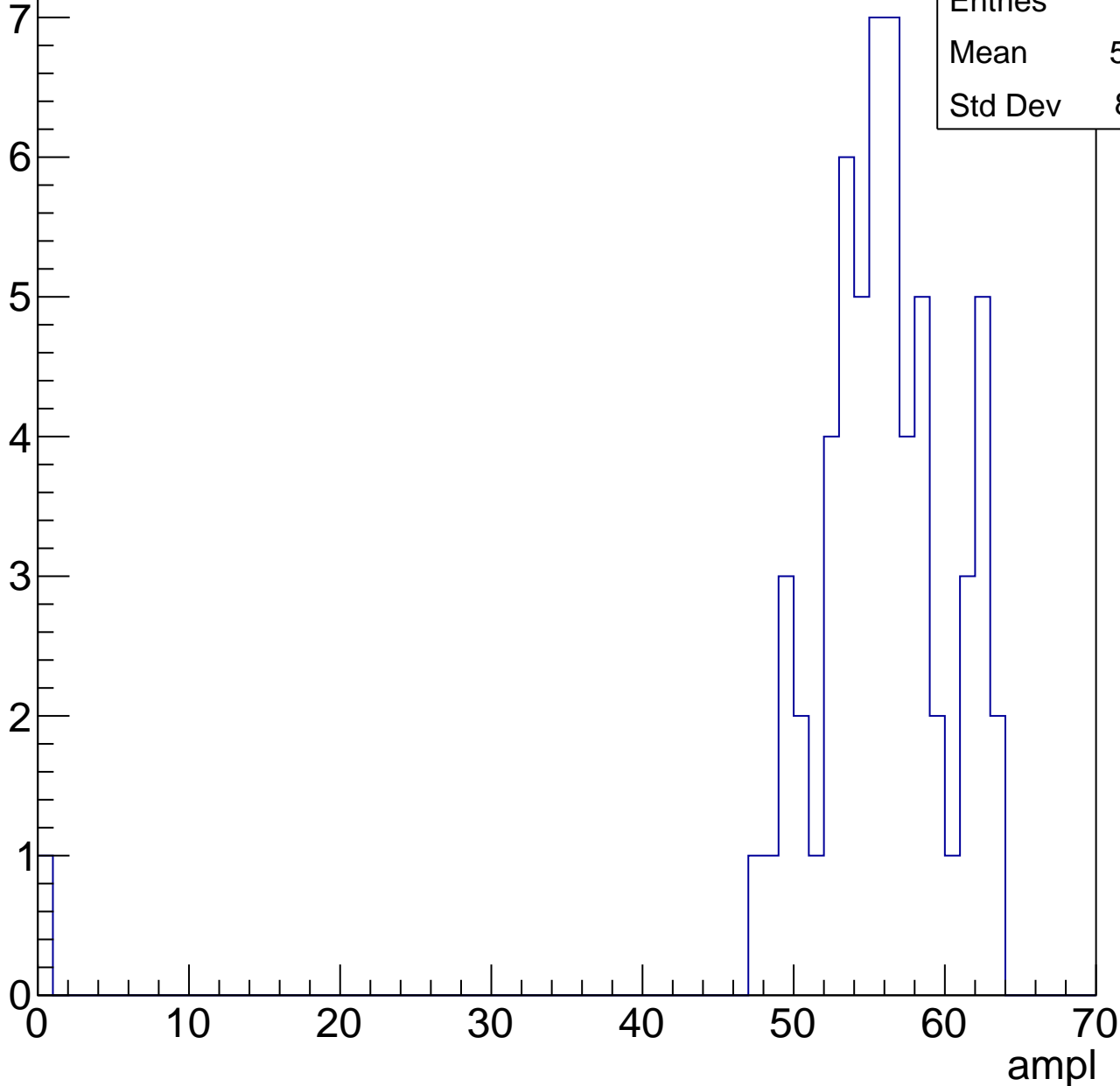


B1L101S, U23-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.68
Std Dev	8.141

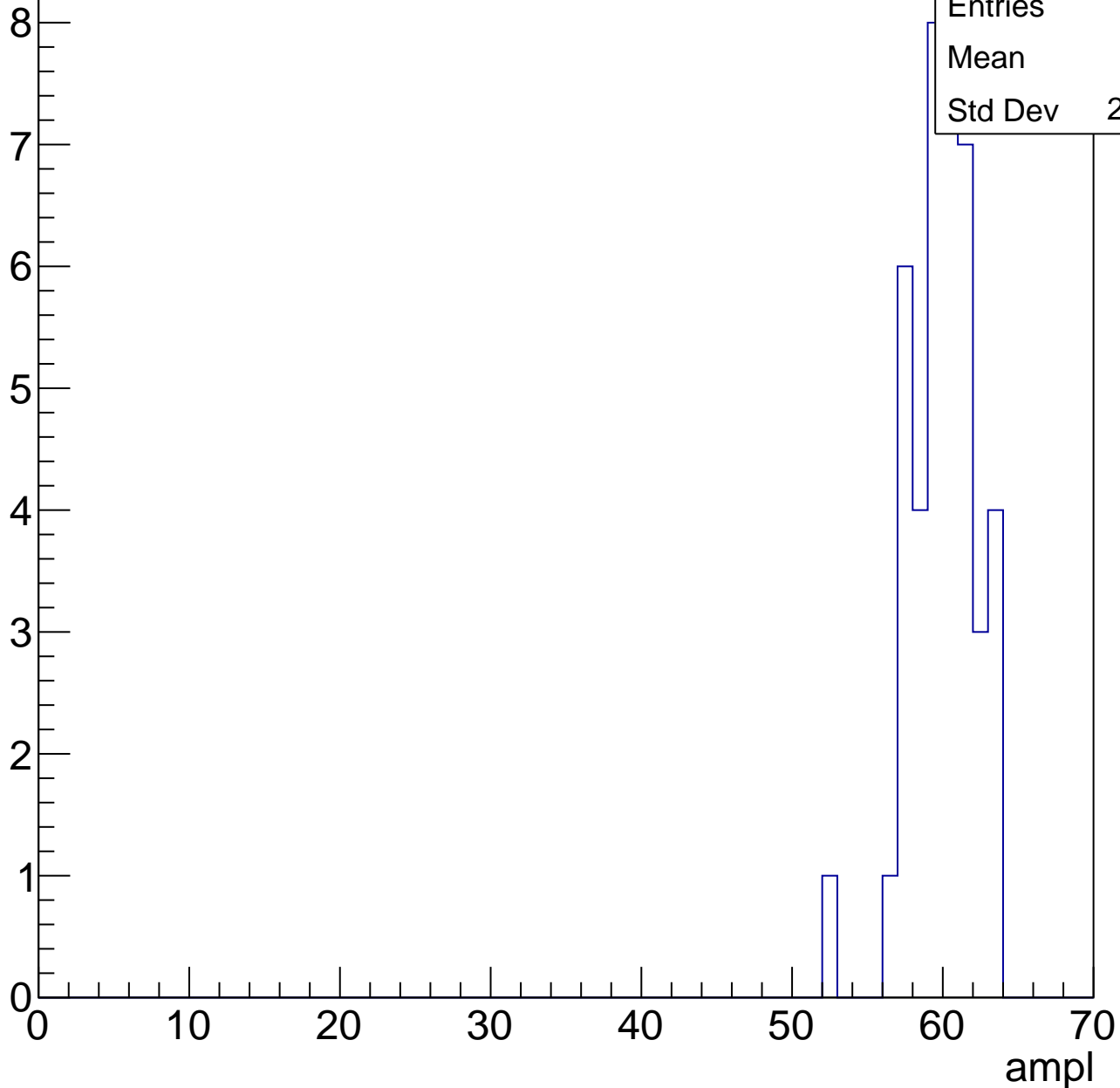


B1L101S, U23-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.5
Std Dev	2.196



B1L101S, U23-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch123, adc0

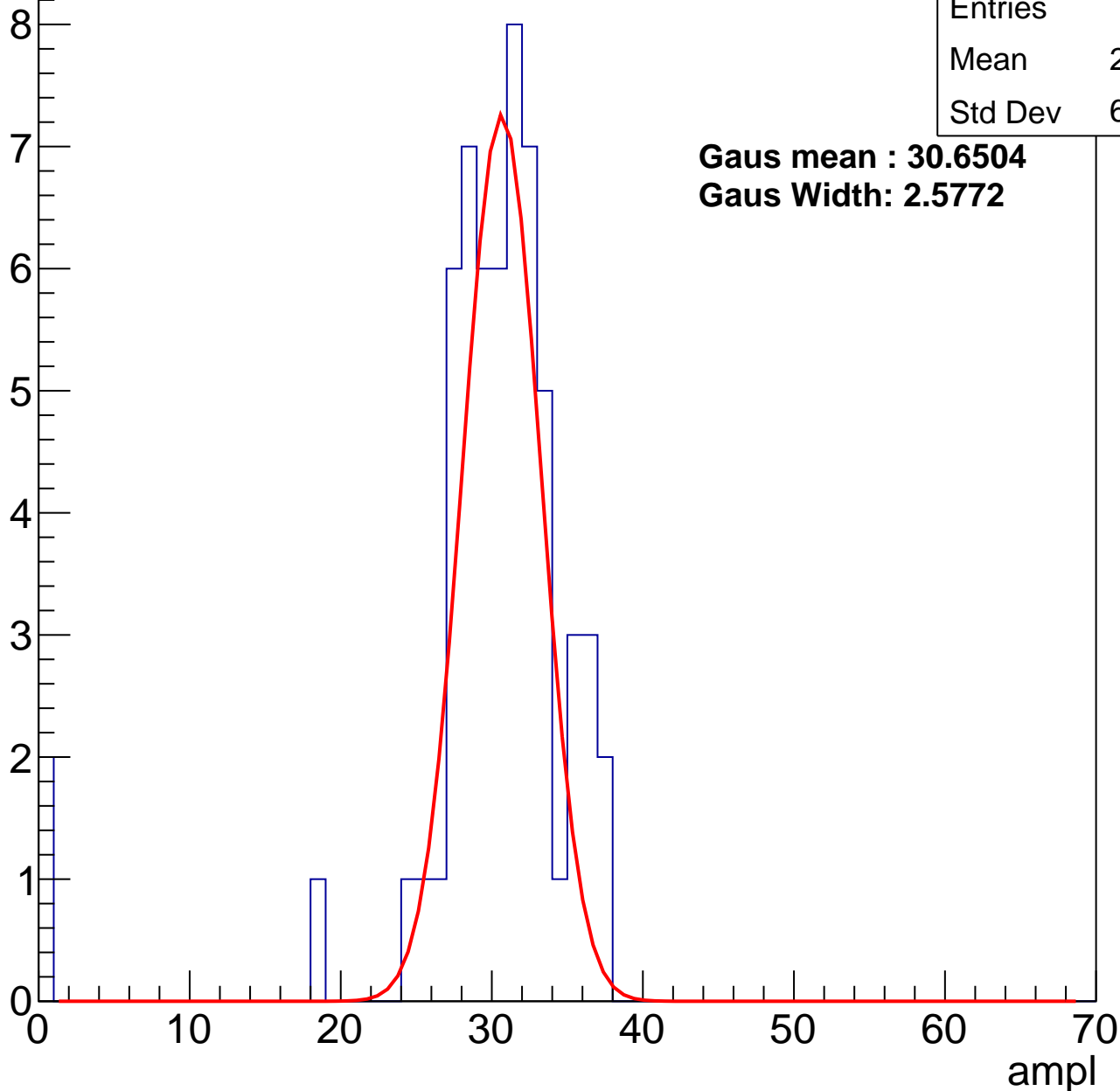
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	29.38
Std Dev	6.409

Gaus mean : 30.6504

Gaus Width: 2.5772



B1L101S, U23-ch123, adc1

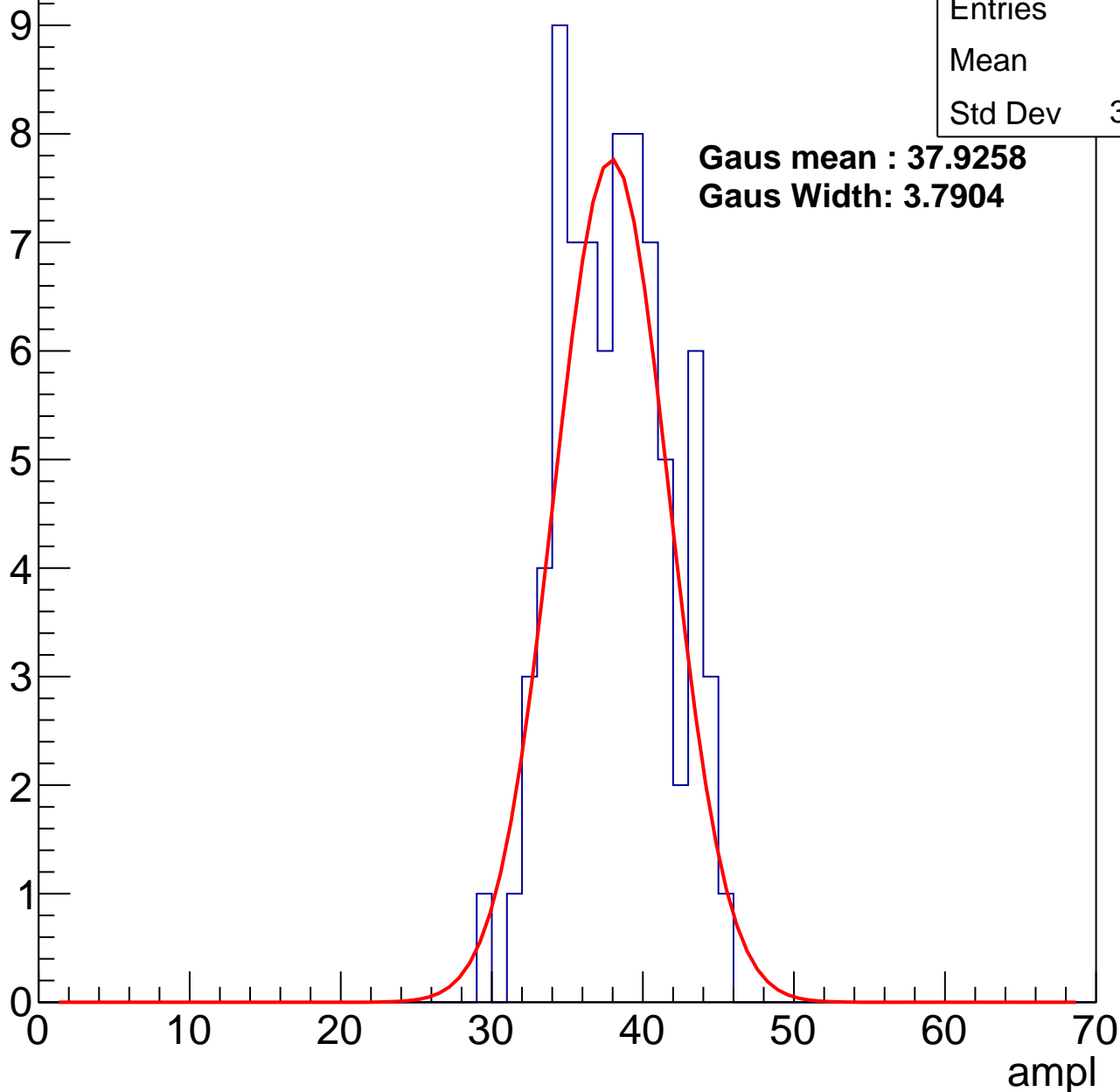
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.6
Std Dev	3.557

Gaus mean : 37.9258

Gaus Width: 3.7904



B1L101S, U23-ch123, adc2

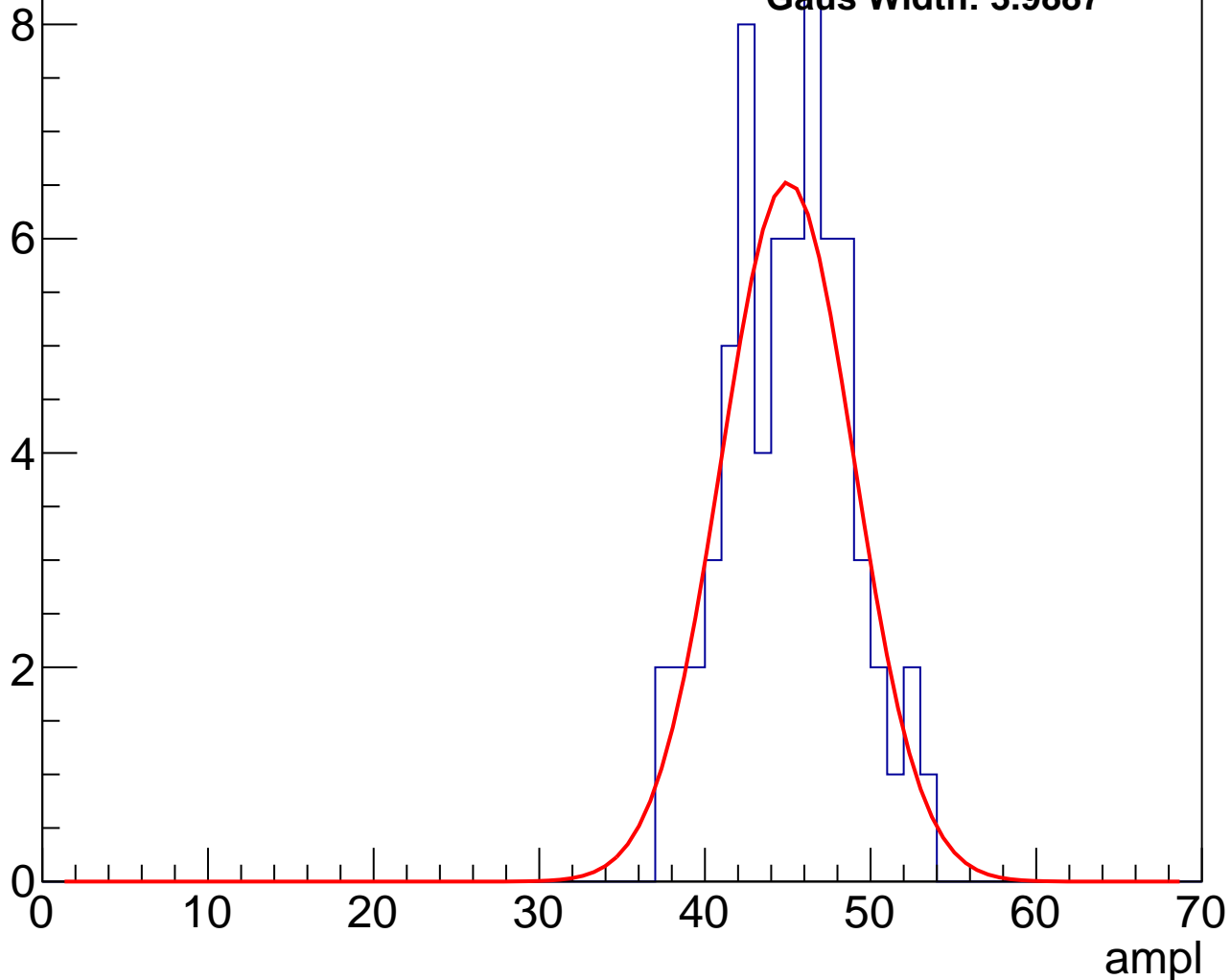
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.64
Std Dev	3.655

Gaus mean : 44.9955

Gaus Width: 3.9887

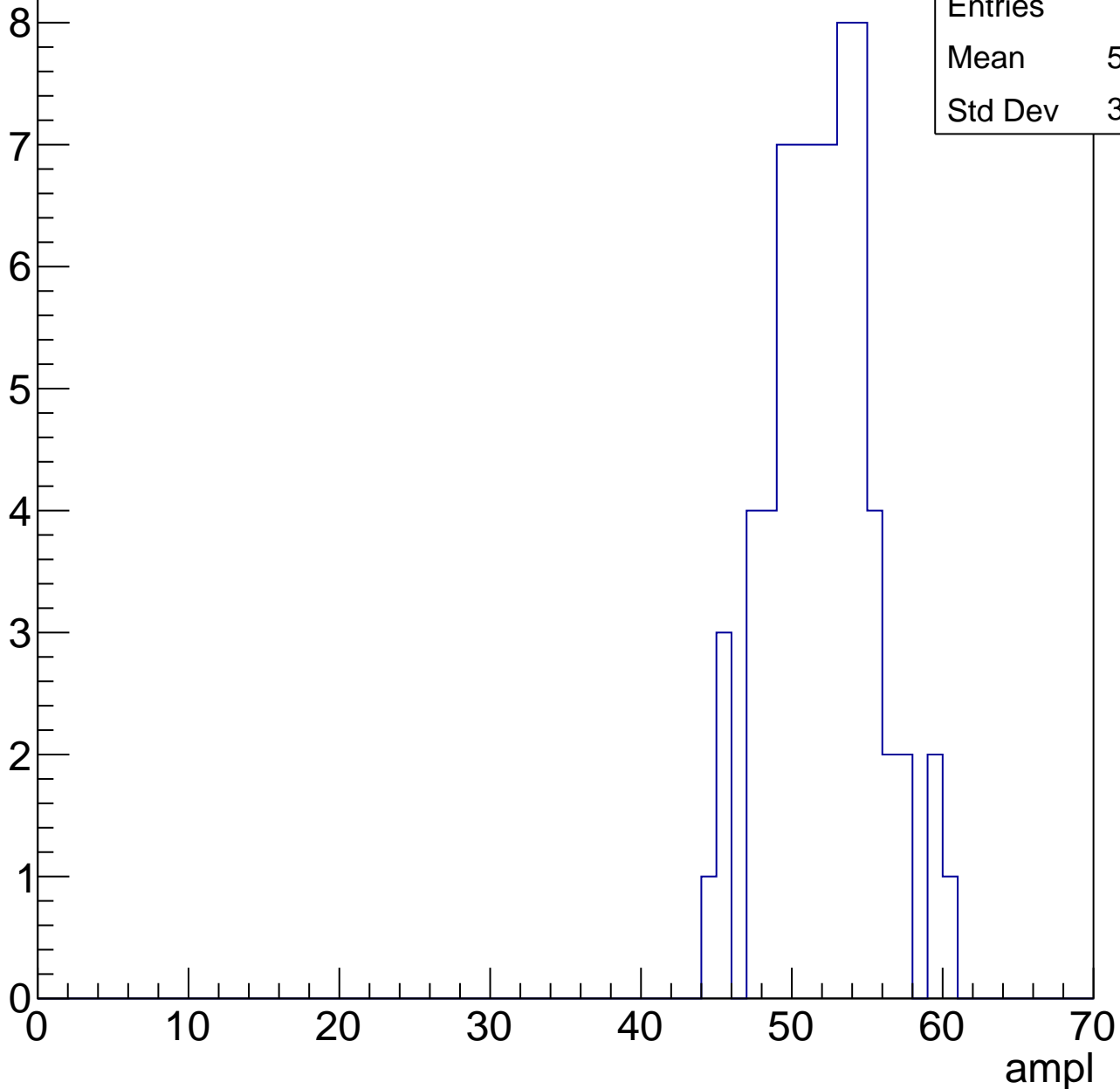


B1L101S, U23-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.54
Std Dev	3.409

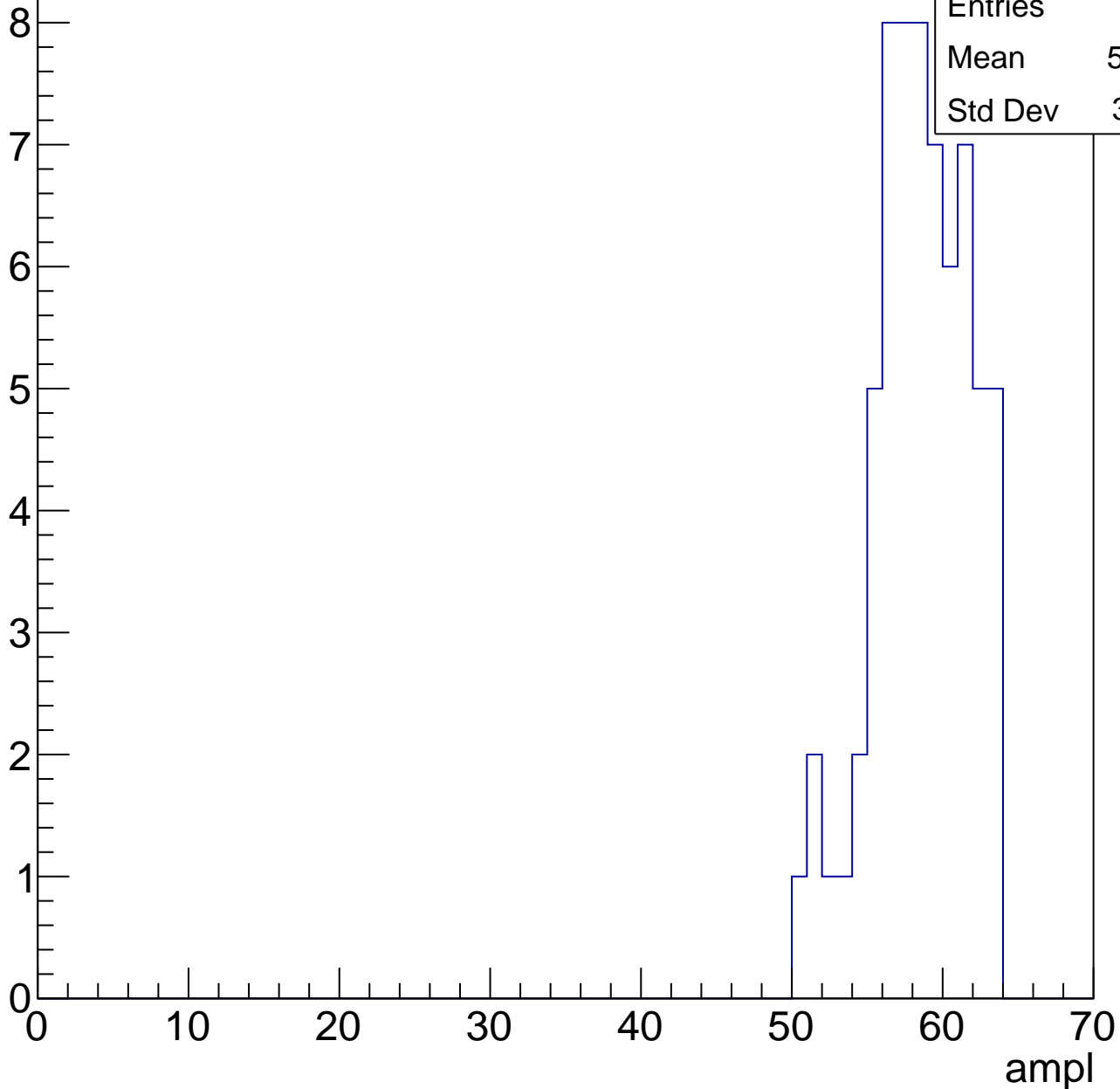


B1L101S, U23-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	58.08
Std Dev	3.111

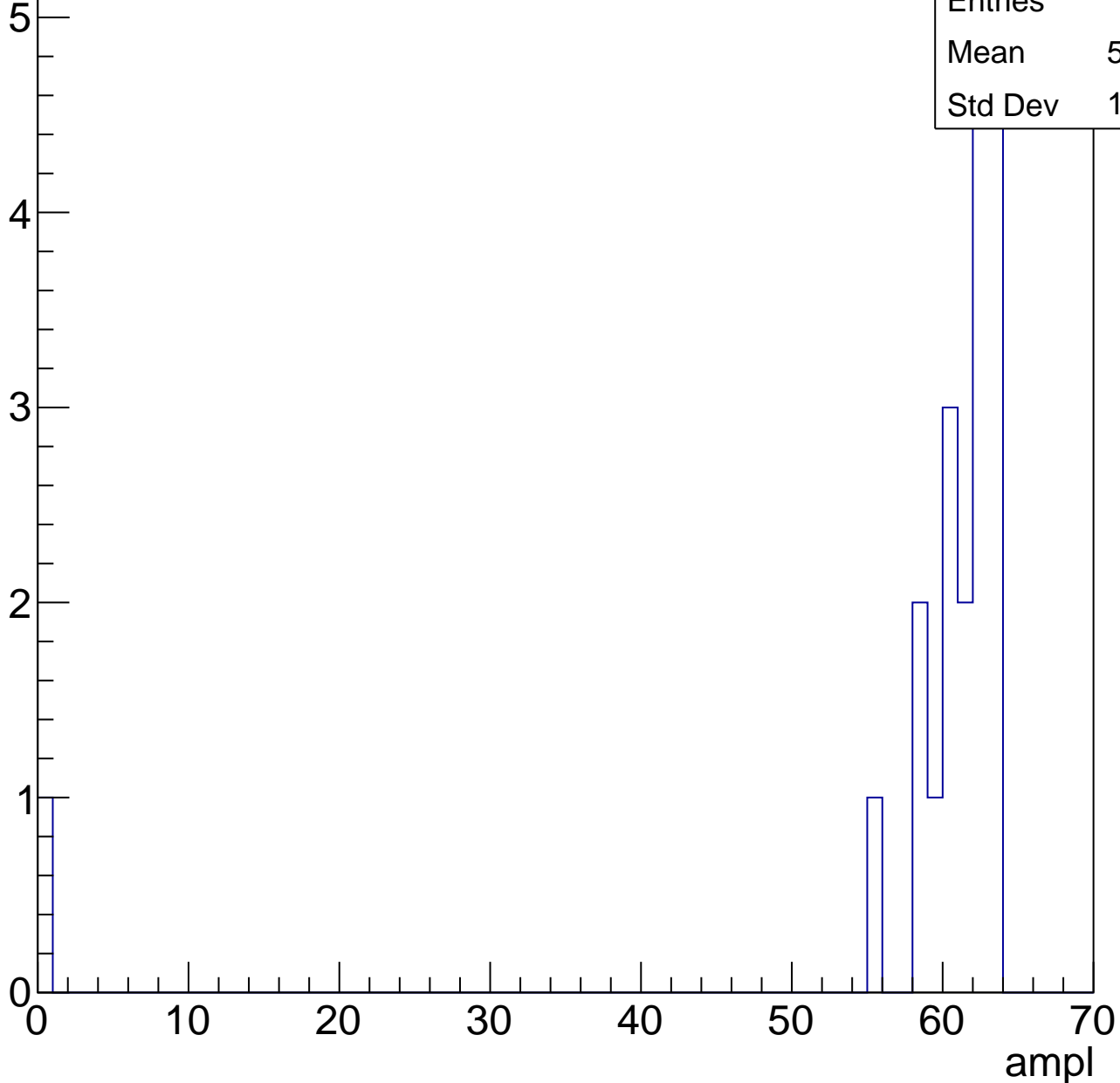


B1L101S, U23-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	20
Mean	57.85
Std Dev	13.43



B1L101S, U23-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

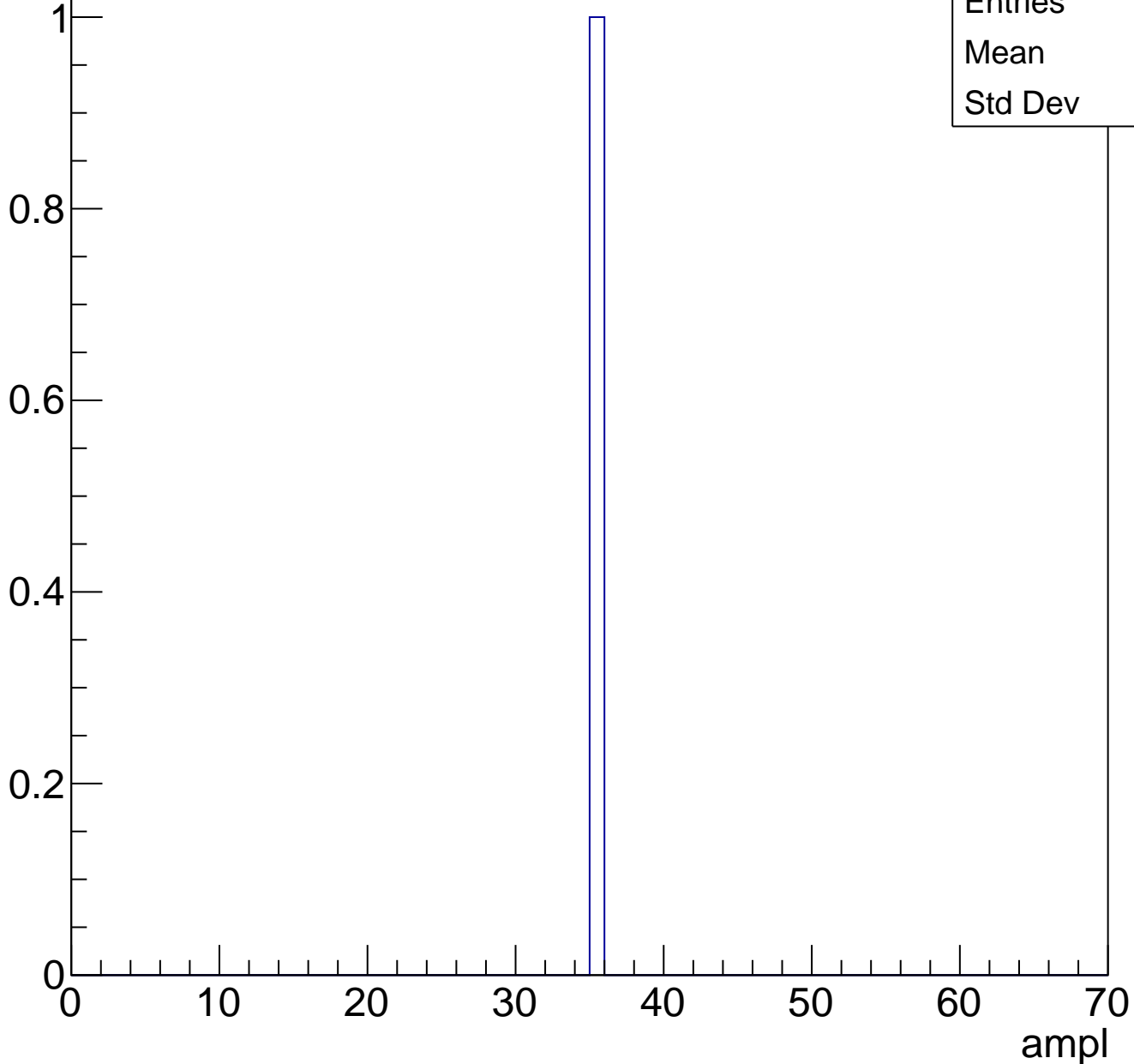
Entry



B1L101S, U23-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch124, adc0

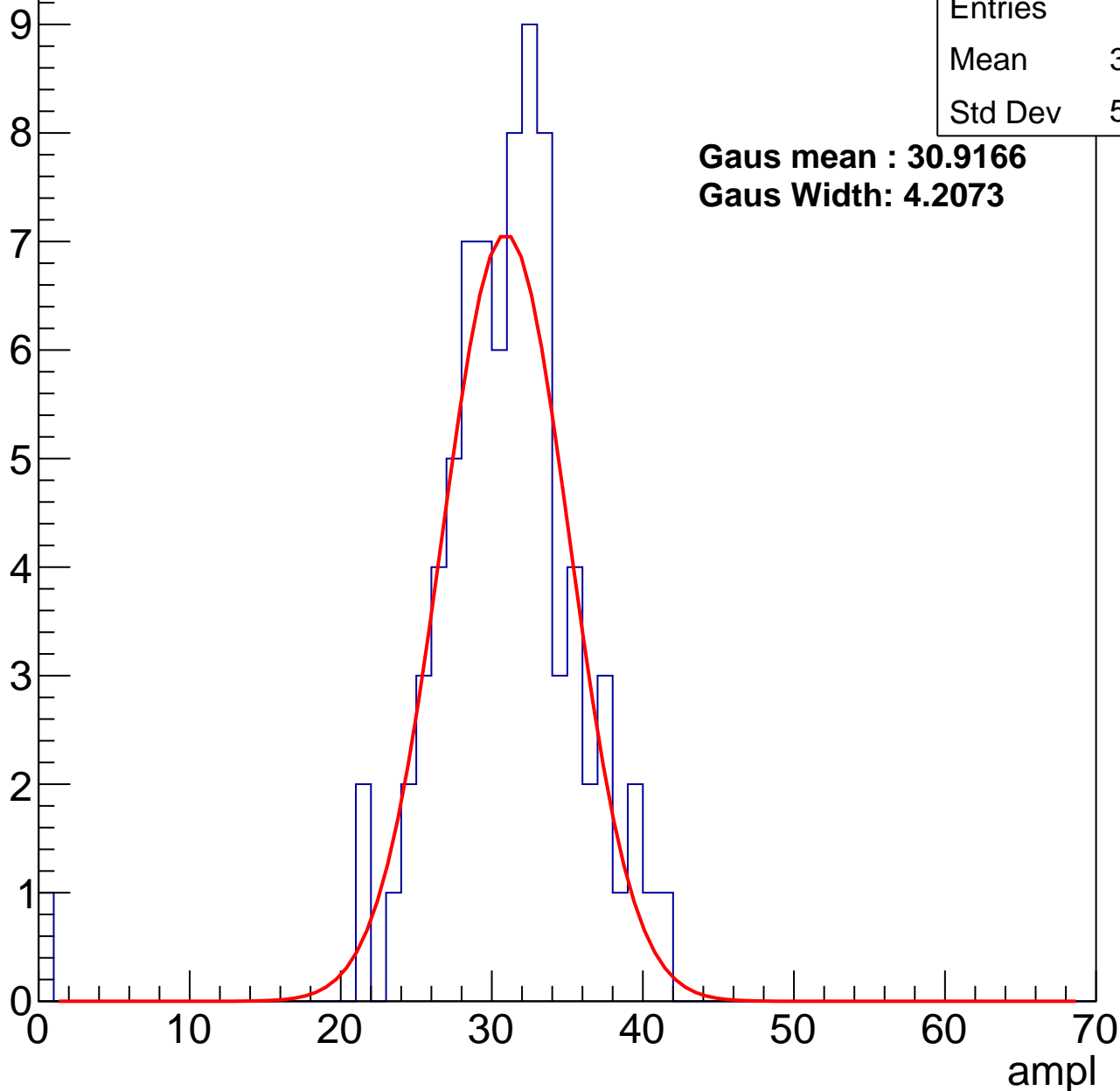
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.35
Std Dev	5.388

Gaus mean : 30.9166

Gaus Width: 4.2073



B1L101S, U23-ch124, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

57

Mean

37.35

Std Dev

3.098

Gaus mean : 38.3117

Gaus Width: 3.5386

0

0

10

20

30

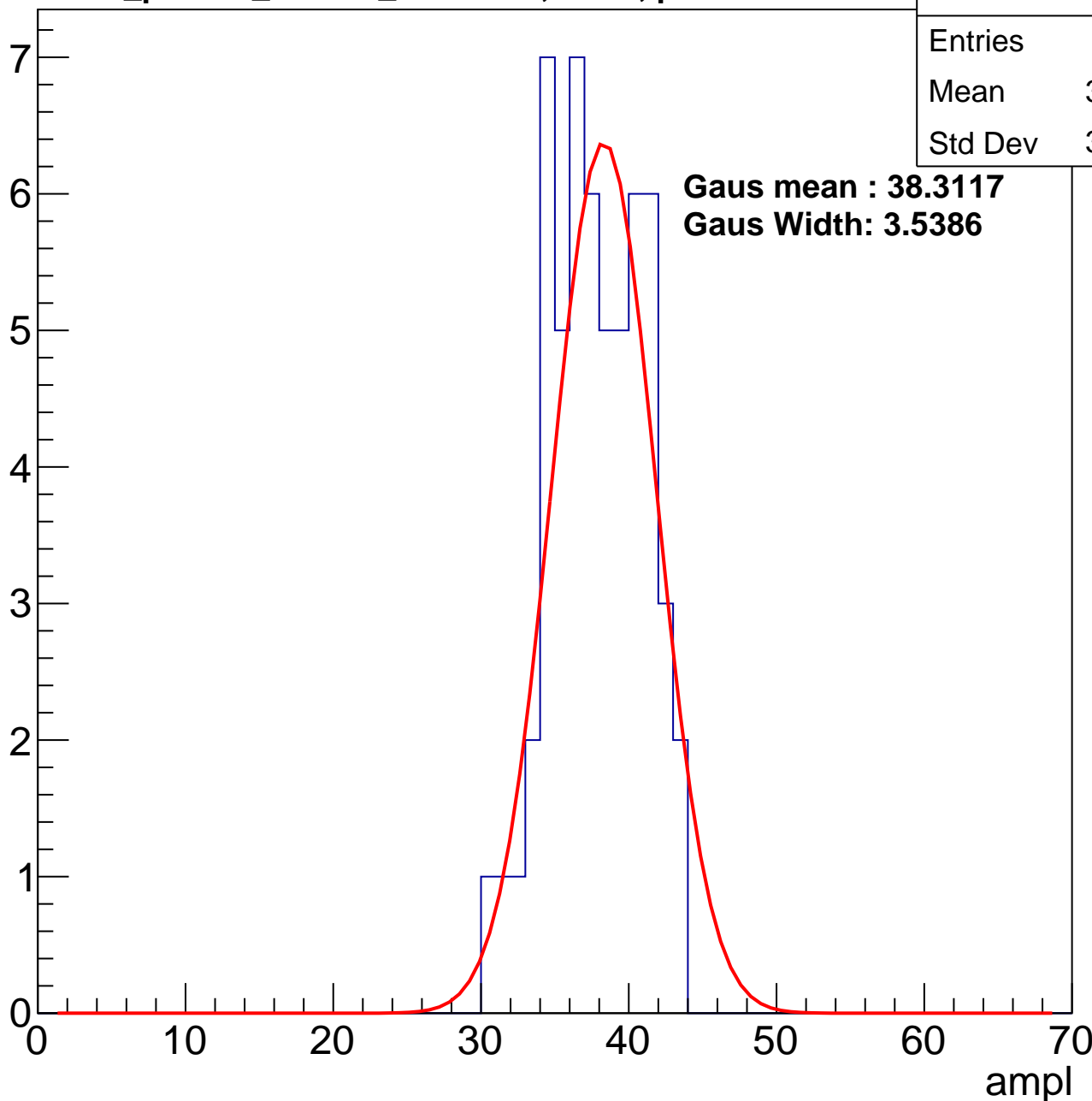
40

50

60

70

ampl



B1L101S, U23-ch124, adc2

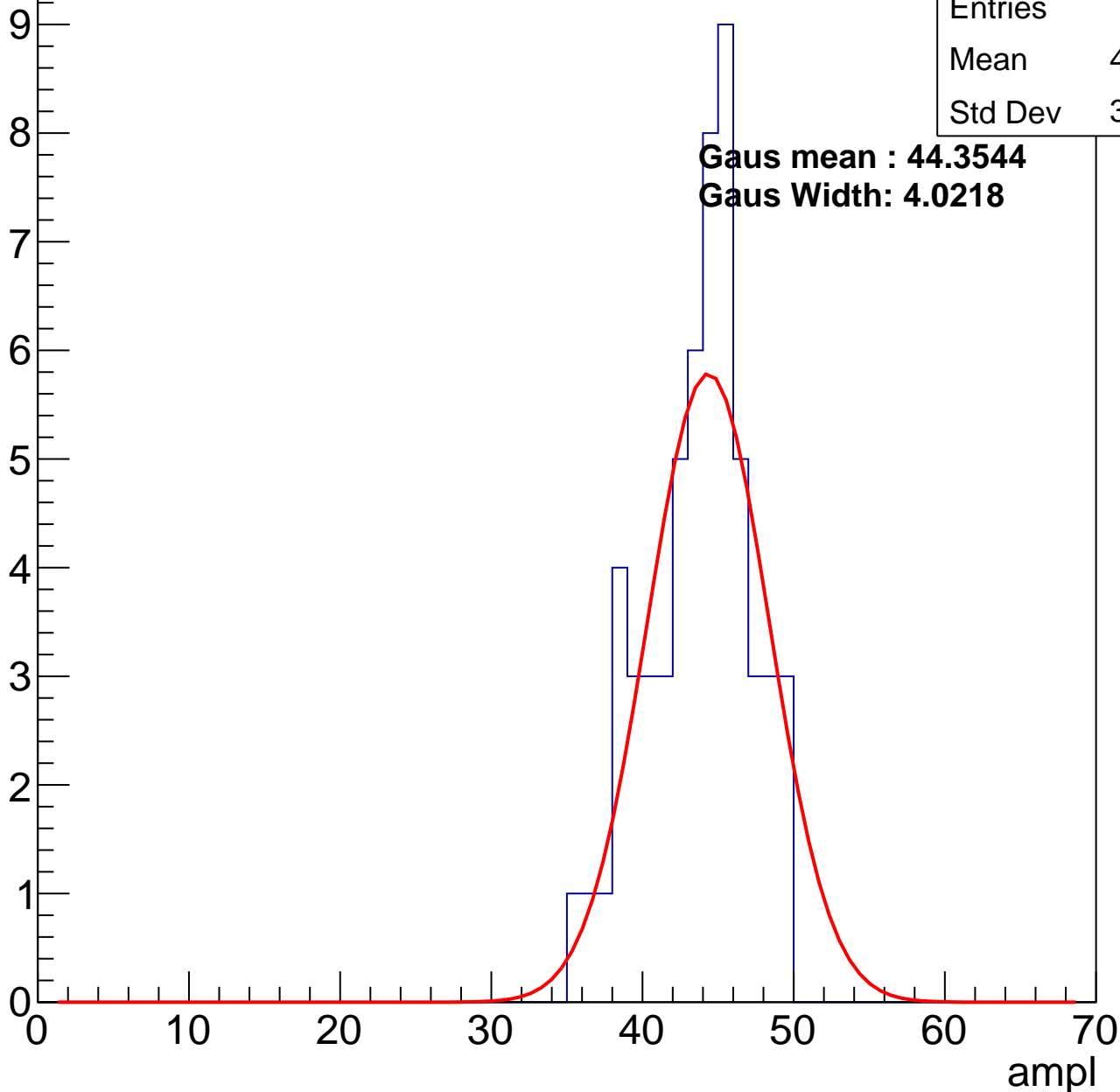
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.22
Std Dev	3.368

Gaus mean : 44.3544

Gaus Width: 4.0218

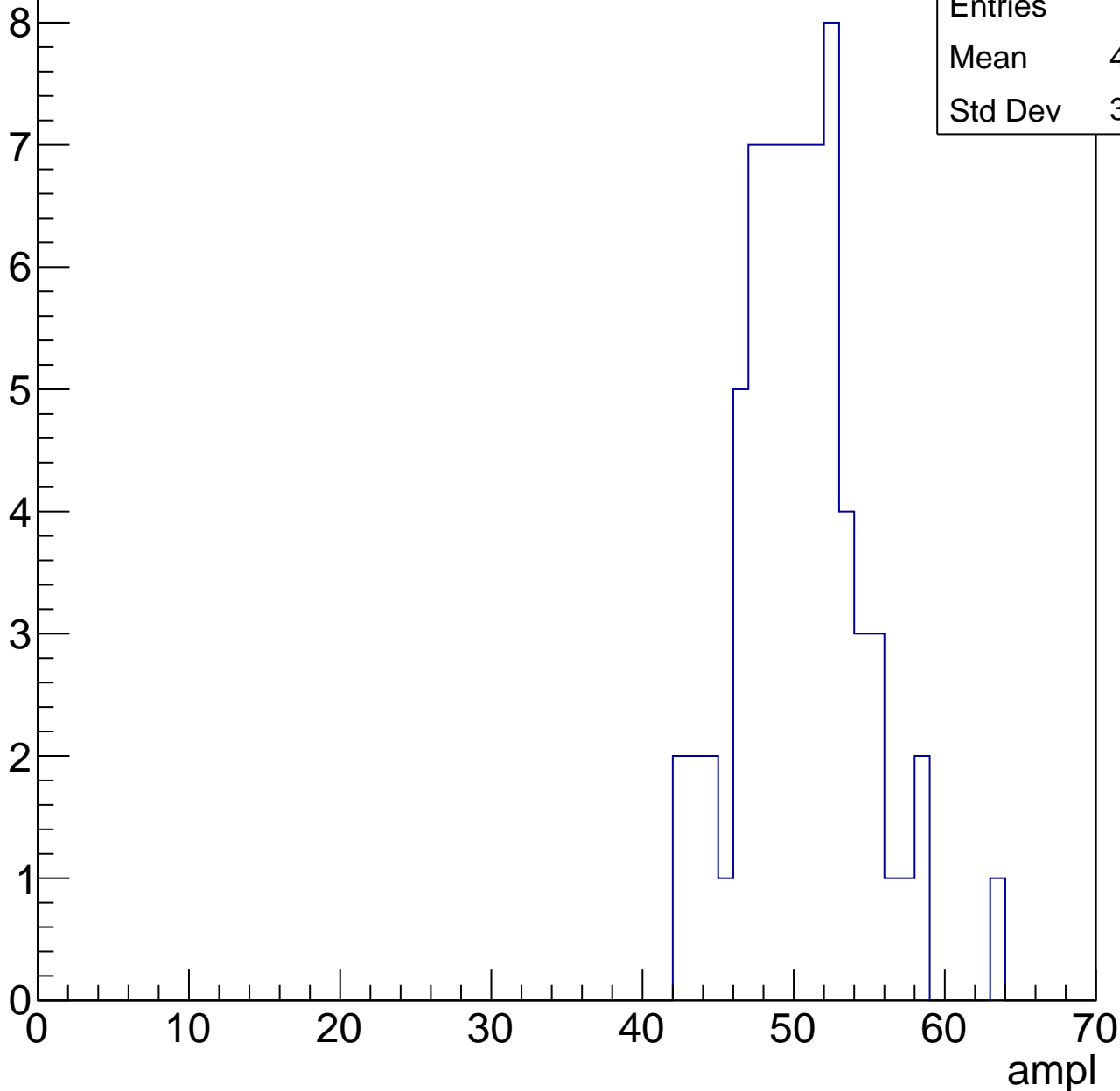


B1L101S, U23-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

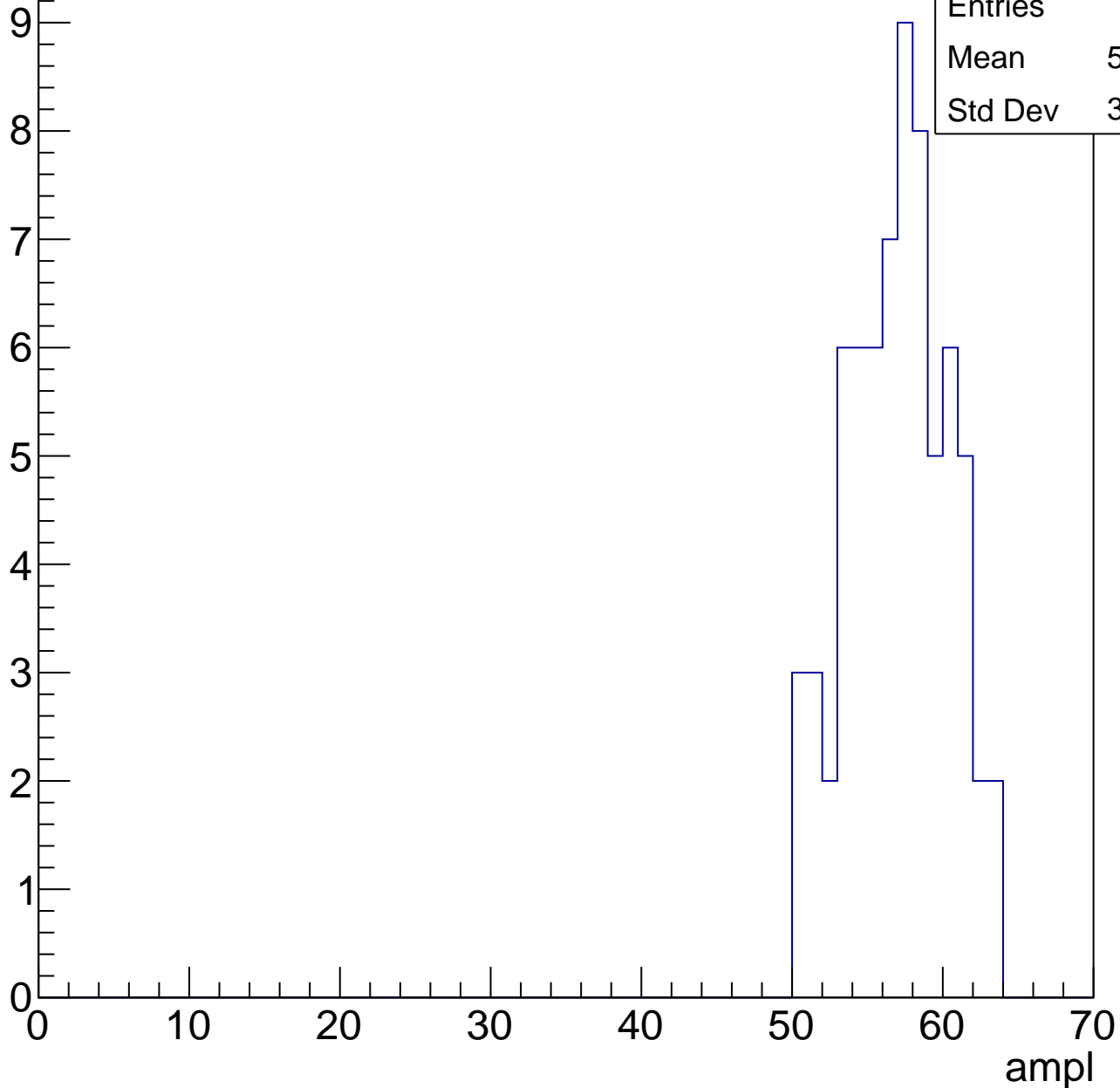
Entries	70
Mean	49.93
Std Dev	3.929



B1L101S, U23-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



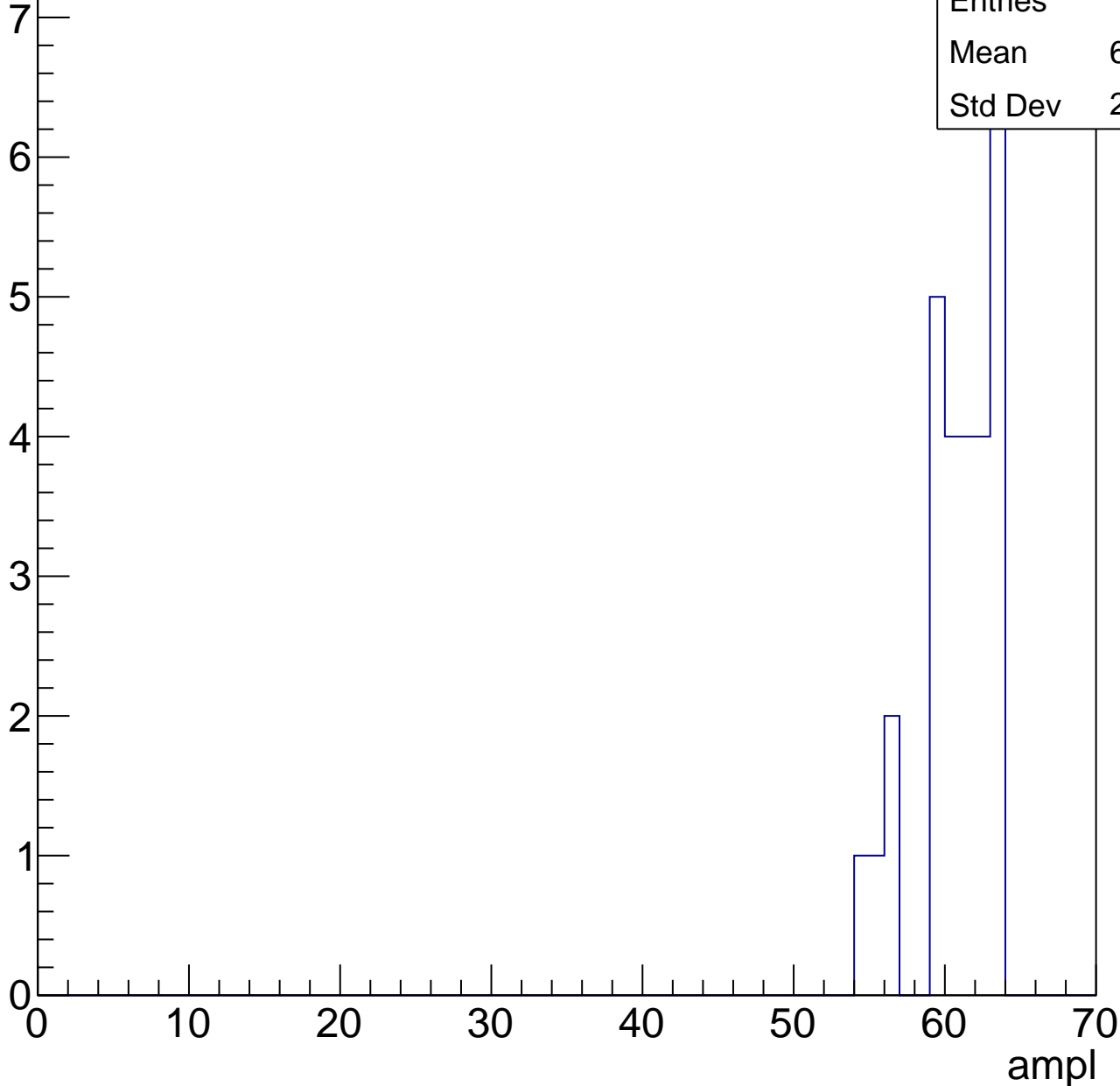
Entries	70
Mean	56.54
Std Dev	3.285

B1L101S, U23-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

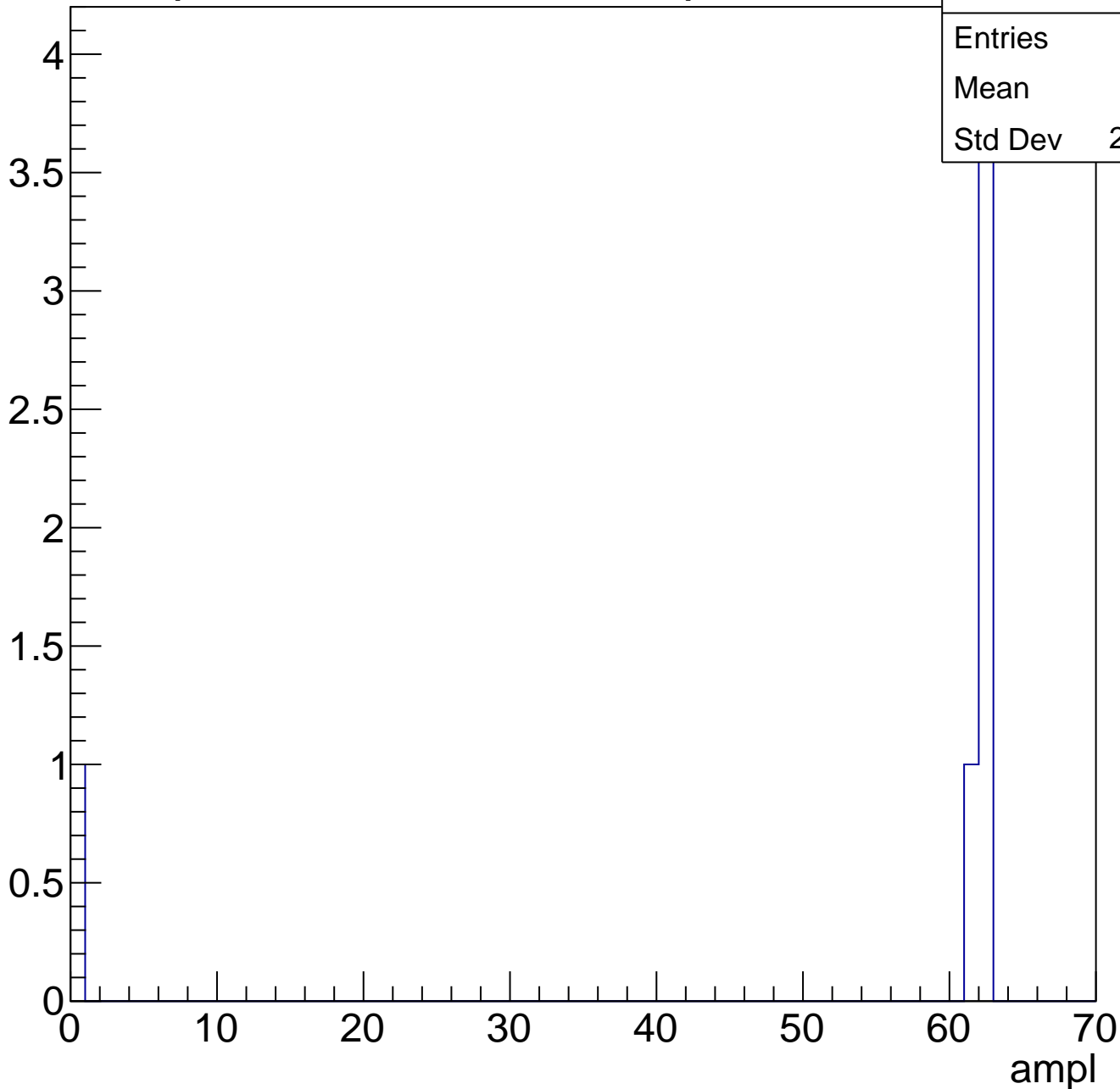
Entries	28
Mean	60.32
Std Dev	2.522



B1L101S, U23-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch125, adc0

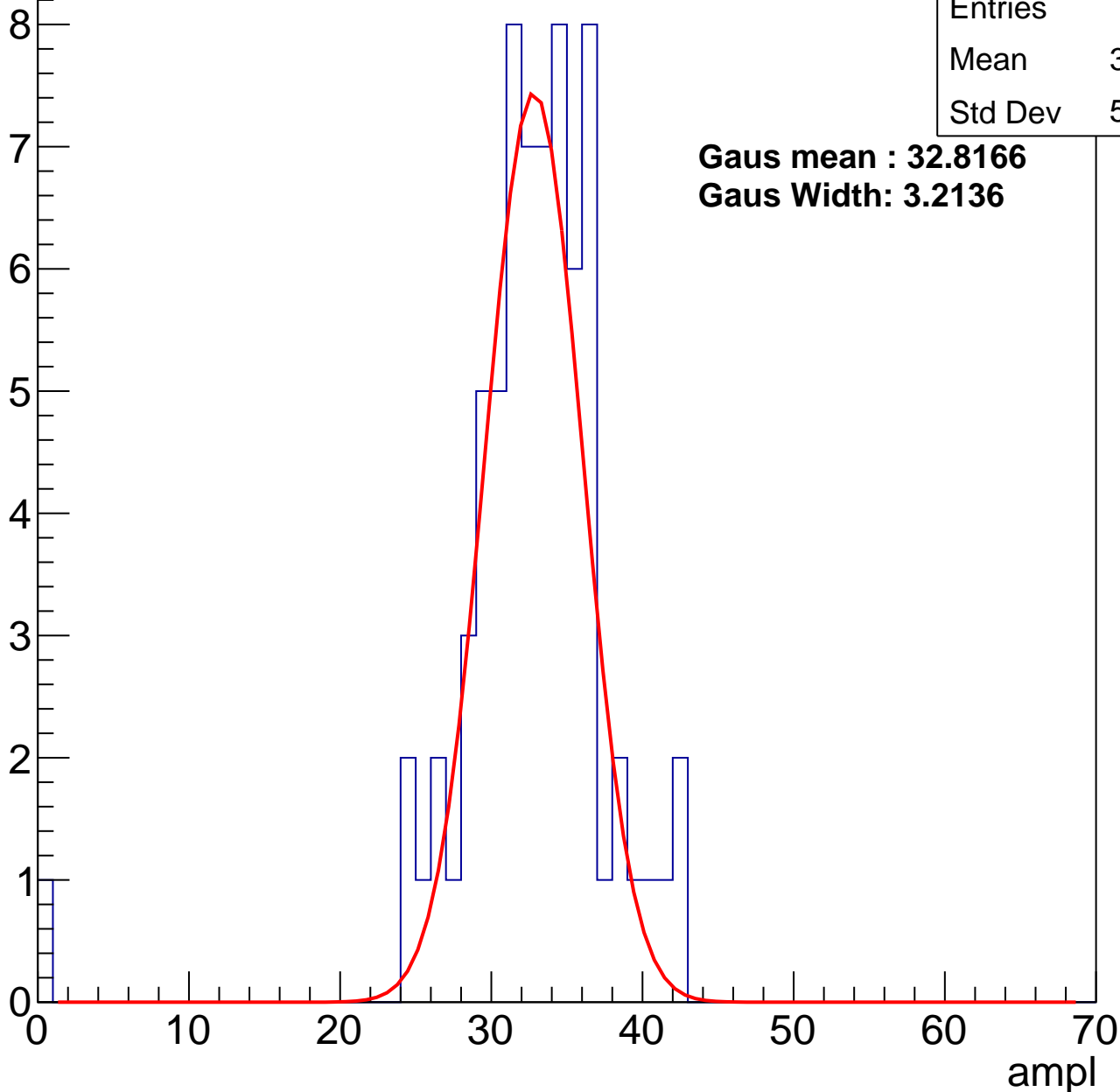
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	32.24
Std Dev	5.435

Gaus mean : 32.8166

Gaus Width: 3.2136



B1L101S, U23-ch125, adc1

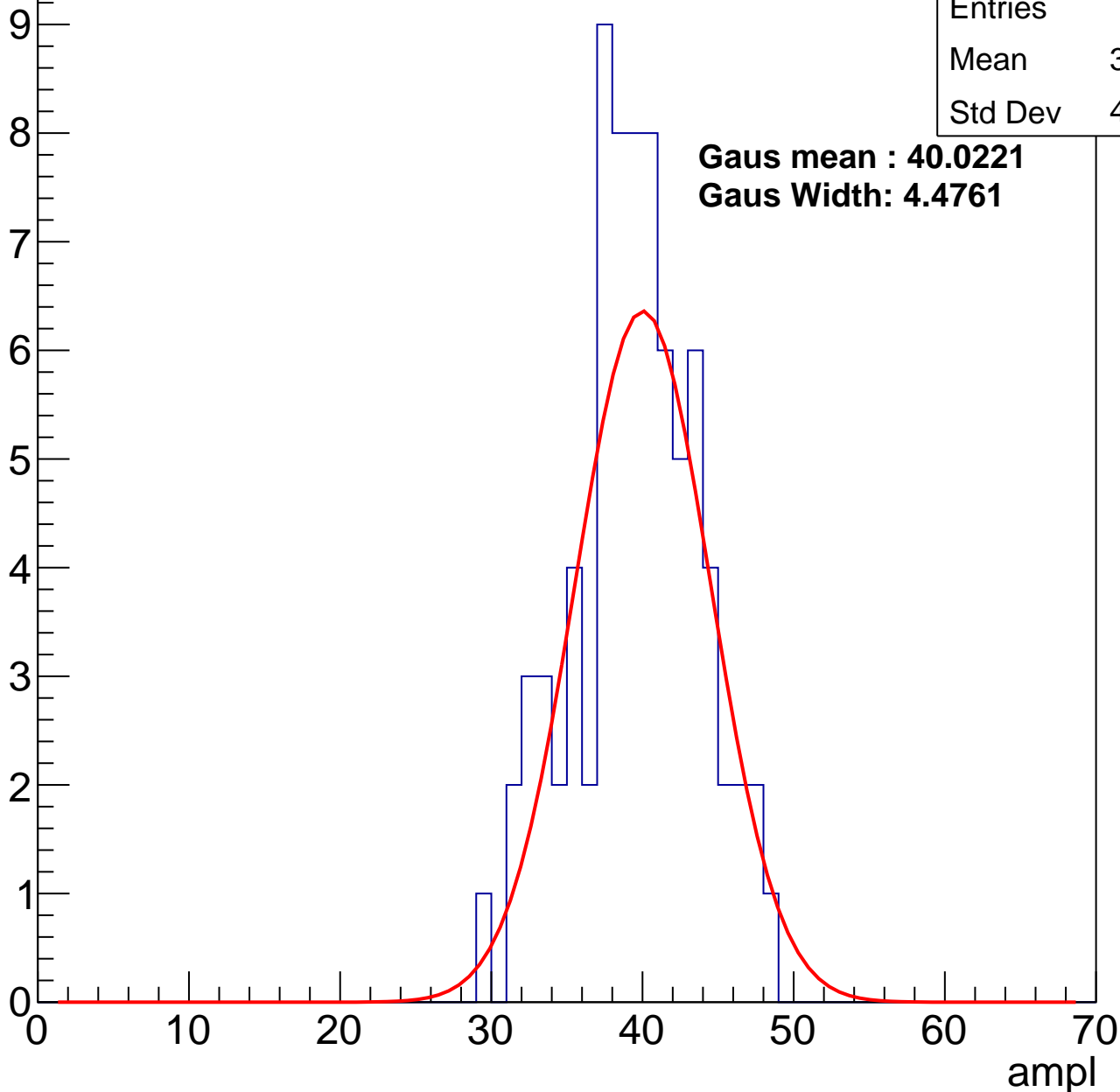
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	39.09
Std Dev	4.099

Gaus mean : 40.0221

Gaus Width: 4.4761



B1L101S, U23-ch125, adc2

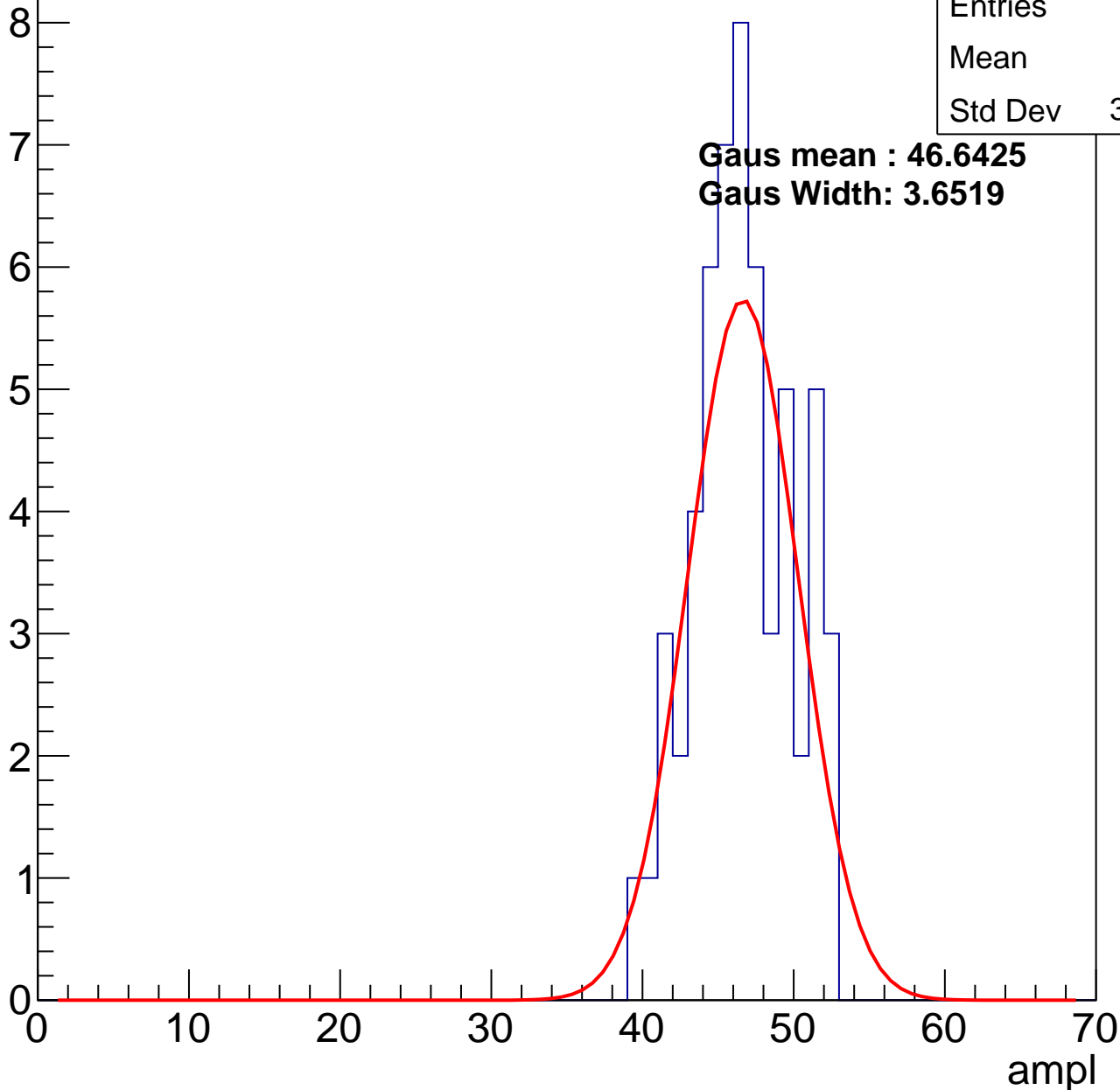
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	46.2
Std Dev	3.232

Gaus mean : 46.6425

Gaus Width: 3.6519

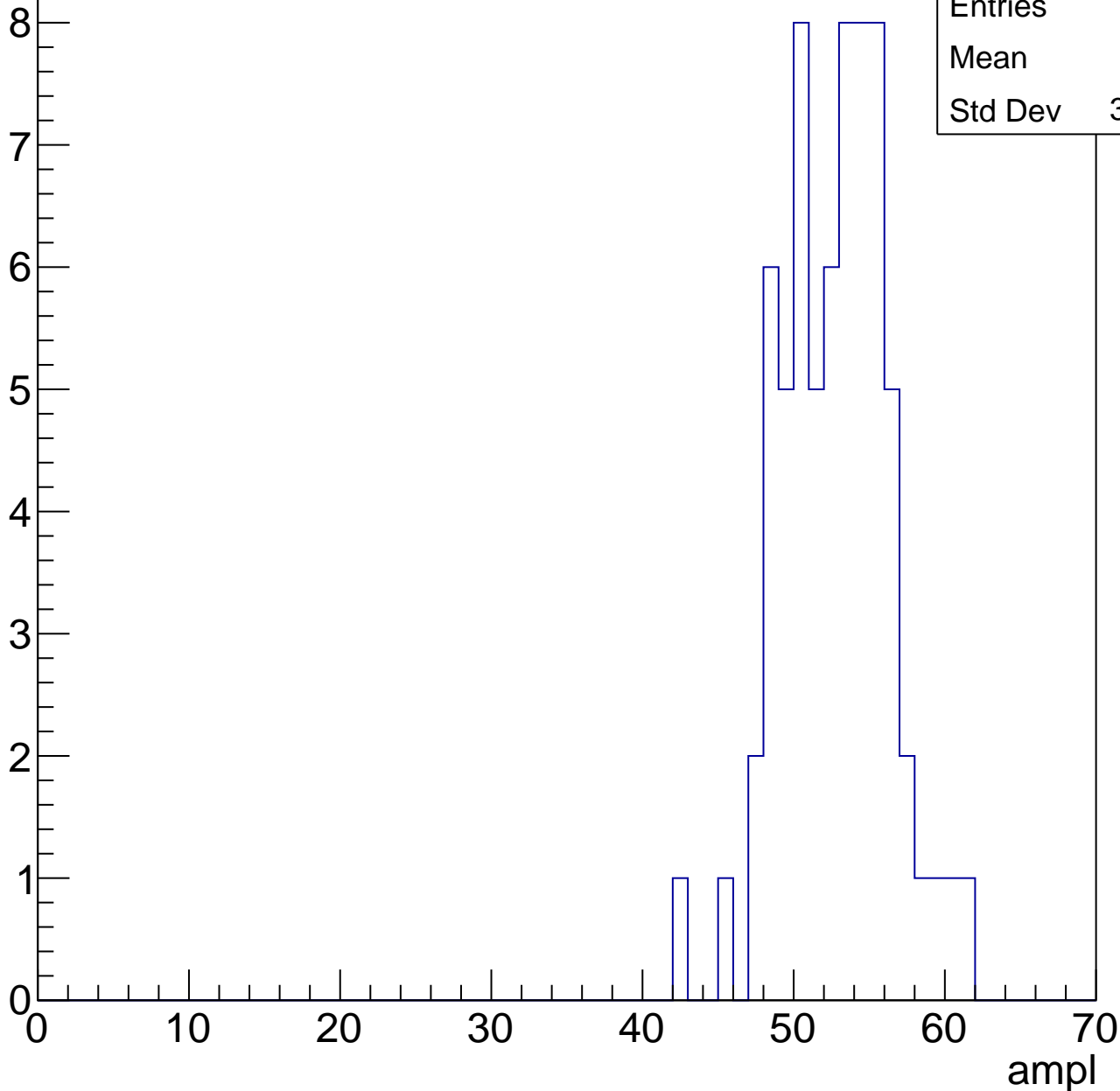


B1L101S, U23-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	52.3
Std Dev	3.495

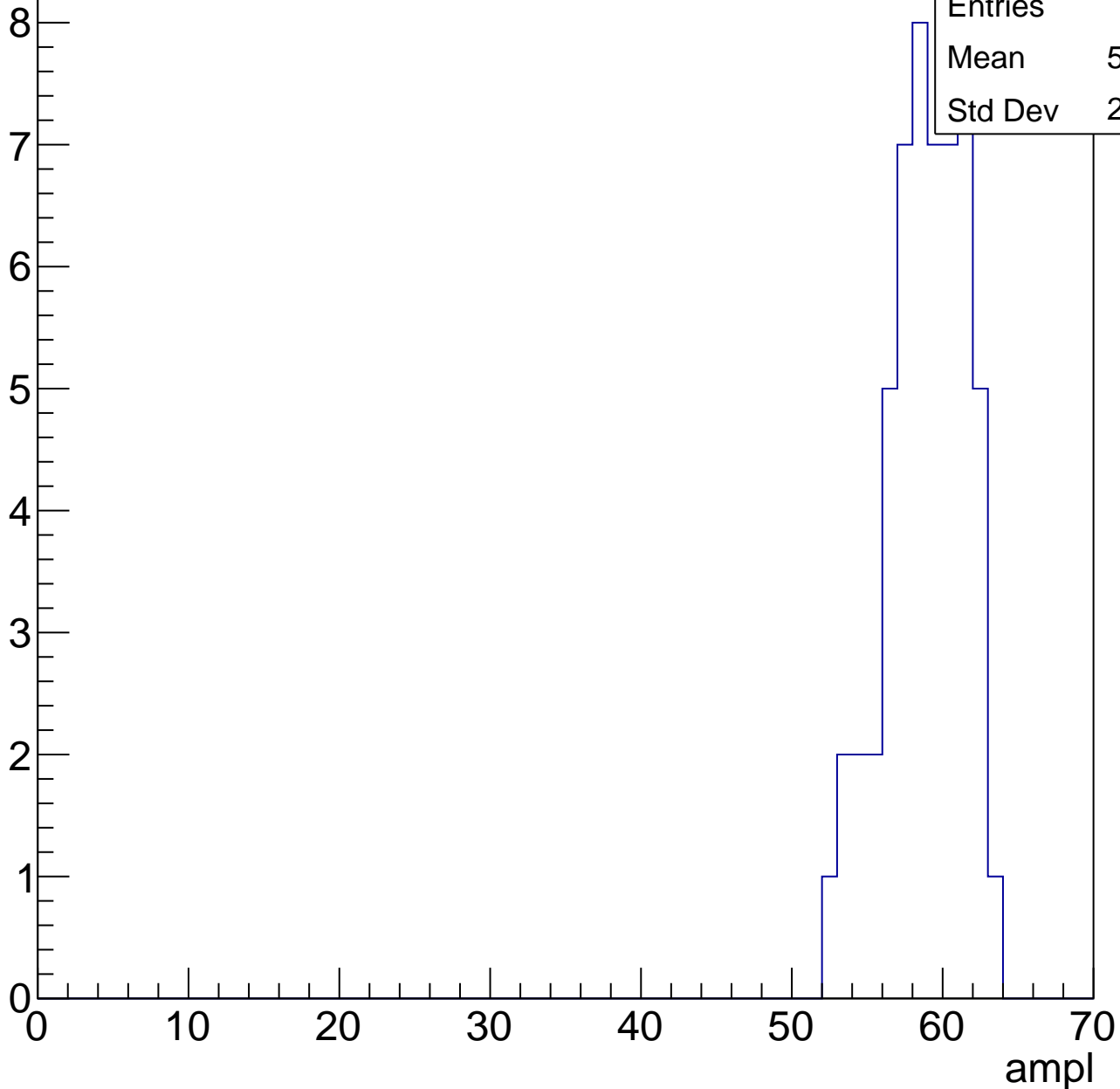


B1L101S, U23-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	58.42
Std Dev	2.577

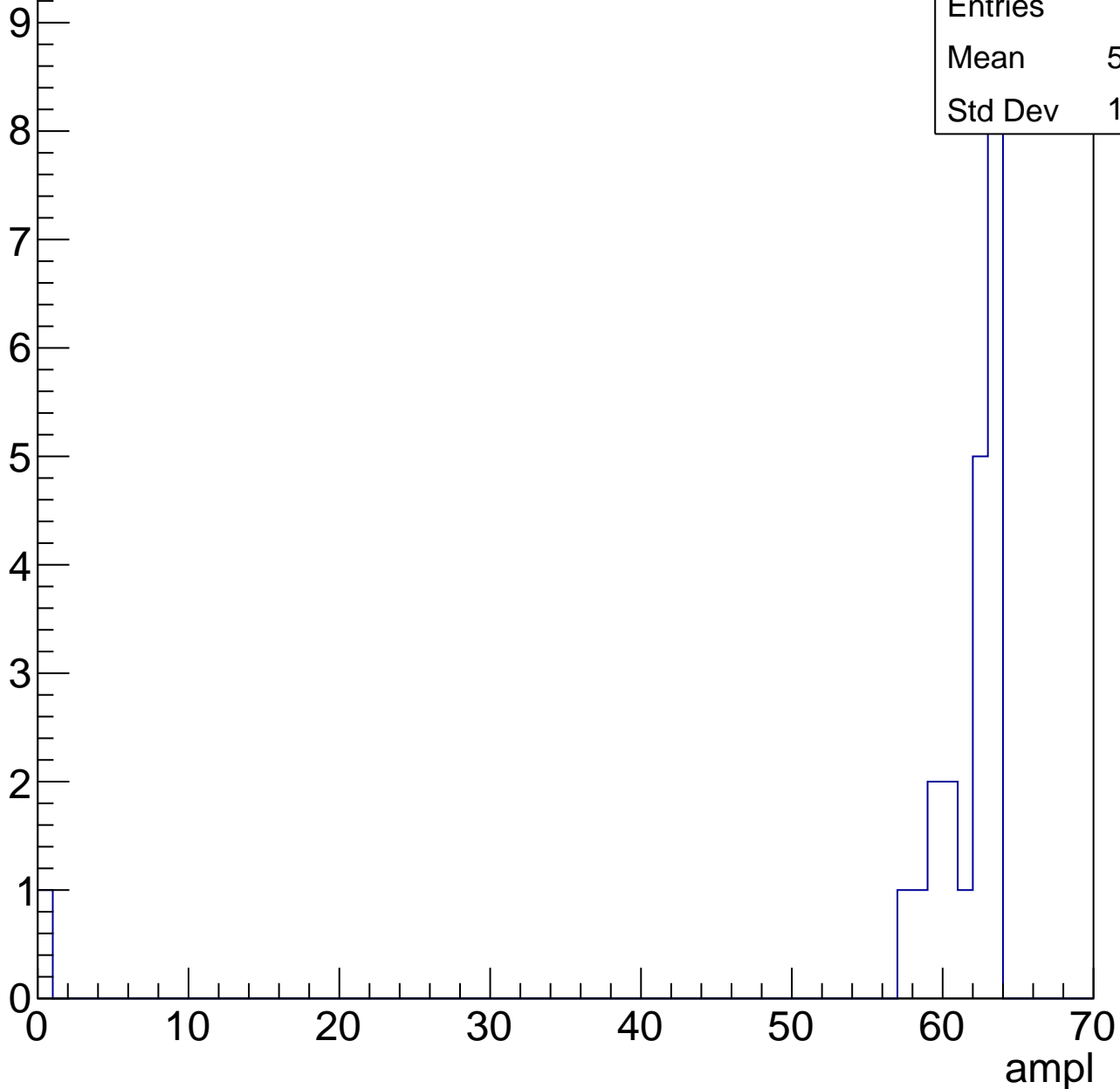


B1L101S, U23-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	58.68
Std Dev	12.93



B1L101S, U23-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

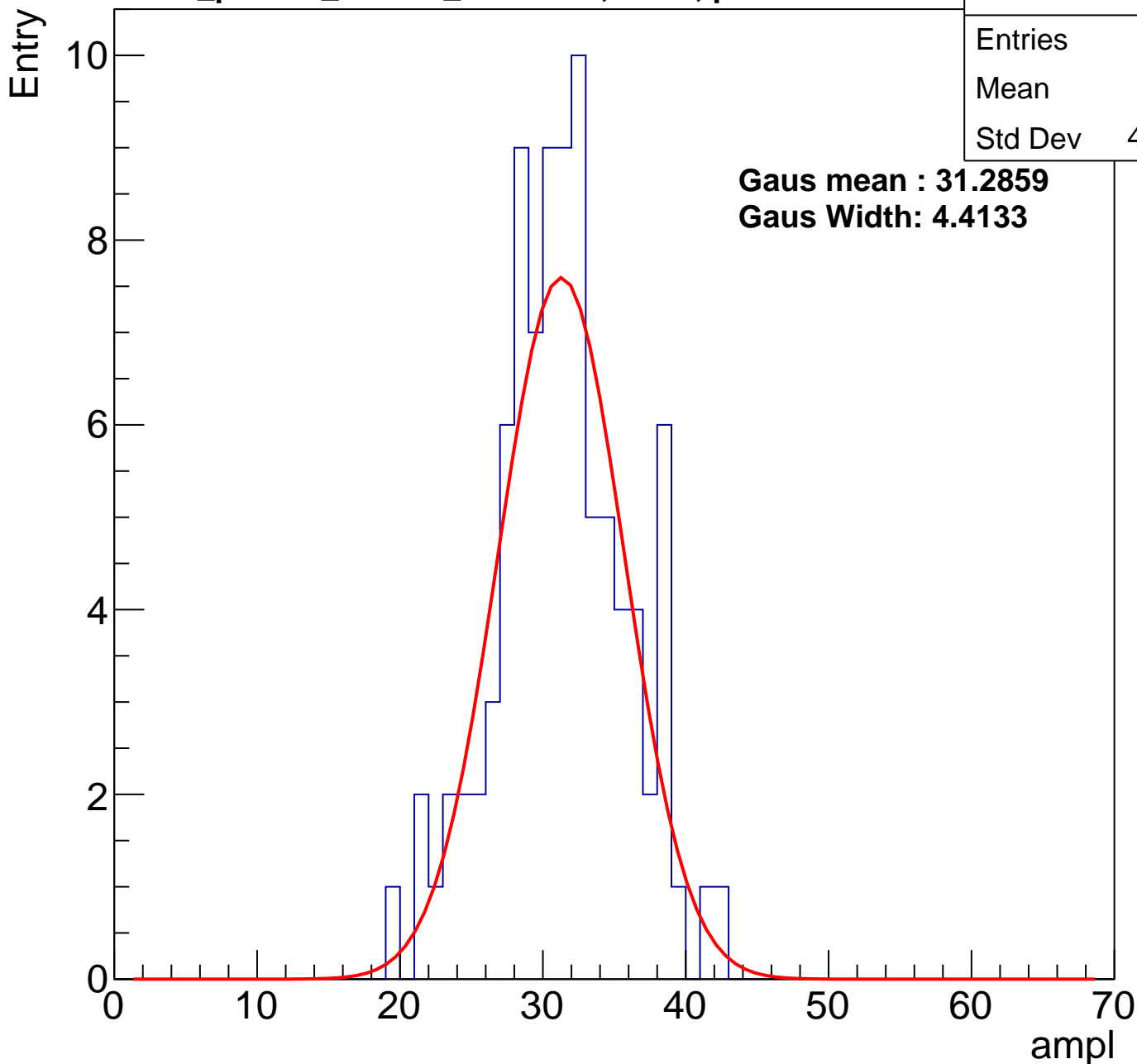
B1L101S, U23-ch126, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	92
Mean	30.8
Std Dev	4.543

Gaus mean : 31.2859

Gaus Width: 4.4133



B1L101S, U23-ch126, adc1

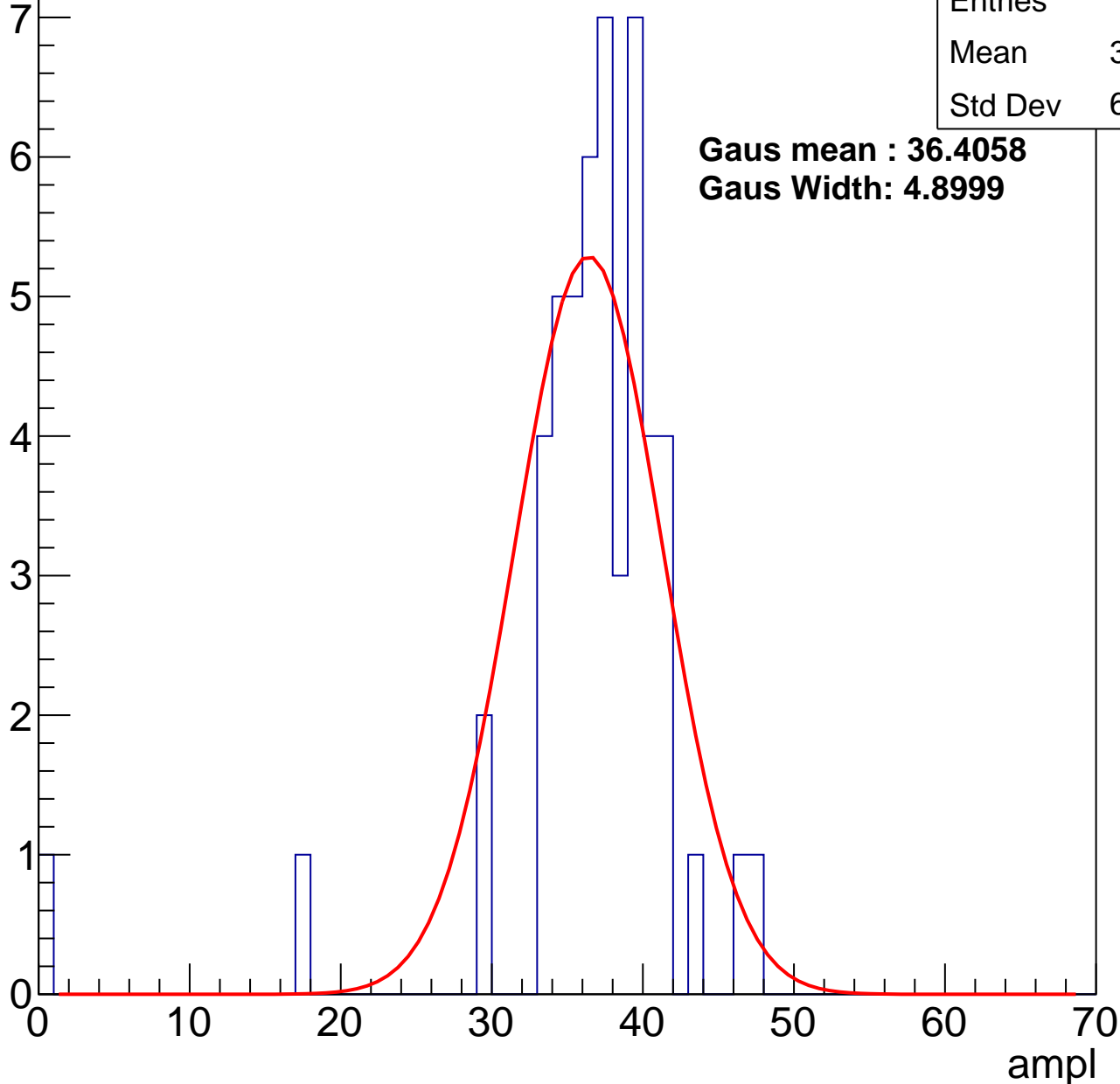
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	36.04
Std Dev	6.697

Gaus mean : 36.4058

Gaus Width: 4.8999



B1L101S, U23-ch126, adc2

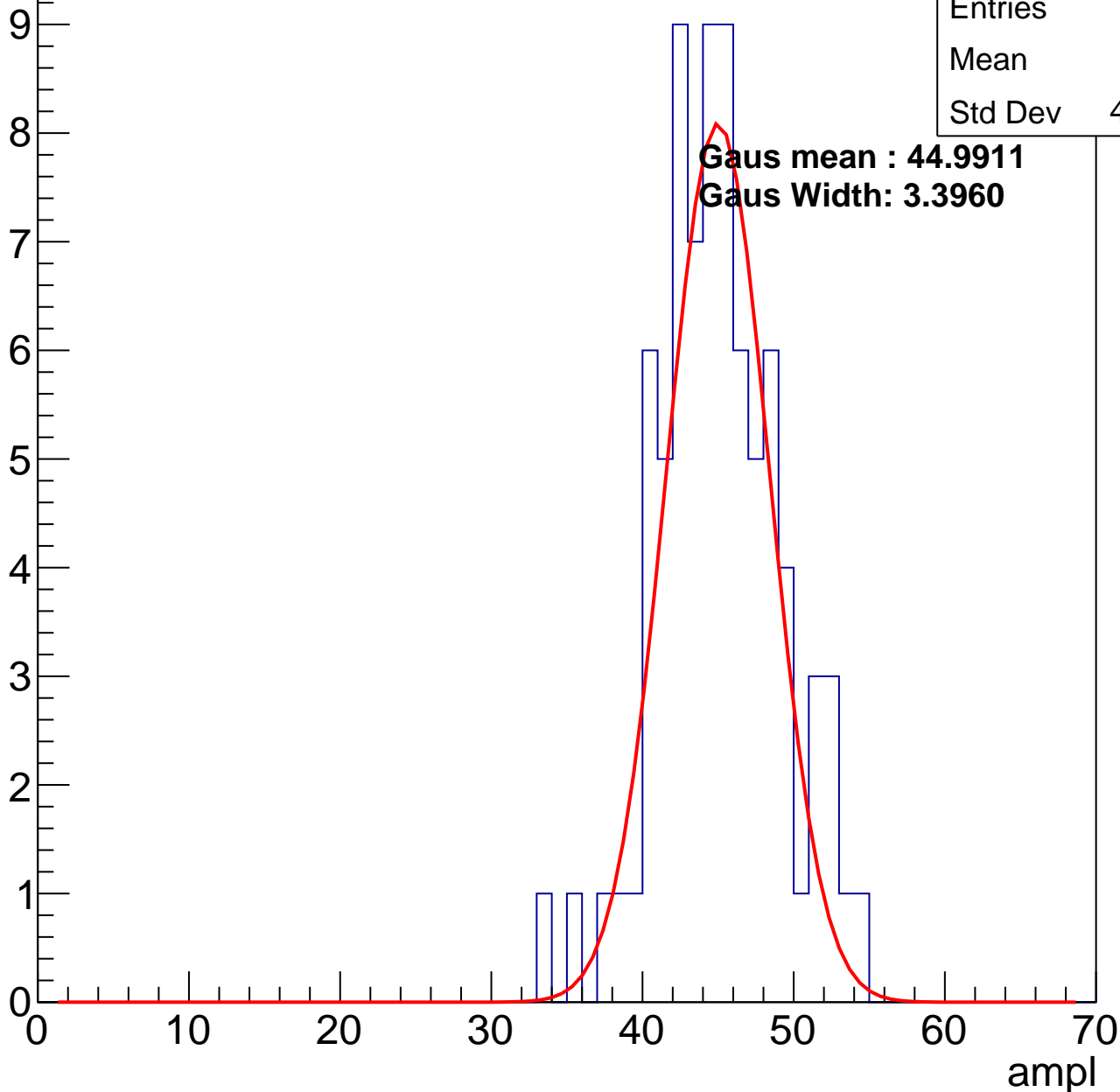
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	44.6
Std Dev	4.027

Gaus mean : 44.9911

Gaus Width: 3.3960

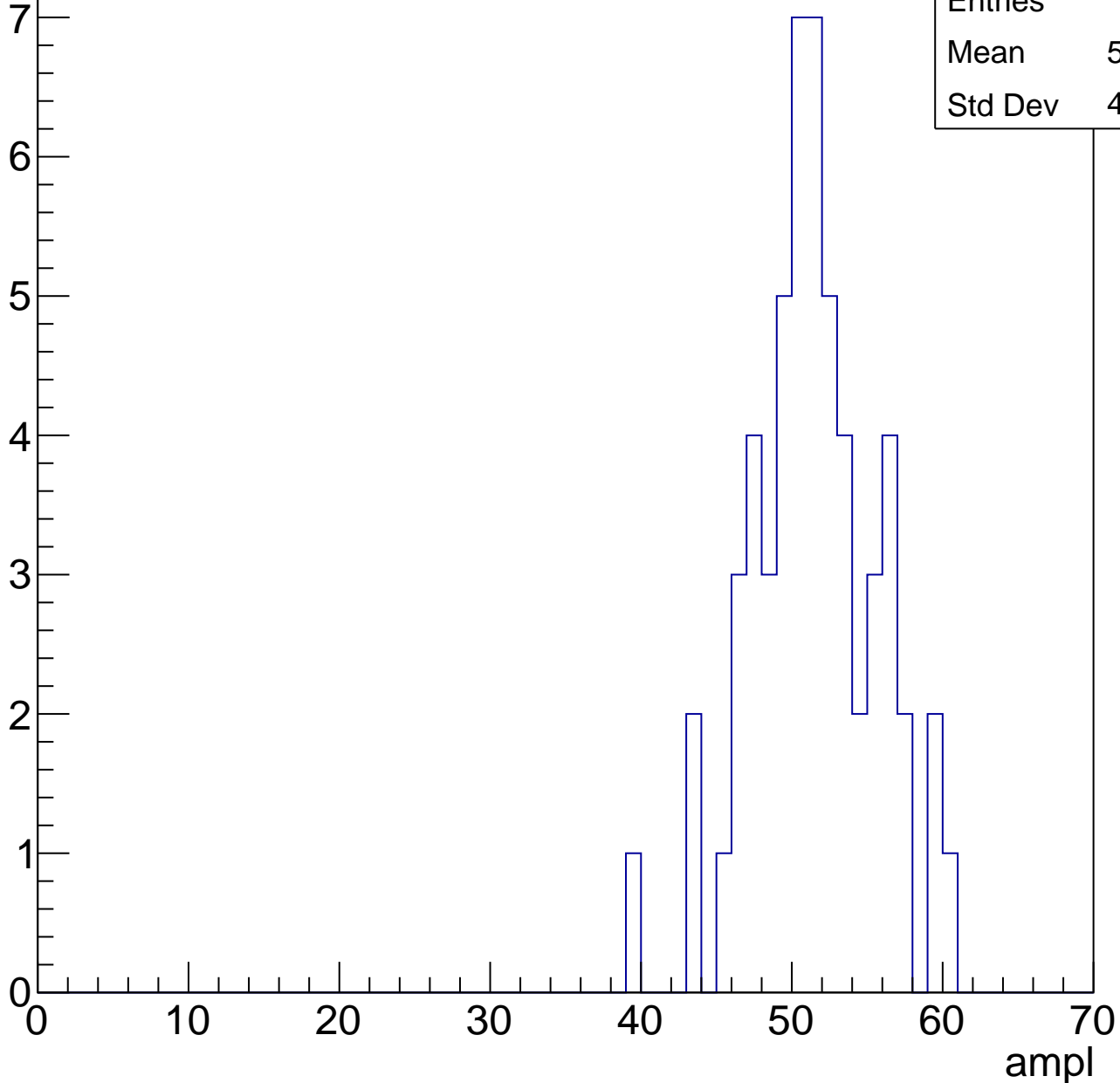


B1L101S, U23-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.95
Std Dev	4.159

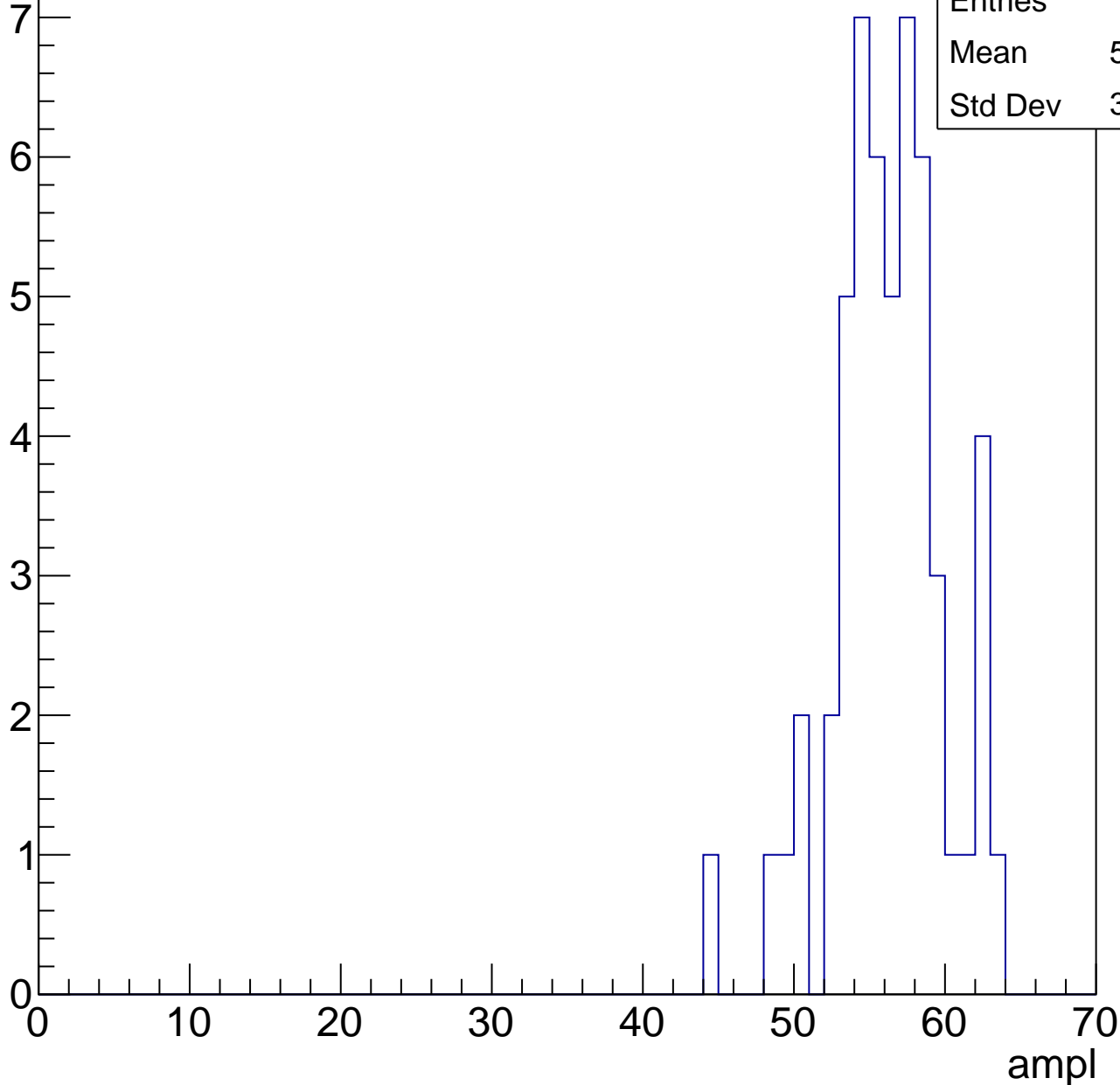


B1L101S, U23-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

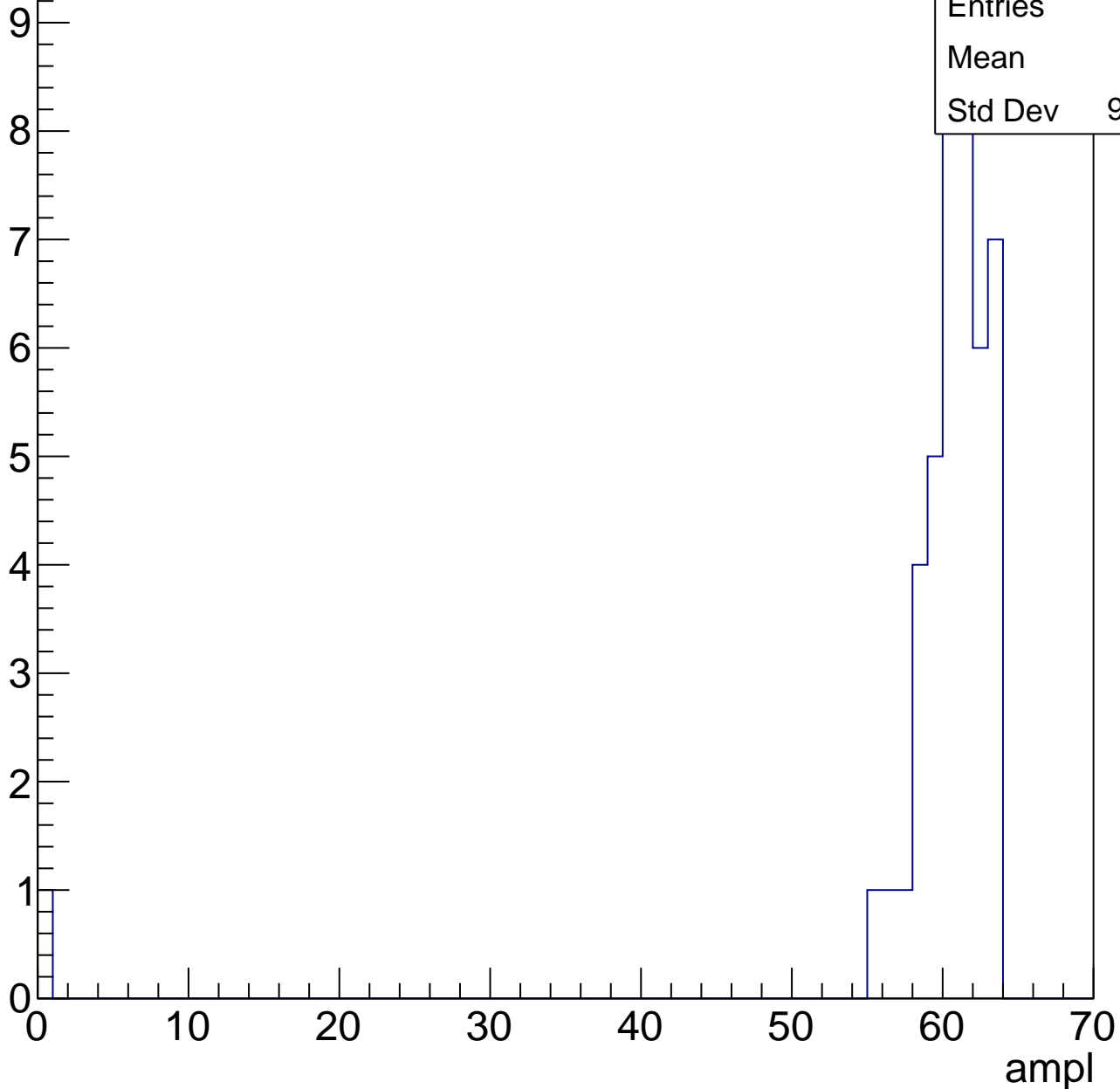
Entries	53
Mean	55.74
Std Dev	3.707



B1L101S, U23-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U23-ch127, adc0

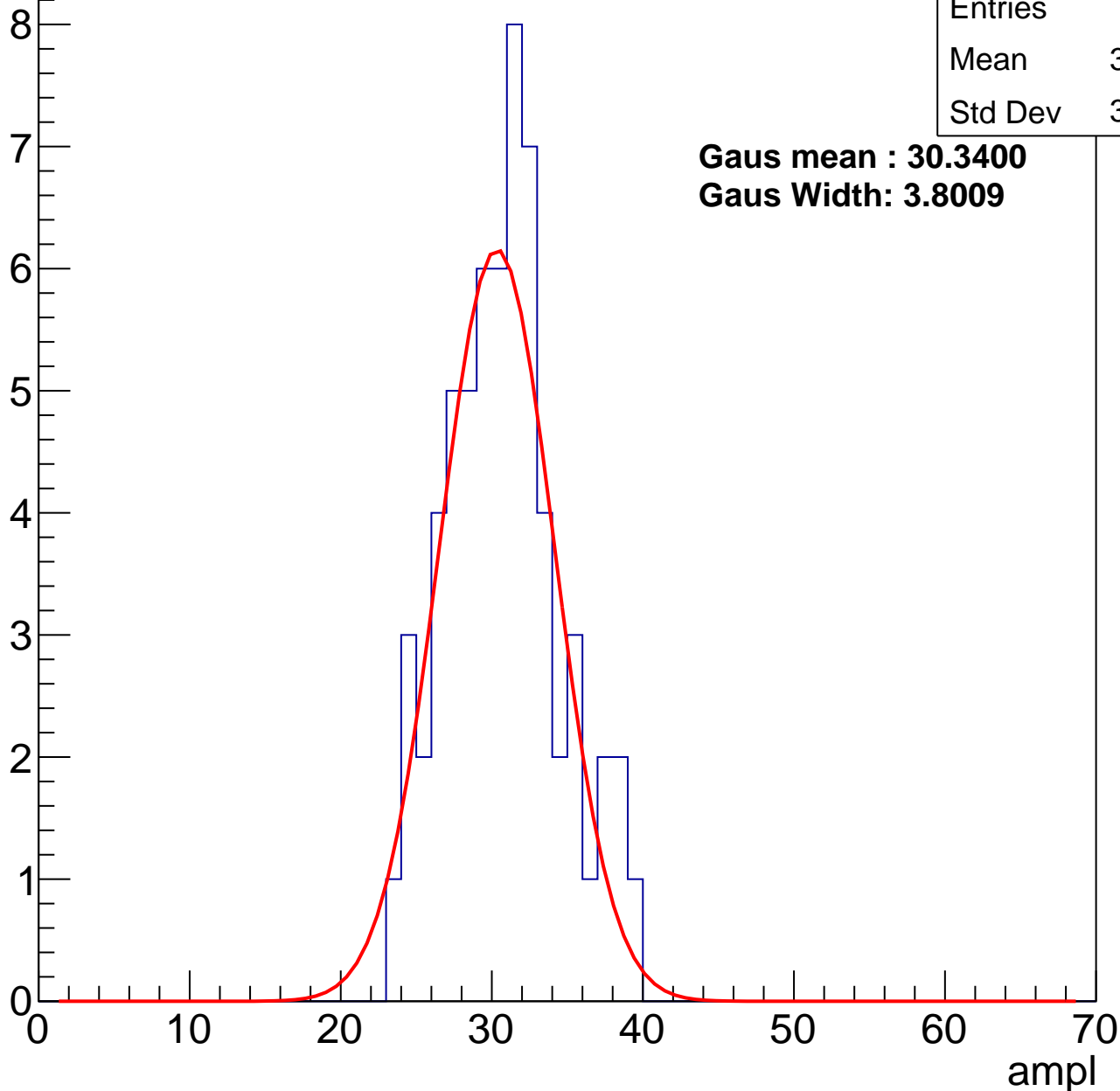
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.32
Std Dev	3.719

Gaus mean : 30.3400

Gaus Width: 3.8009



B1L101S, U23-ch127, adc1

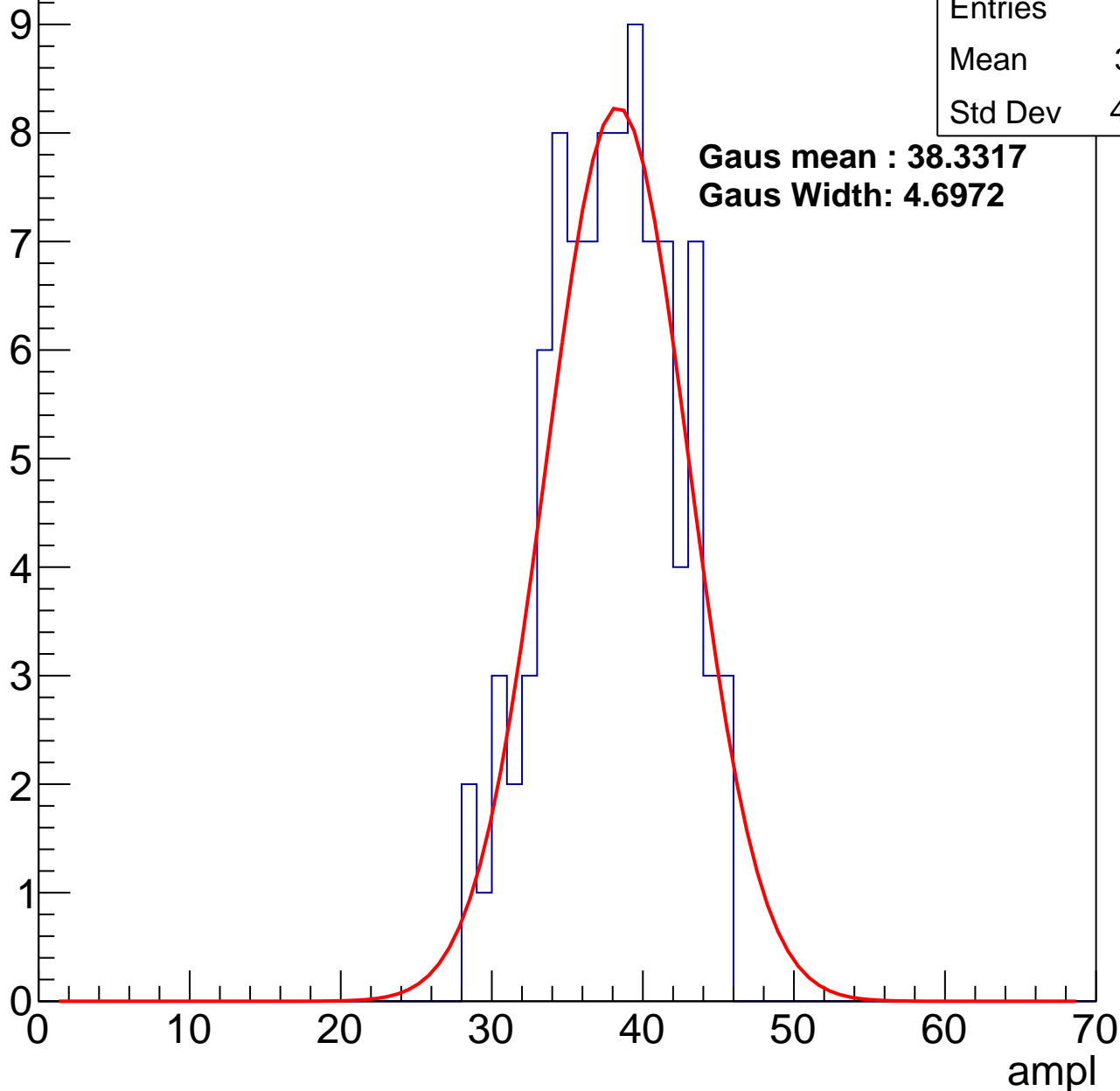
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	95
Mean	37.41
Std Dev	4.126

Gaus mean : 38.3317

Gaus Width: 4.6972



B1L101S, U23-ch127, adc2

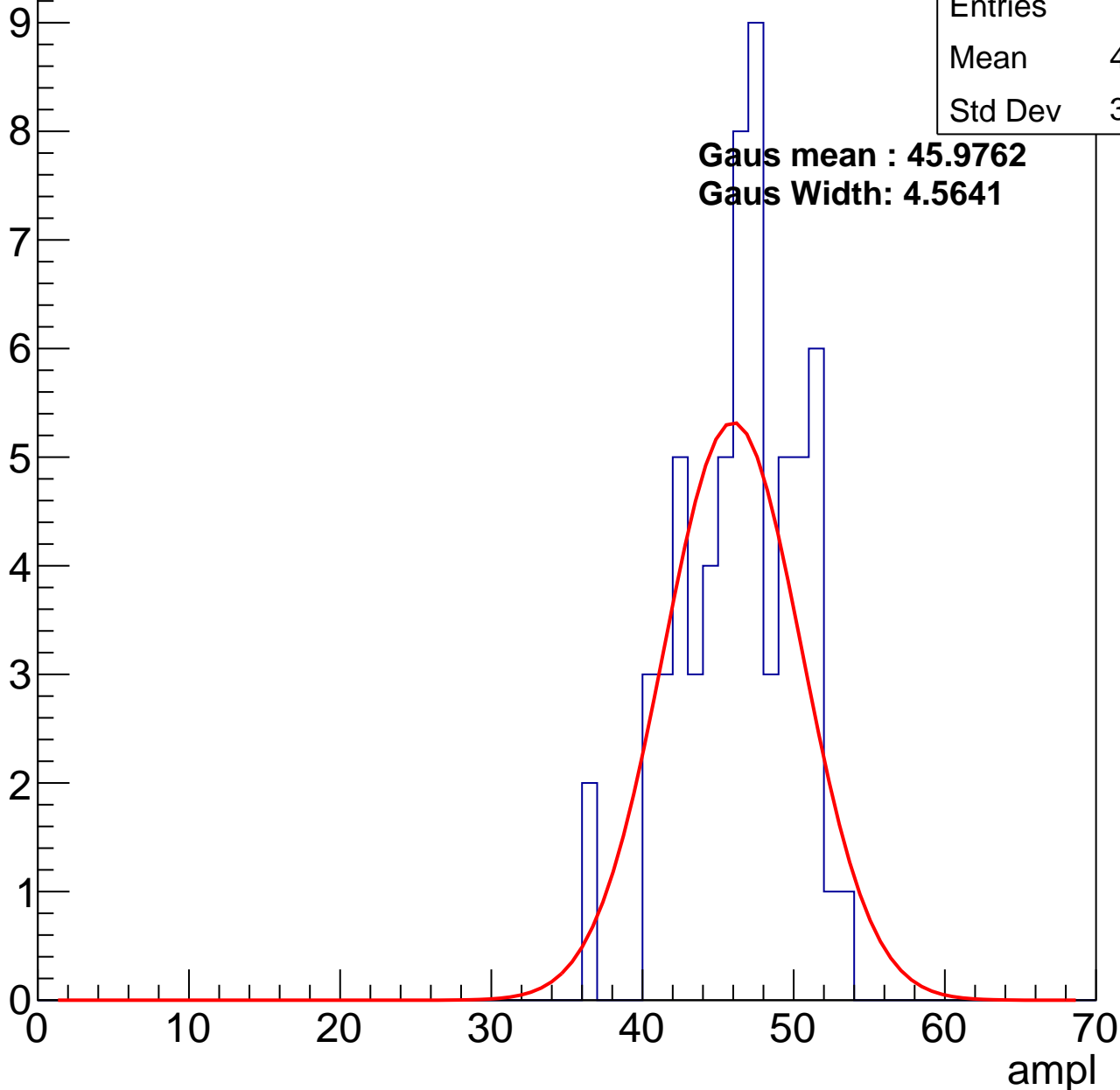
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	45.97
Std Dev	3.763

Gaus mean : 45.9762

Gaus Width: 4.5641

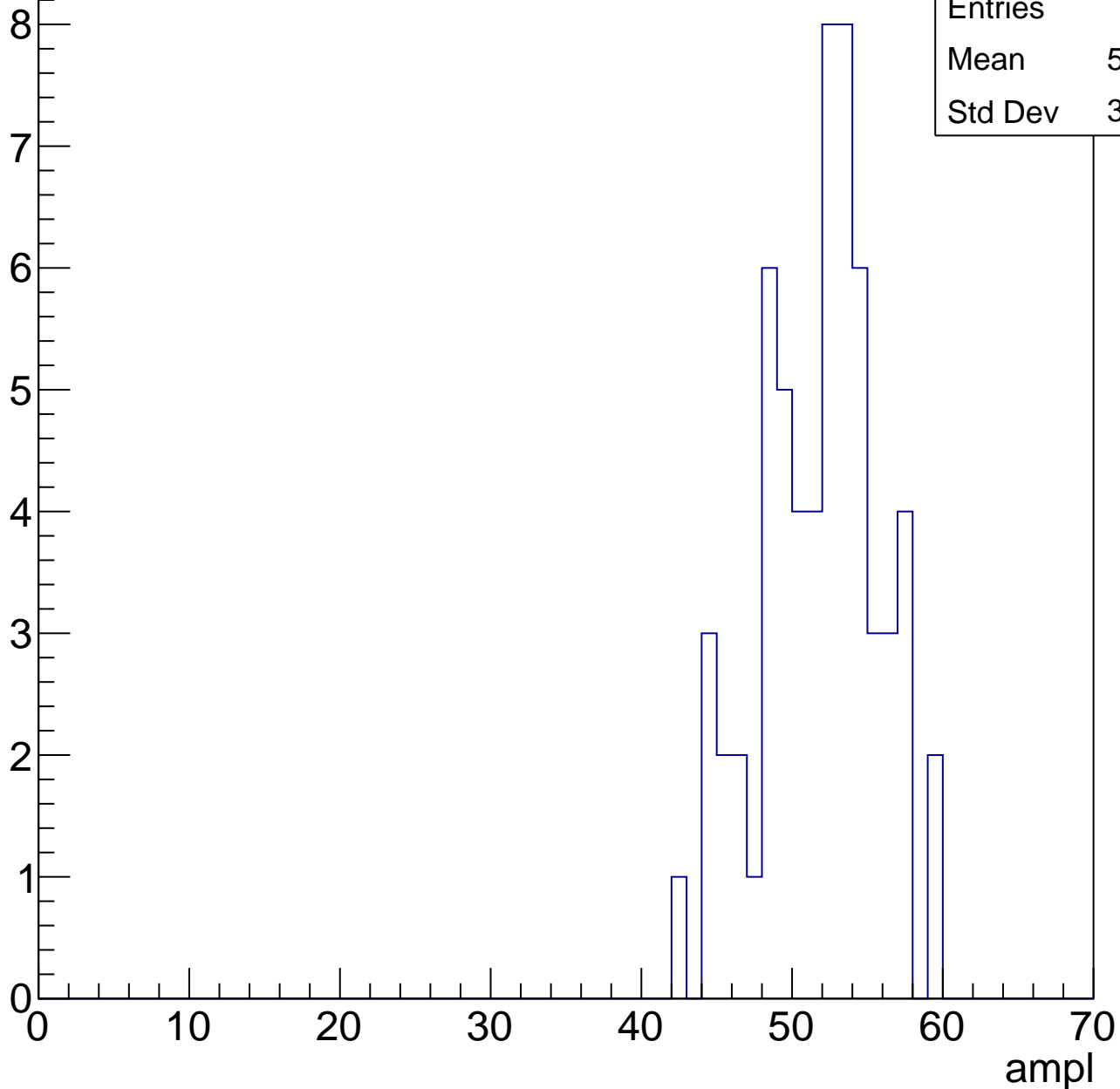


B1L101S, U23-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

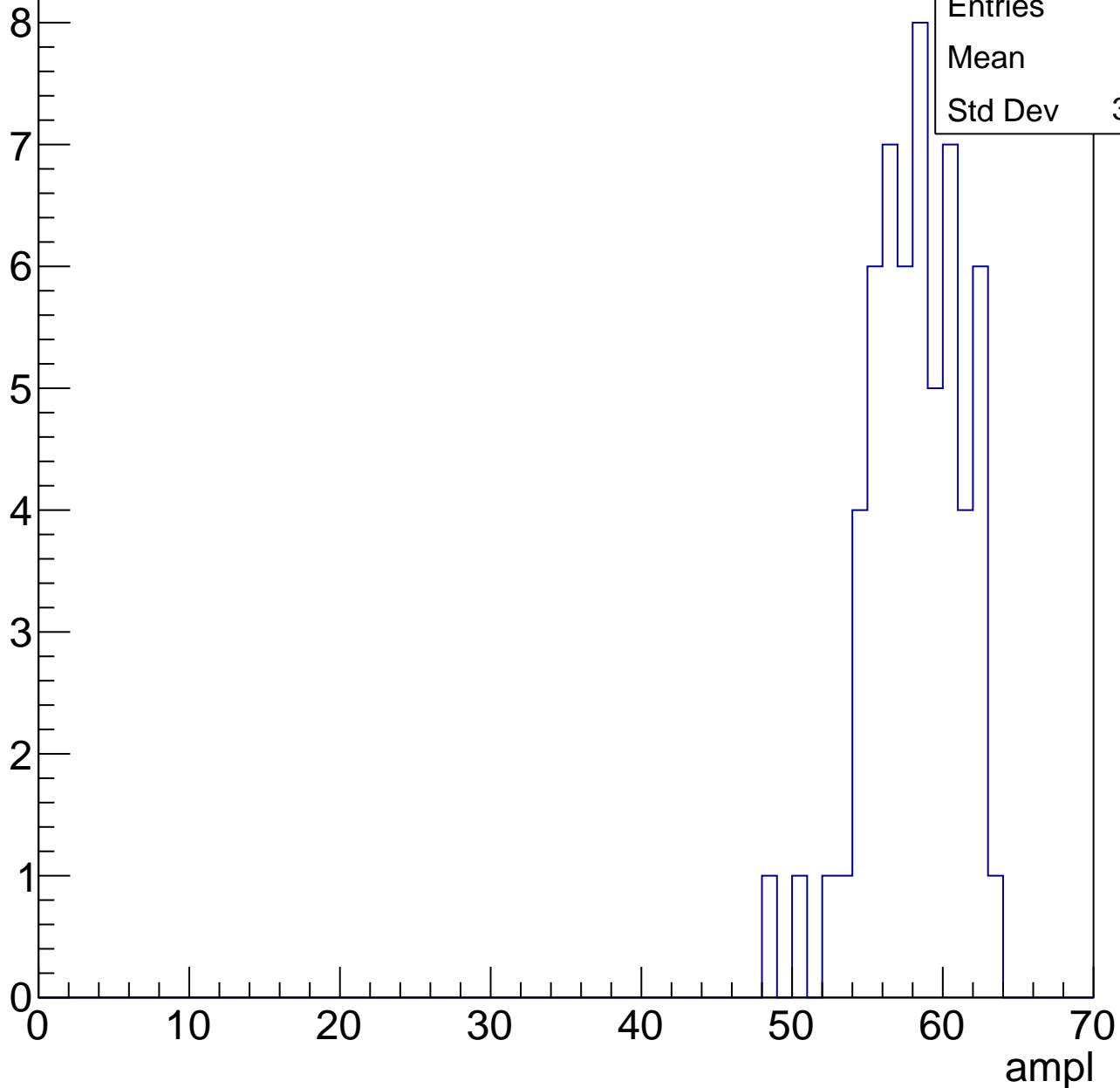
Entries	62
Mean	51.34
Std Dev	3.864



B1L101S, U23-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

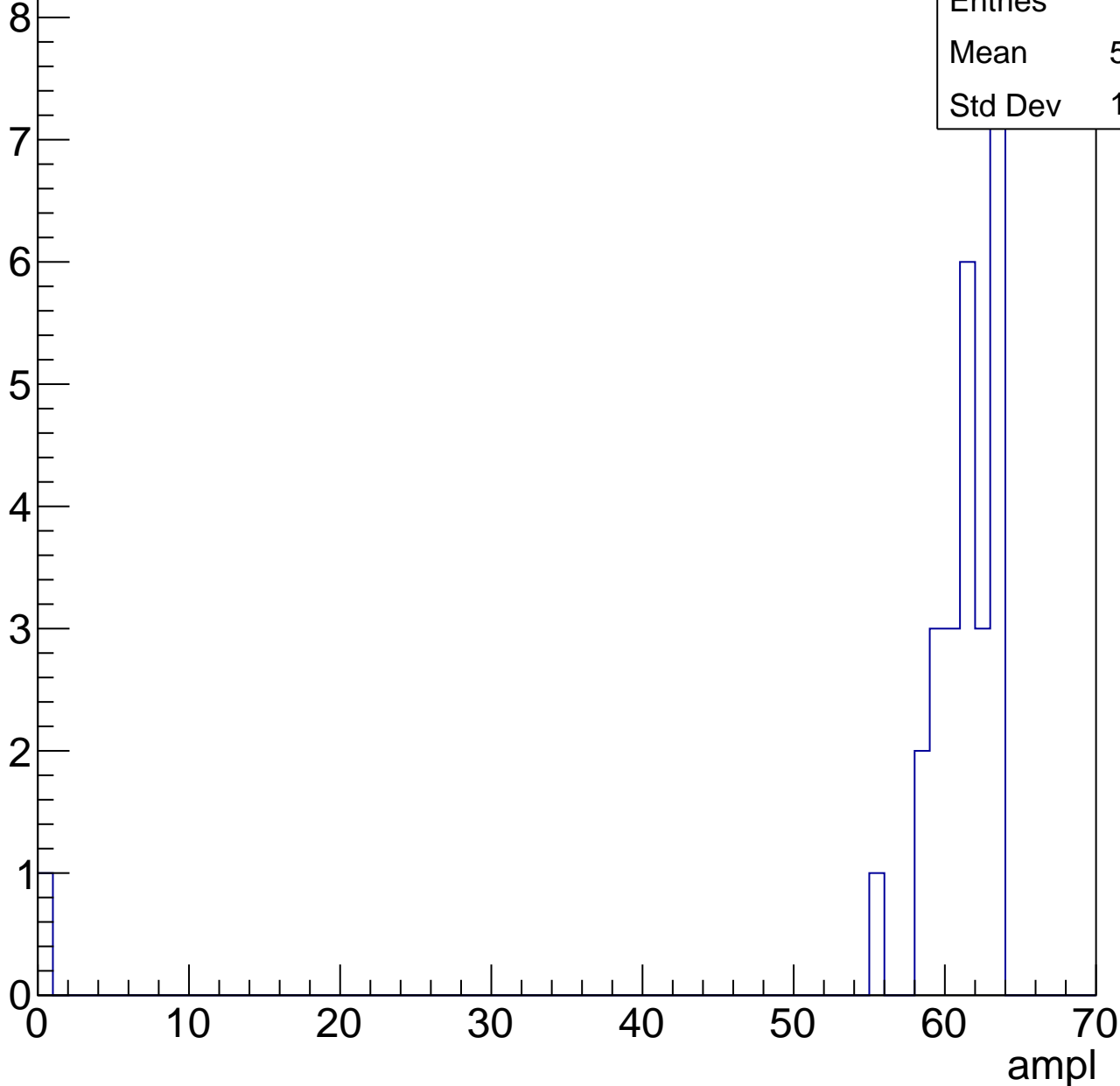


B1L101S, U23-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.67
Std Dev	11.67



B1L101S, U23-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U23-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

