



# B0L002S, U24-ch0

calib\_packv5\_042523\_0143.root, FC#8, port C1

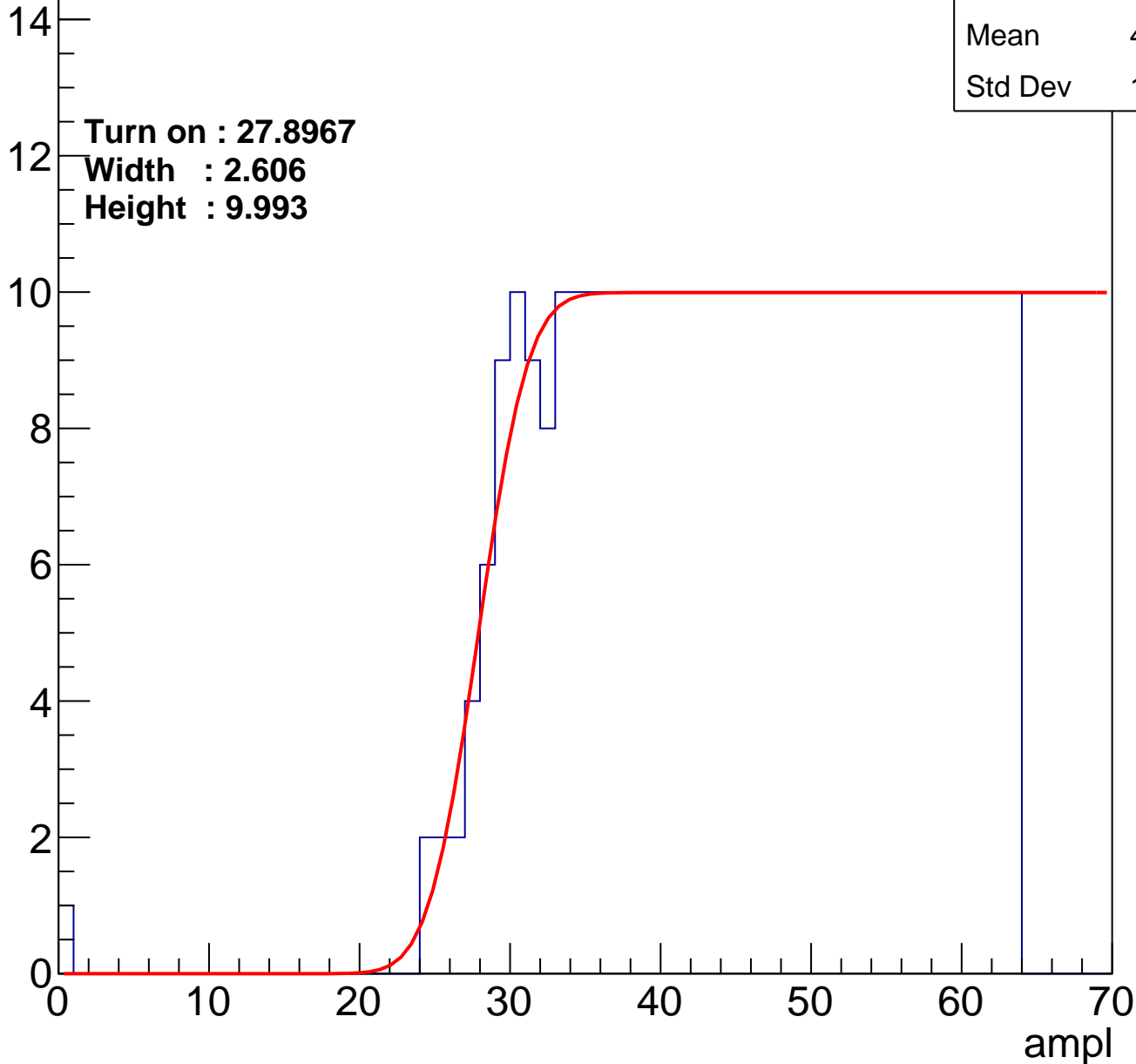
Entry

Entries	363
Mean	45.18
Std Dev	10.85

Turn on : 27.8967

Width : 2.606

Height : 9.993



# B0L002S, U24-ch1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.47
Std Dev	11.89

Turn on : 27.2775

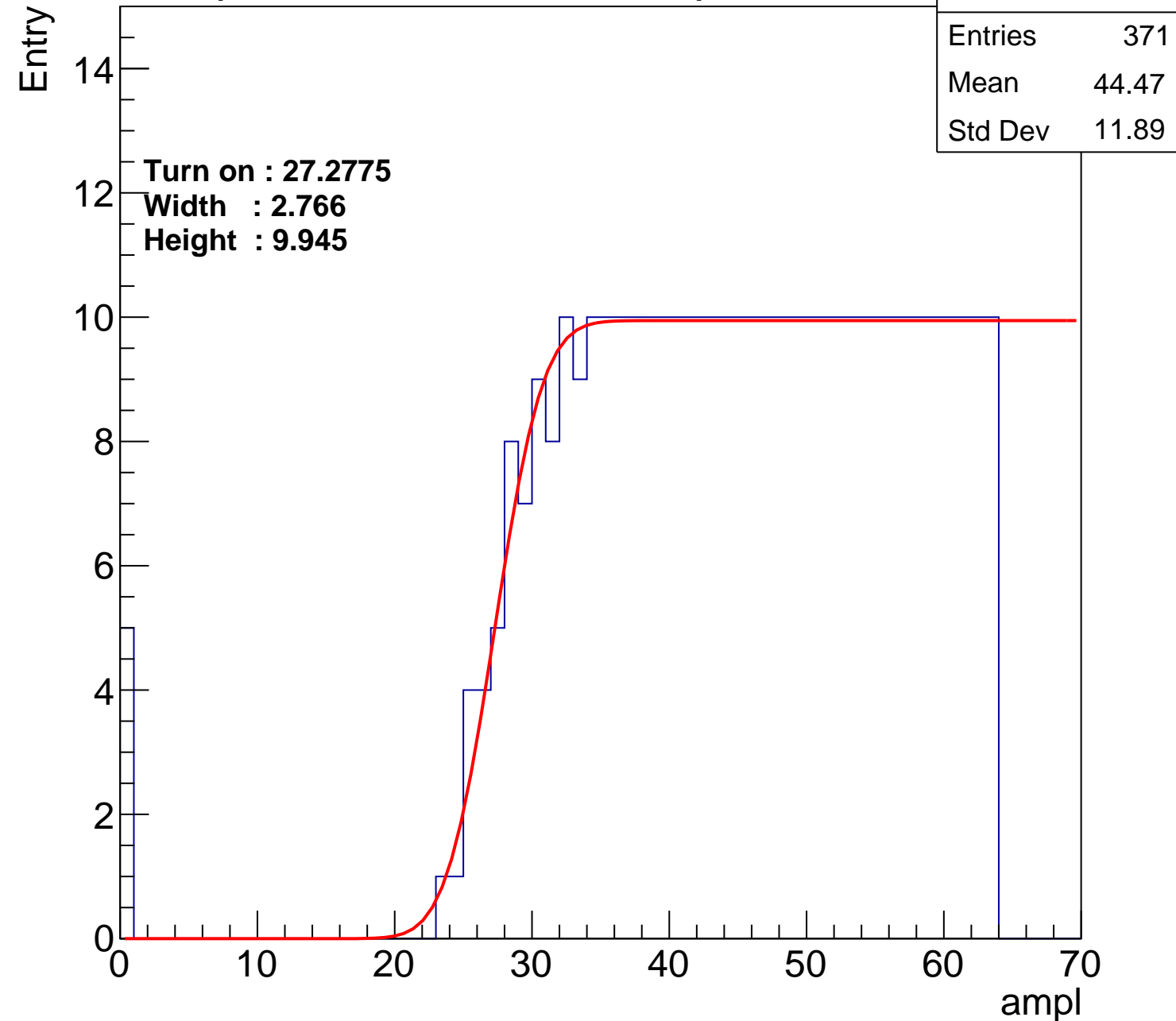
Width : 2.766

Height : 9.945

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch2

calib\_packv5\_042523\_0143.root, FC#8, port C1

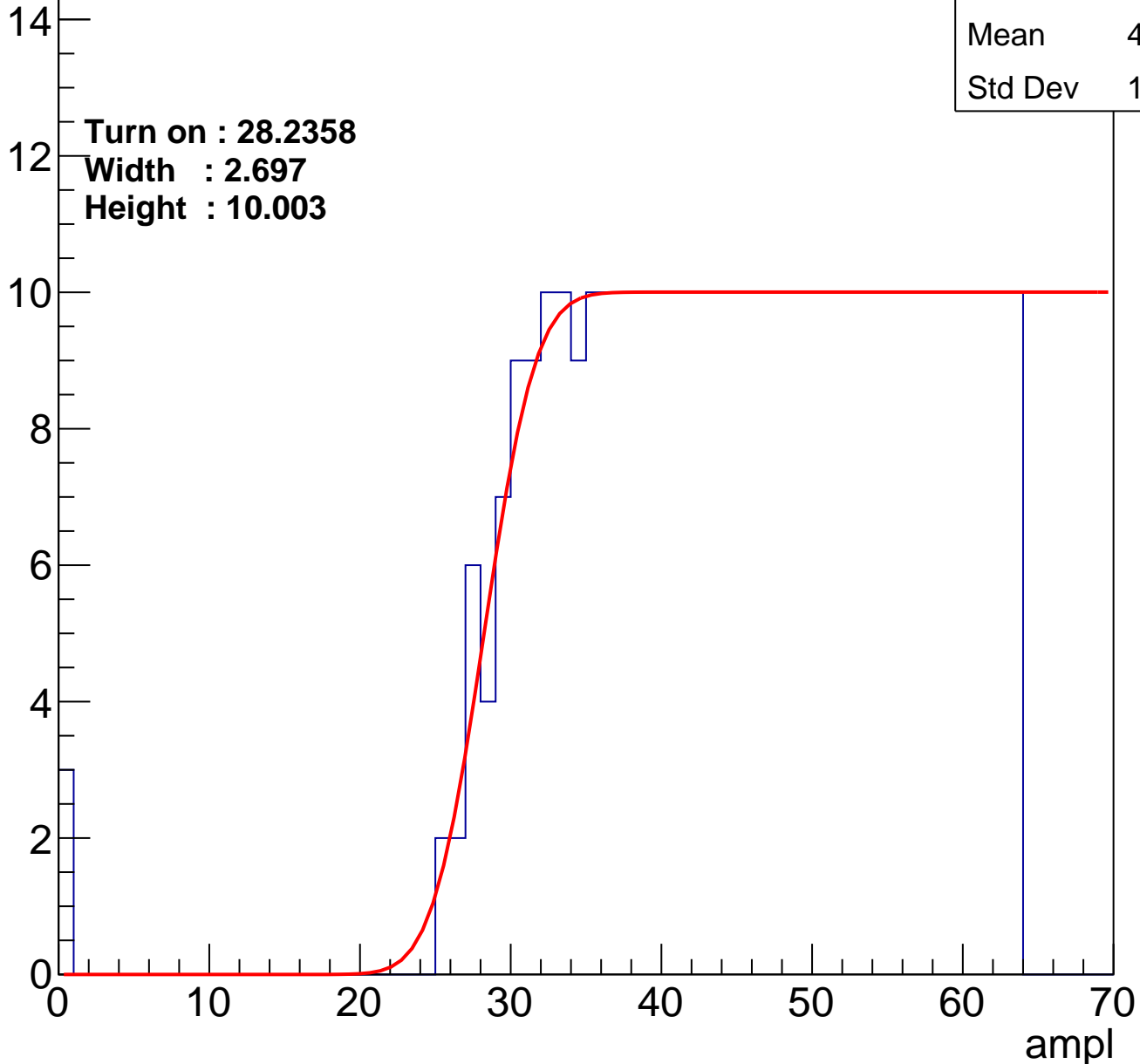
Entries	361
Mean	45.14
Std Dev	11.22

**Turn on : 28.2358**

**Width : 2.697**

**Height : 10.003**

Entry



# B0L002S, U24-ch3

calib\_packv5\_042523\_0143.root, FC#8, port C1

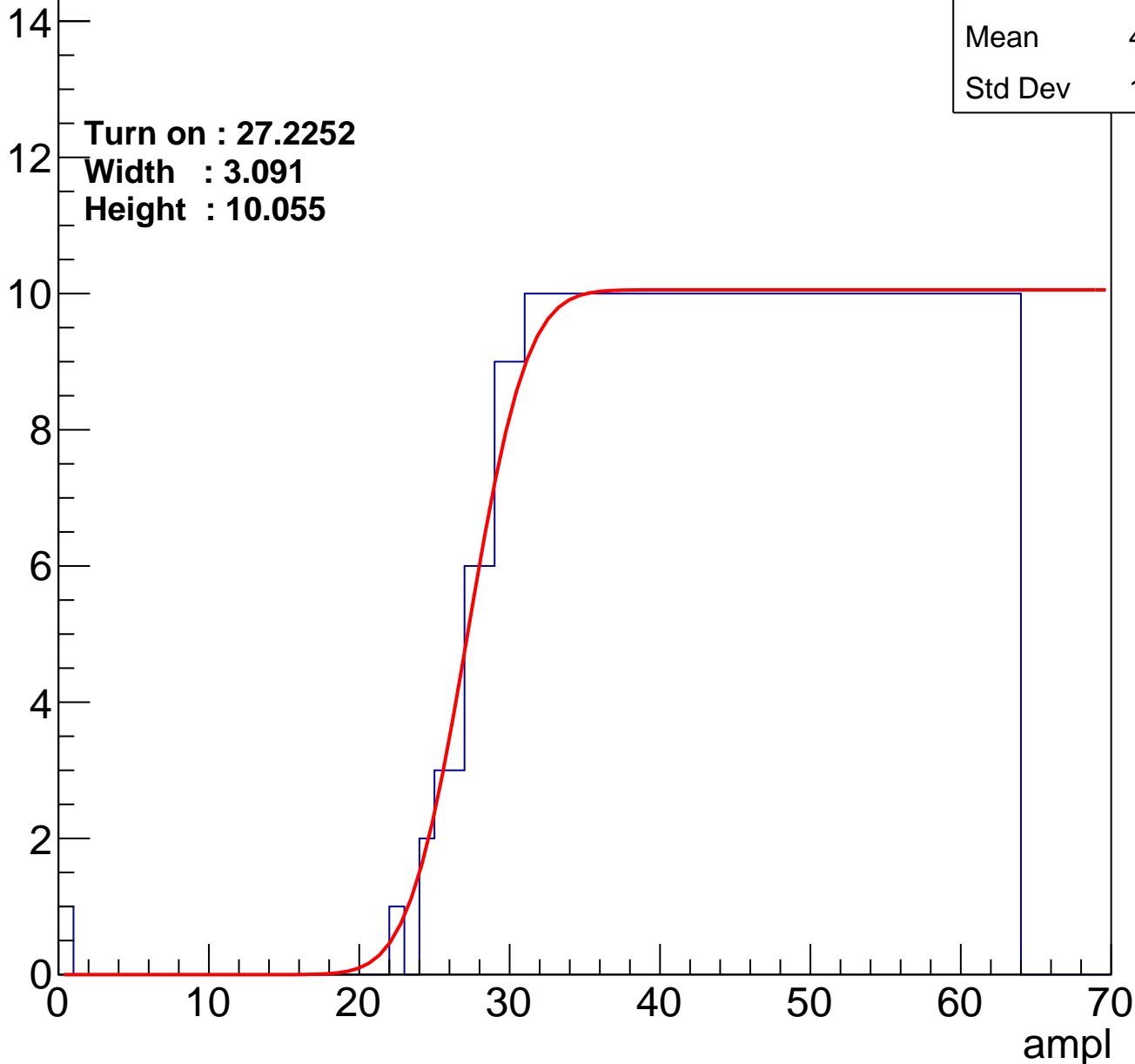
Entry

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.2252

Width : 3.091

Height : 10.055



# B0L002S, U24-ch4

calib\_packv5\_042523\_0143.root, FC#8, port C1

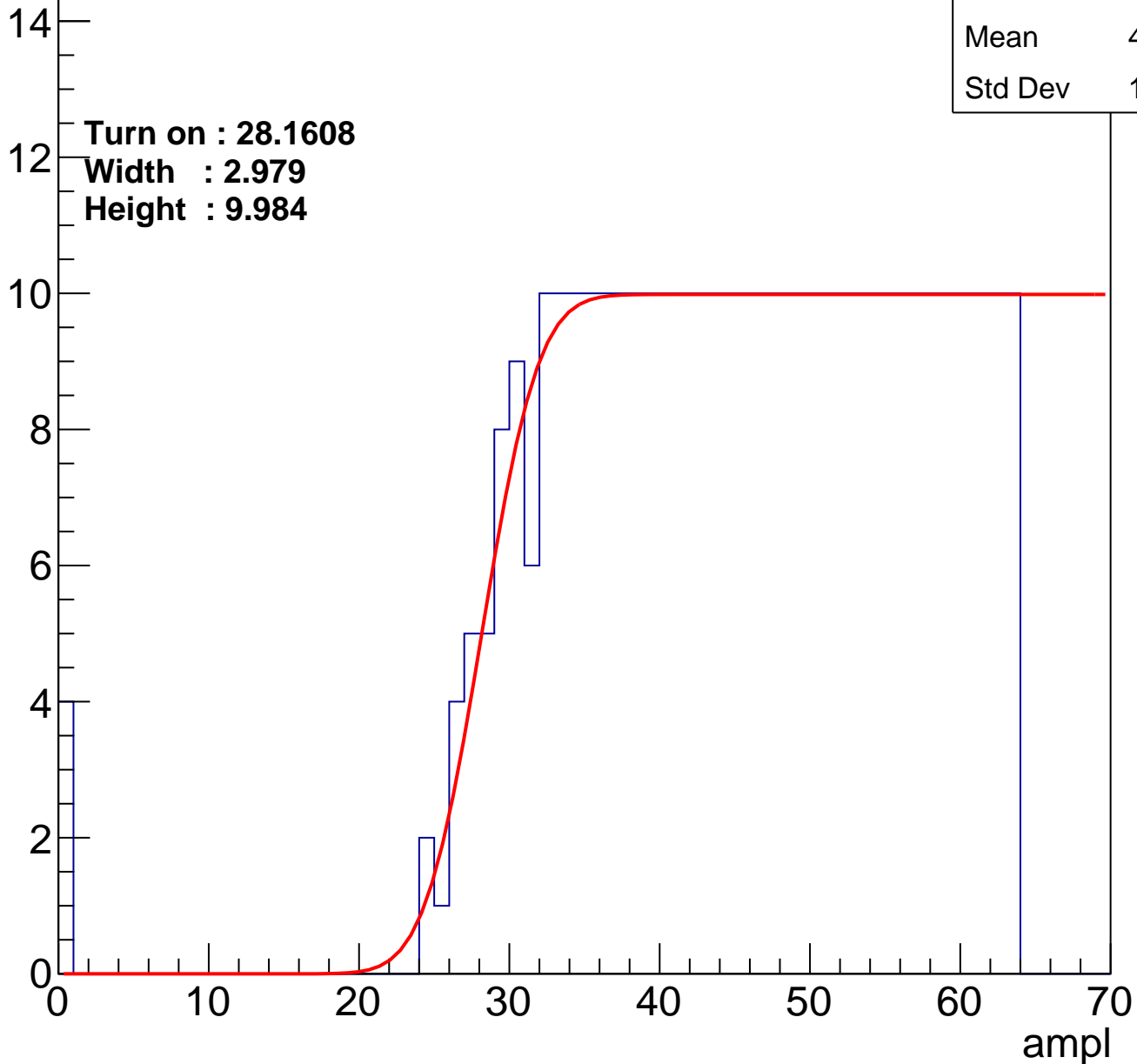
Entry

Entries	364
Mean	44.89
Std Dev	11.53

Turn on : 28.1608

Width : 2.979

Height : 9.984



# B0L002S, U24-ch5

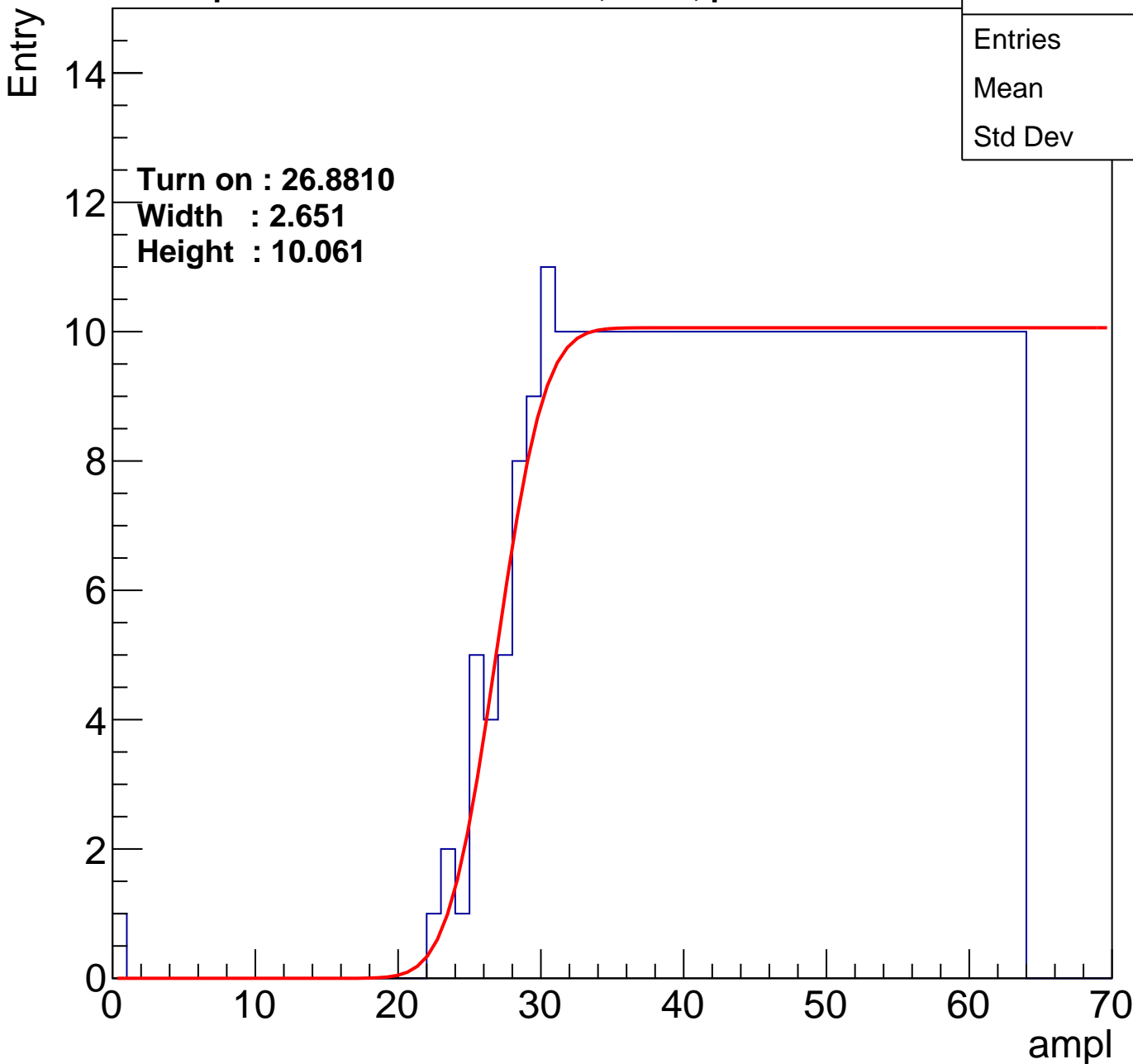
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.51
Std Dev	11.2

Turn on : 26.8810

Width : 2.651

Height : 10.061



# B0L002S, U24-ch6

calib\_packv5\_042523\_0143.root, FC#8, port C1

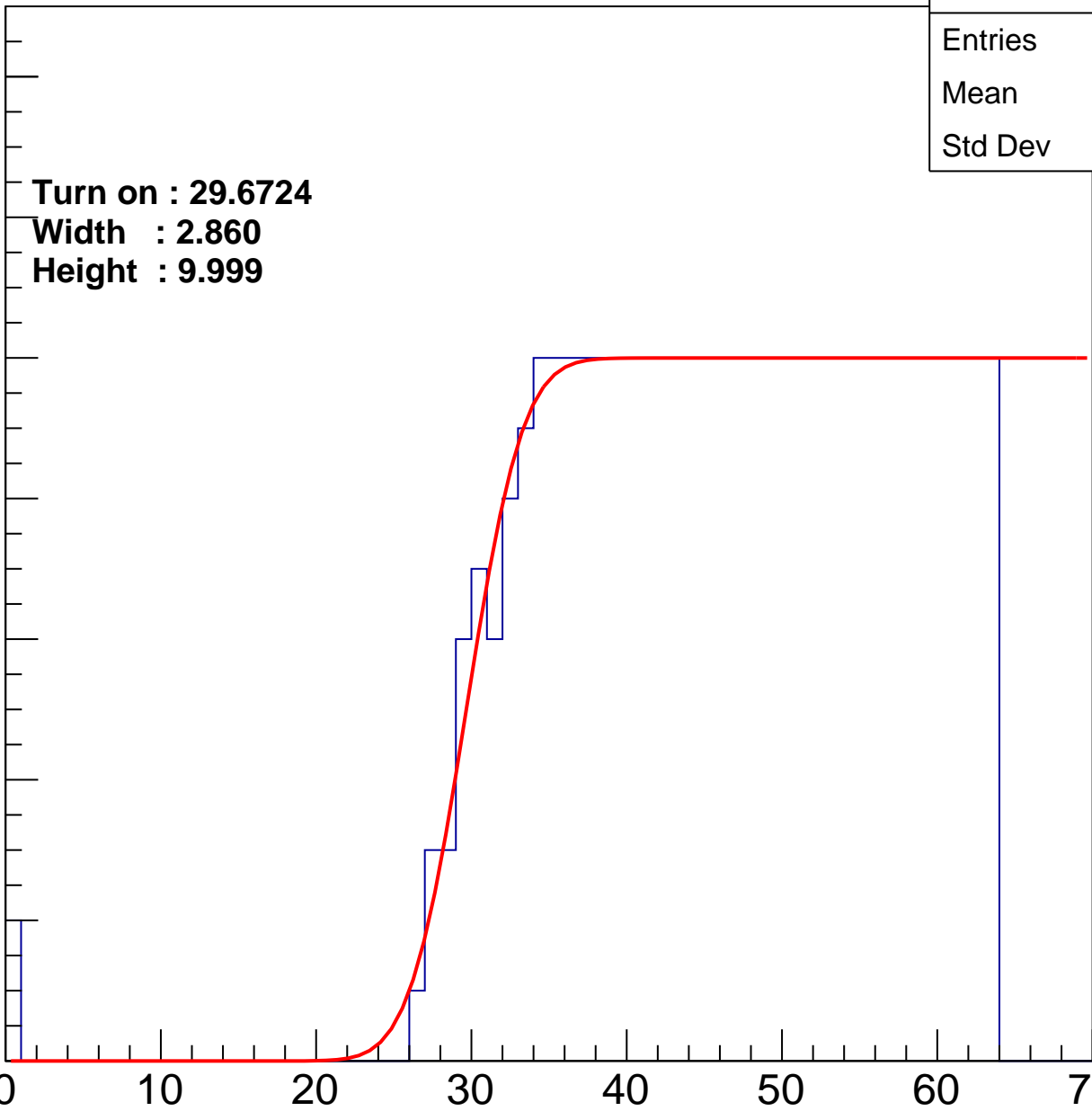
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 29.6724  
Width : 2.860  
Height : 9.999

Entries	345
Mean	45.98
Std Dev	10.63

ampl





# B0L002S, U24-ch7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.92

Turn on : 28.1603

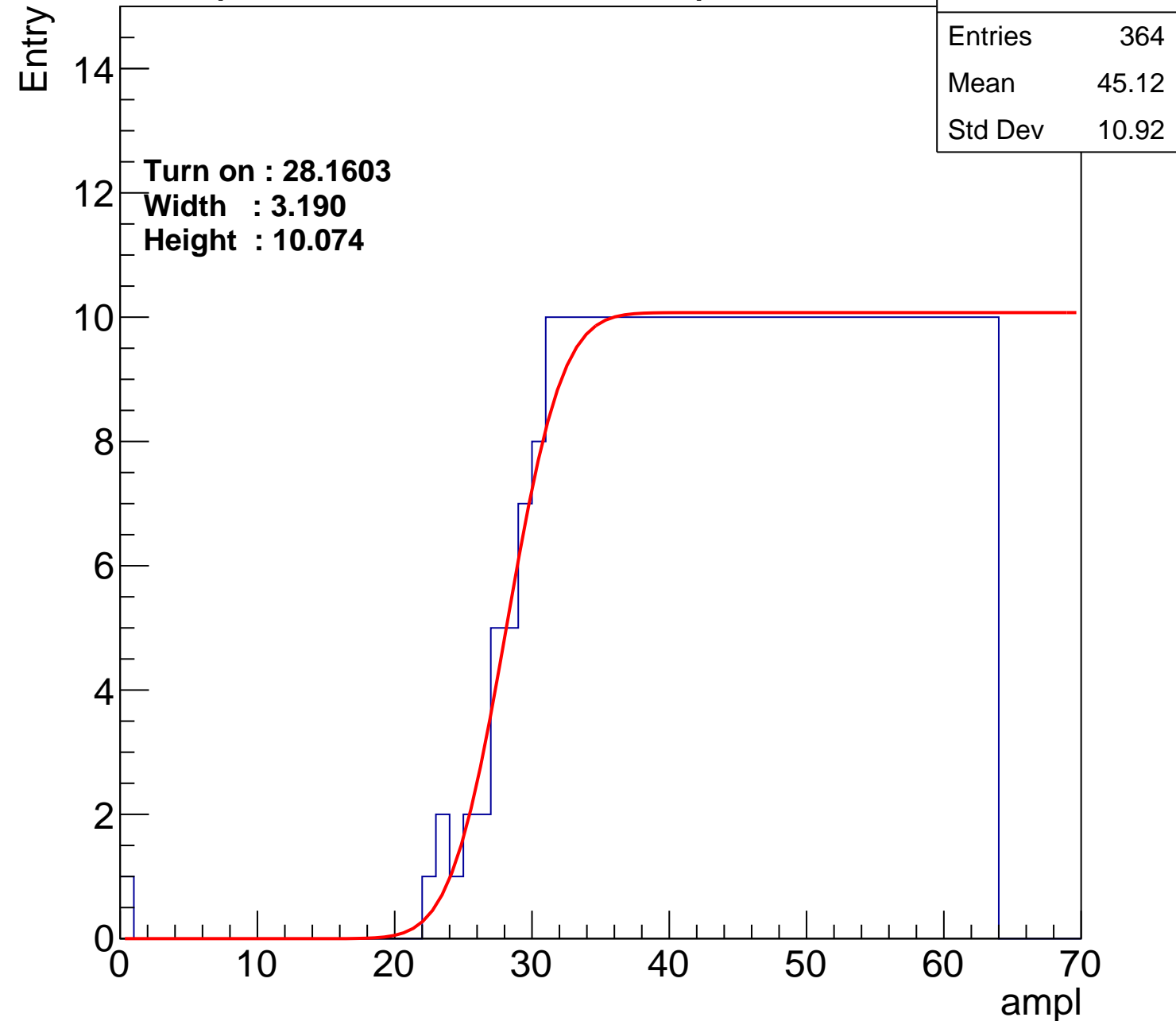
Width : 3.190

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch8

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.06
Std Dev	11.48

**Turn on : 28.5609**

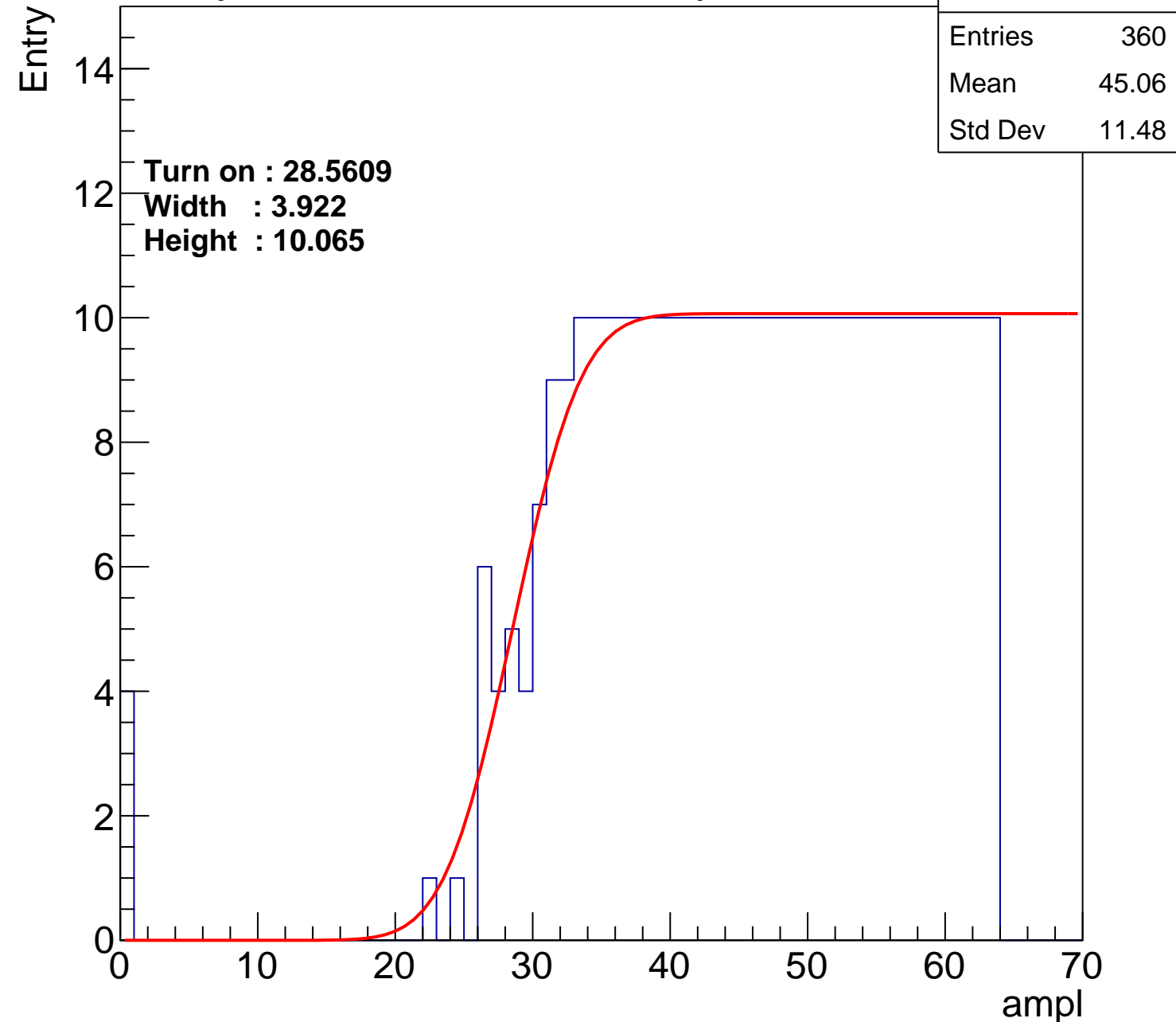
**Width : 3.922**

**Height : 10.065**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch9

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.73
Std Dev	11.25

Turn on : 27.9515

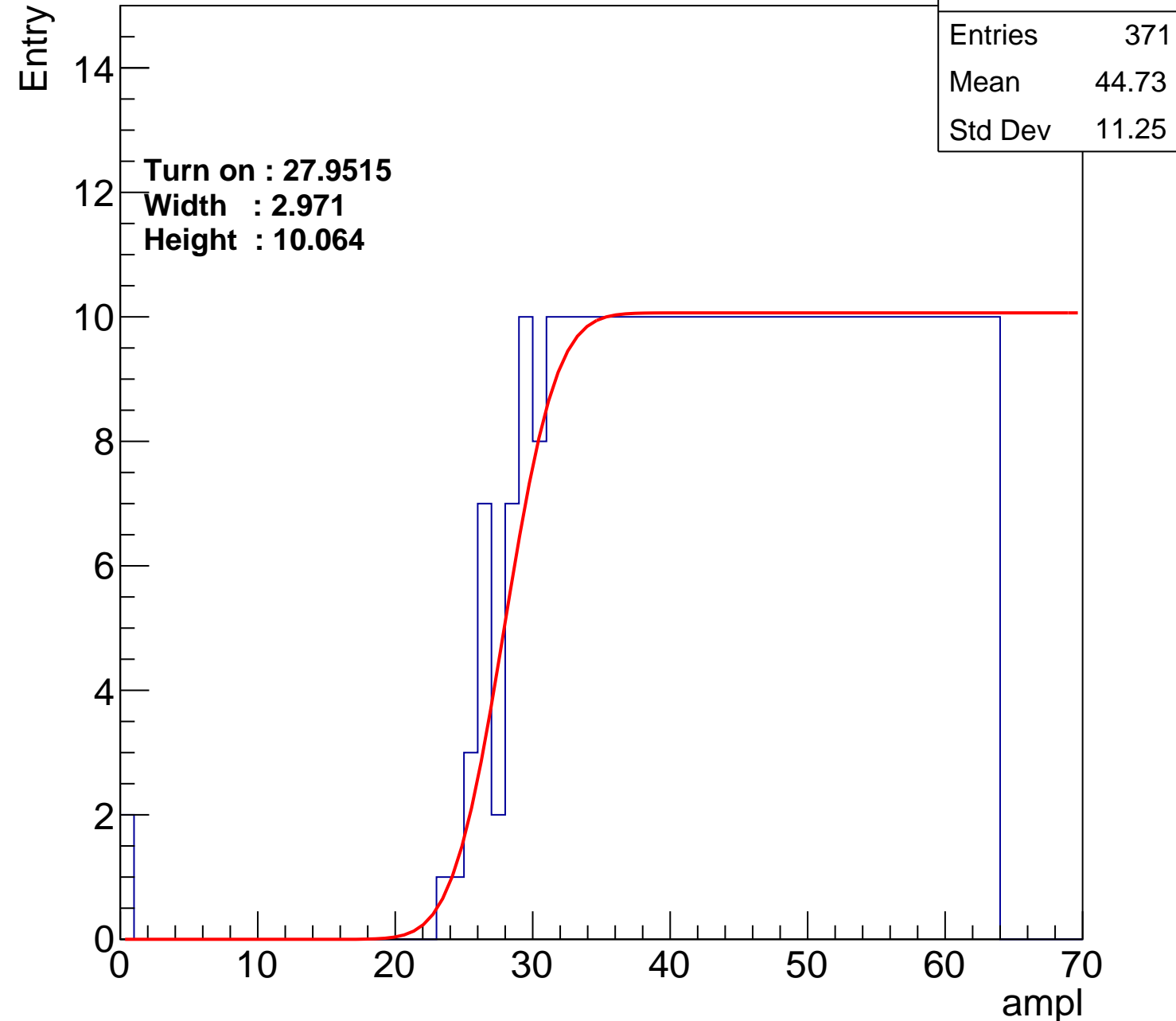
Width : 2.971

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch10

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	354
Mean	45.53
Std Dev	10.87

Turn on : 29.3118

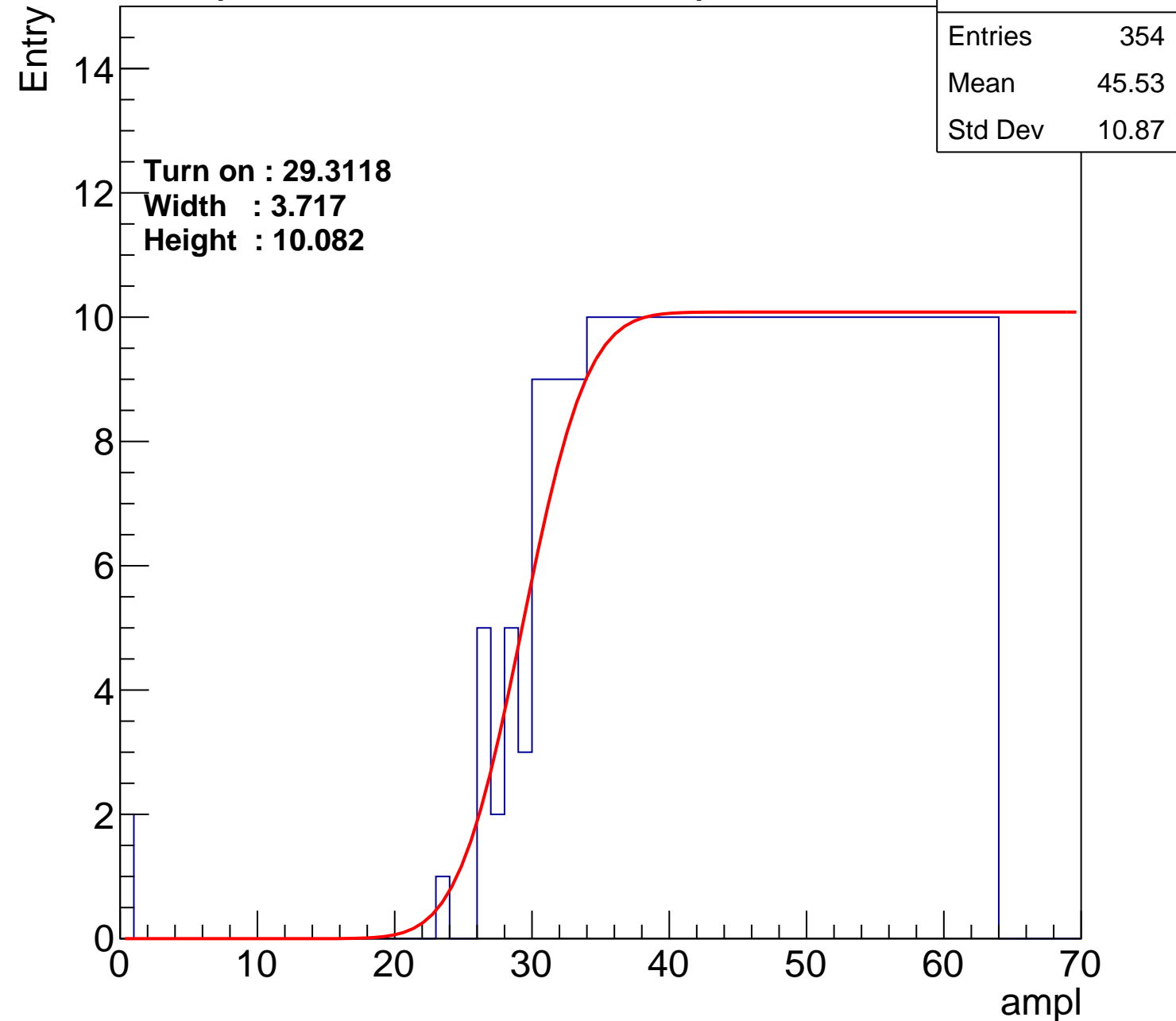
Width : 3.717

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch11

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.59
Std Dev	10.99

**Turn on : 28.9078**

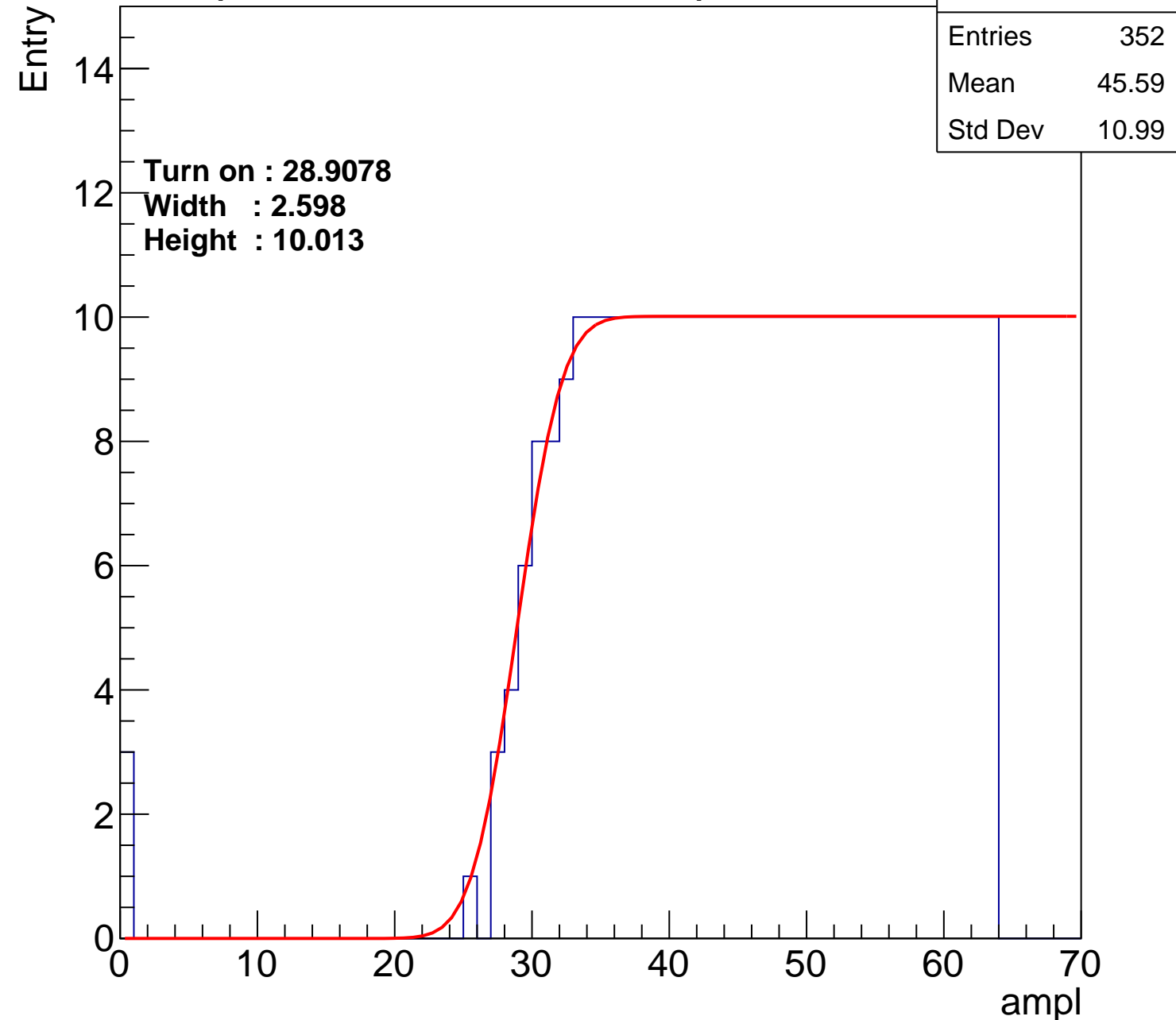
**Width : 2.598**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch12

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.48
Std Dev	10.69

Turn on : 28.5986

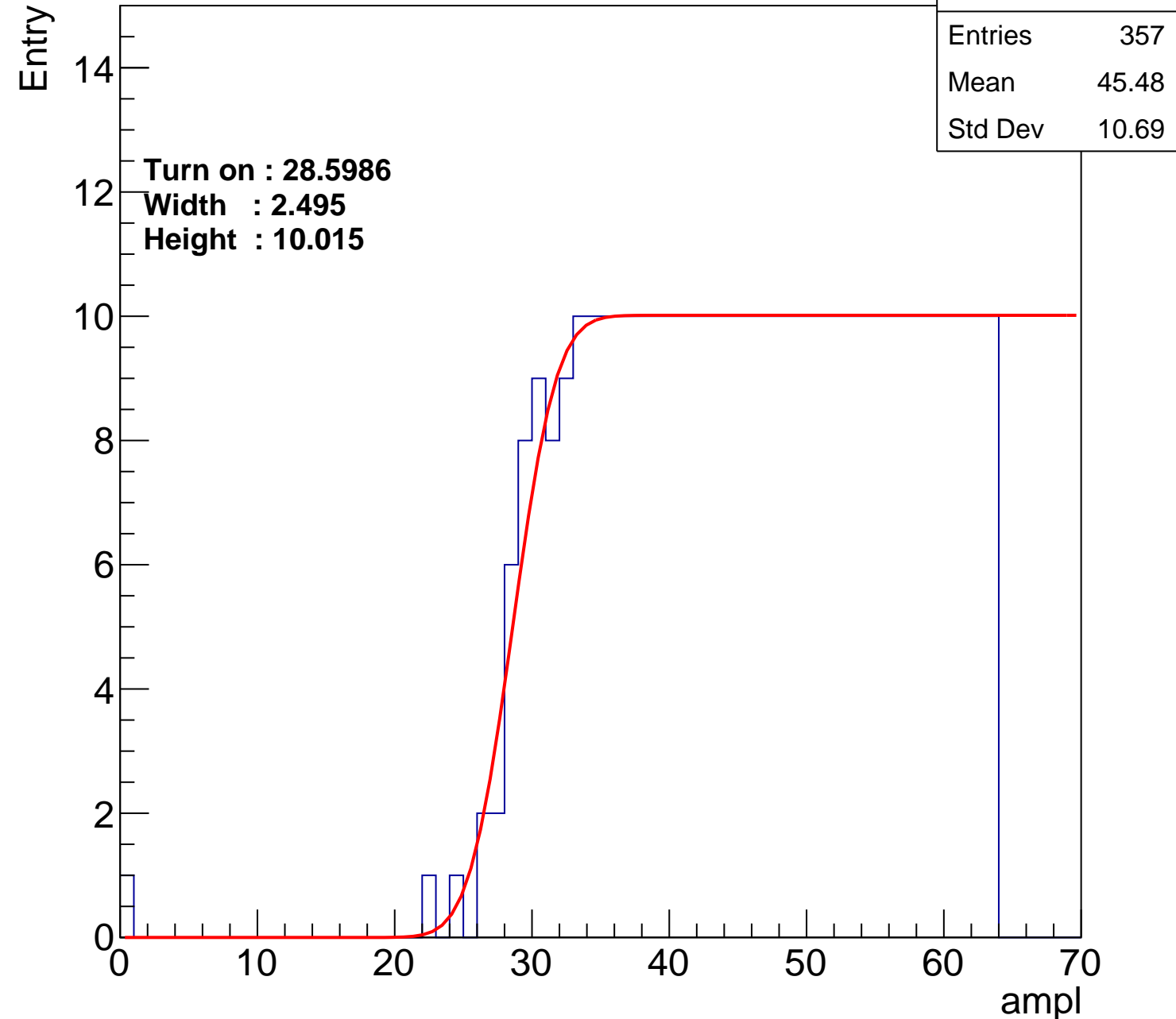
Width : 2.495

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch13

calib\_packv5\_042523\_0143.root, FC#8, port C1

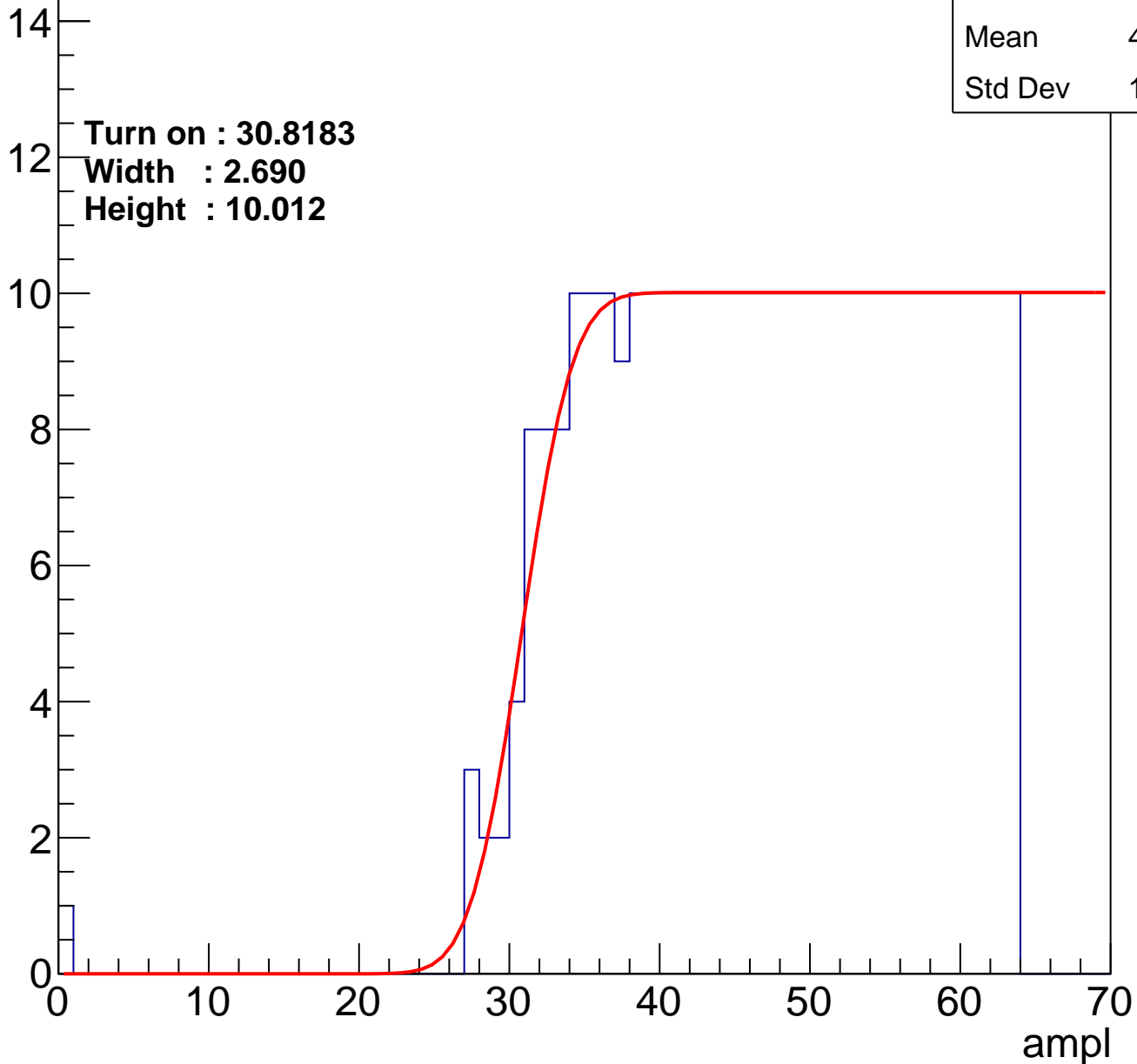
Entries	335
Mean	46.56
Std Dev	10.12

Turn on : 30.8183

Width : 2.690

Height : 10.012

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.7646  
Width : 3.189  
Height : 10.058





# B0L002S, U24-ch15

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.88
Std Dev	11.3

Turn on : 27.6334

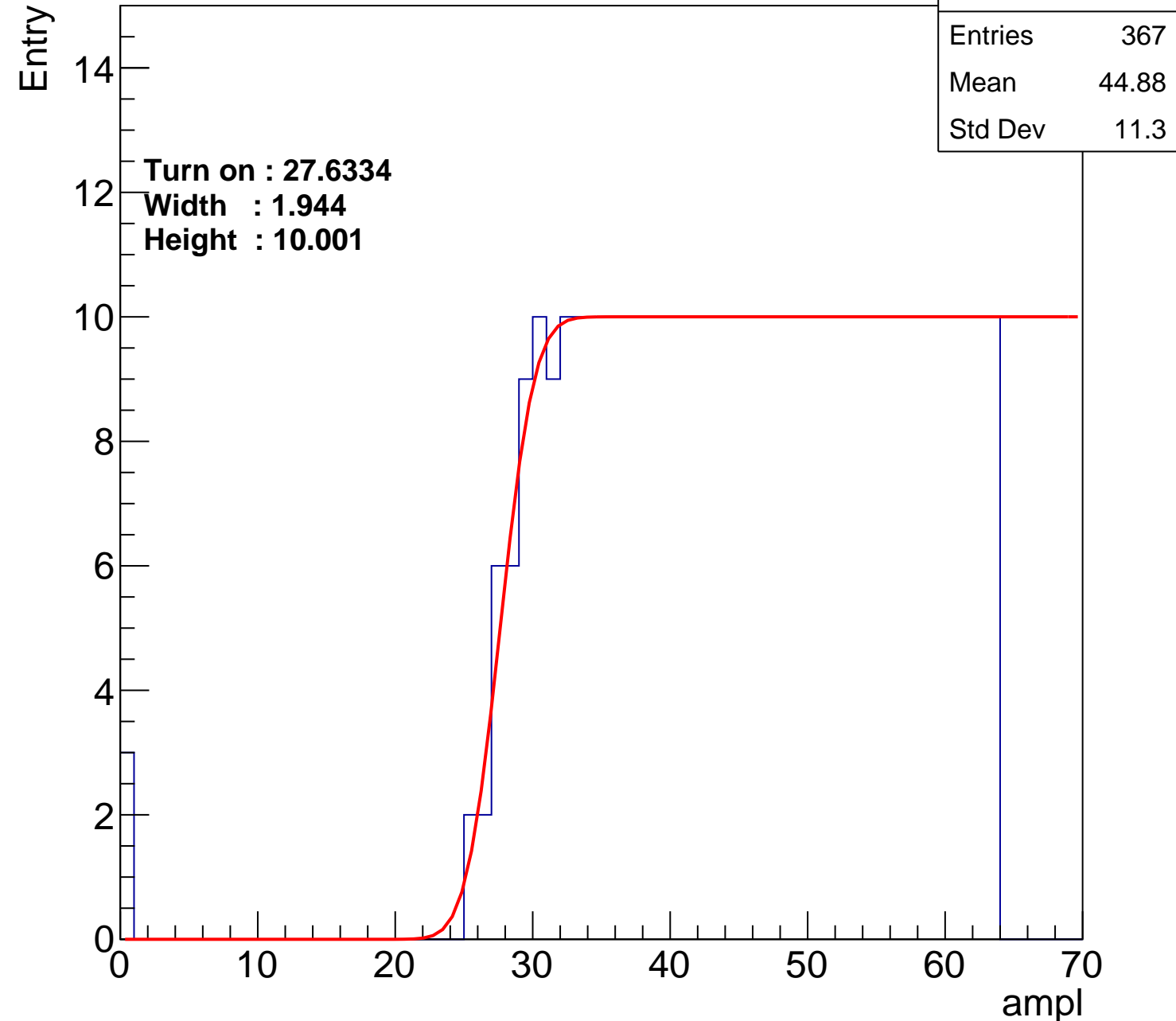
Width : 1.944

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch16

calib\_packv5\_042523\_0143.root, FC#8, port C1

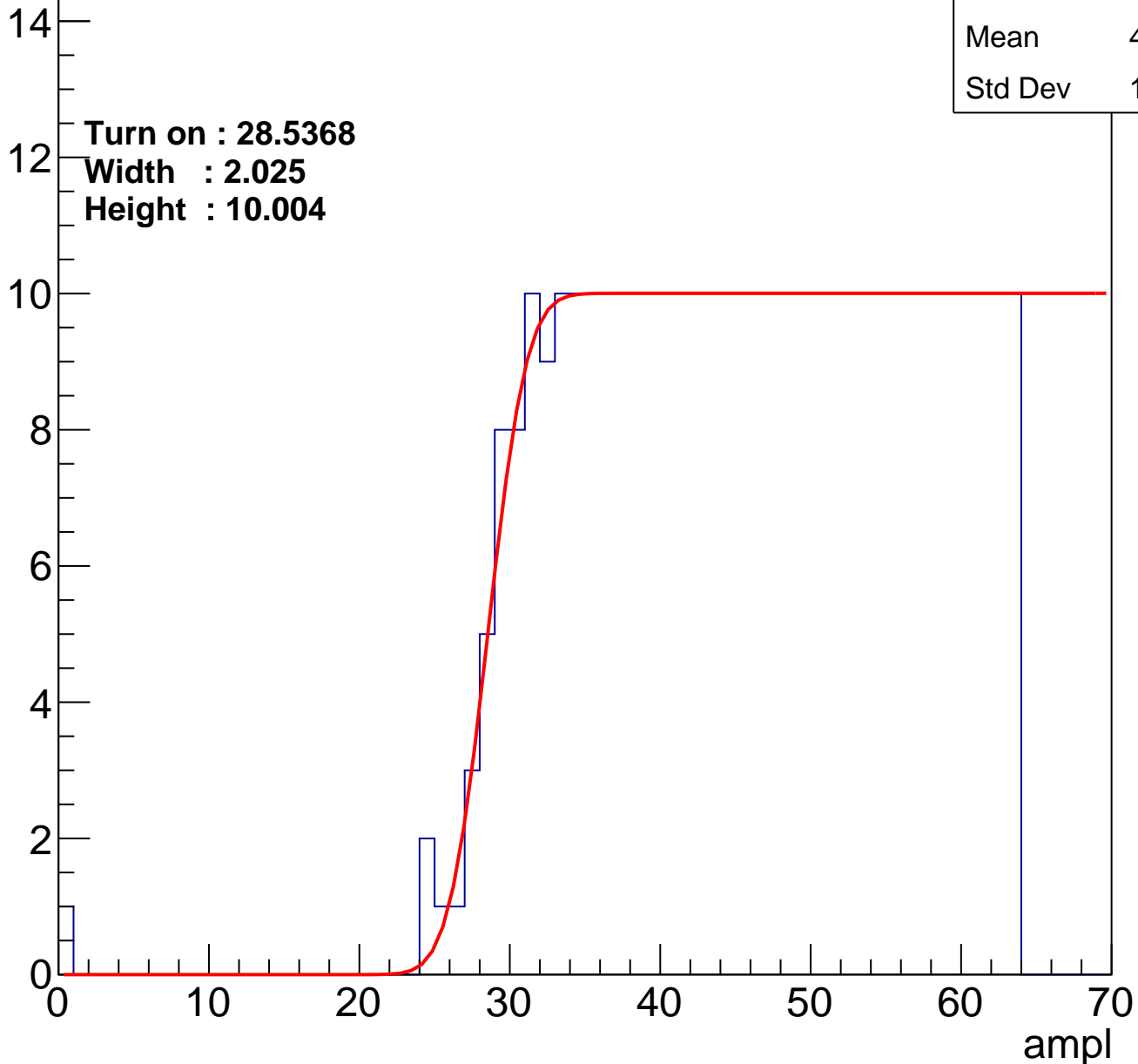
Entries	358
Mean	45.45
Std Dev	10.69

Turn on : 28.5368

Width : 2.025

Height : 10.004

Entry



# B0L002S, U24-ch17

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.88
Std Dev	11.41

Turn on : 27.6452

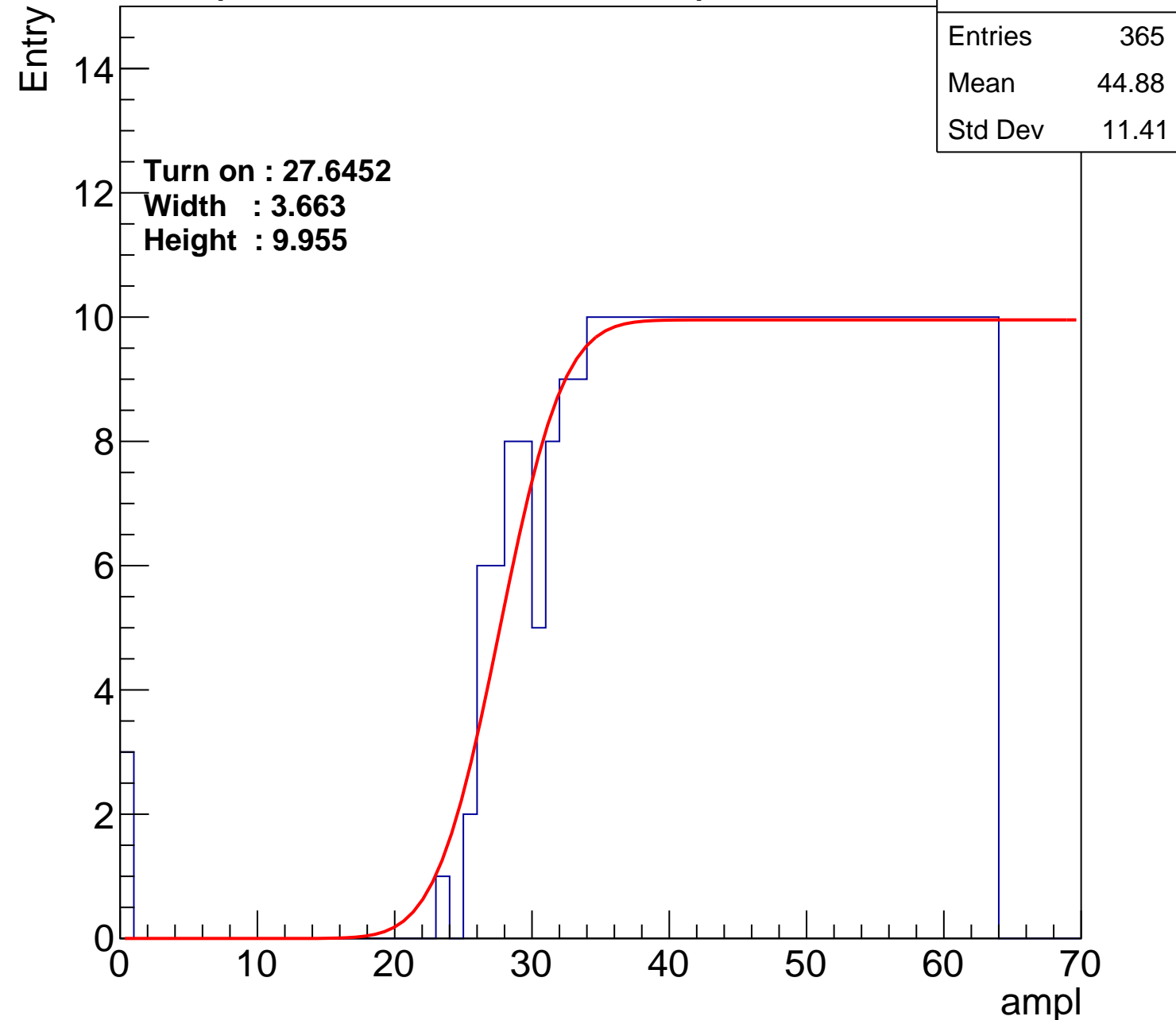
Width : 3.663

Height : 9.955

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch18

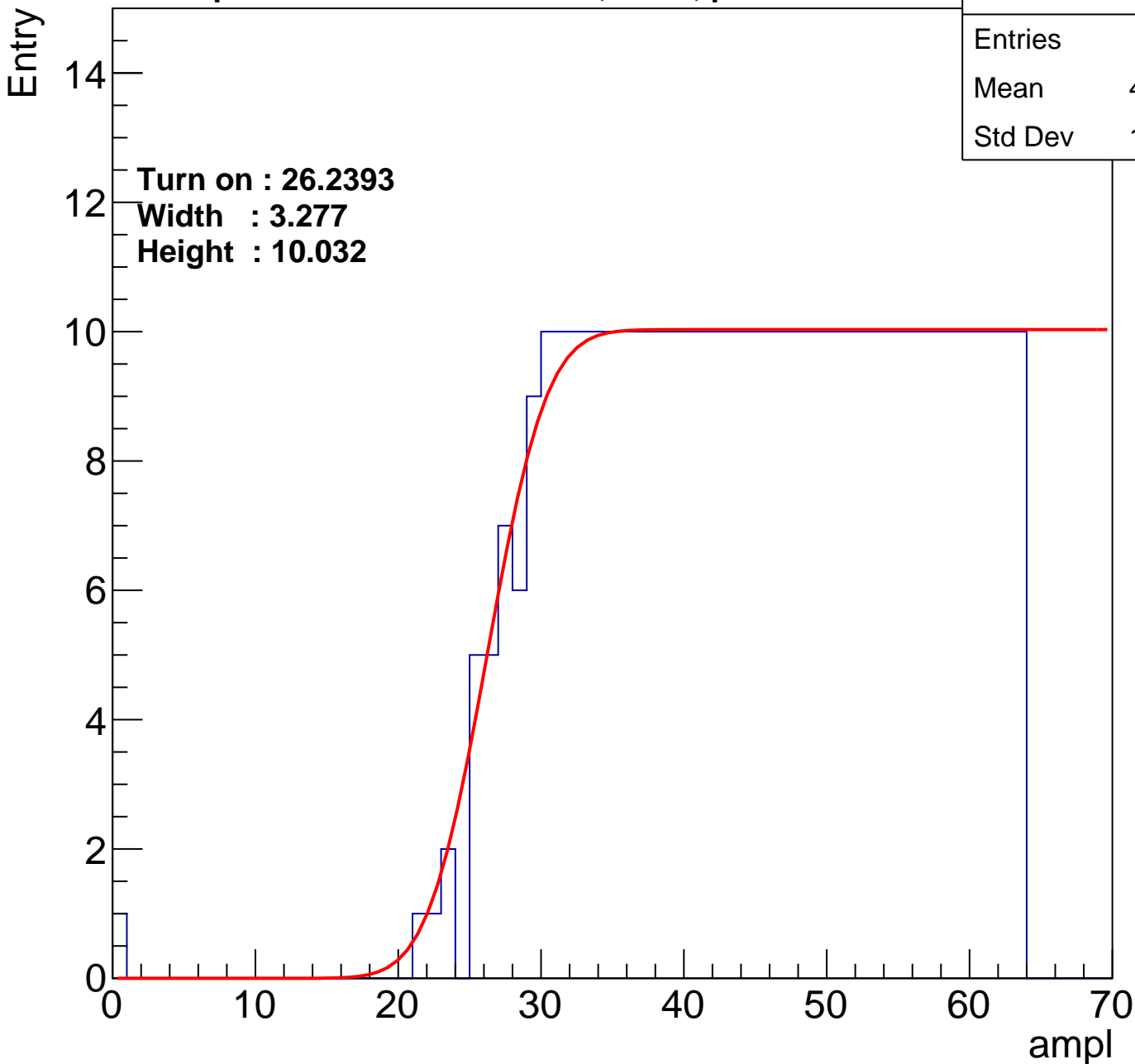
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	377
Mean	44.49
Std Dev	11.24

**Turn on : 26.2393**

**Width : 3.277**

**Height : 10.032**



# B0L002S, U24-ch19

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.61
Std Dev	11.47

**Turn on : 27.1144**

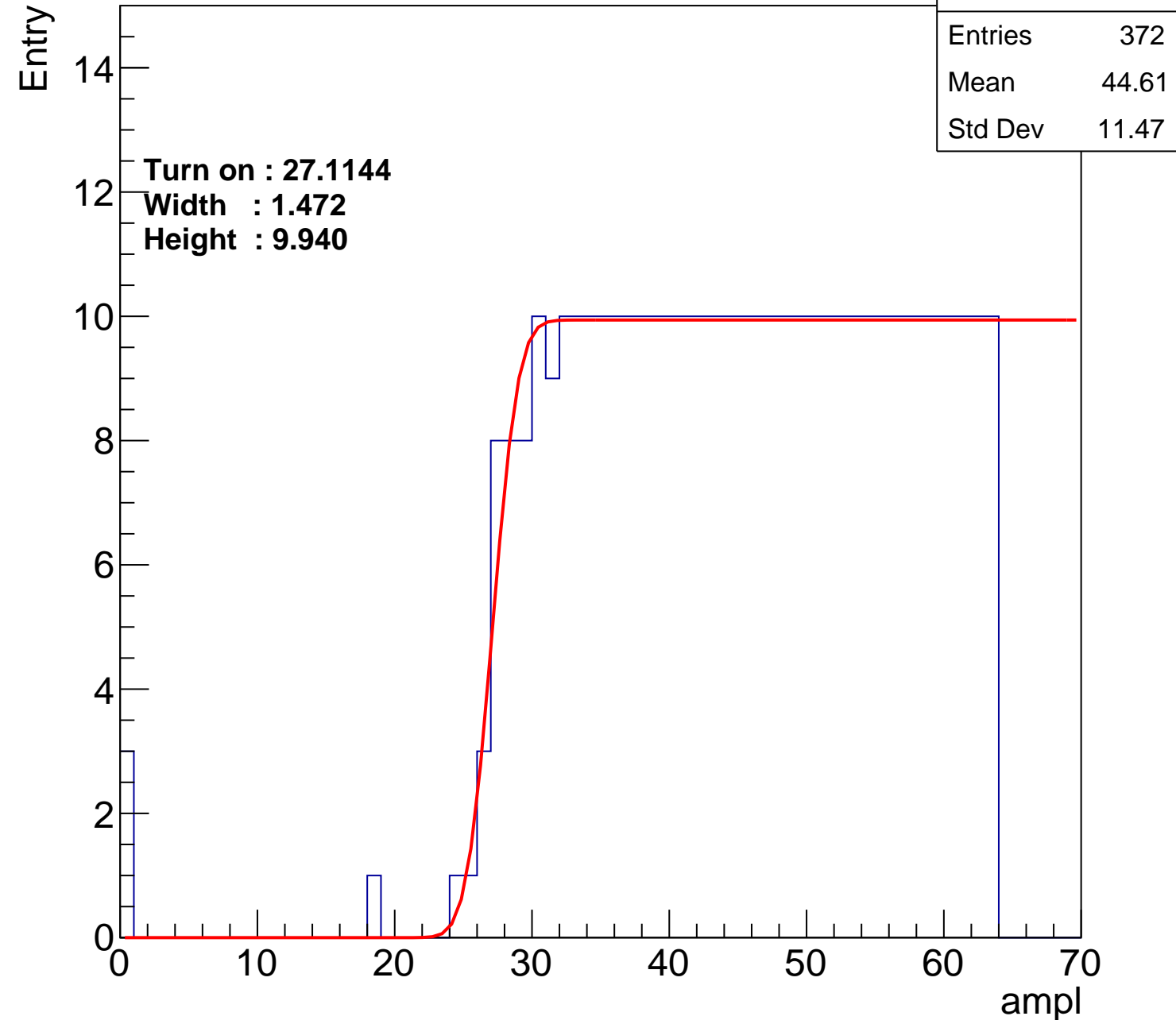
**Width : 1.472**

**Height : 9.940**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch20

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.71
Std Dev	10.75

**Turn on : 29.1003**

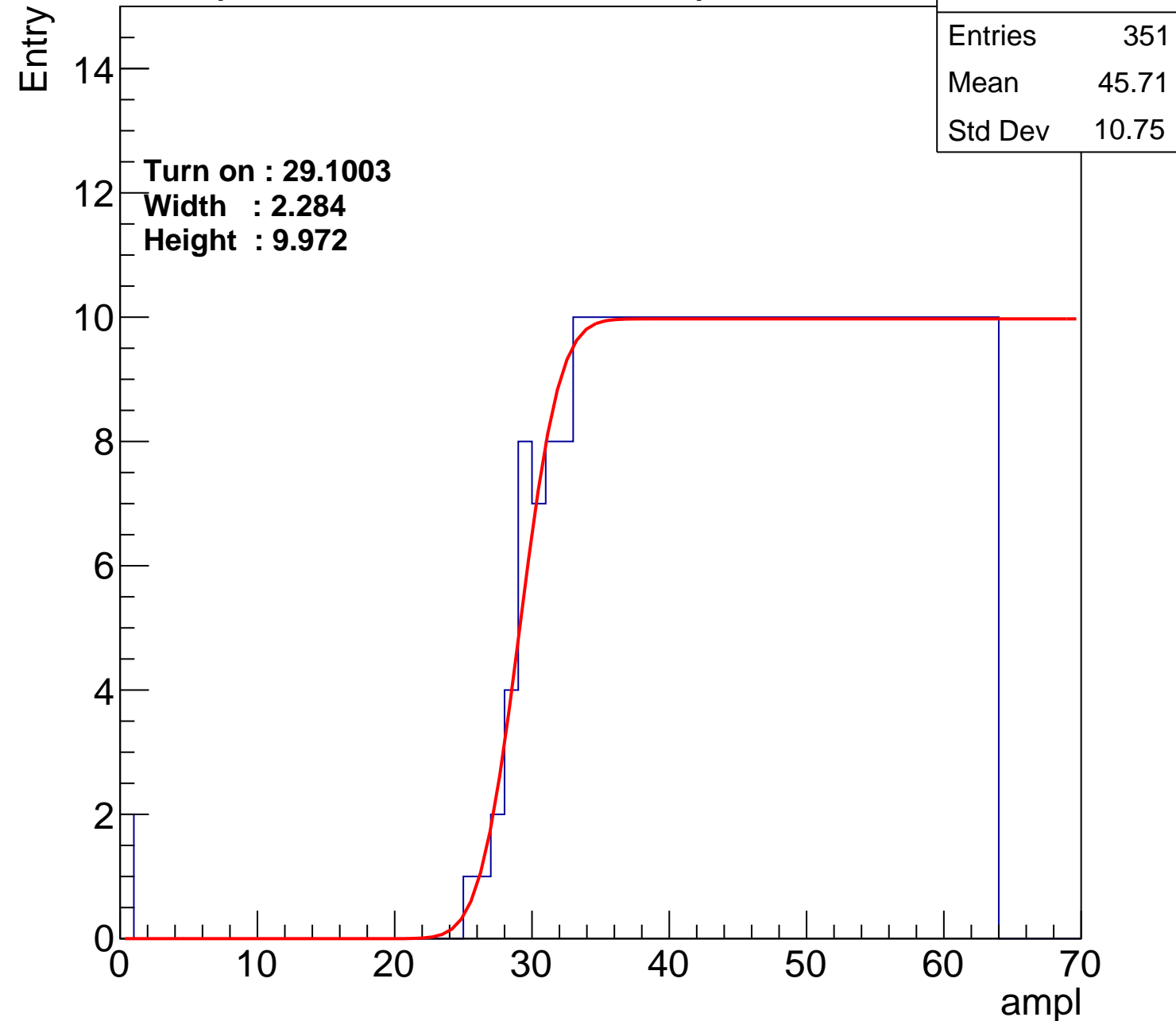
**Width : 2.284**

**Height : 9.972**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch21

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.56
Std Dev	11.68

Turn on : 27.5866

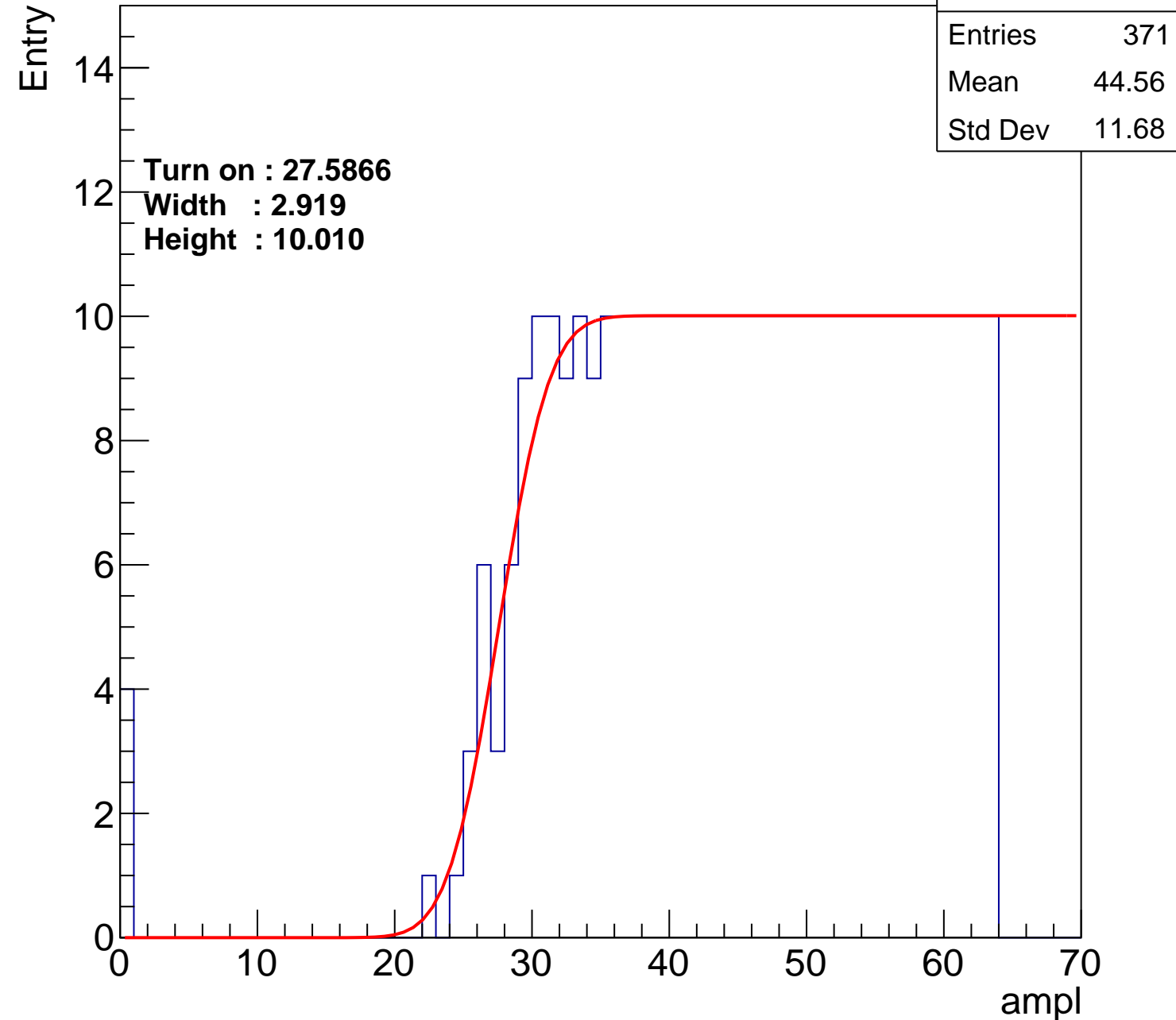
Width : 2.919

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch22

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.85
Std Dev	11.08

Turn on : 27.2046

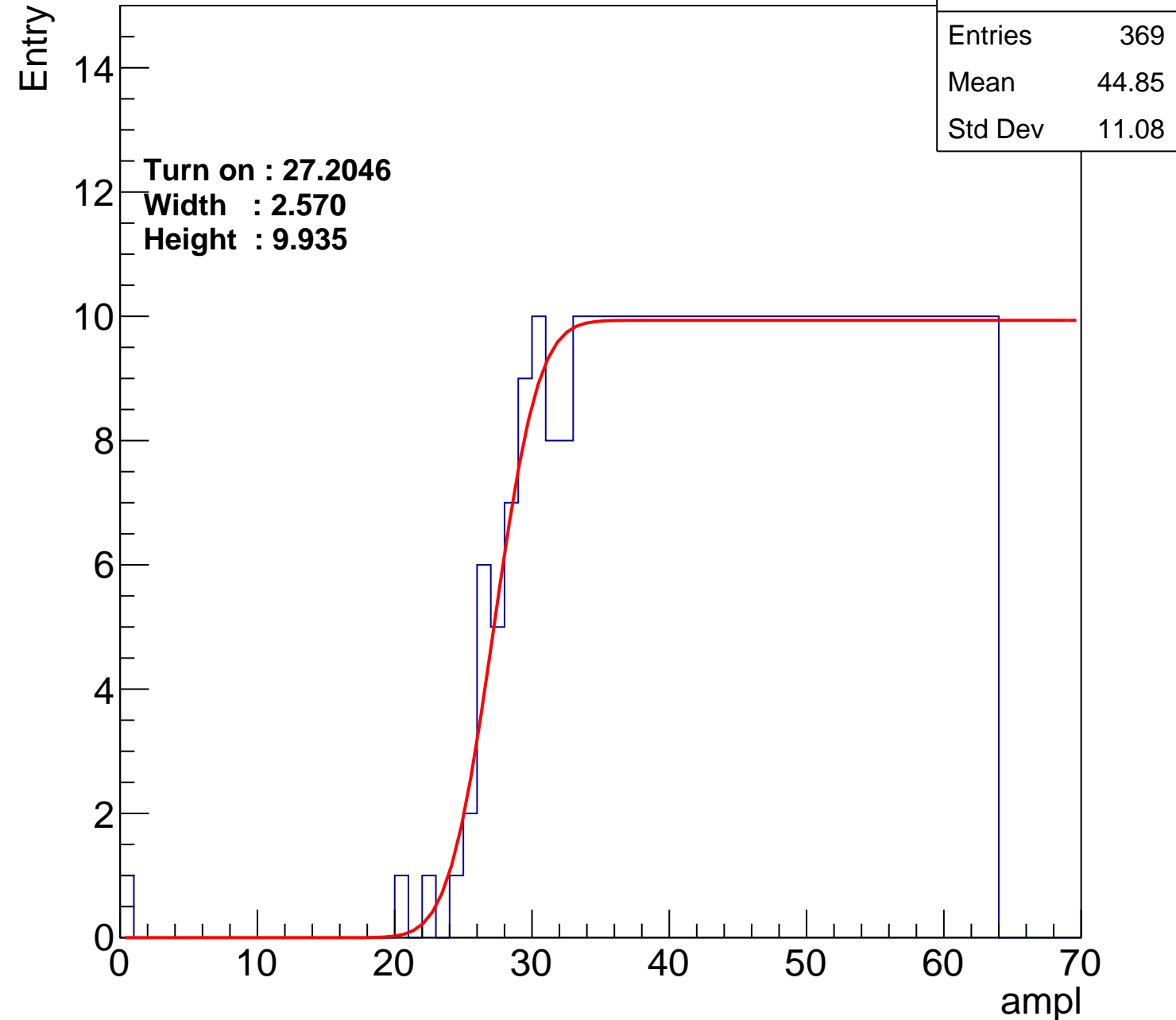
Width : 2.570

Height : 9.935

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch23

calib\_packv5\_042523\_0143.root, FC#8, port C1

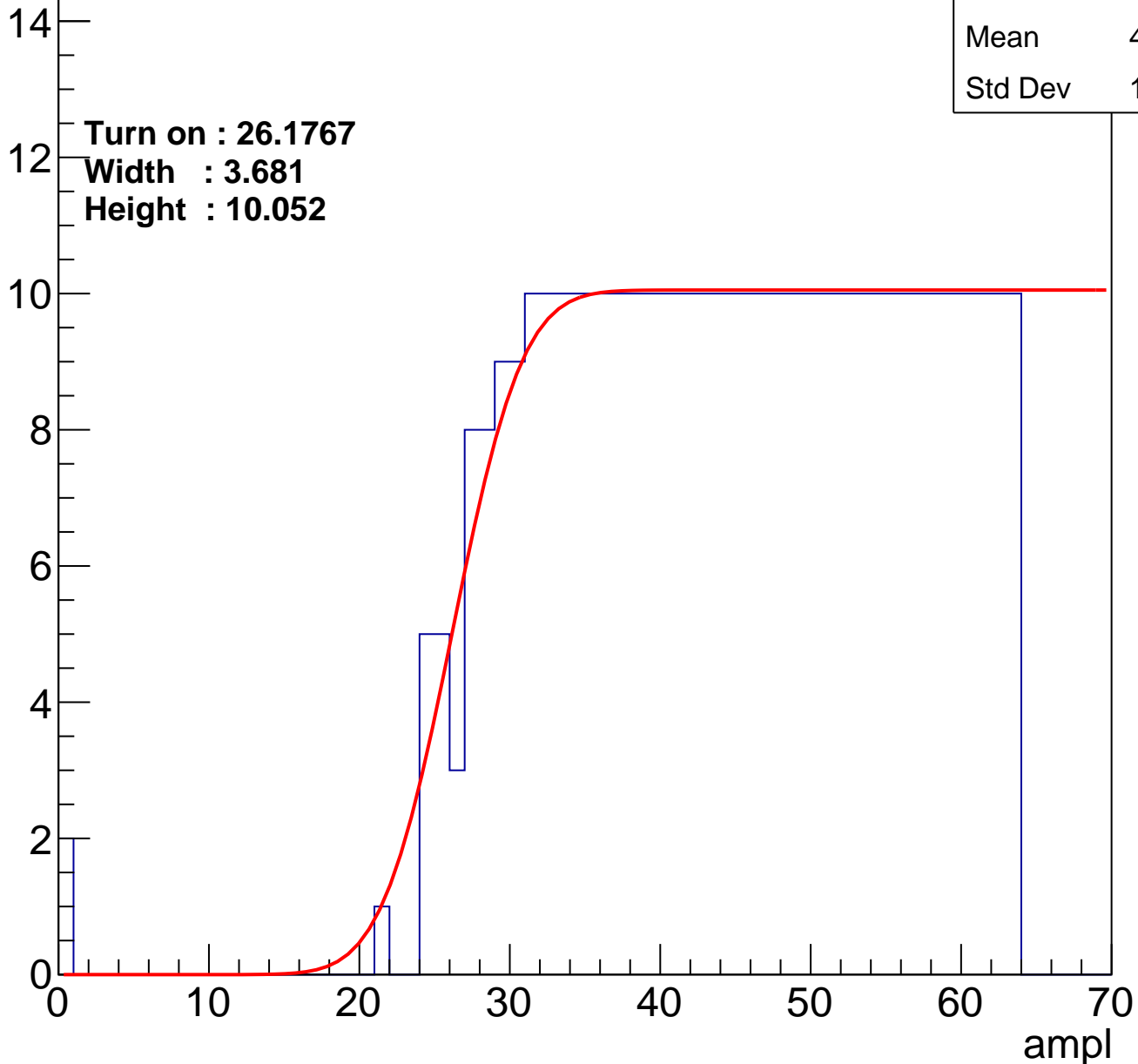
Entry

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.1767

Width : 3.681

Height : 10.052



# B0L002S, U24-ch24

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.87
Std Dev	11.31

Turn on : 27.7048

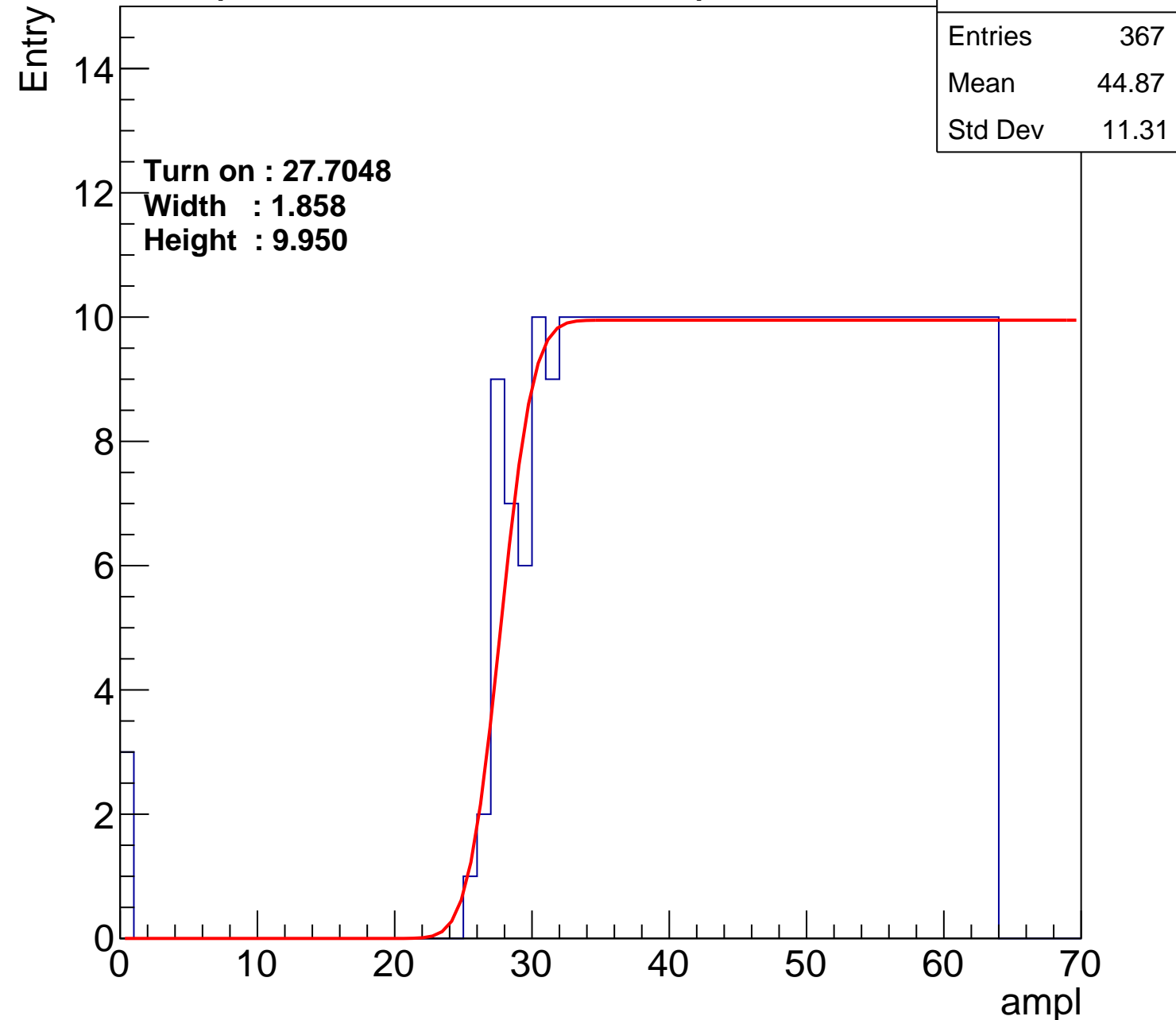
Width : 1.858

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch25

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.4954

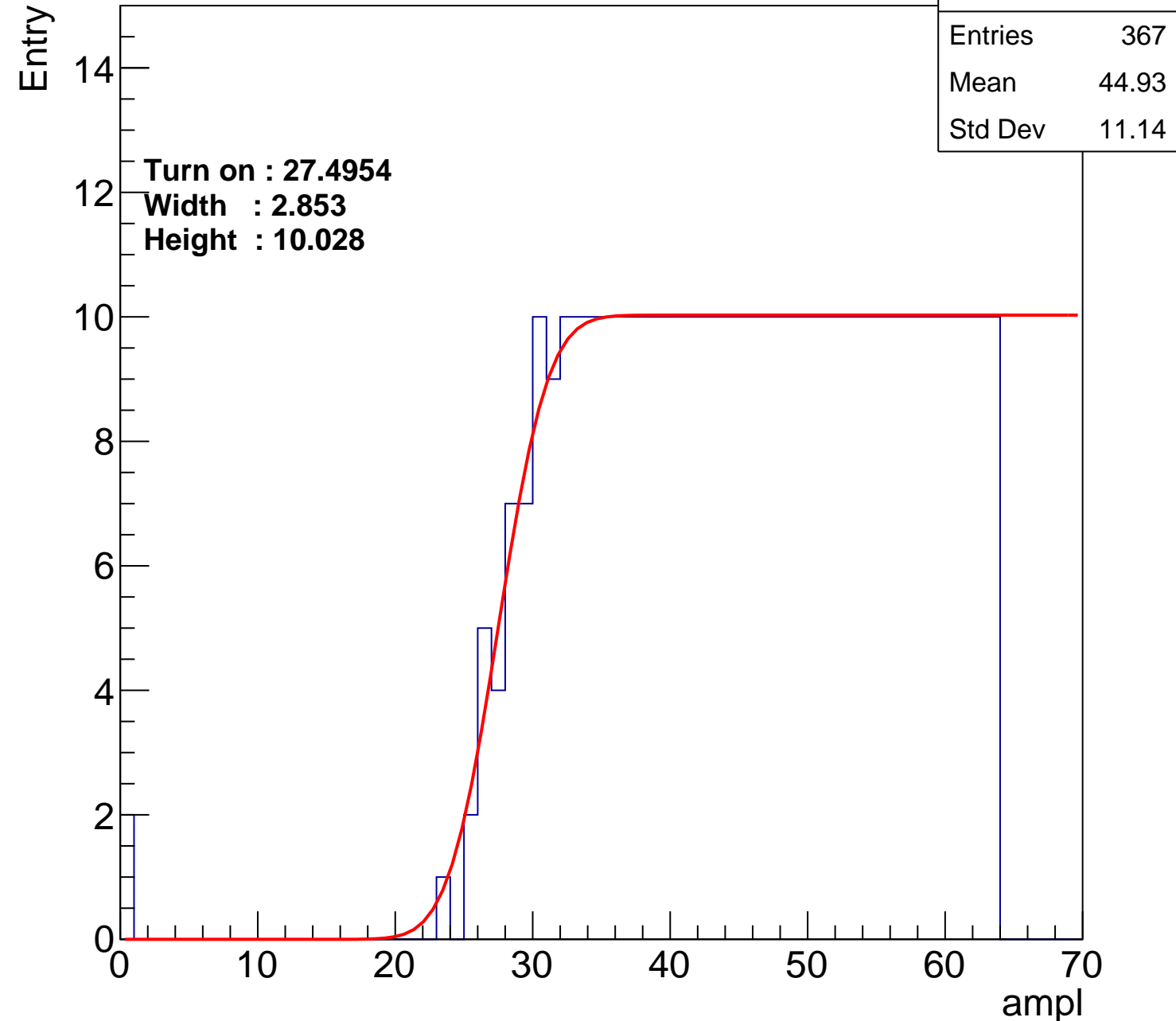
Width : 2.853

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch26

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 27.8418

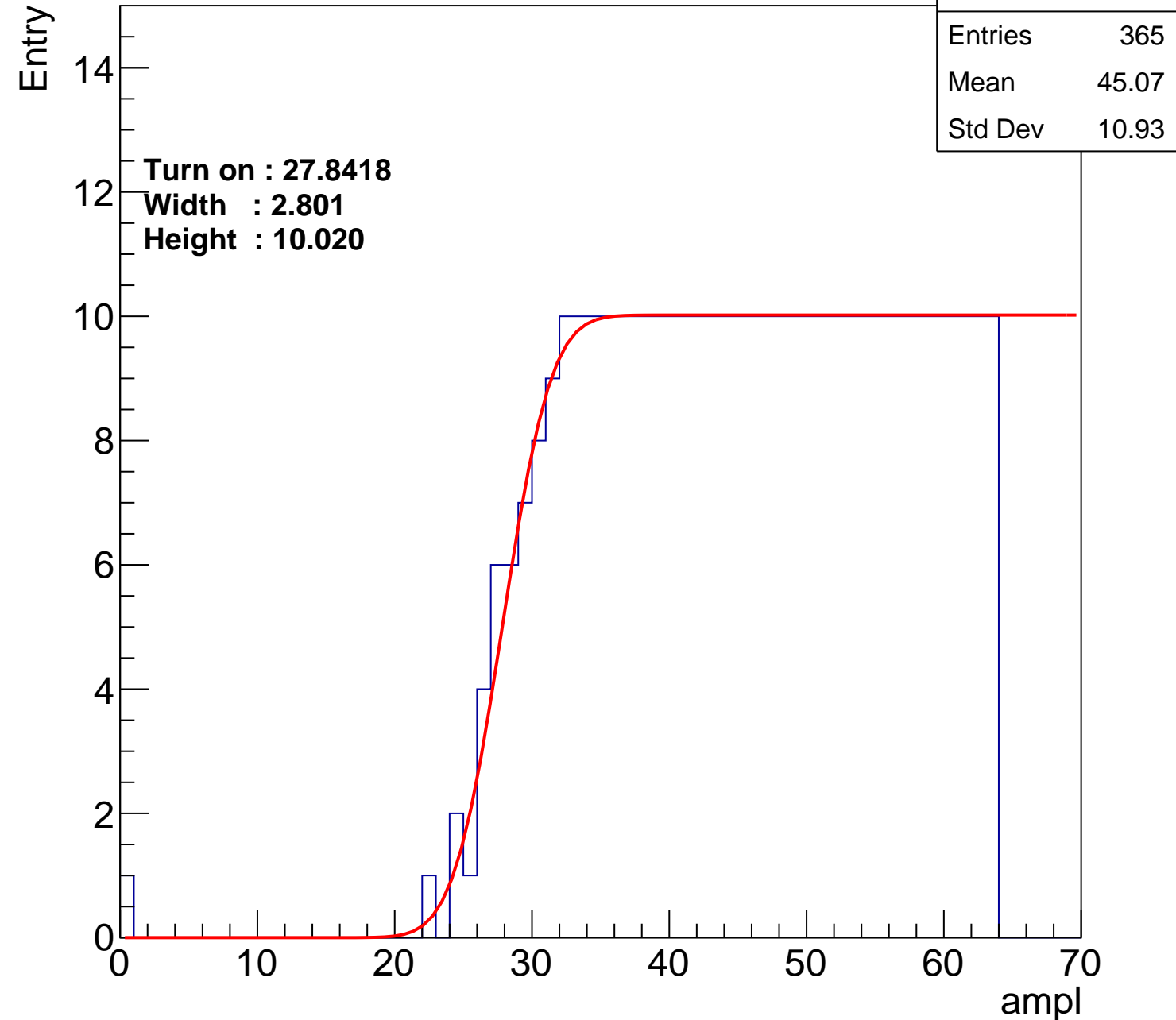
Width : 2.801

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch27

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	344
Mean	45.92
Std Dev	10.9

Turn on : 29.5464

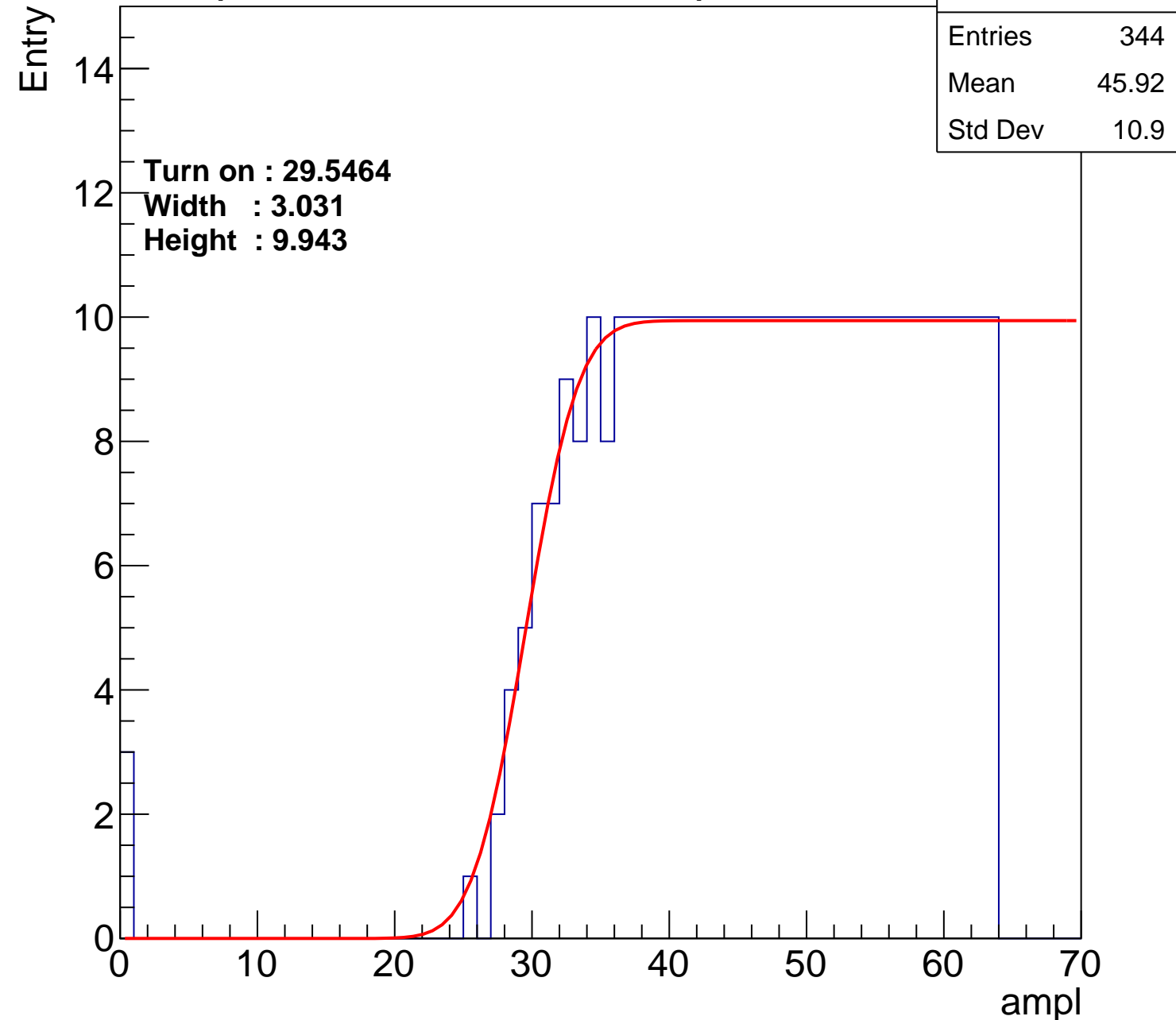
Width : 3.031

Height : 9.943

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch28

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.37
Std Dev	11.64

Turn on : 27.1290

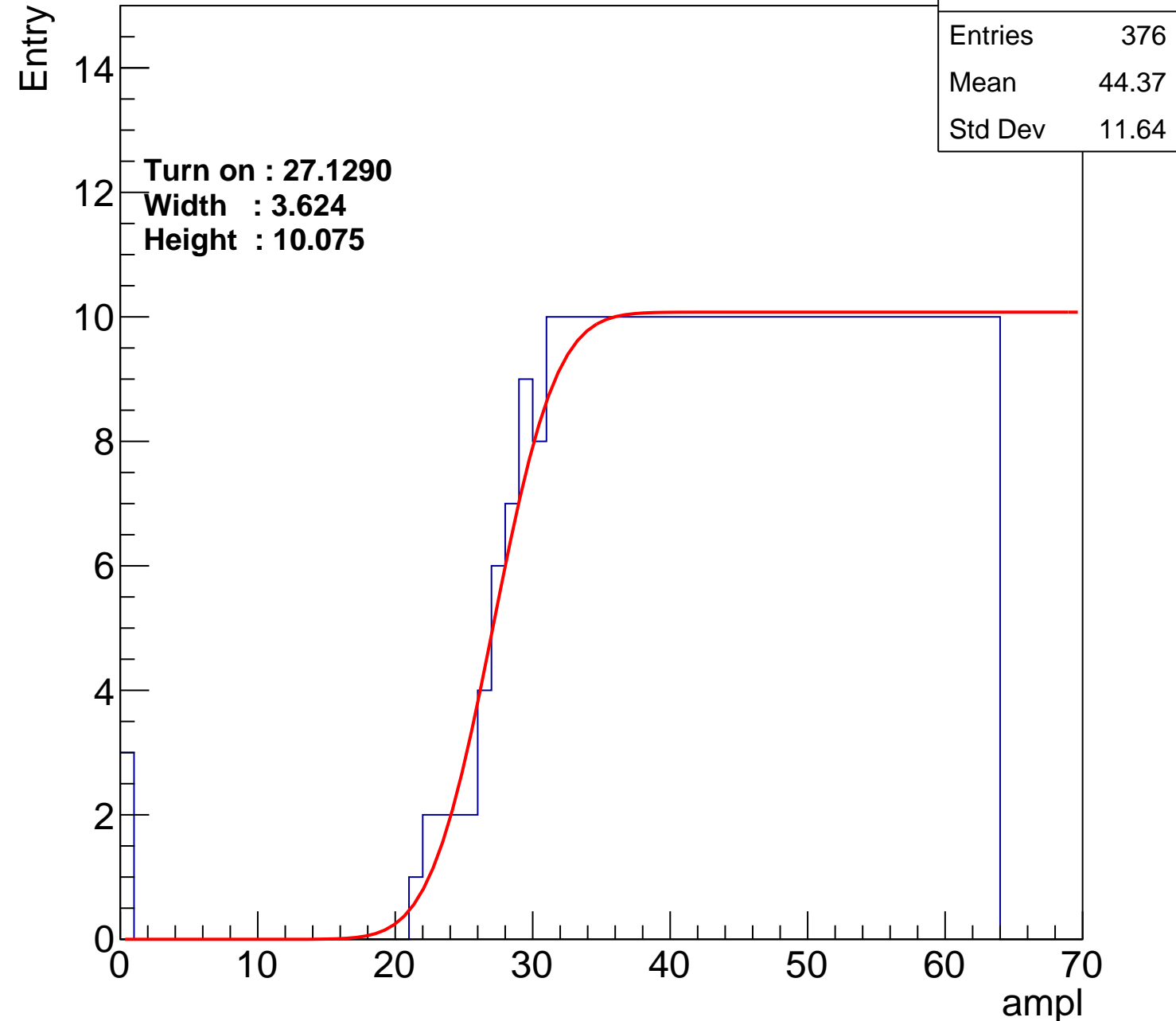
Width : 3.624

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch29

calib\_packv5\_042523\_0143.root, FC#8, port C1

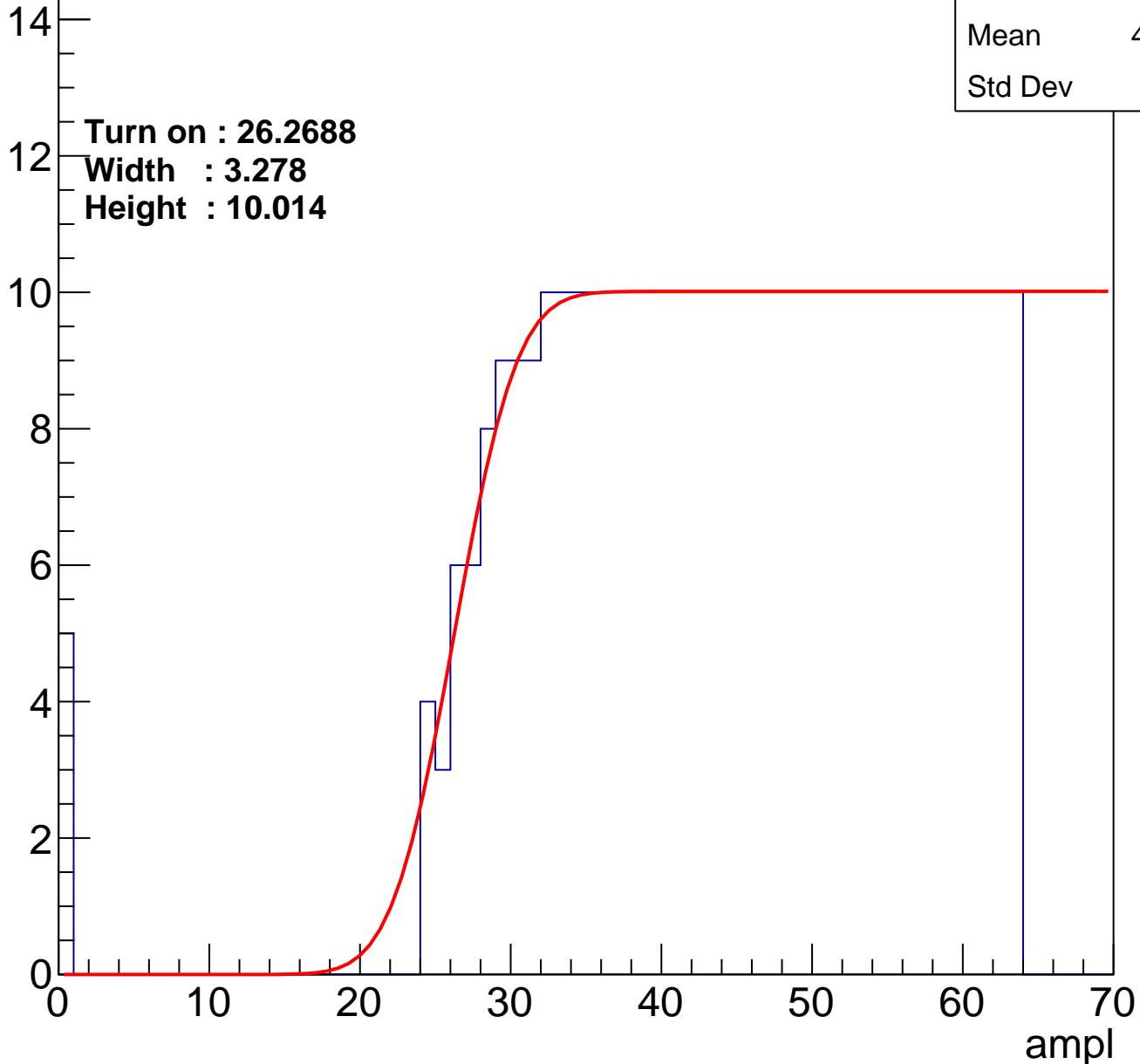
Entries	379
Mean	44.12
Std Dev	12

**Turn on : 26.2688**

**Width : 3.278**

**Height : 10.014**

Entry



# B0L002S, U24-ch30

calib\_packv5\_042523\_0143.root, FC#8, port C1

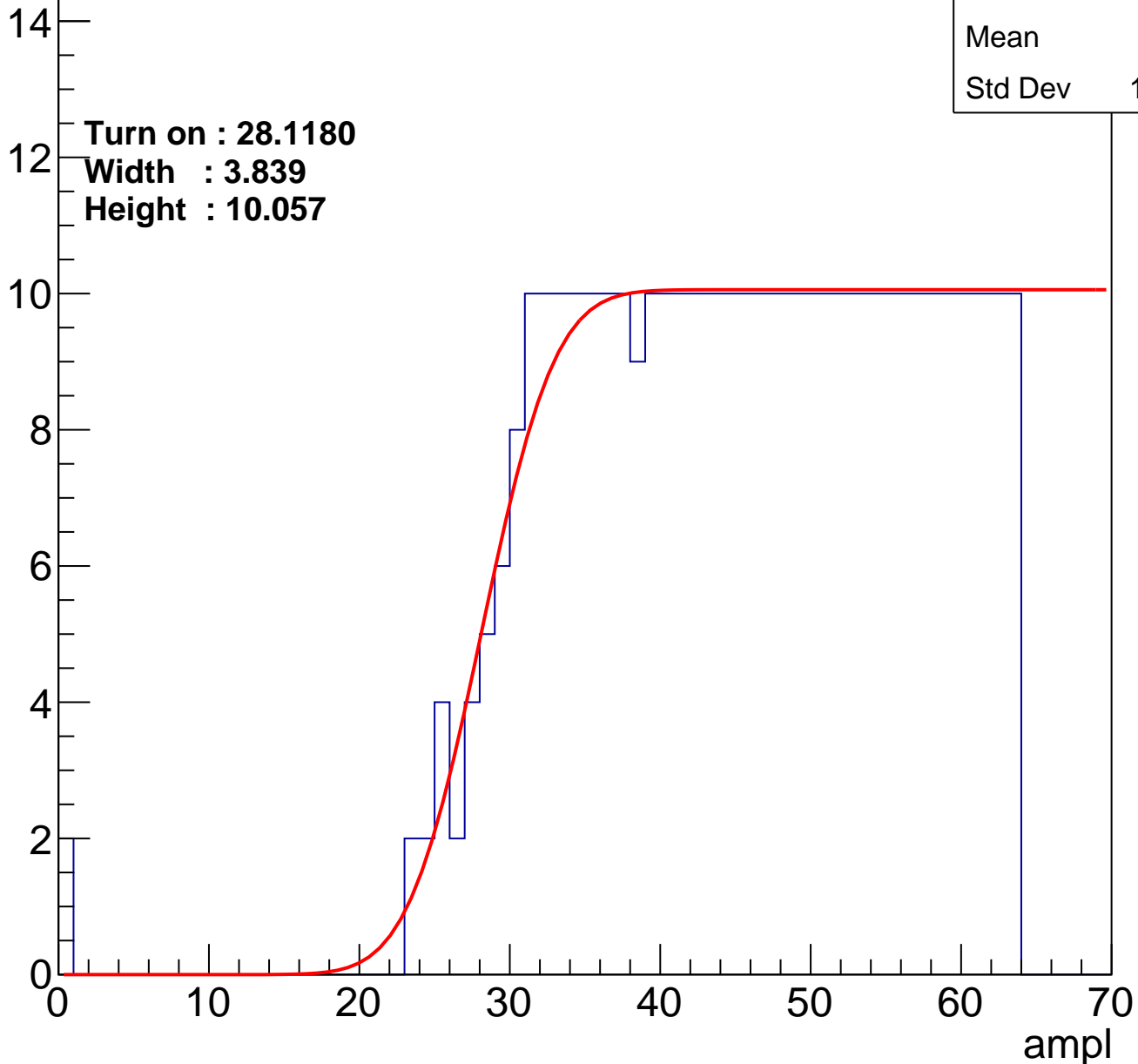
Entries	364
Mean	45
Std Dev	11.18

Turn on : 28.1180

Width : 3.839

Height : 10.057

Entry





# B0L002S, U24-ch31

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.17
Std Dev	11.23

Turn on : 28.5452

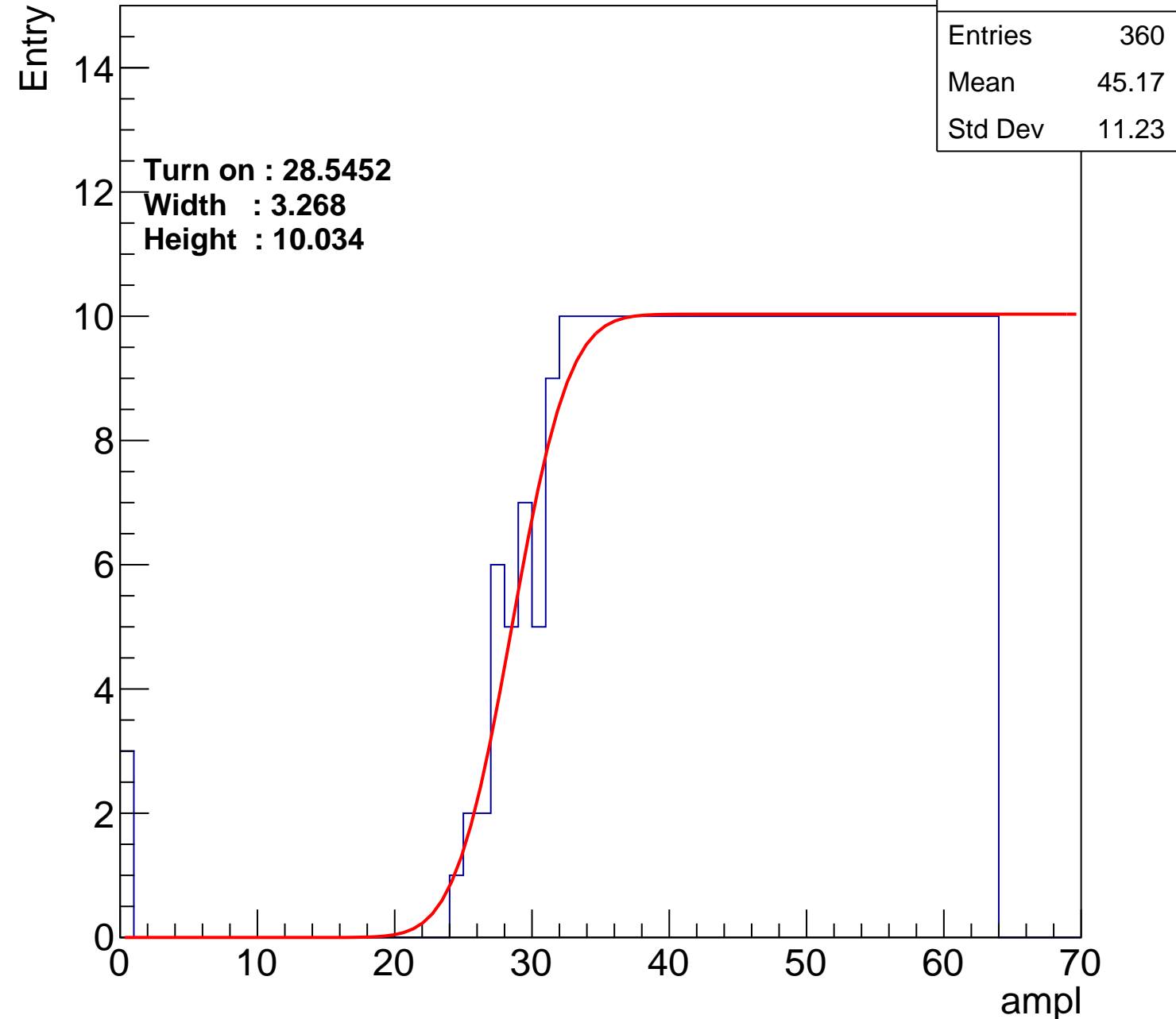
Width : 3.268

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch32

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.49
Std Dev	11.28

Turn on : 26.7298

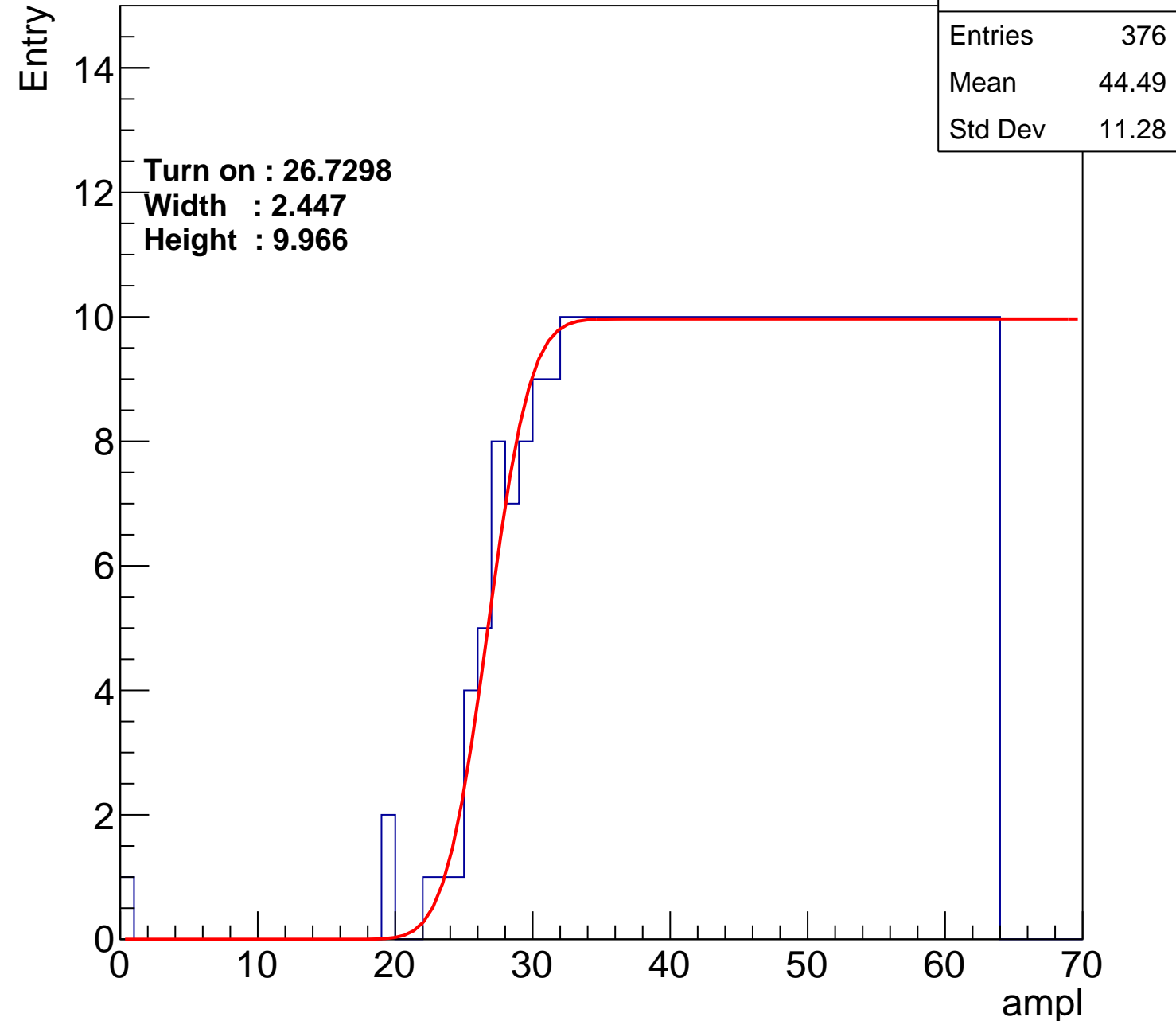
Width : 2.447

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch33

calib\_packv5\_042523\_0143.root, FC#8, port C1

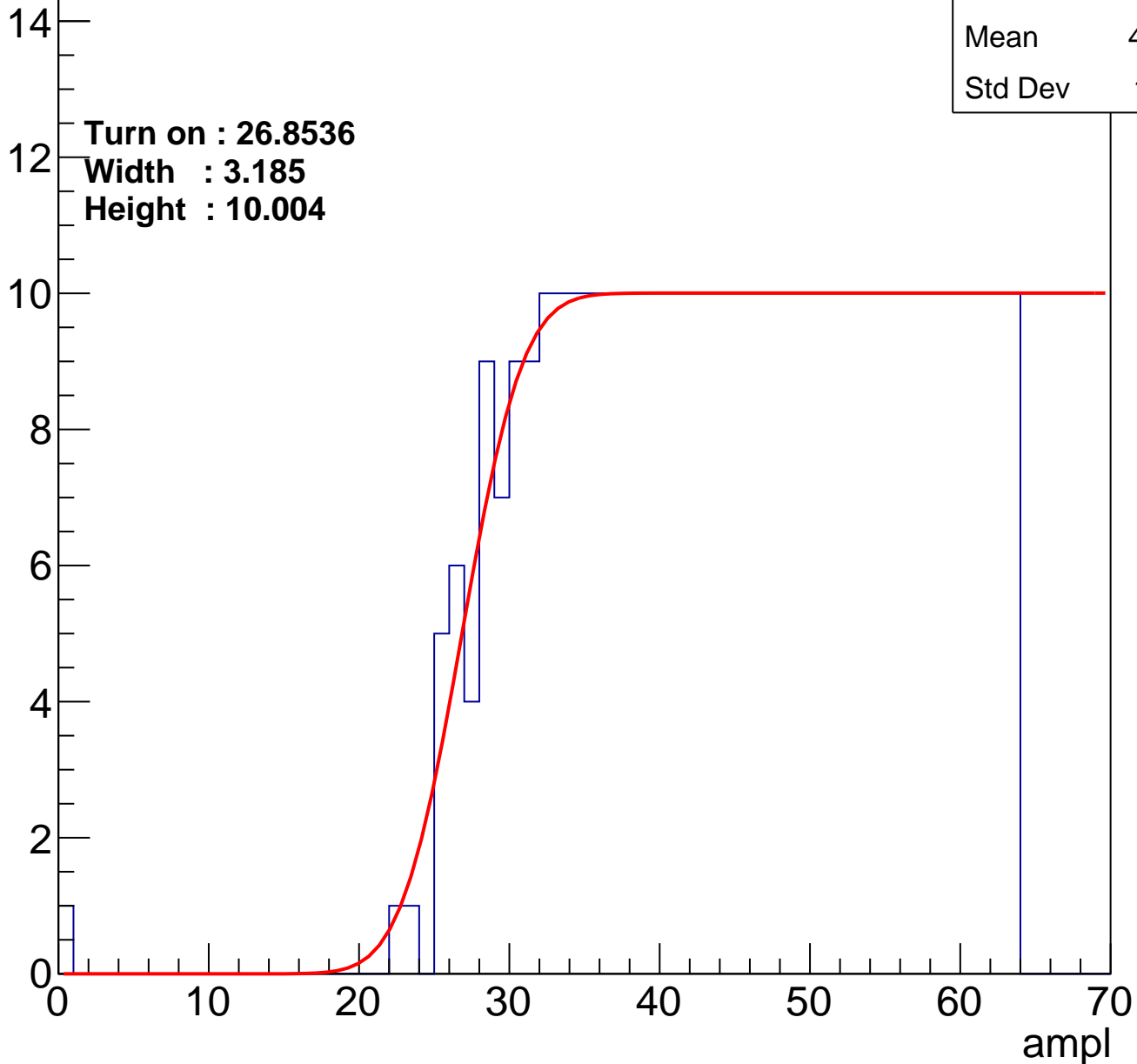
Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 26.8536

Width : 3.185

Height : 10.004

Entry



# B0L002S, U24-ch34

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.03
Std Dev	11.09

Turn on : 28.2806

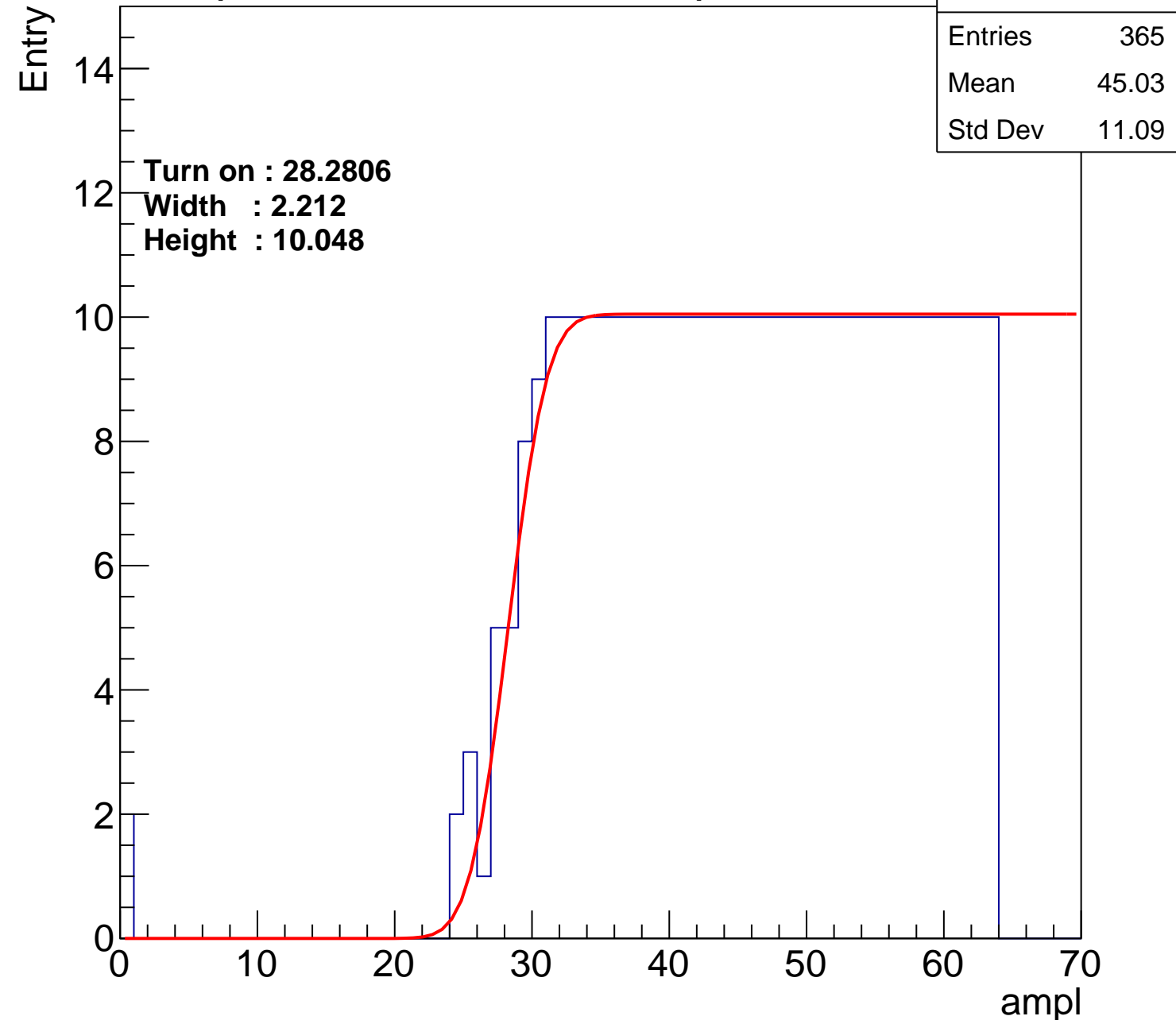
Width : 2.212

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch35

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.1
Std Dev	11.23

Turn on : 28.3471

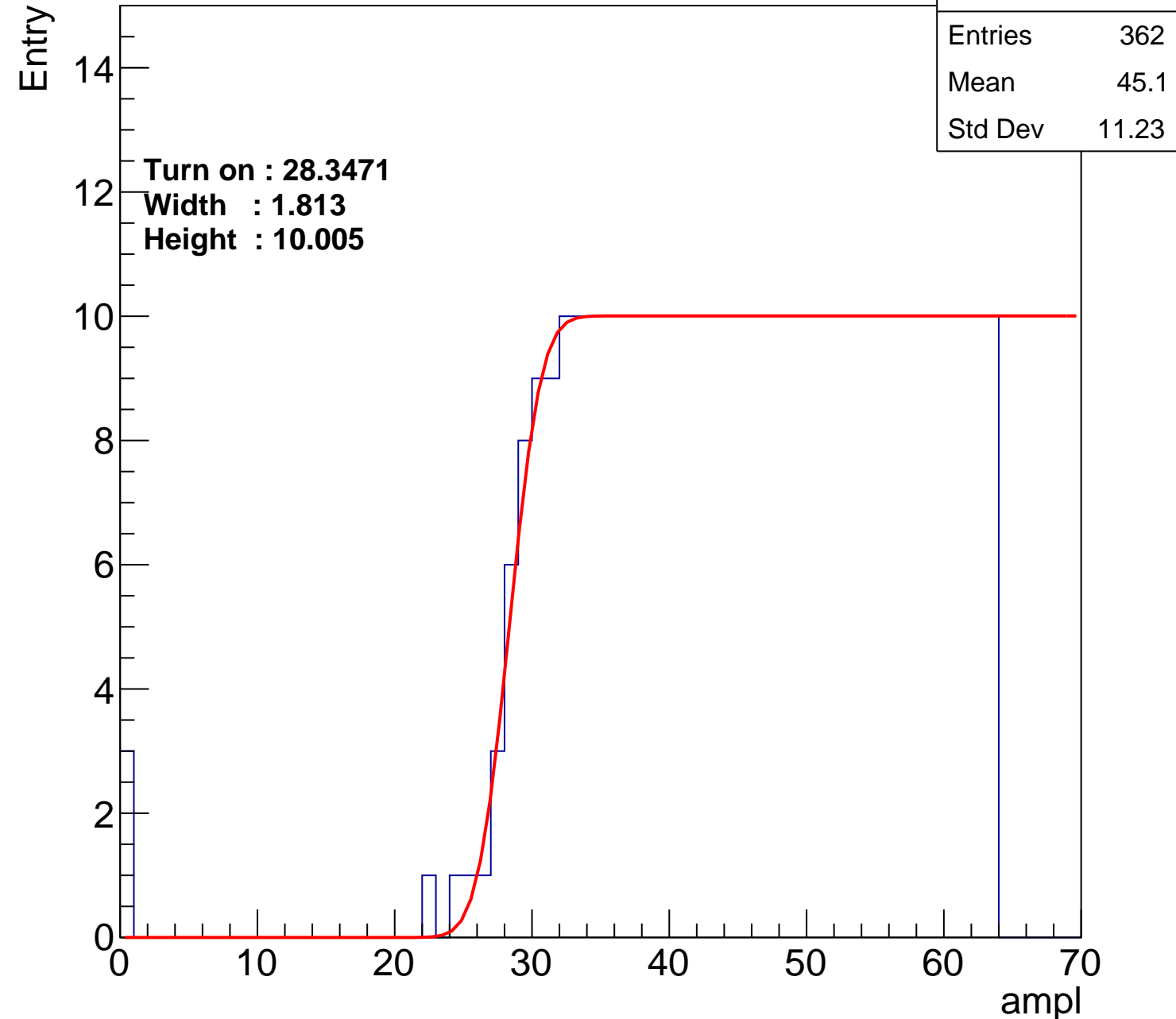
Width : 1.813

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch36

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.05
Std Dev	11.1

Turn on : 28.2234

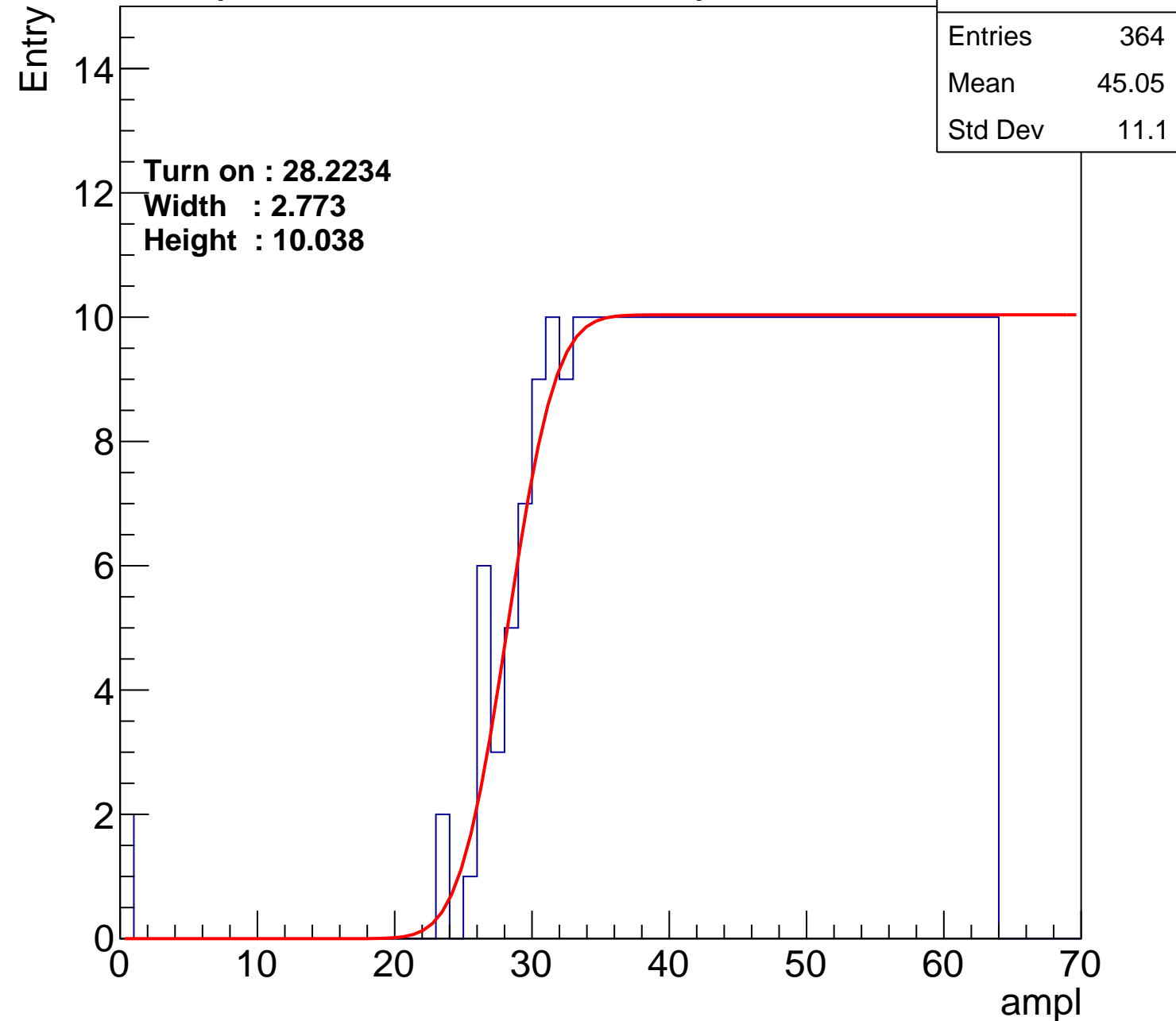
Width : 2.773

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch37

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.56
Std Dev	10.61

**Turn on : 28.8722**

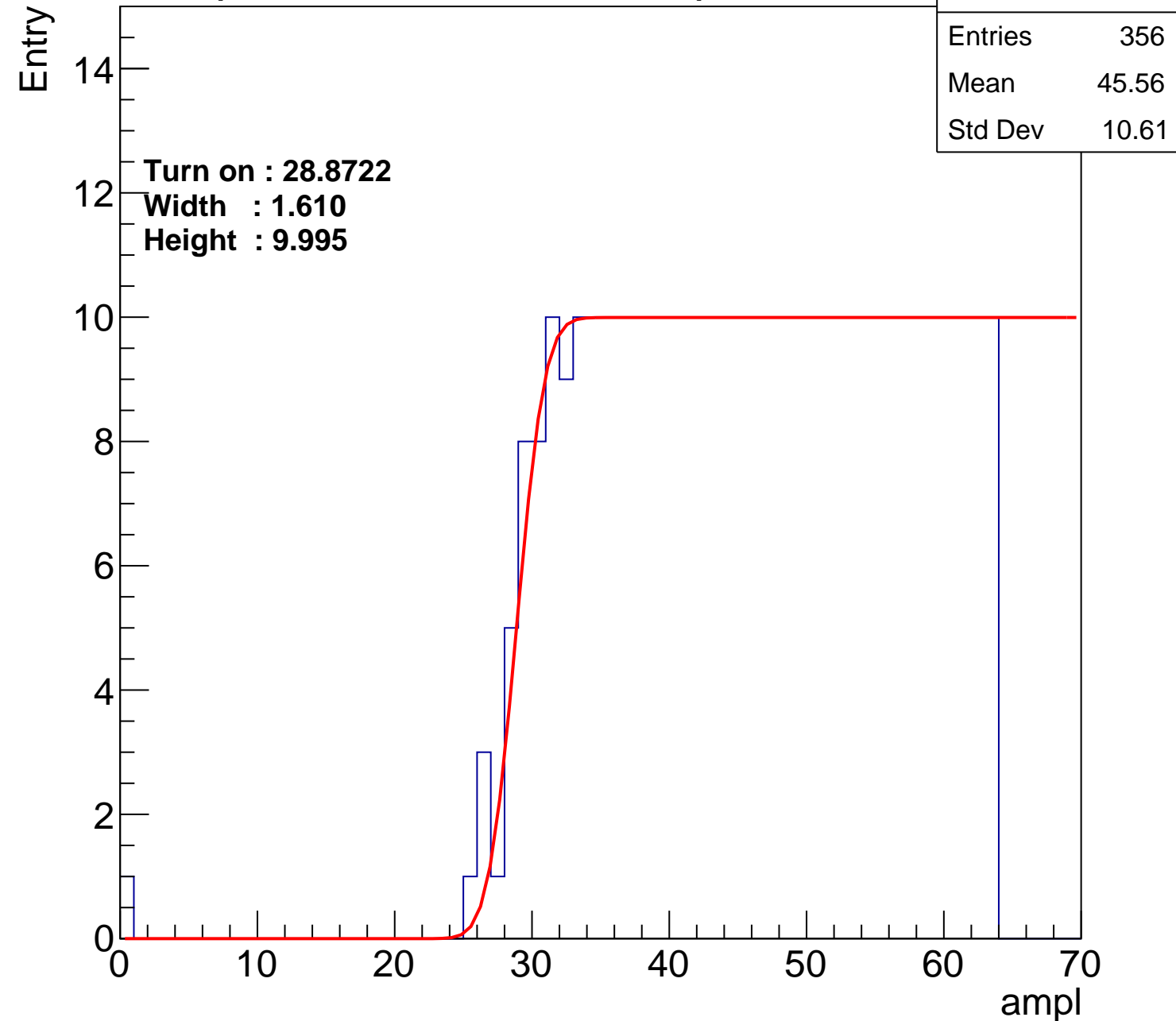
**Width : 1.610**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch38

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.19
Std Dev	11.24

Turn on : 28.8557

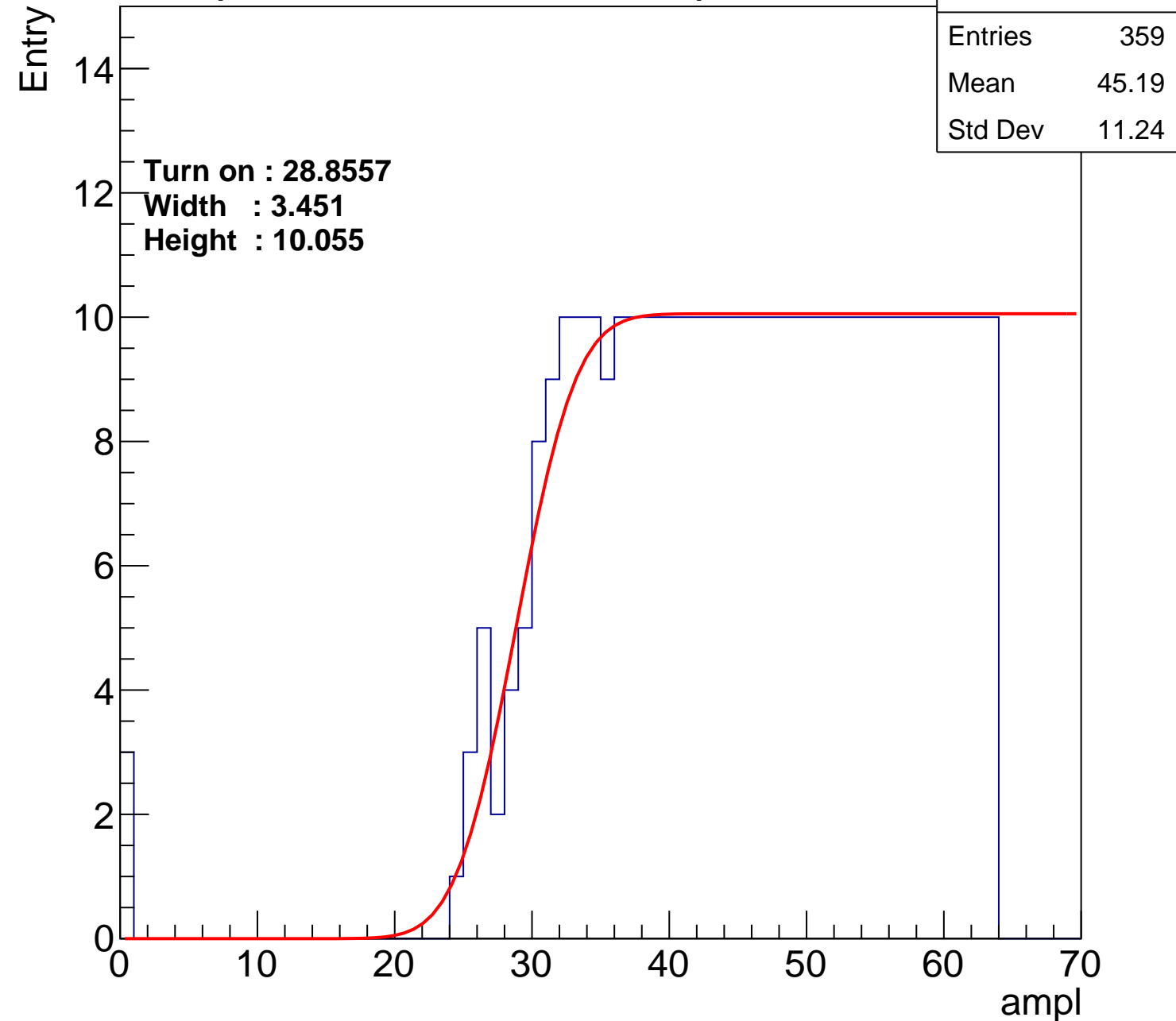
Width : 3.451

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch39

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.46
Std Dev	10.72

**Turn on : 28.6099**

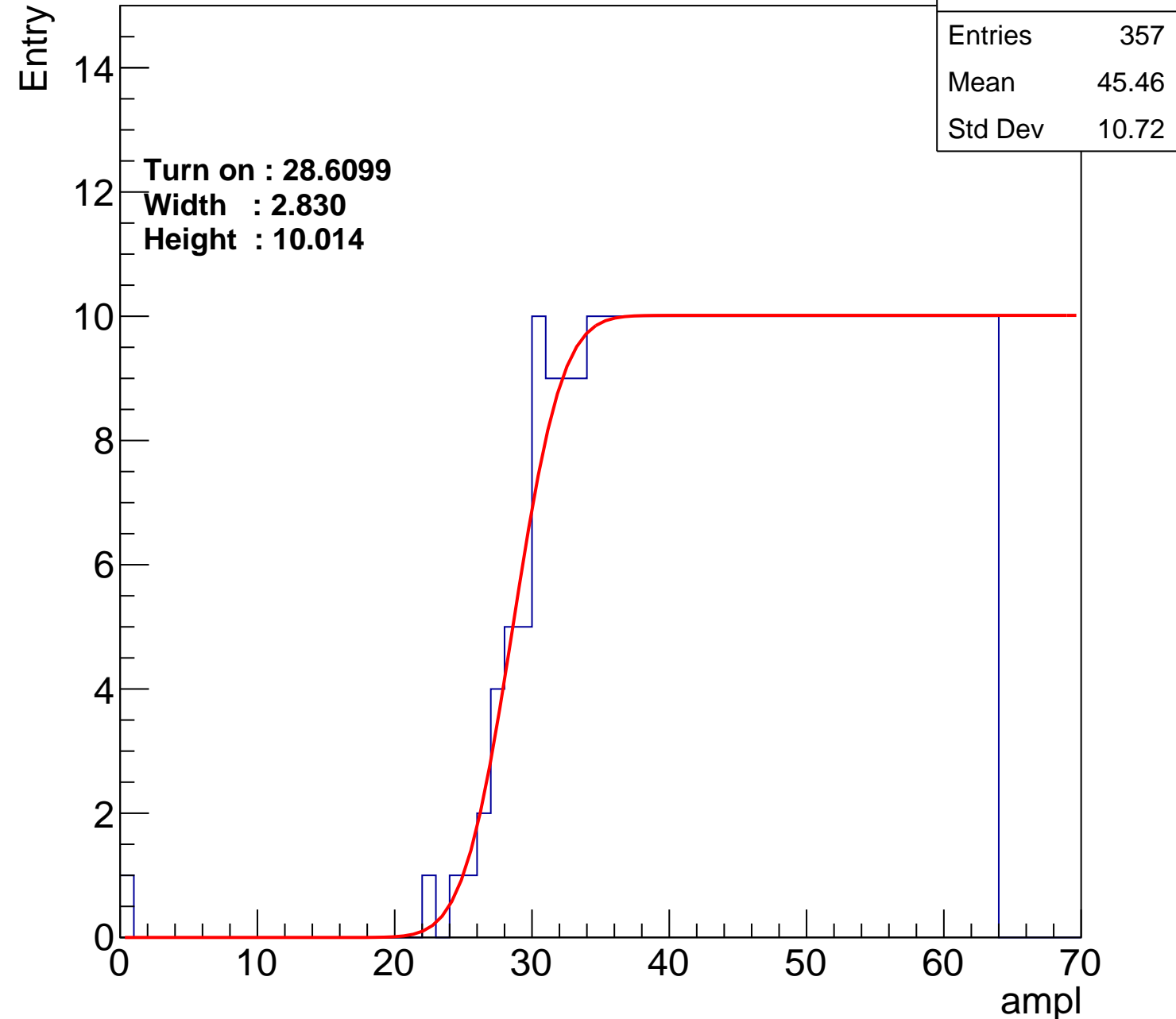
**Width : 2.830**

**Height : 10.014**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch40

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	340
Mean	46.23
Std Dev	10.51

**Turn on : 30.2935**

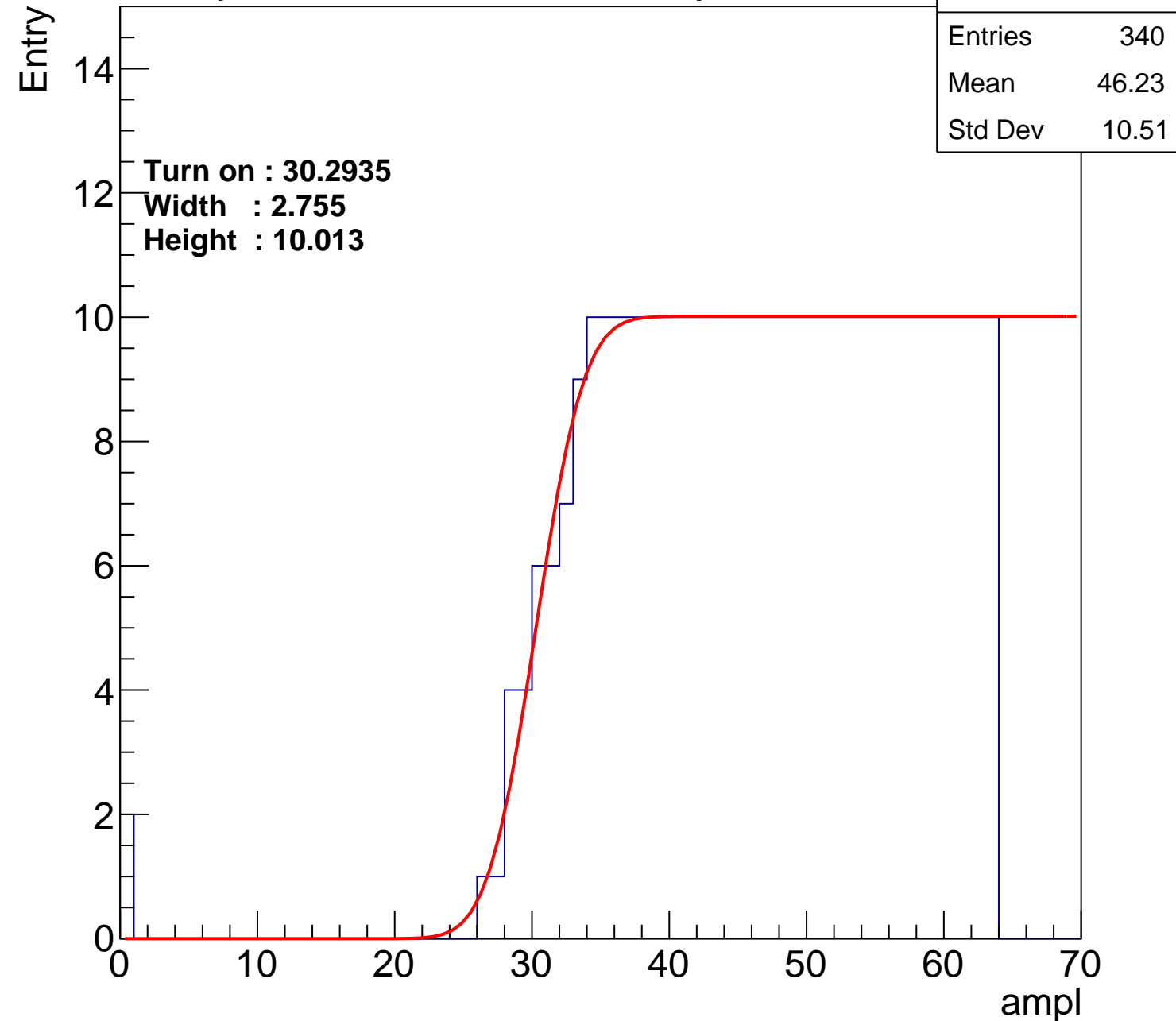
**Width : 2.755**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch41

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.89
Std Dev	11.21

**Turn on : 27.6285**

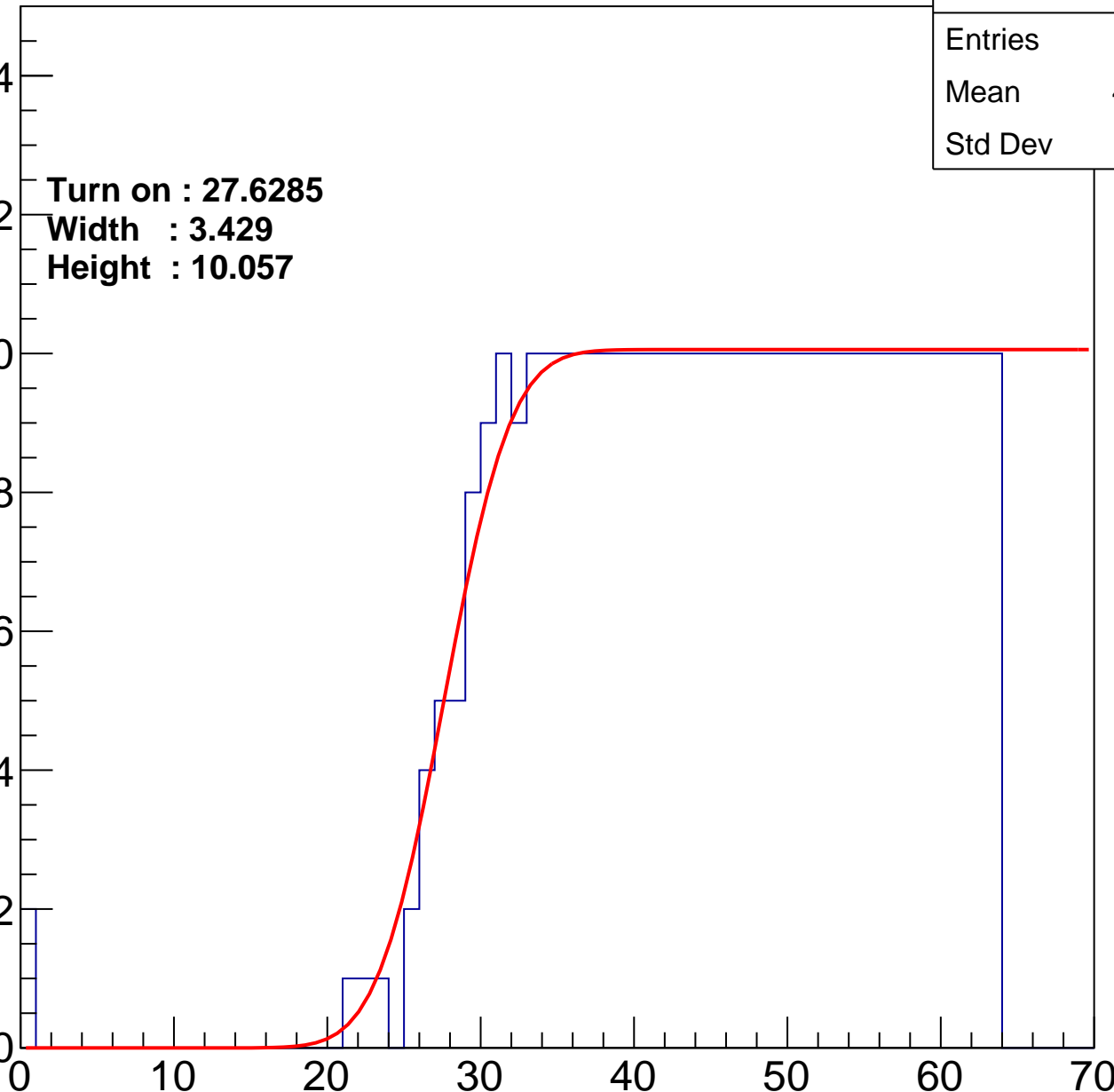
**Width : 3.429**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch42

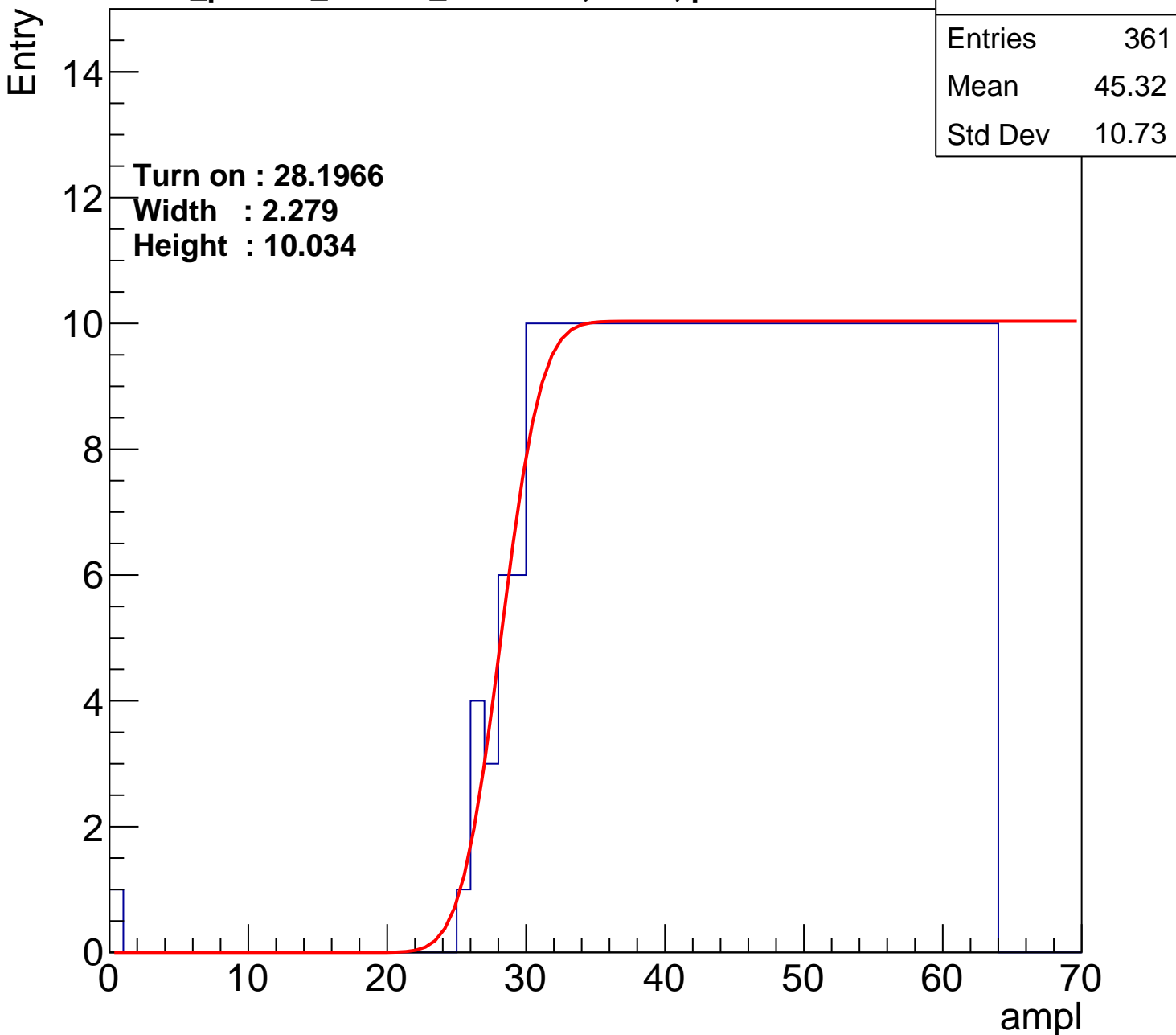
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**Turn on : 28.1966**

**Width : 2.279**

**Height : 10.034**

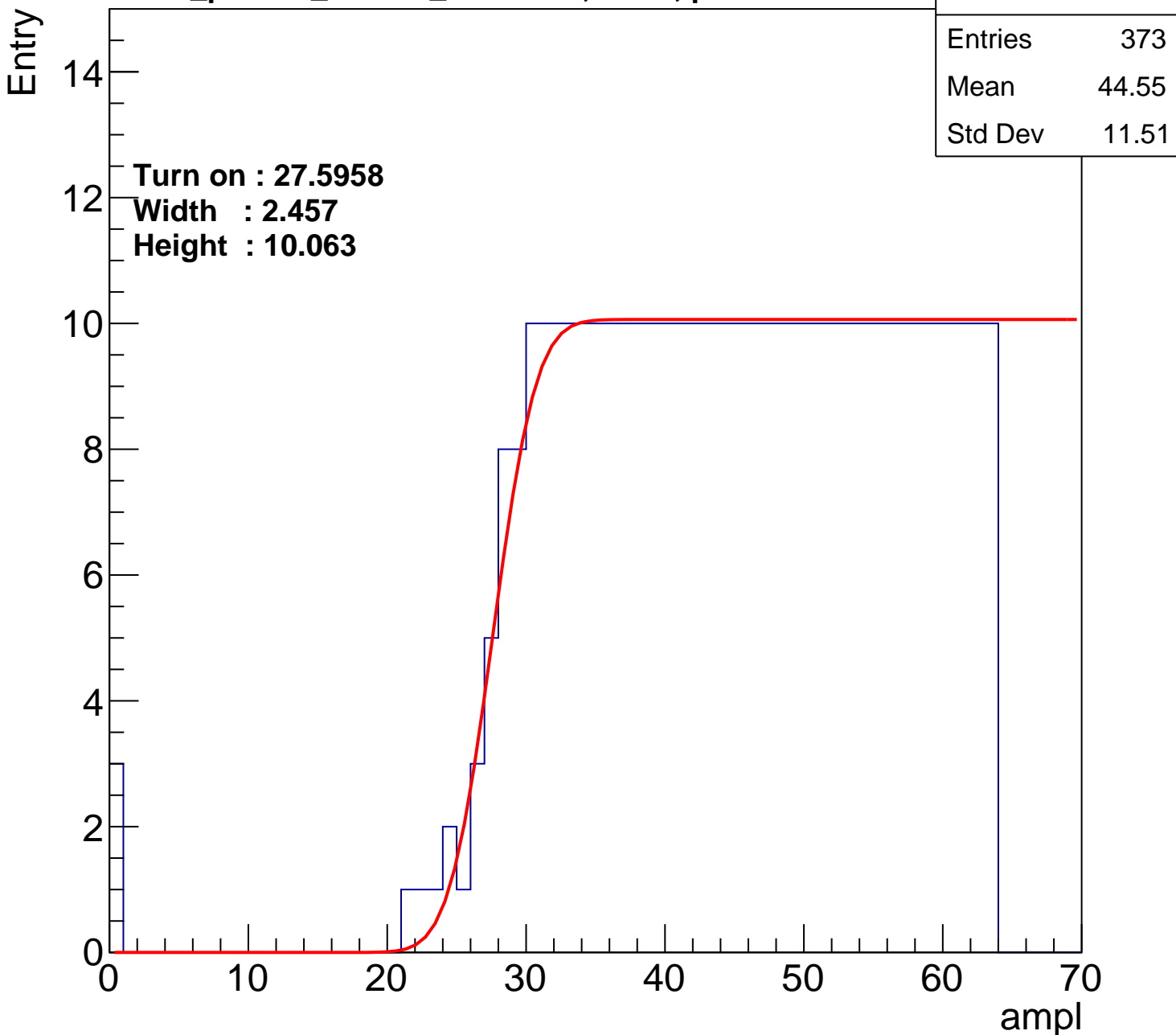
Entries	361
Mean	45.32
Std Dev	10.73



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.5958  
Width : 2.457  
Height : 10.063



# B0L002S, U24-ch44

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.87
Std Dev	11.05

Turn on : 27.6275

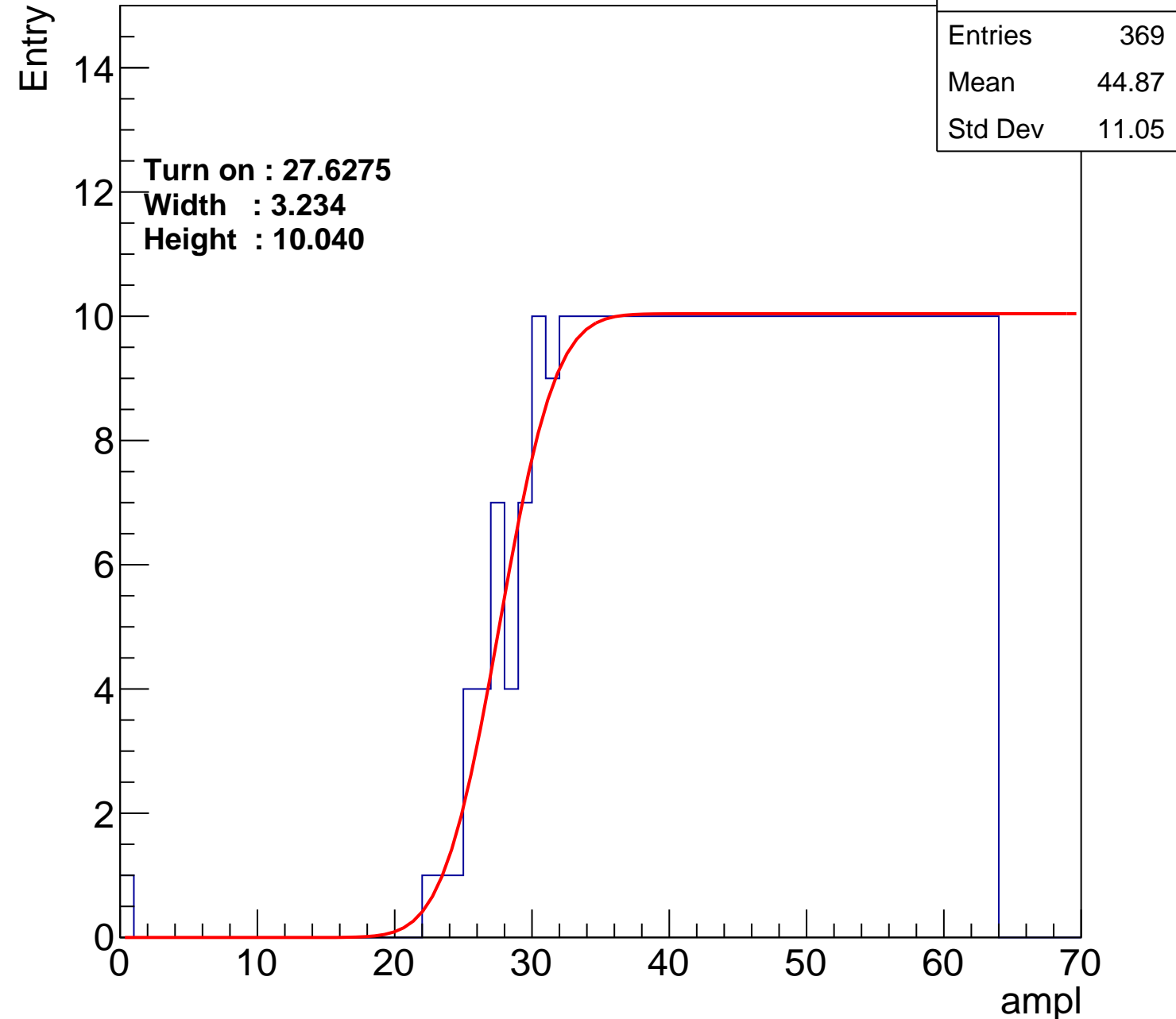
Width : 3.234

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch45

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	344
Mean	46.12
Std Dev	10.35

Turn on : 29.5473

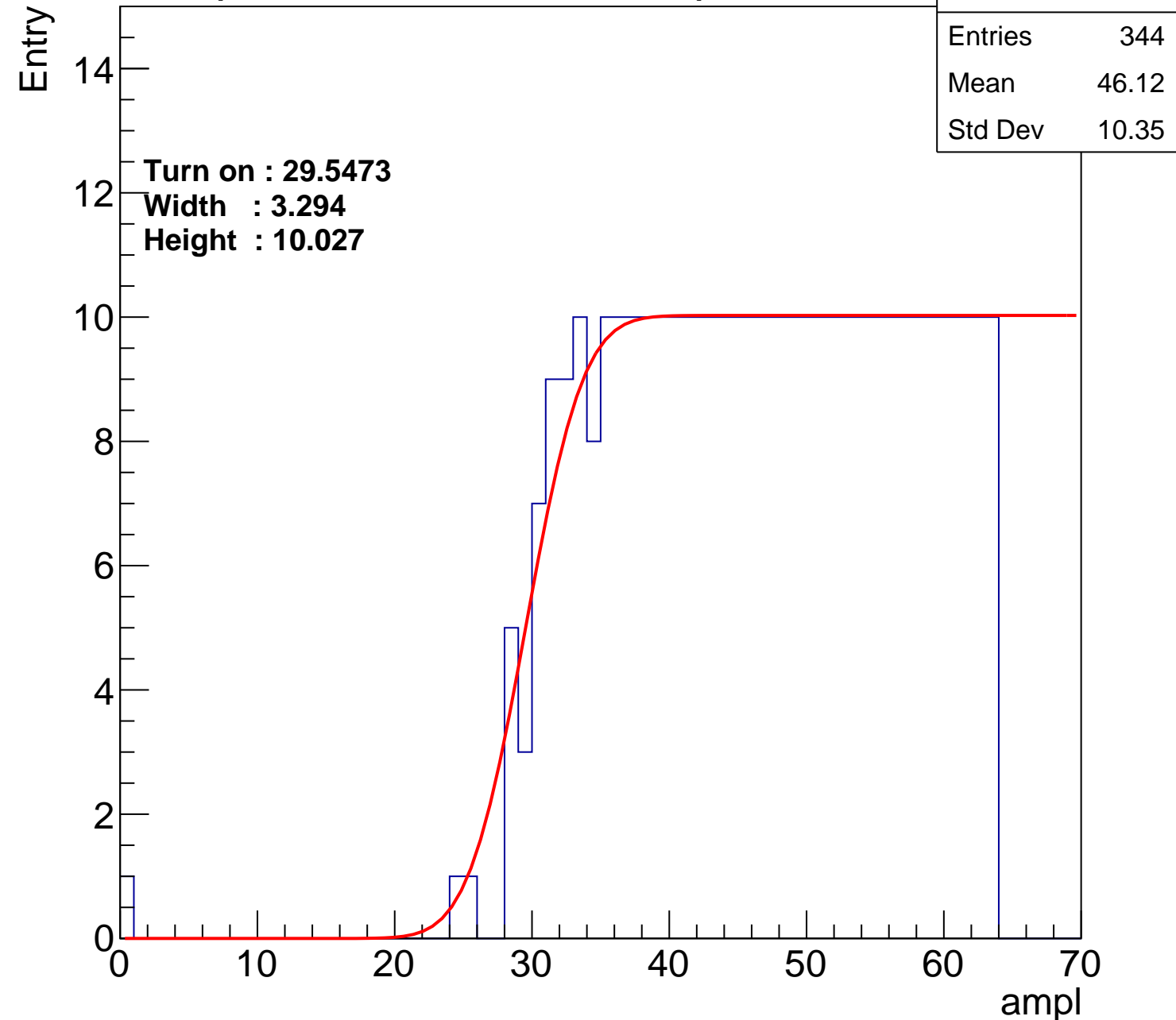
Width : 3.294

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch46

calib\_packv5\_042523\_0143.root, FC#8, port C1

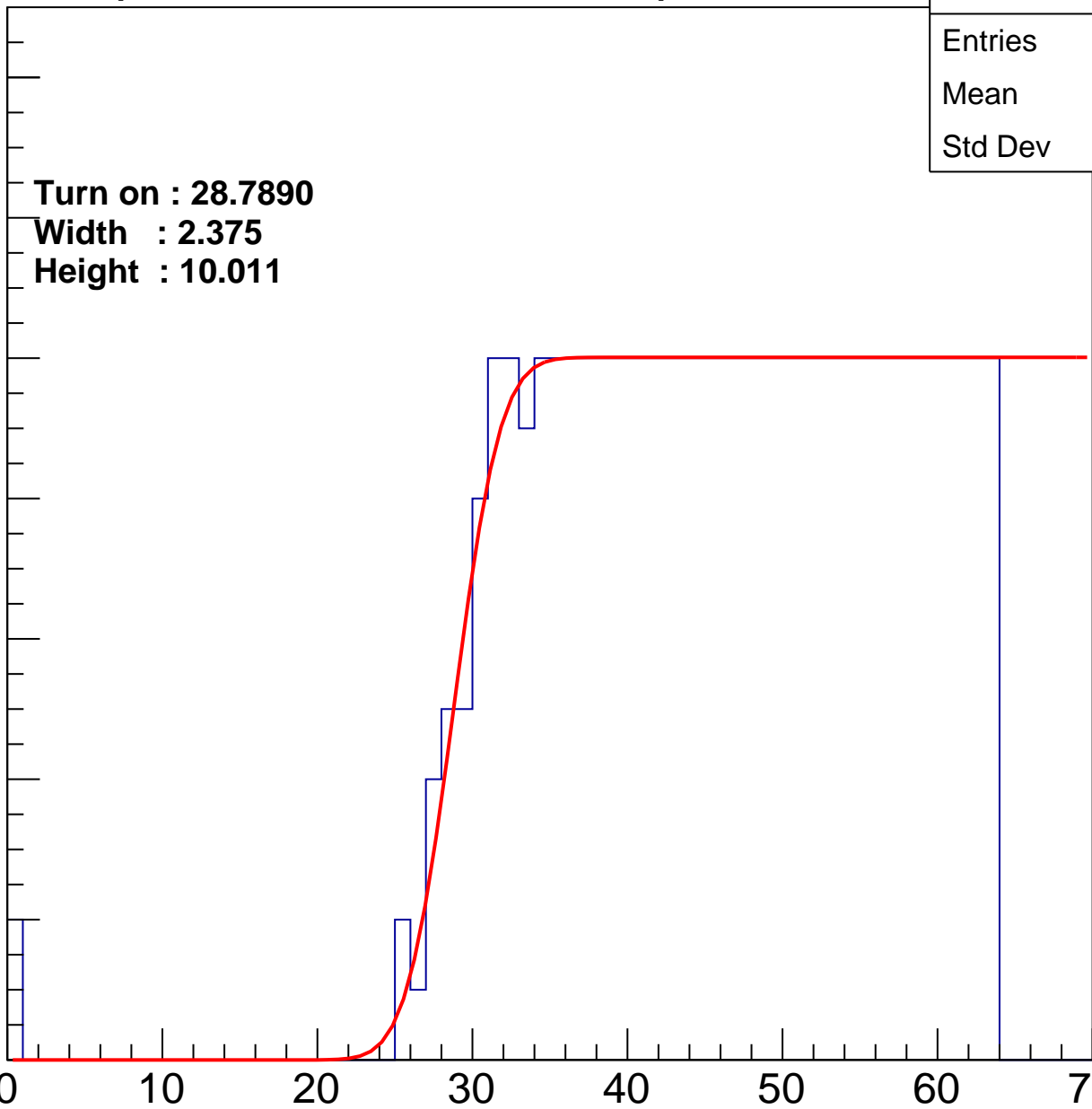
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.7890**  
**Width : 2.375**  
**Height : 10.011**

Entries	356
Mean	45.47
Std Dev	10.87

ampl





# B0L002S, U24-ch47

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.23
Std Dev	11.04

Turn on : 28.8176

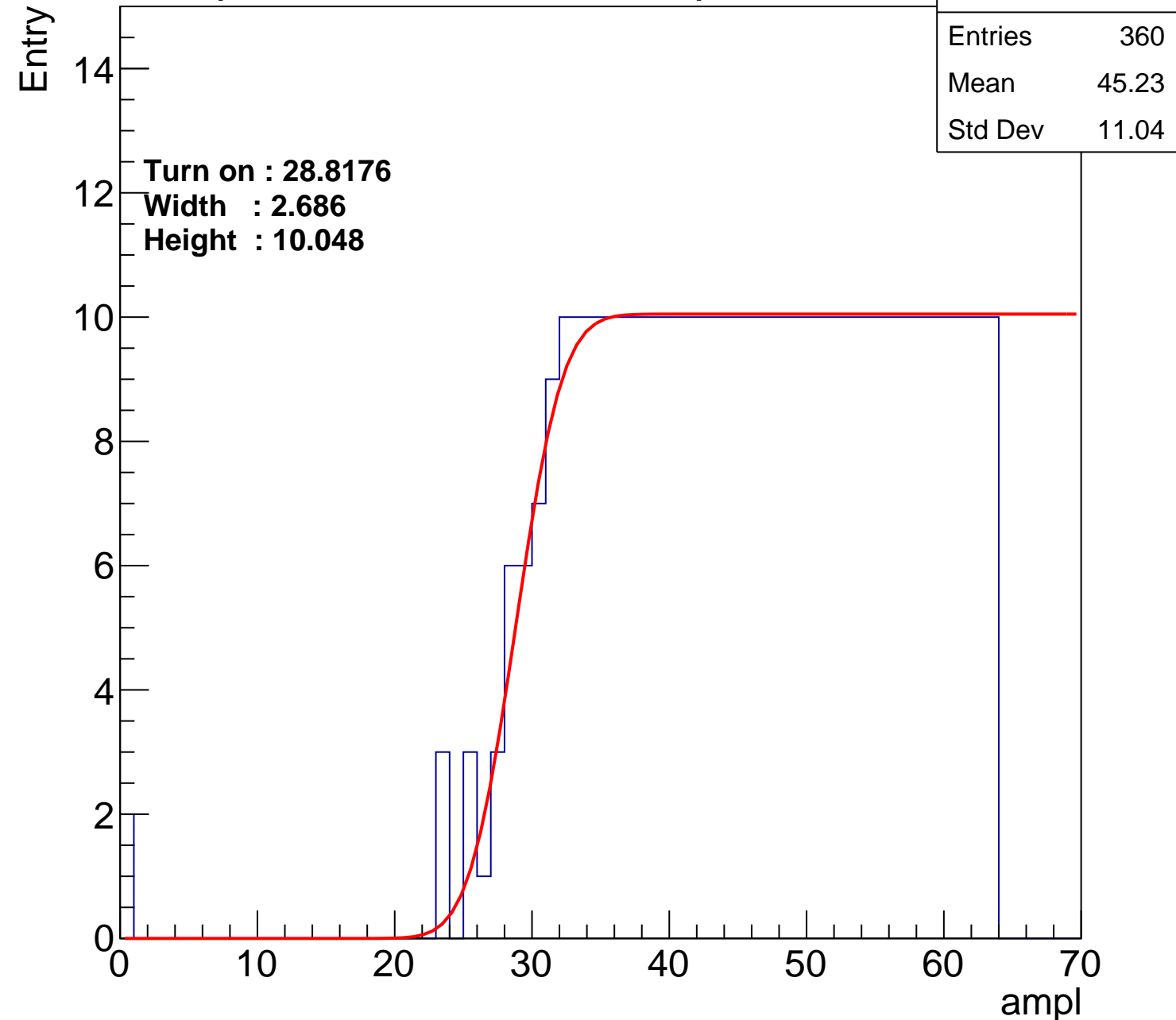
Width : 2.686

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch48

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.13
Std Dev	11.09

**Turn on : 28.0793**

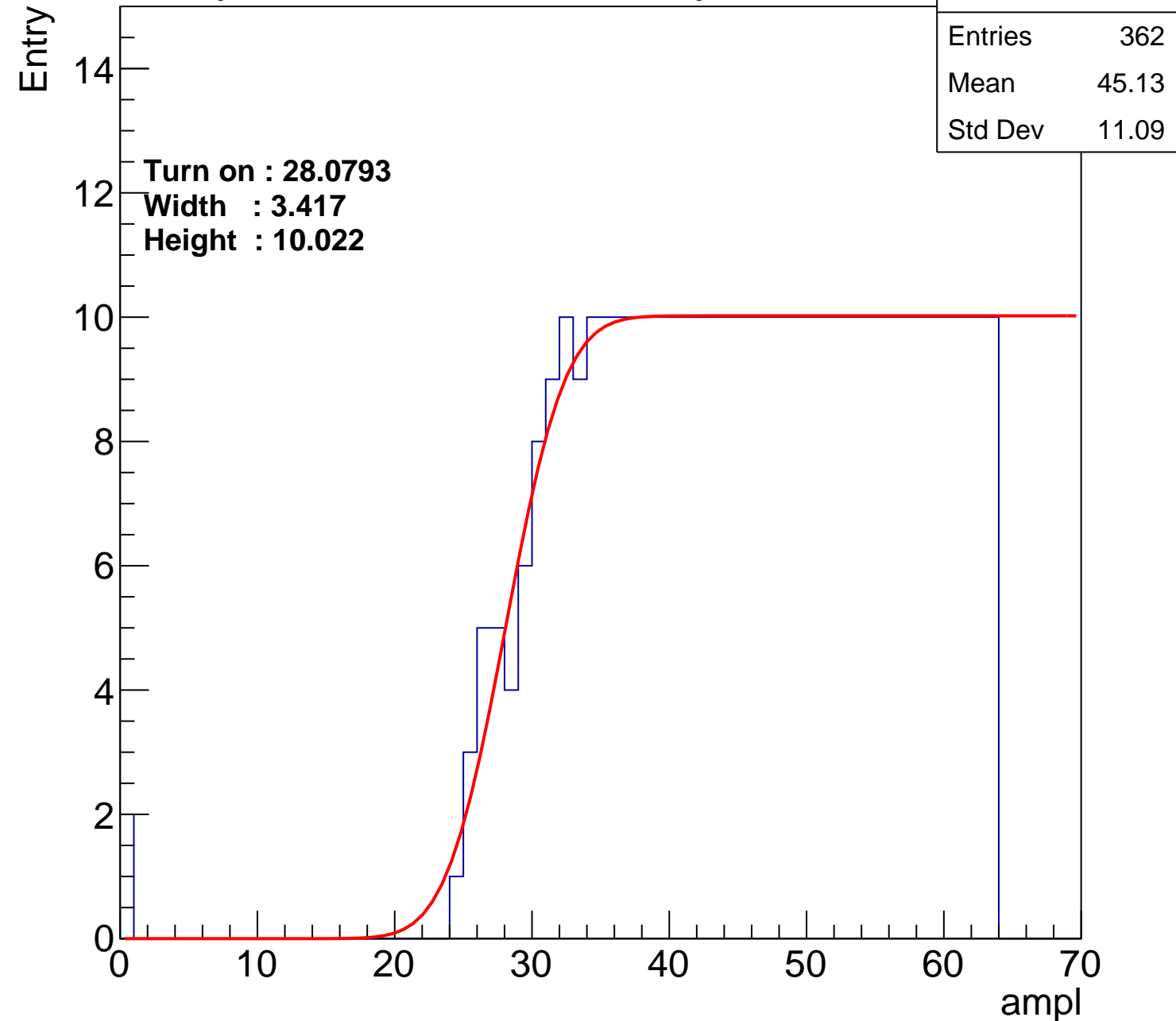
**Width : 3.417**

**Height : 10.022**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch49

calib\_packv5\_042523\_0143.root, FC#8, port C1

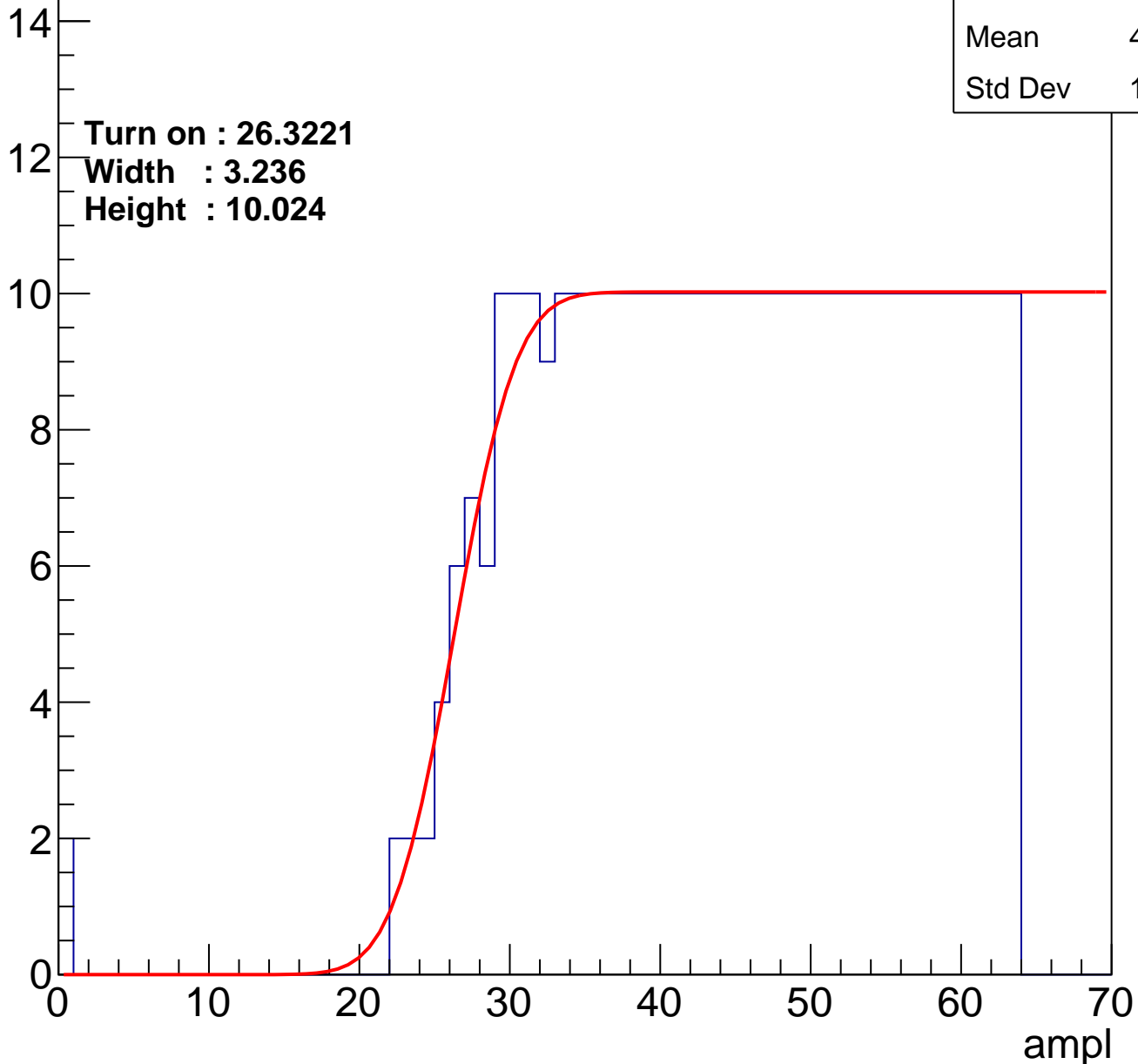
Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.3221

Width : 3.236

Height : 10.024

Entry



# B0L002S, U24-ch50

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 28.1220

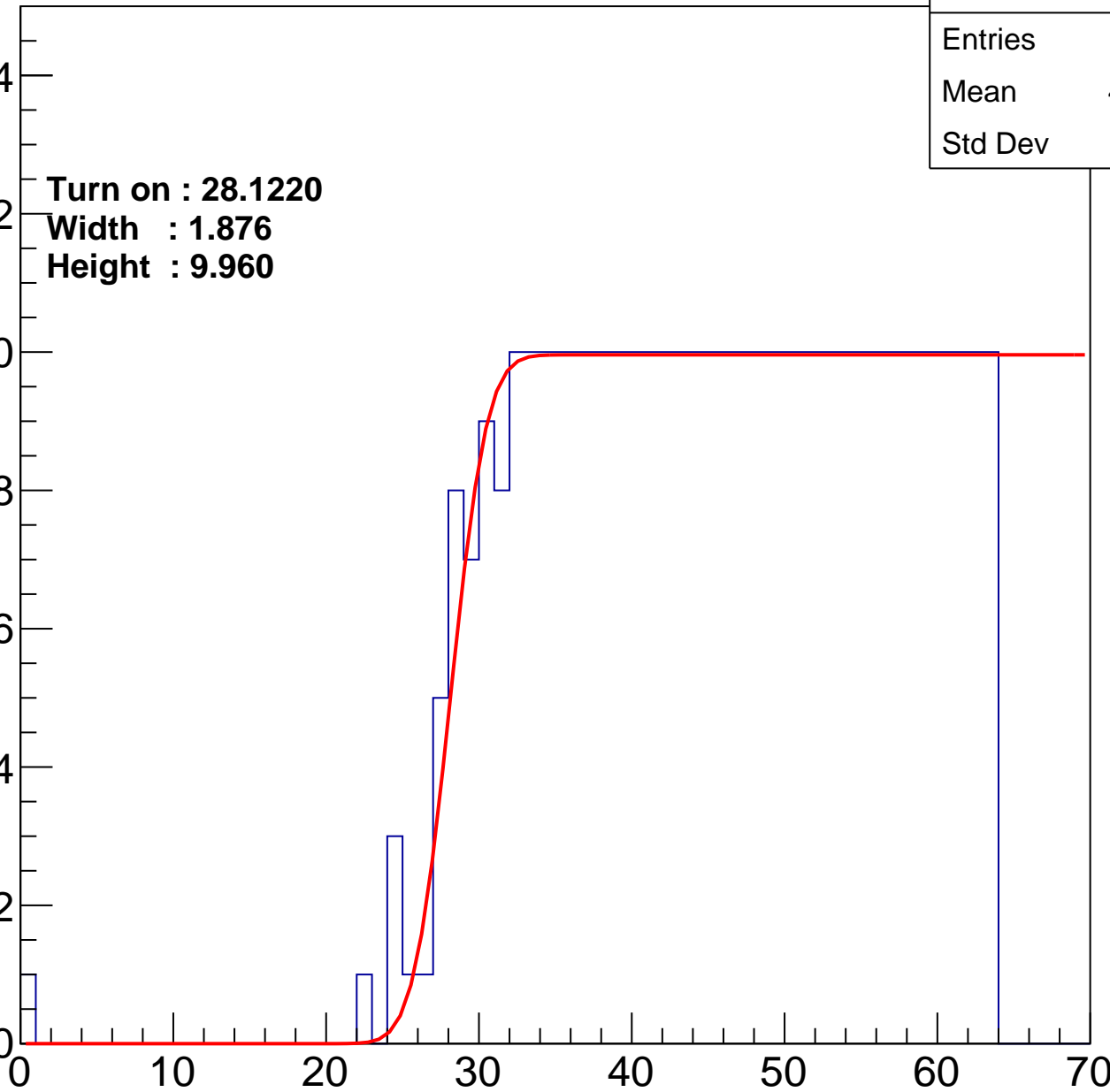
Width : 1.876

Height : 9.960

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch51

calib\_packv5\_042523\_0143.root, FC#8, port C1

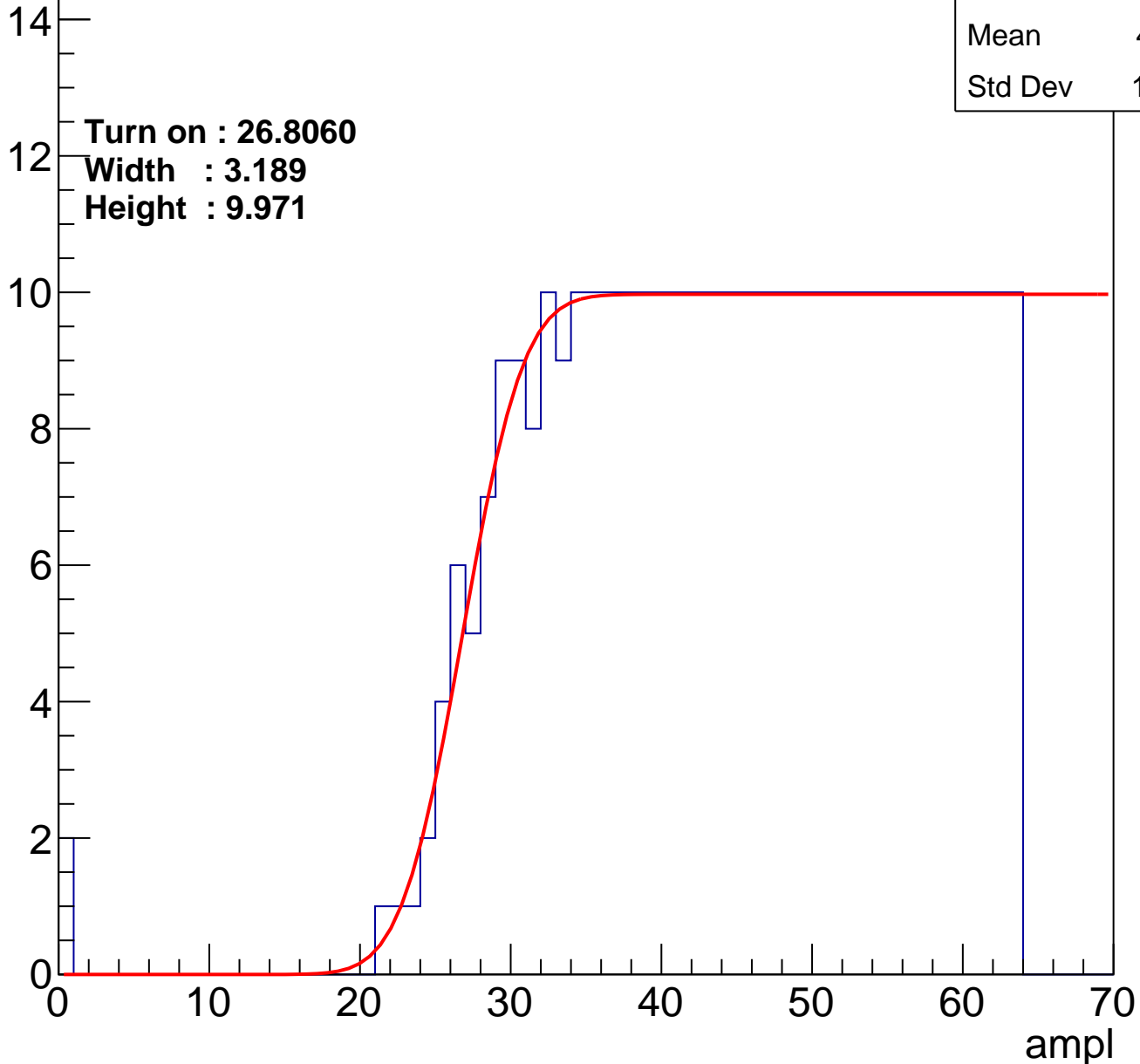
Entries	374
Mean	44.51
Std Dev	11.43

Turn on : 26.8060

Width : 3.189

Height : 9.971

Entry



# B0L002S, U24-ch52

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.86
Std Dev	11.05

Turn on : 26.7886

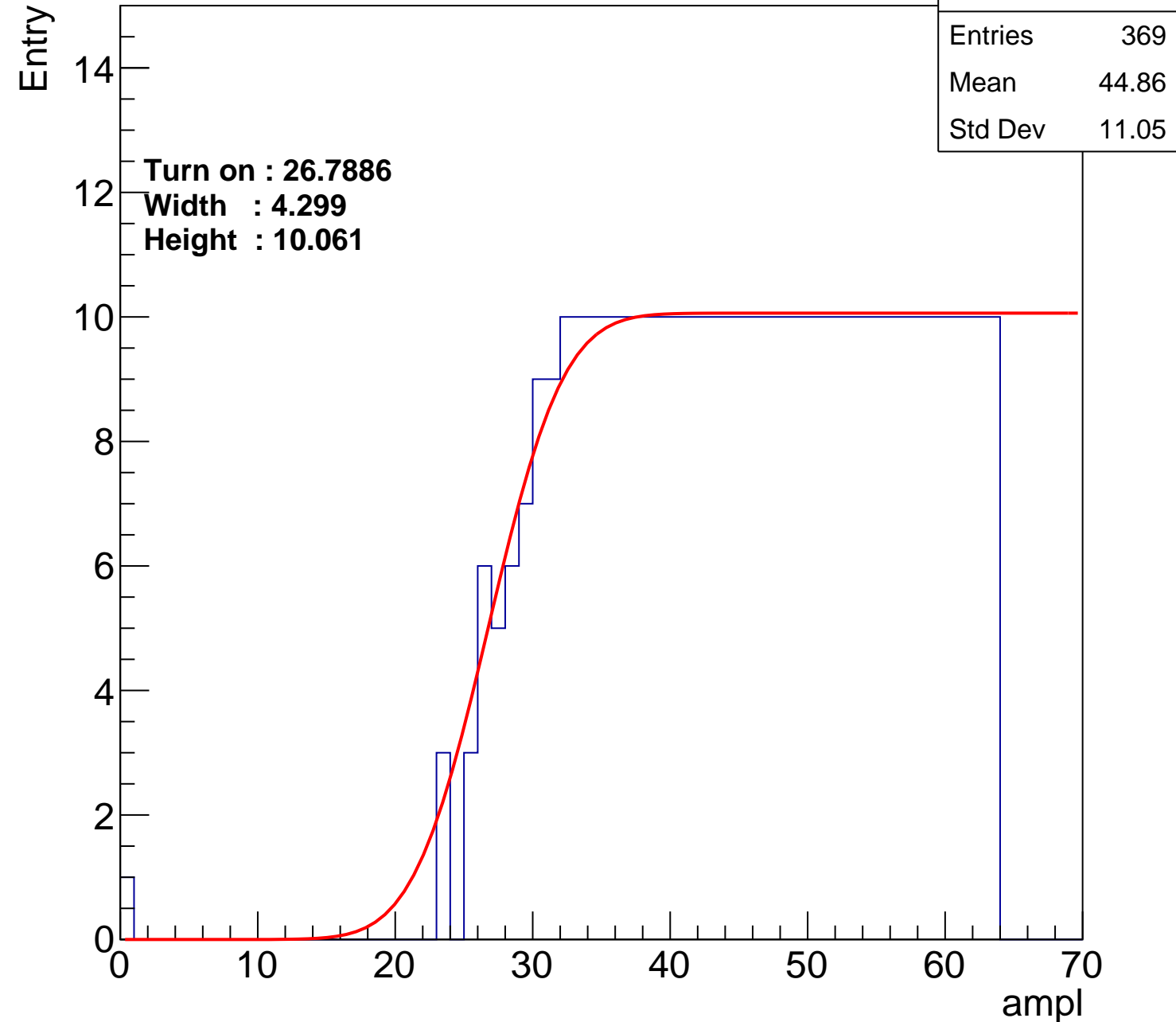
Width : 4.299

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch53

calib\_packv5\_042523\_0143.root, FC#8, port C1

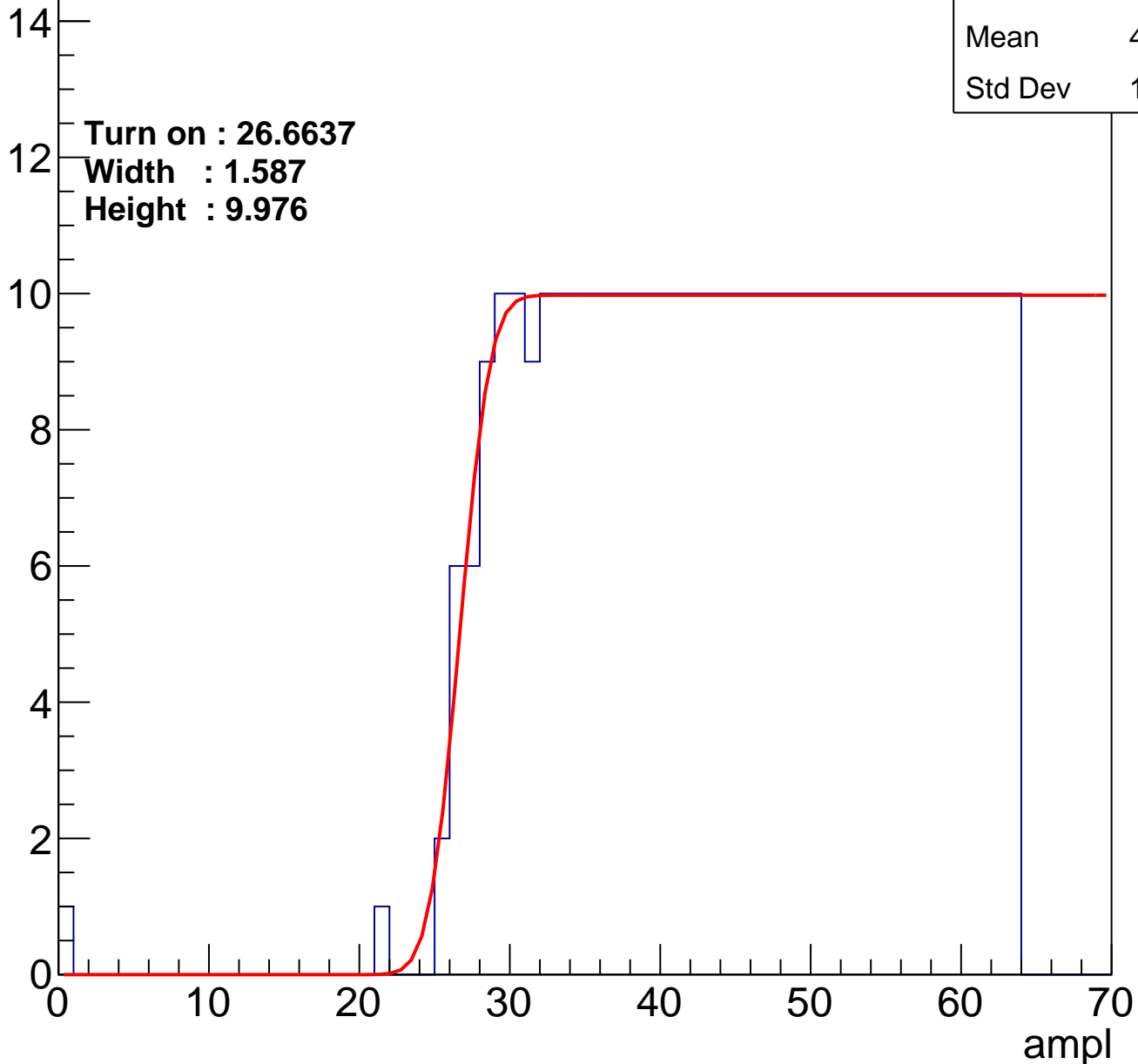
Entries	374
Mean	44.68
Std Dev	11.08

Turn on : 26.6637

Width : 1.587

Height : 9.976

Entry



# B0L002S, U24-ch54

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.87
Std Dev	11.13

Turn on : 27.4215

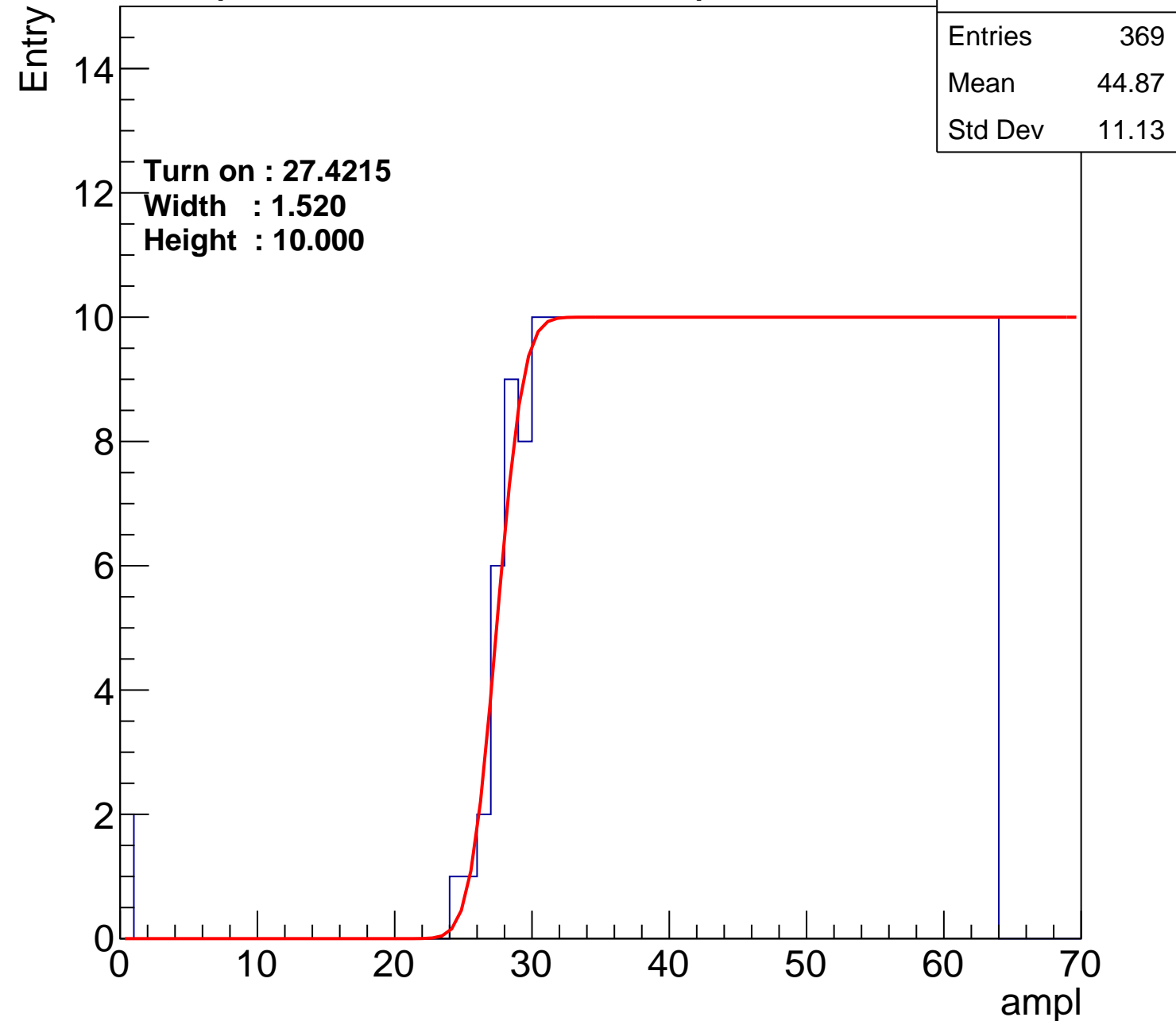
Width : 1.520

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch55

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.01
Std Dev	11.32

Turn on : 27.9761

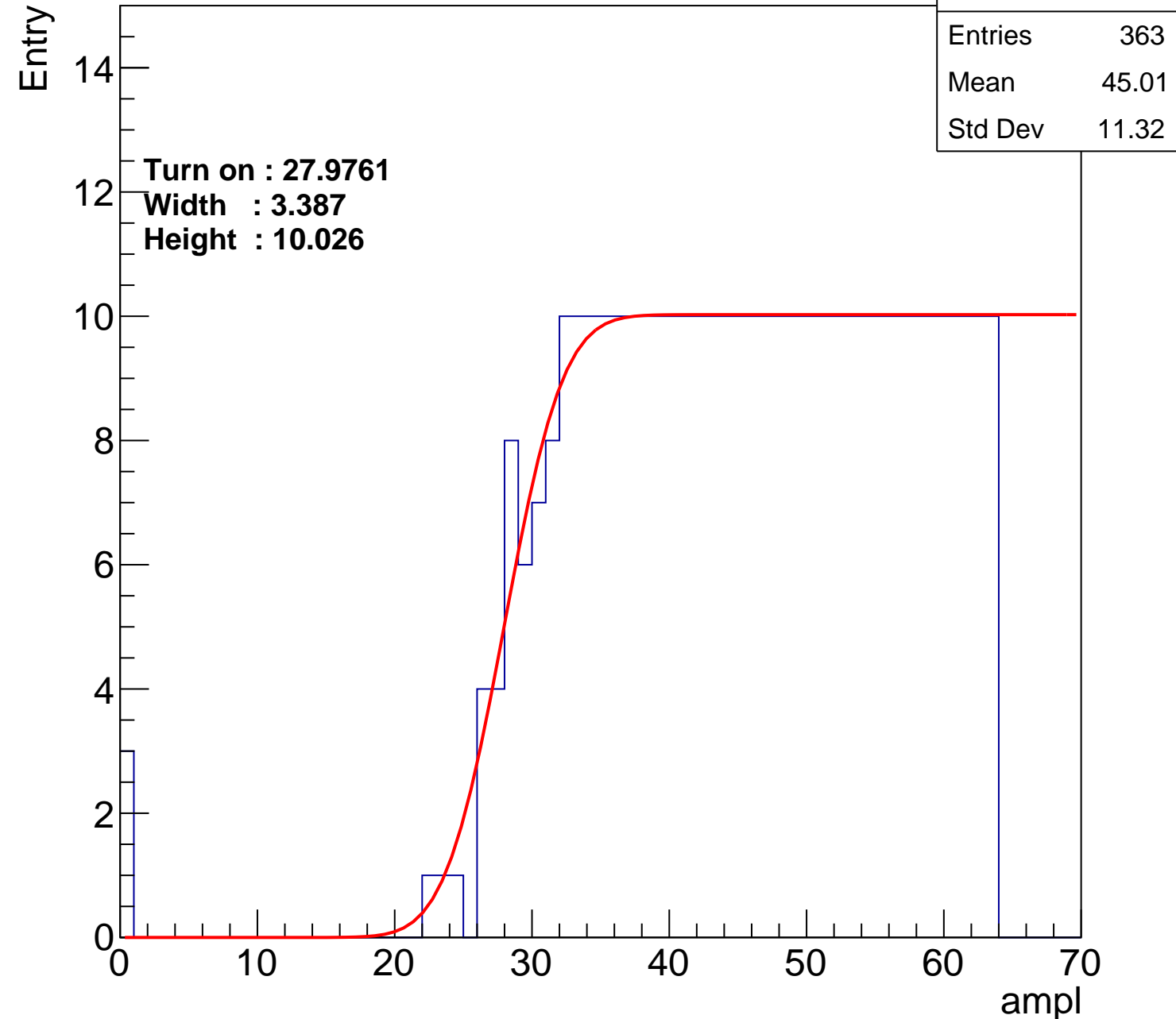
Width : 3.387

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch56

calib\_packv5\_042523\_0143.root, FC#8, port C1

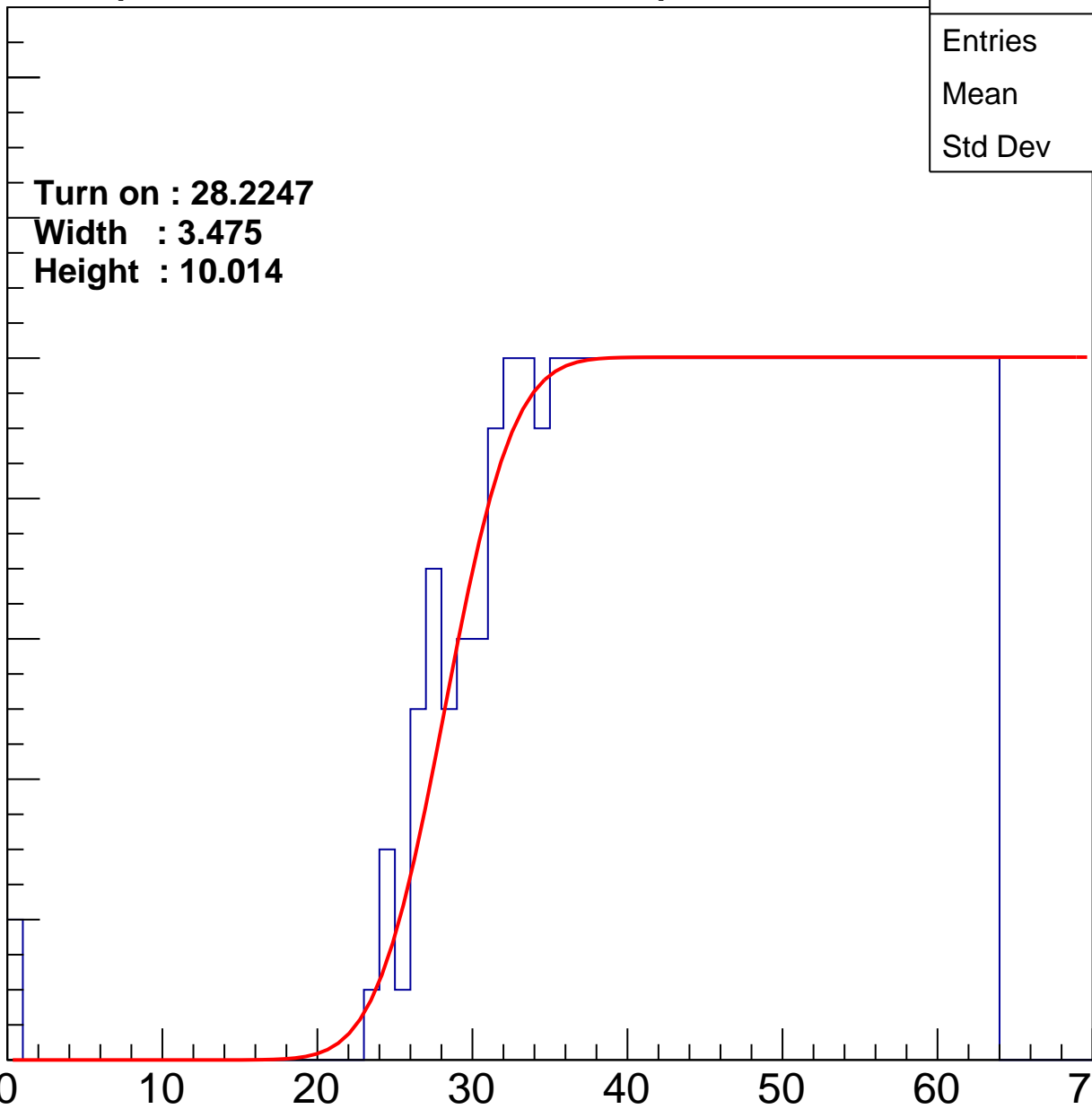
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.2247**  
**Width : 3.475**  
**Height : 10.014**

Entries	364
Mean	44.99
Std Dev	11.19

ampl



# B0L002S, U24-ch57

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	344
Mean	45.82
Std Dev	11.16

**Turn on : 29.7419**

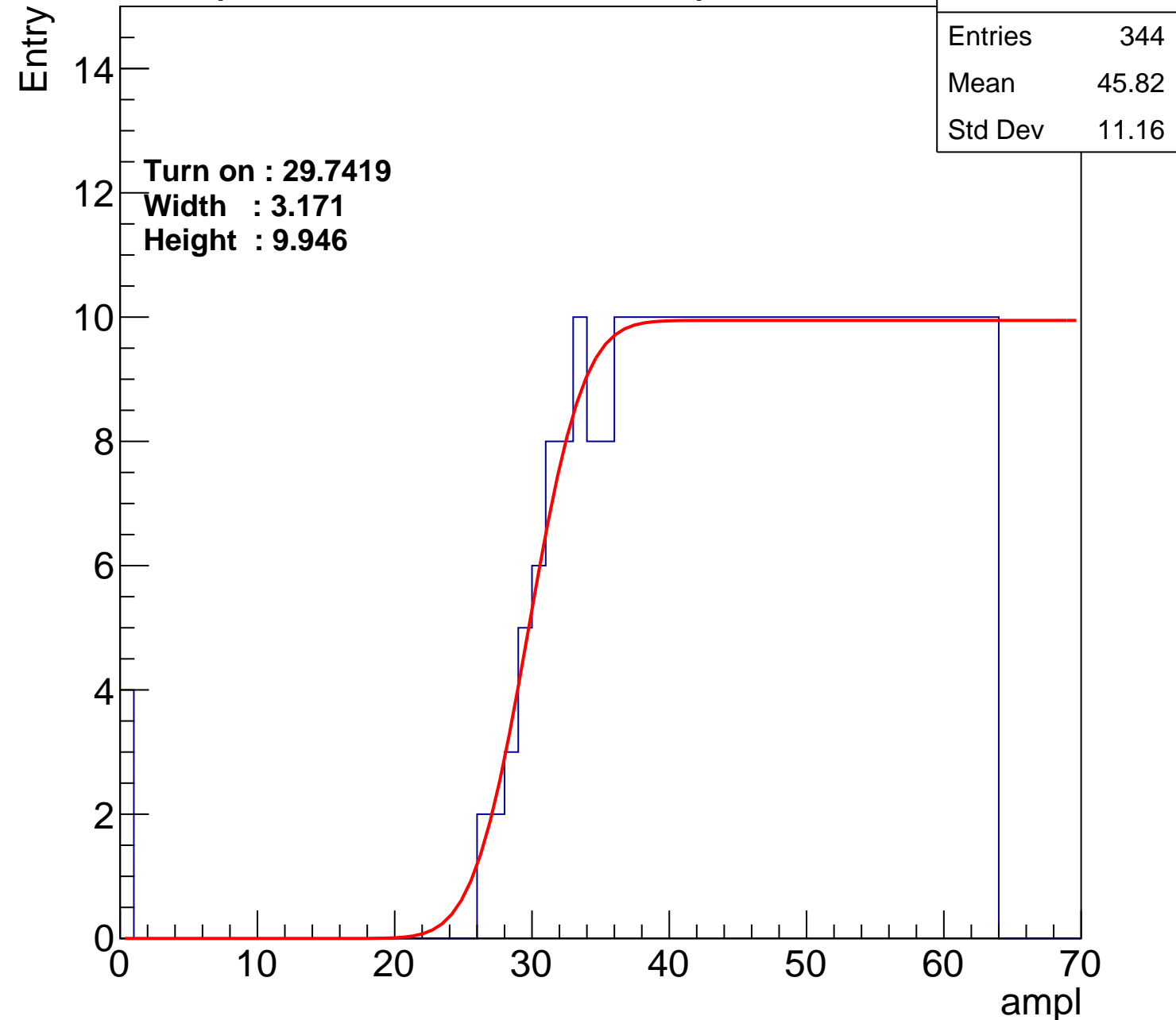
**Width : 3.171**

**Height : 9.946**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch58

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.4191

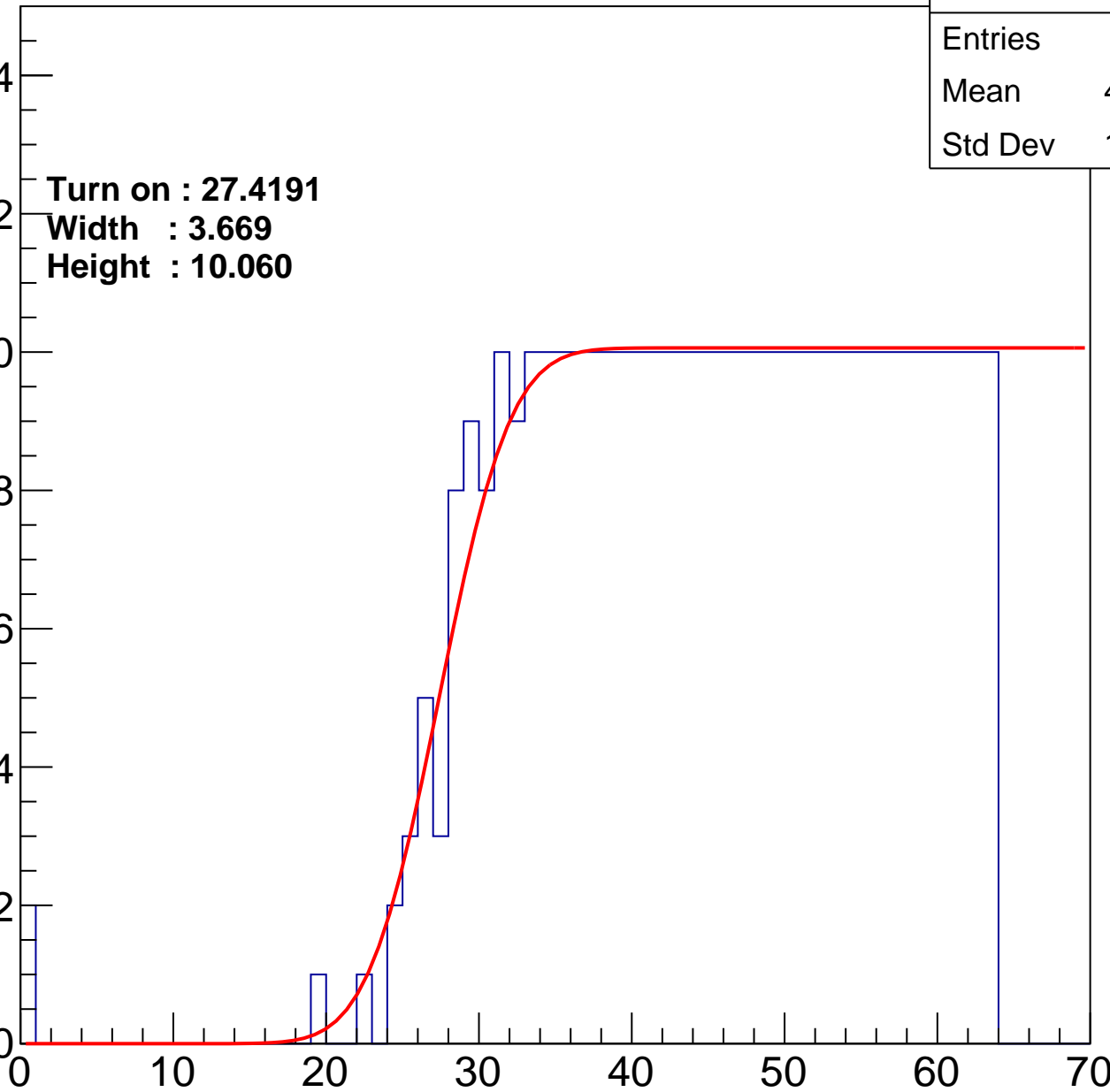
Width : 3.669

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

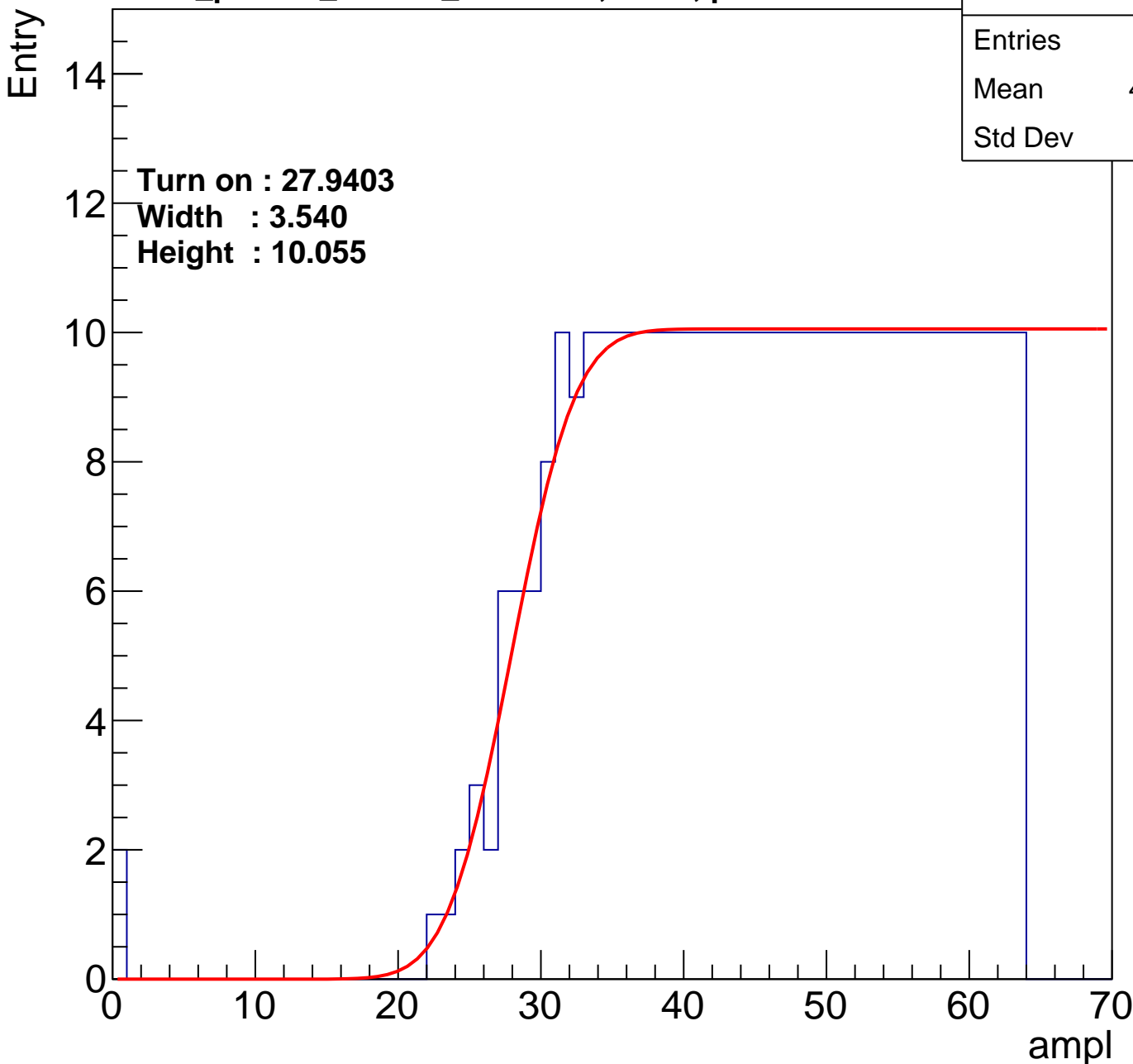


**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	366
Mean	44.92
Std Dev	11.2

**Height : 10.055**



# B0L002S, U24-ch60

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.9301

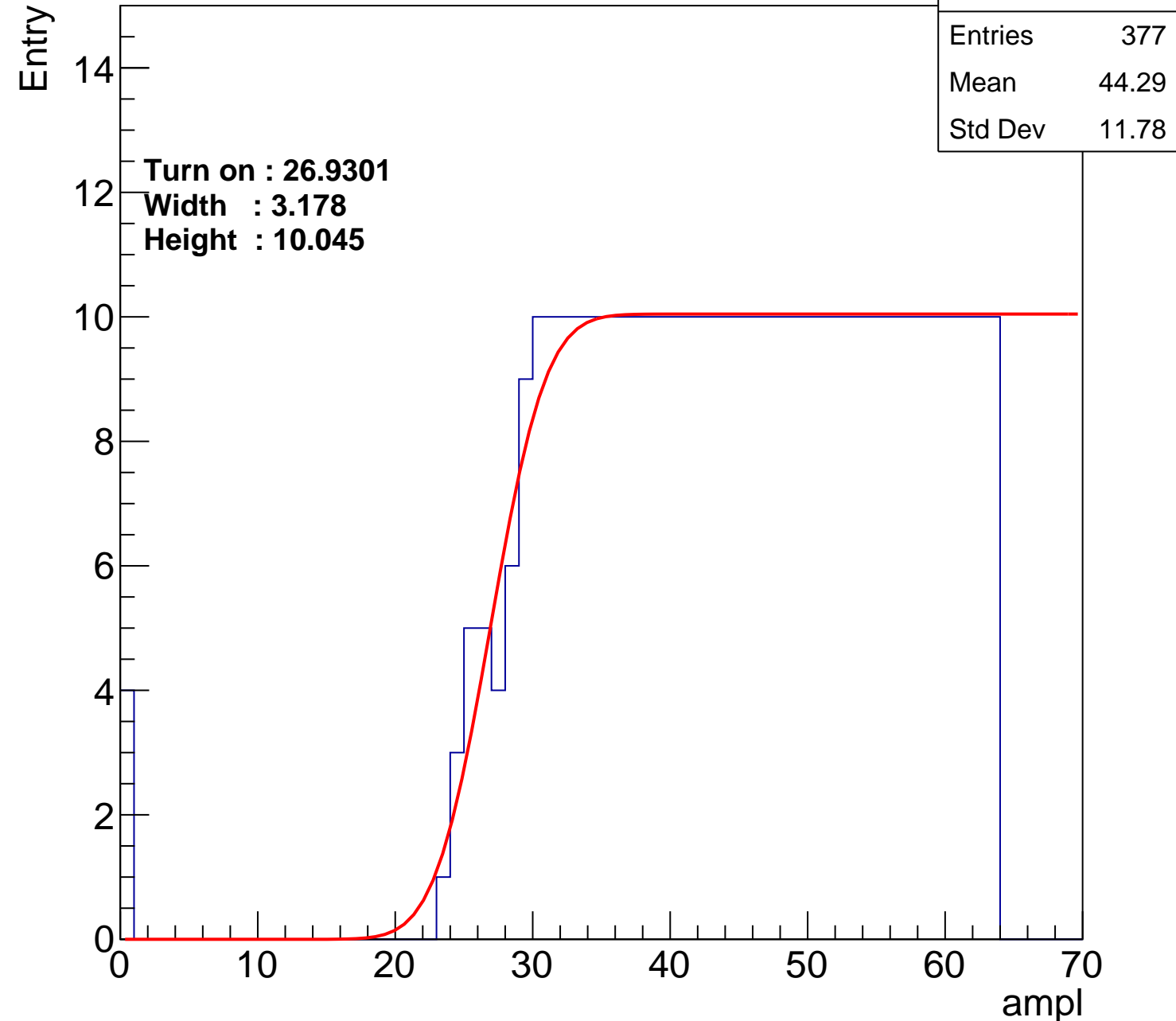
Width : 3.178

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch61

calib\_packv5\_042523\_0143.root, FC#8, port C1

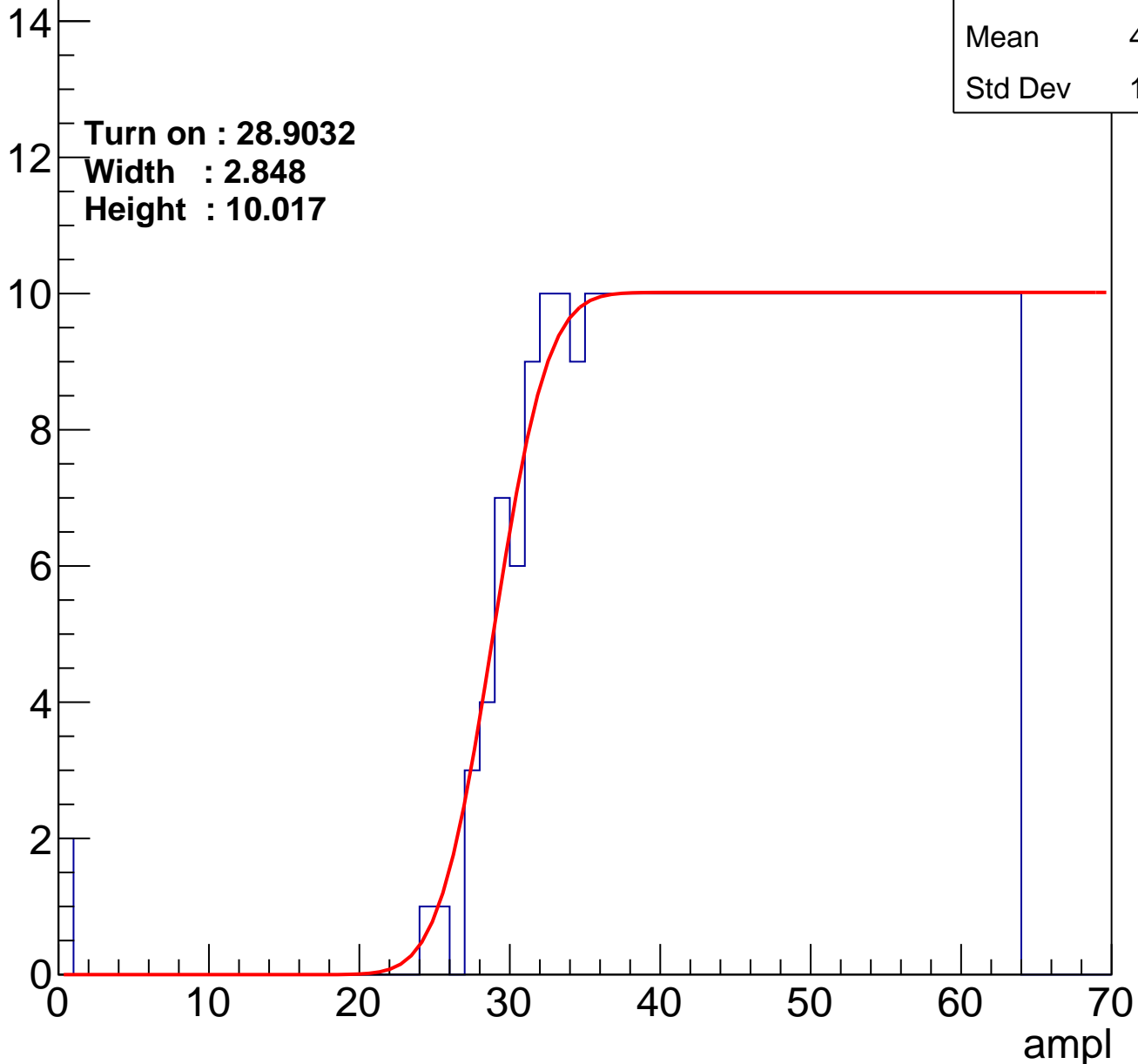
Entries	352
Mean	45.65
Std Dev	10.78

Turn on : 28.9032

Width : 2.848

Height : 10.017

Entry



# B0L002S, U24-ch62

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.14
Std Dev	11.42

**Turn on : 28.9077**

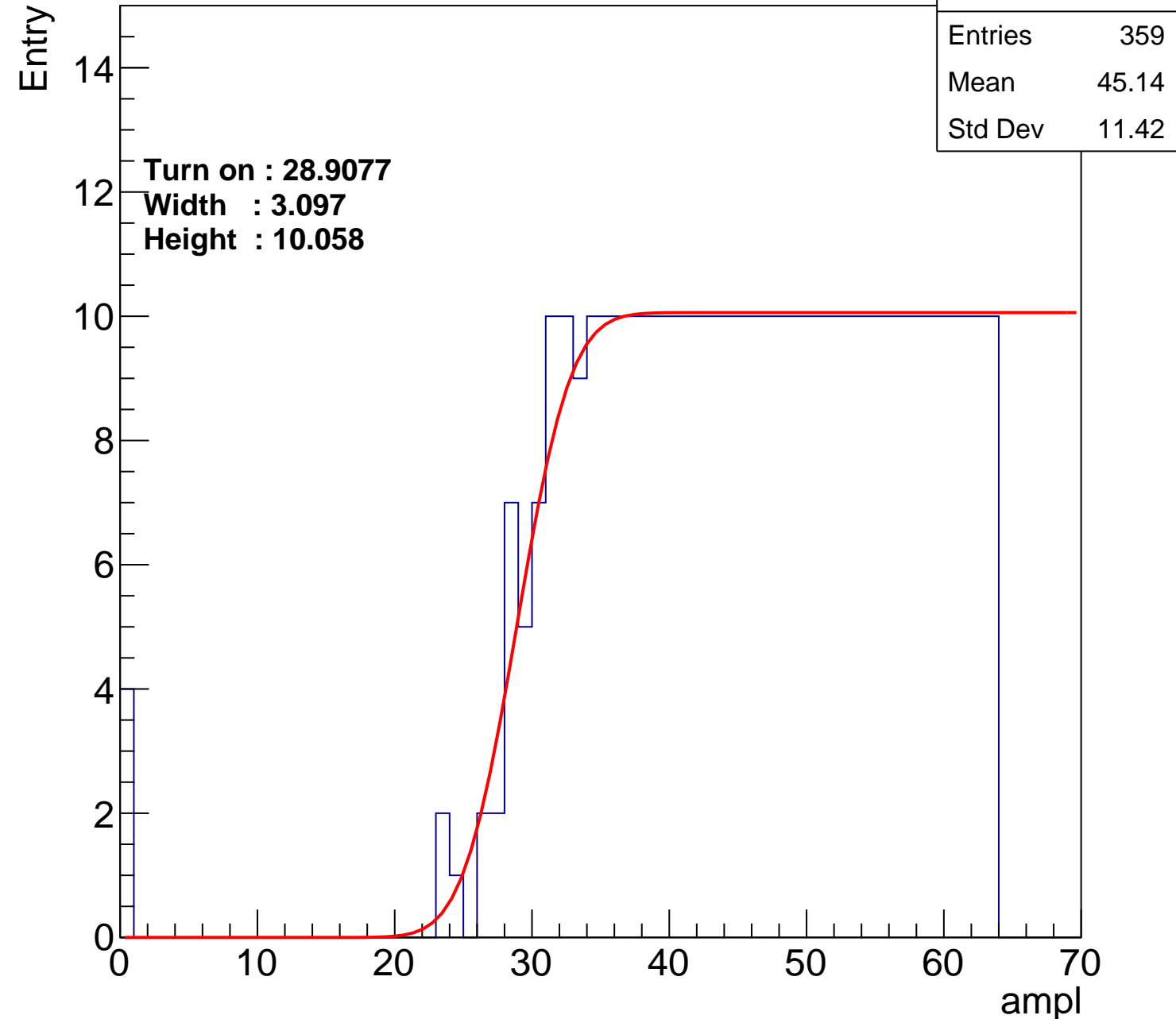
**Width : 3.097**

**Height : 10.058**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch63

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.29
Std Dev	10.83

**Turn on : 27.5353**

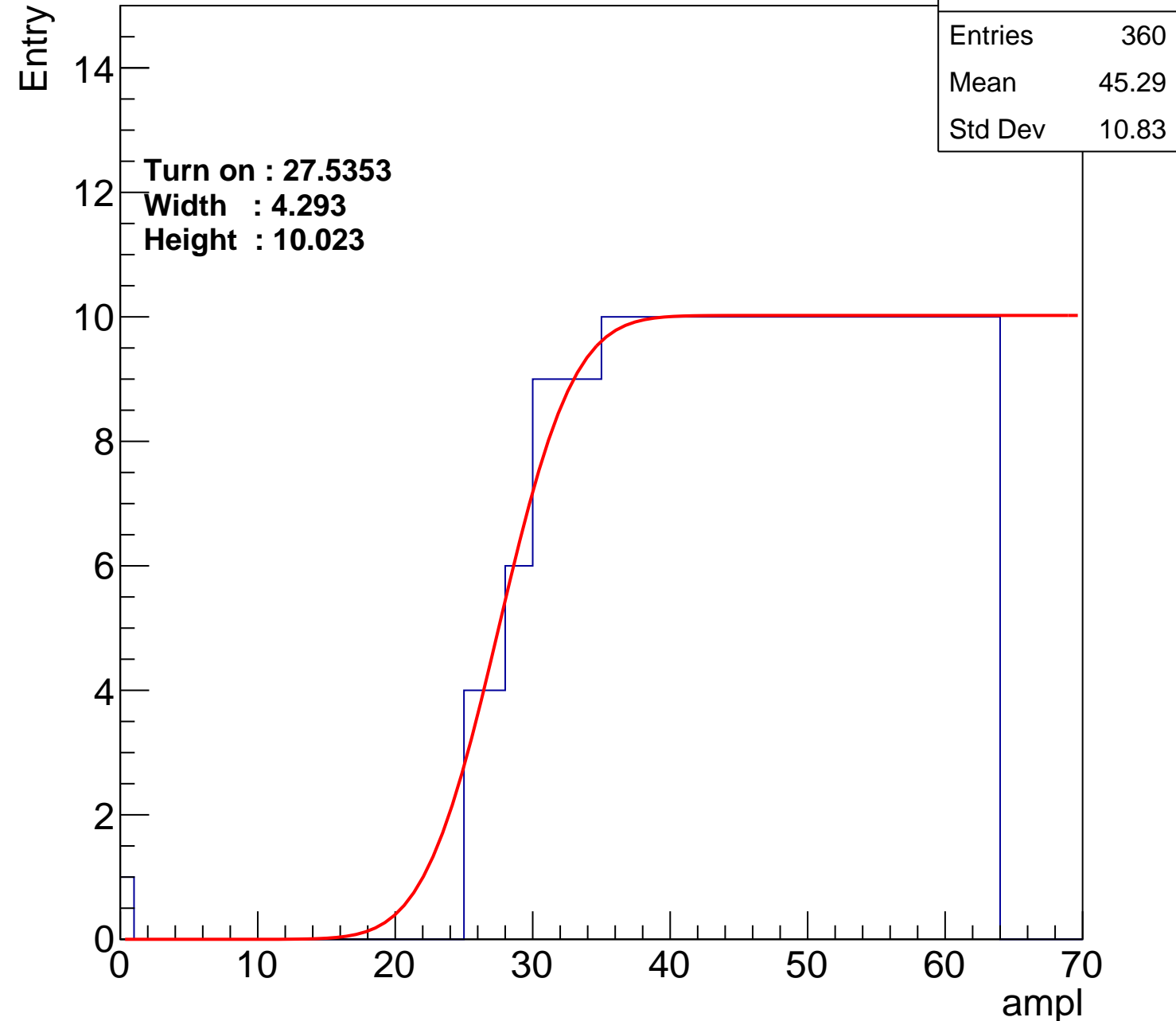
**Width : 4.293**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch64

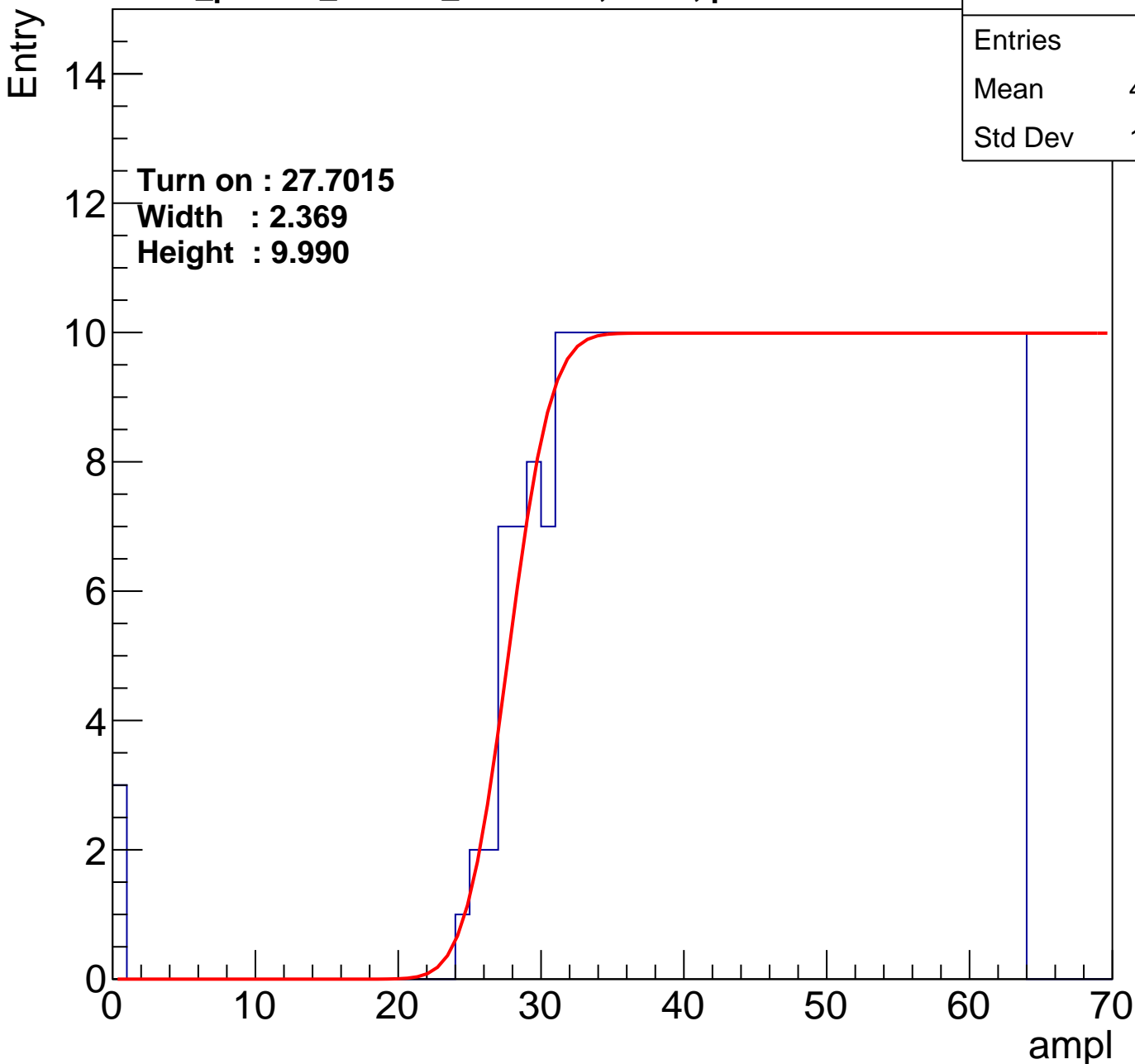
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	367
Mean	44.86
Std Dev	11.34

**Turn on : 27.7015**

**Width : 2.369**

**Height : 9.990**



# B0L002S, U24-ch65

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.1815

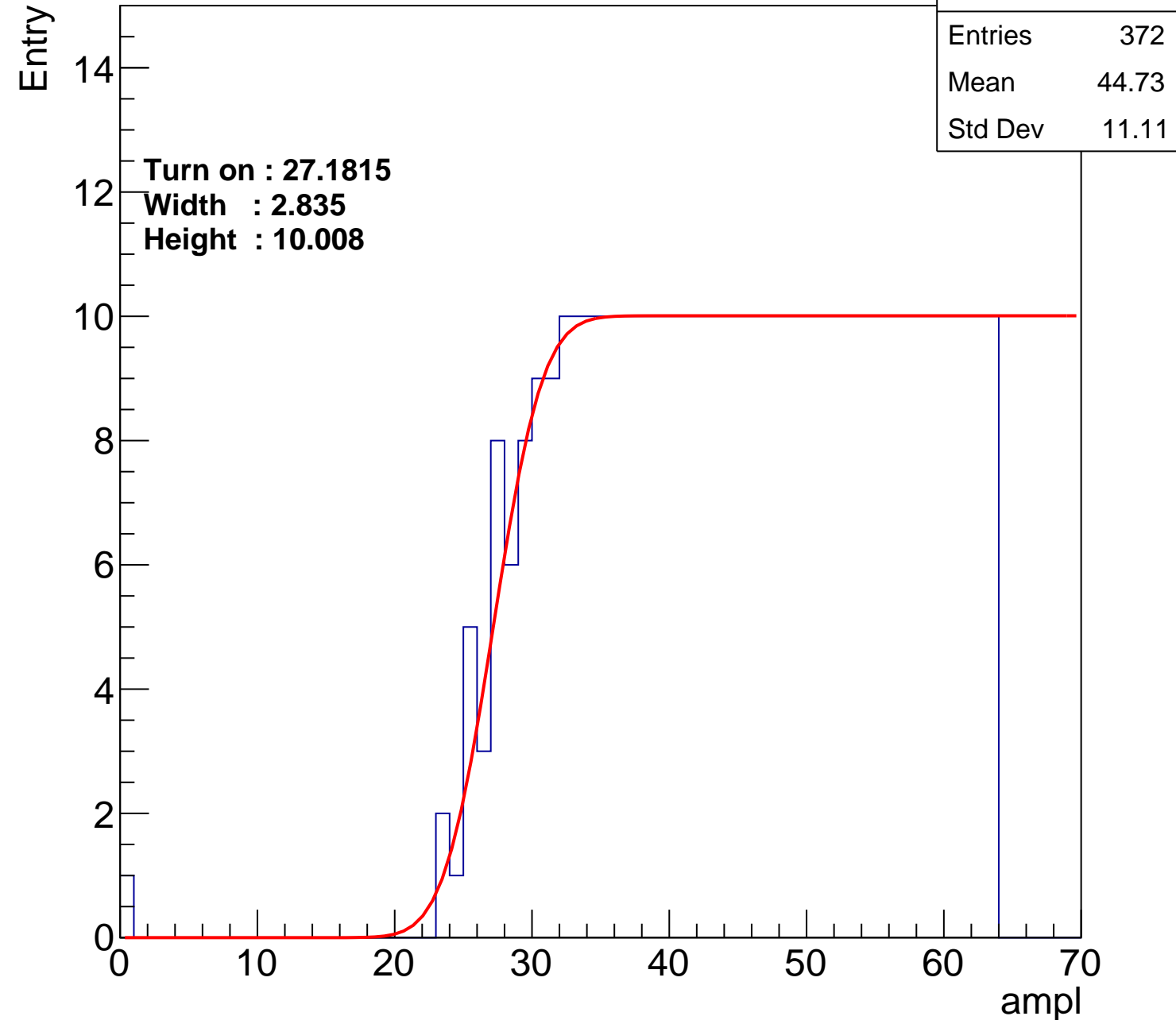
Width : 2.835

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch66

calib\_packv5\_042523\_0143.root, FC#8, port C1

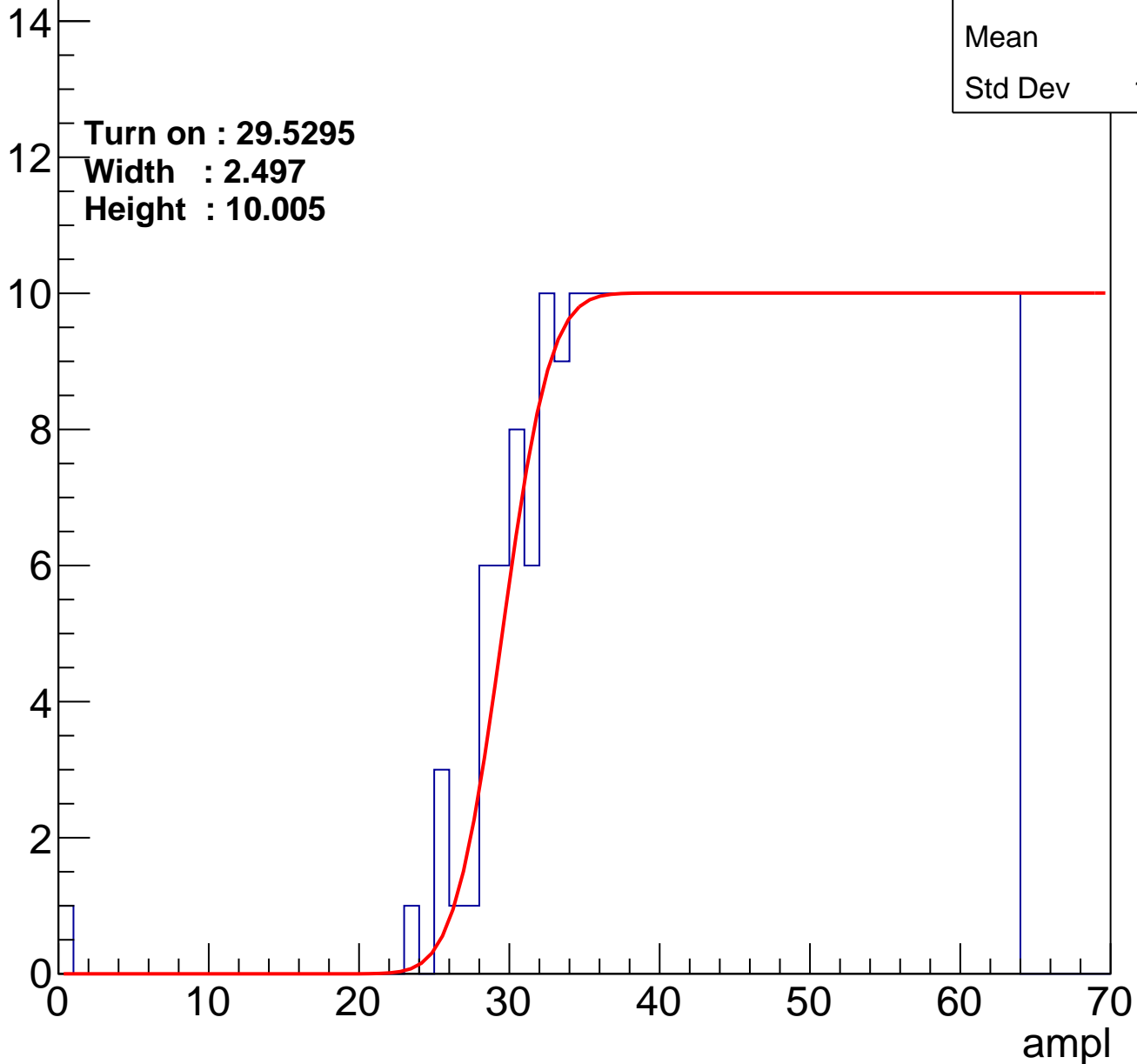
Entries	352
Mean	45.7
Std Dev	10.61

Turn on : 29.5295

Width : 2.497

Height : 10.005

Entry



# B0L002S, U24-ch67

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.02
Std Dev	11.16

Turn on : 28.3832

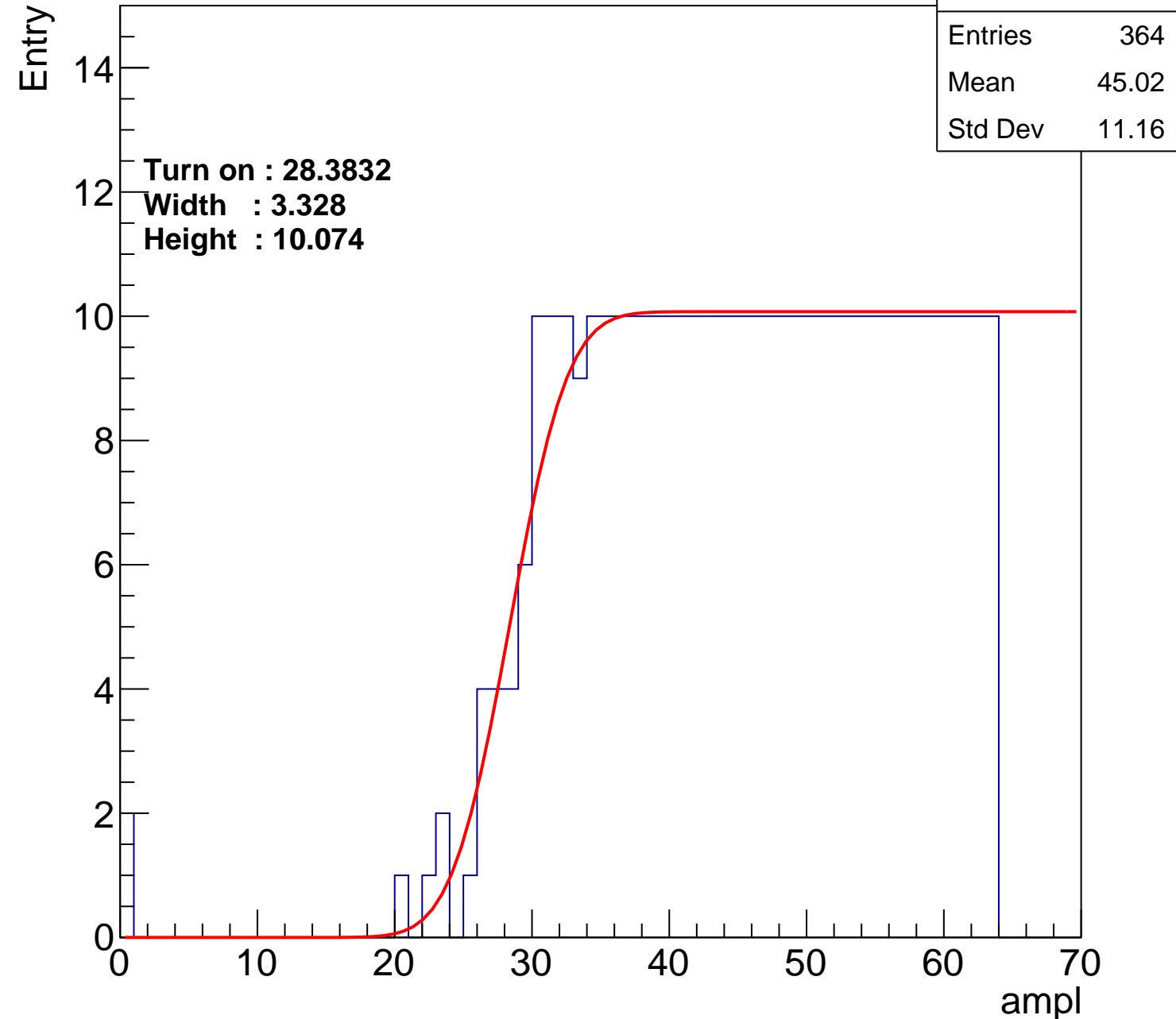
Width : 3.328

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch68

calib\_packv5\_042523\_0143.root, FC#8, port C1

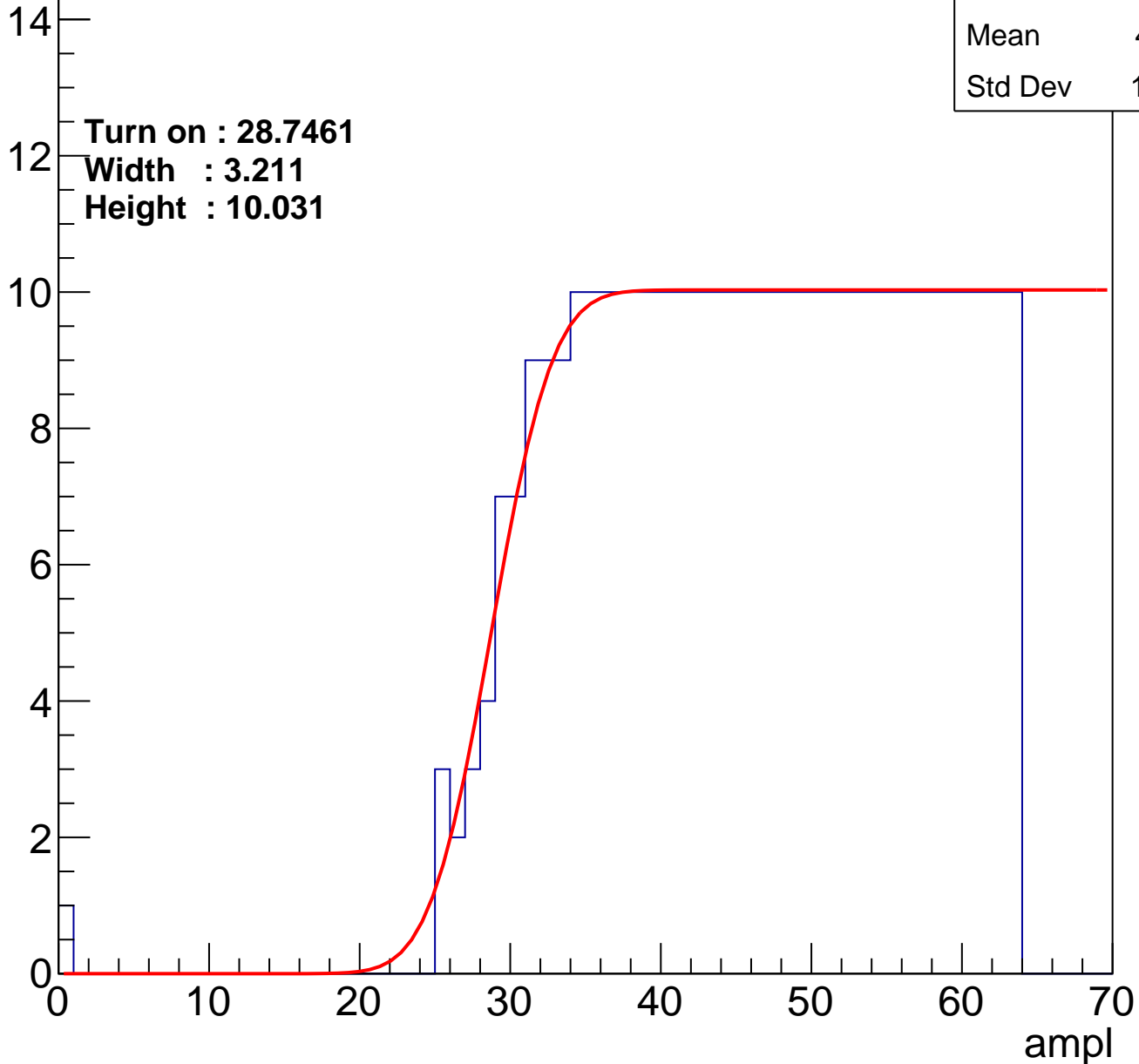
Entries	354
Mean	45.61
Std Dev	10.64

Turn on : 28.7461

Width : 3.211

Height : 10.031

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	371
Mean	44.58
Std Dev	11.64

Entries	371
Mean	44.58
Std Dev	11.64

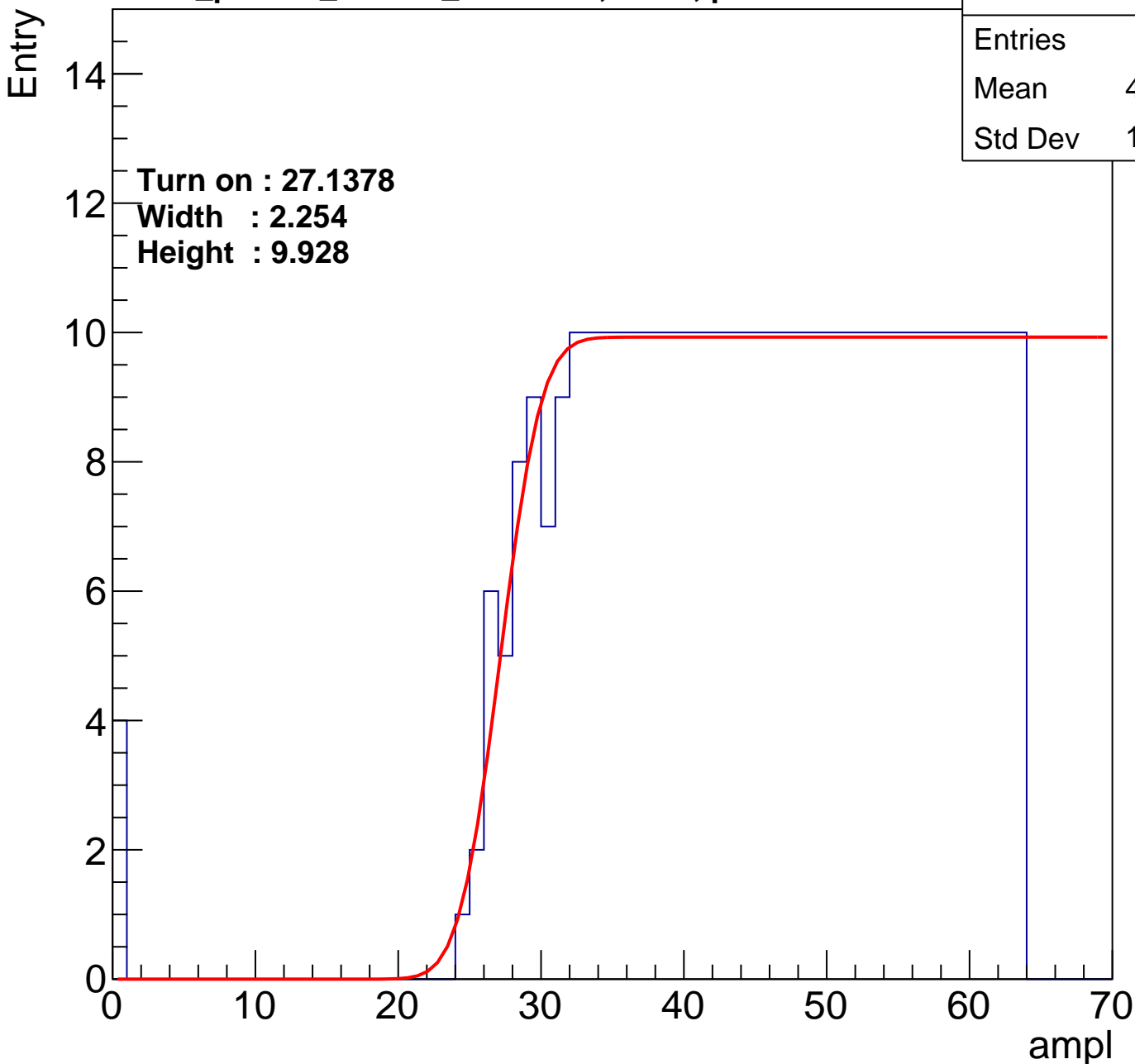
Mean	44.58
------	-------

Std Dev	11.64
---------	-------

**Turn on : 27.1378**

**Width : 2.254**

**Height : 9.928**



# B0L002S, U24-ch70

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	44.88
Std Dev	11.55

**Turn on : 28.3997**

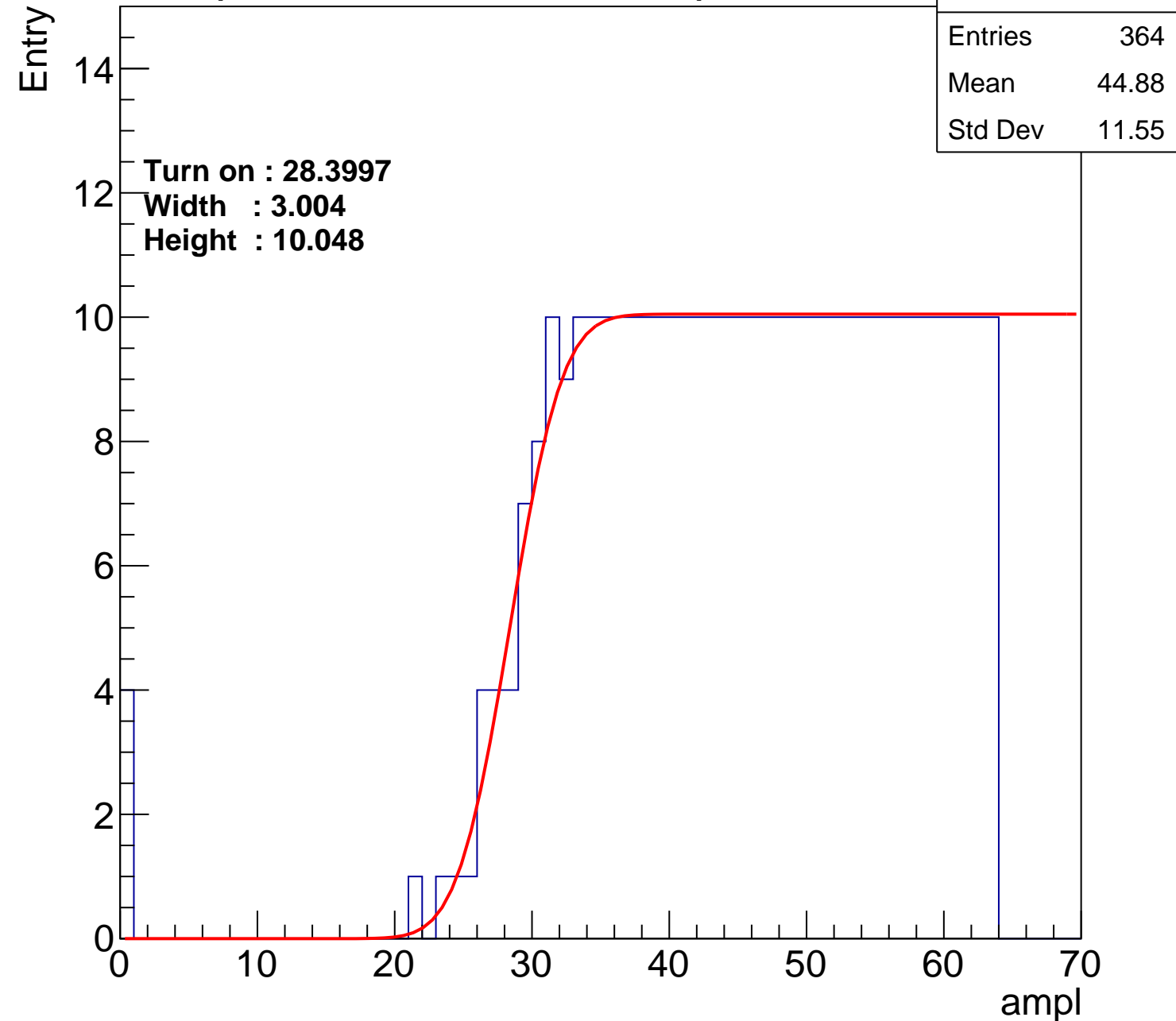
**Width : 3.004**

**Height : 10.048**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch71

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.2
Std Dev	10.87

Turn on : 28.0577

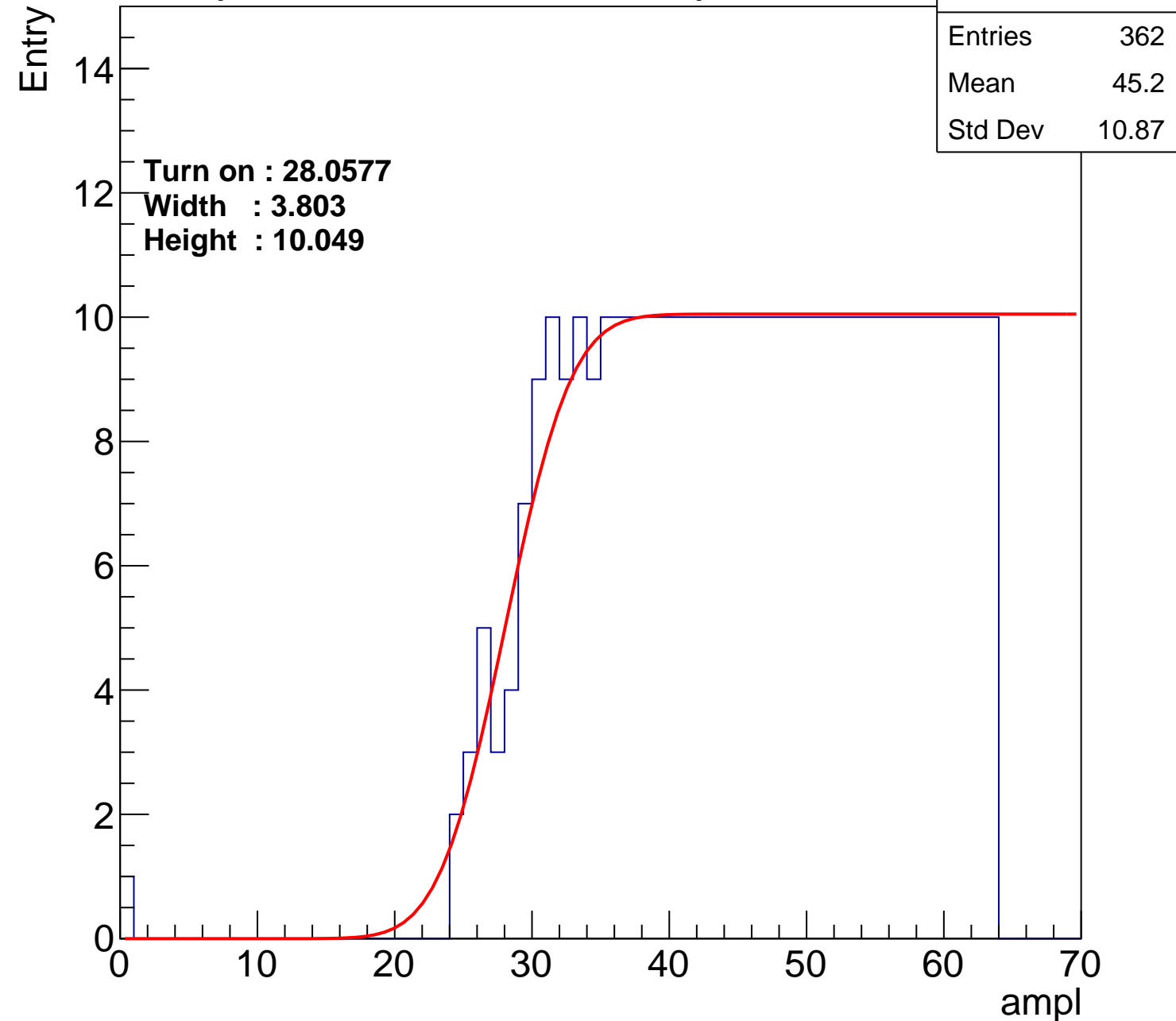
Width : 3.803

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch72

calib\_packv5\_042523\_0143.root, FC#8, port C1

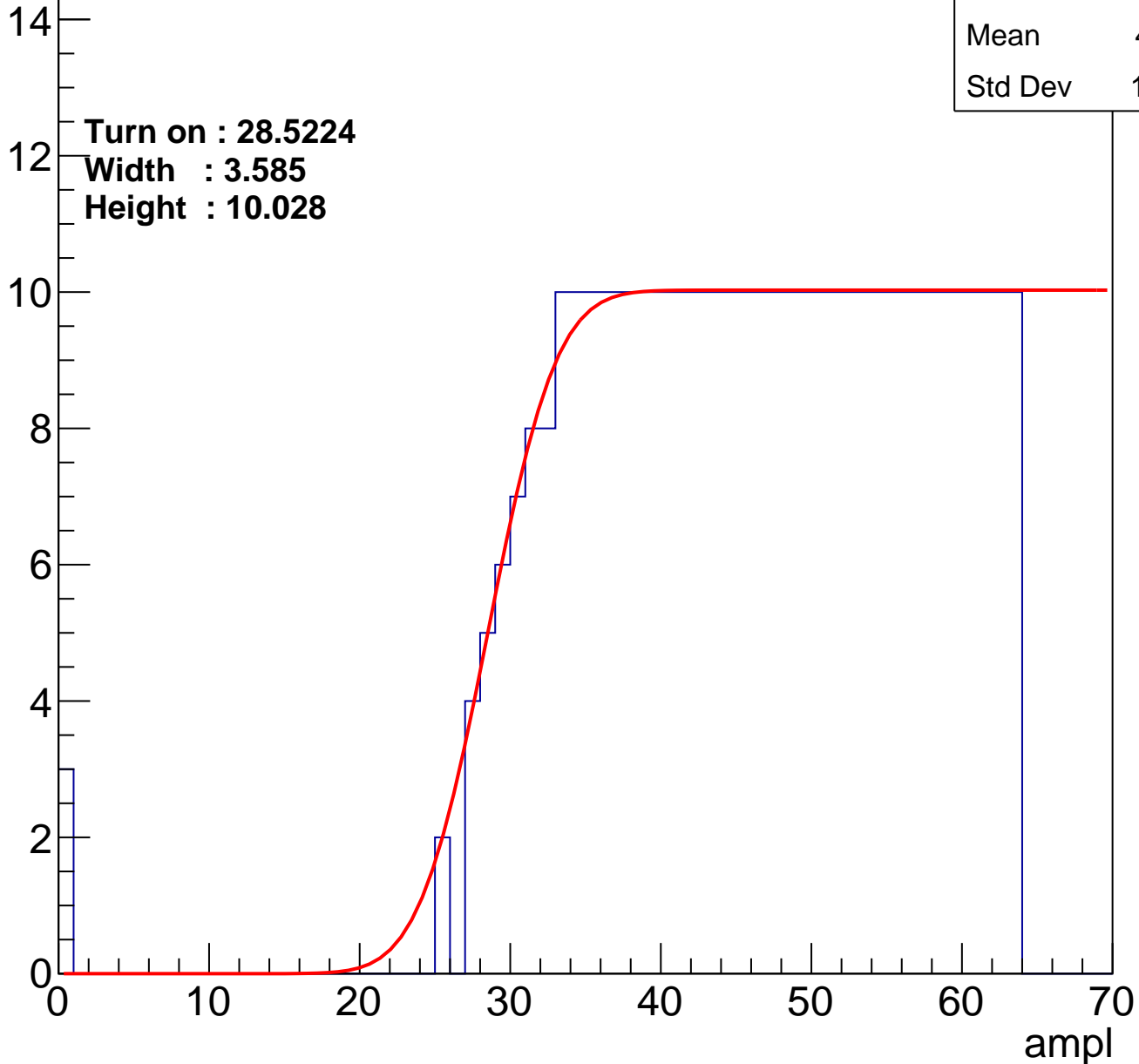
Entries	353
Mean	45.51
Std Dev	11.06

Turn on : 28.5224

Width : 3.585

Height : 10.028

Entry



# B0L002S, U24-ch73

calib\_packv5\_042523\_0143.root, FC#8, port C1

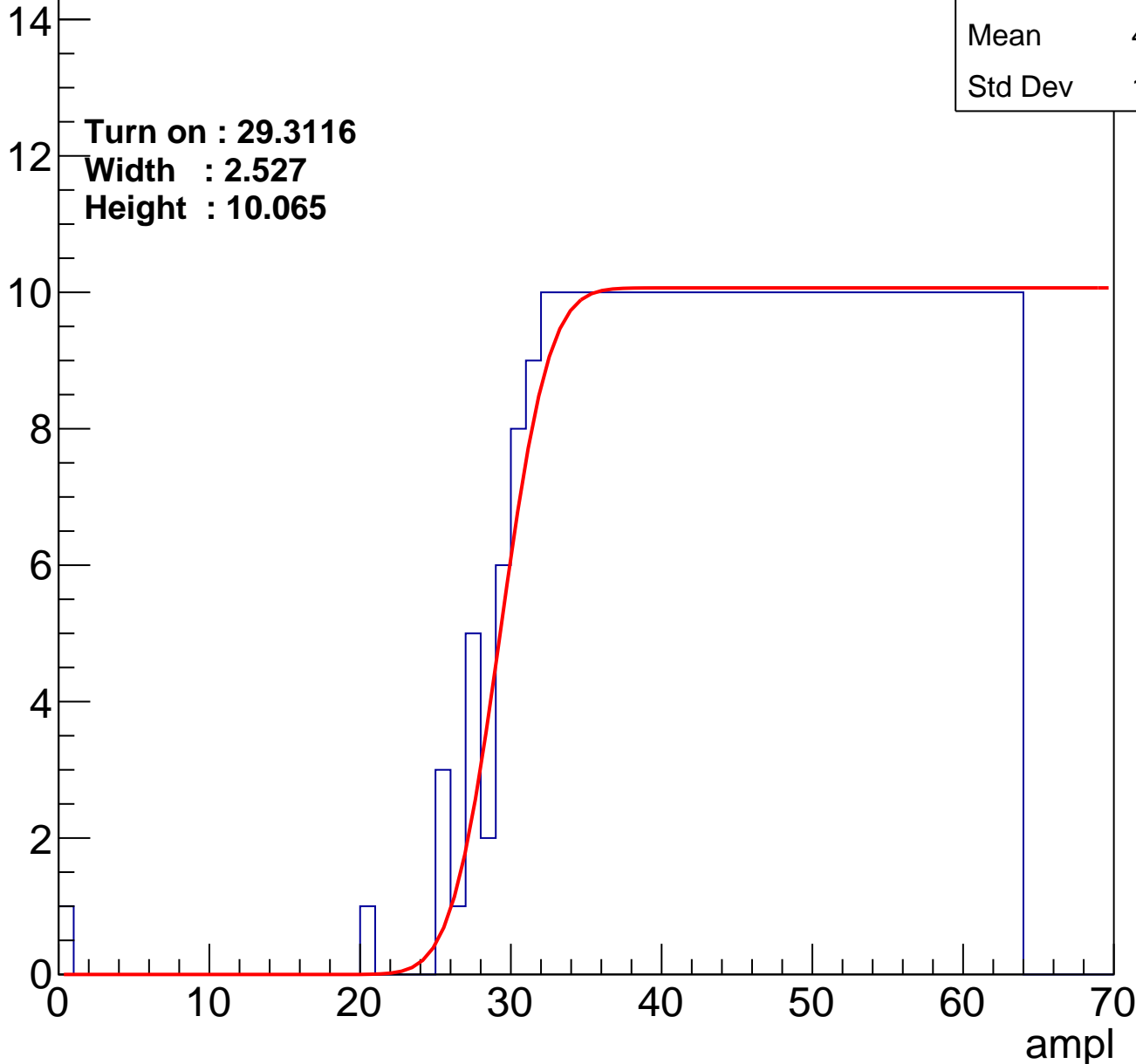
Entries	356
Mean	45.52
Std Dev	10.69

**Turn on : 29.3116**

**Width : 2.527**

**Height : 10.065**

Entry



# B0L002S, U24-ch74

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.36
Std Dev	10.79

**Turn on : 28.7633**

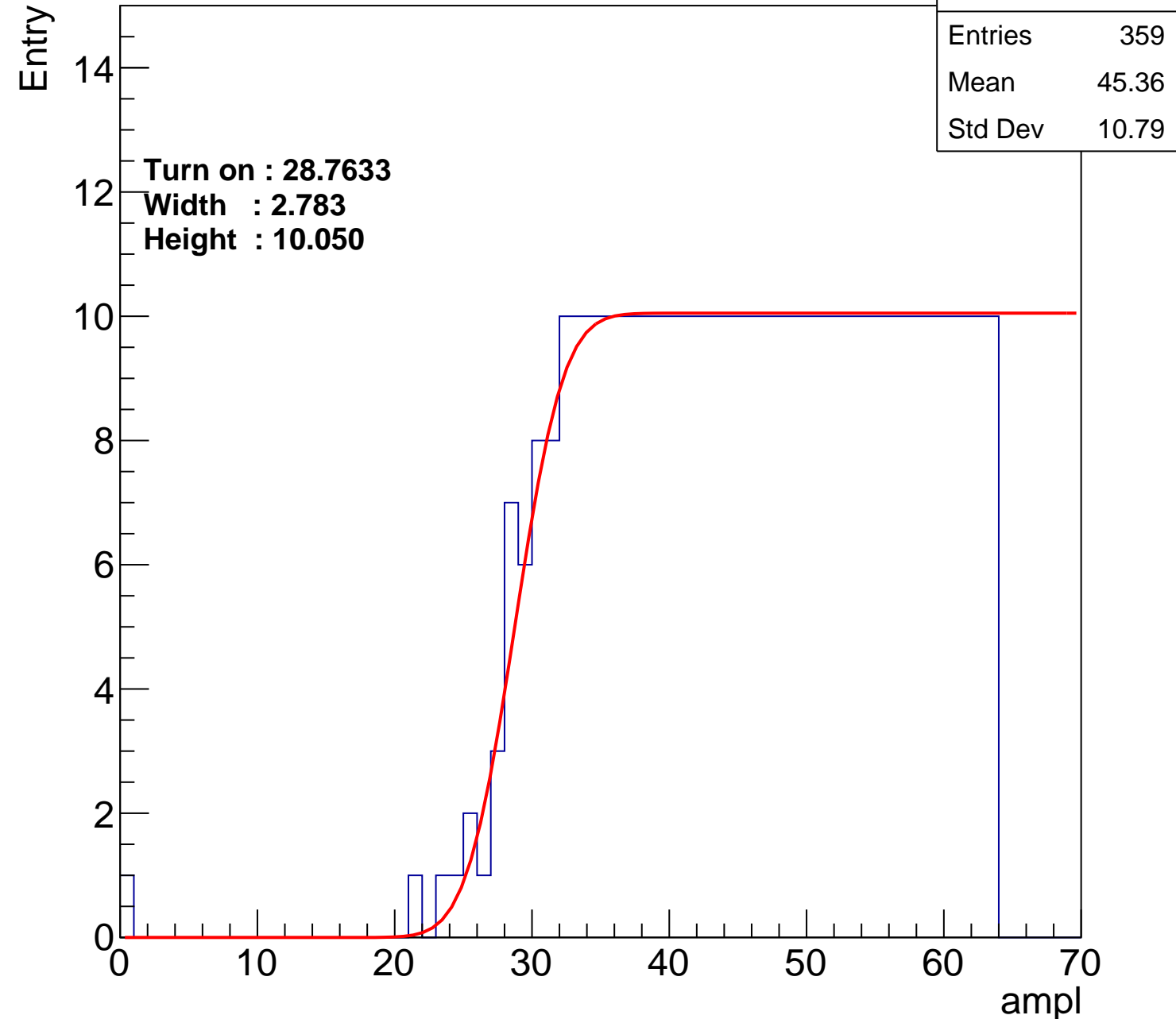
**Width : 2.783**

**Height : 10.050**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch75

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 25.5769

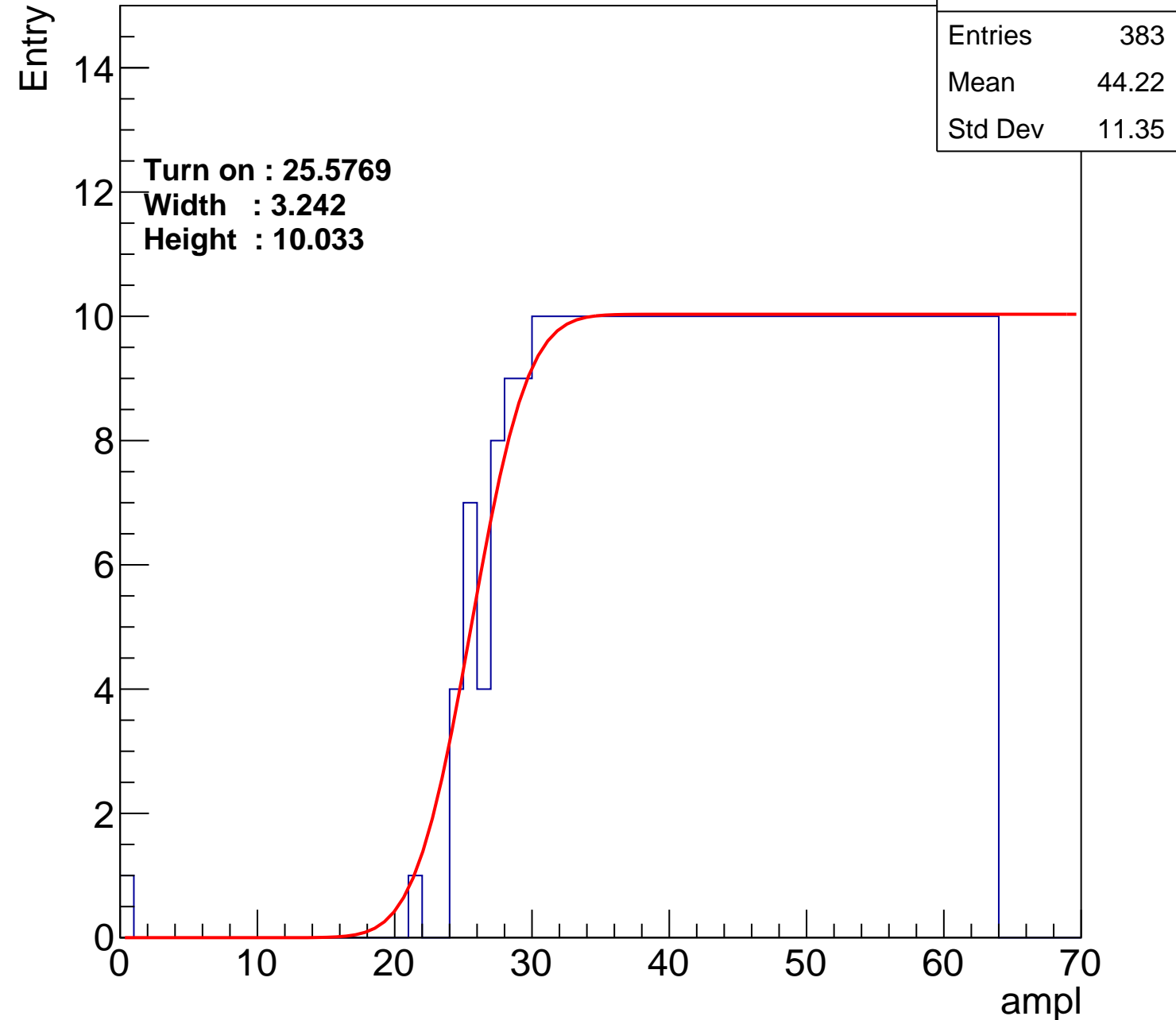
Width : 3.242

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch76

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.33
Std Dev	11.34

**Turn on : 26.7907**

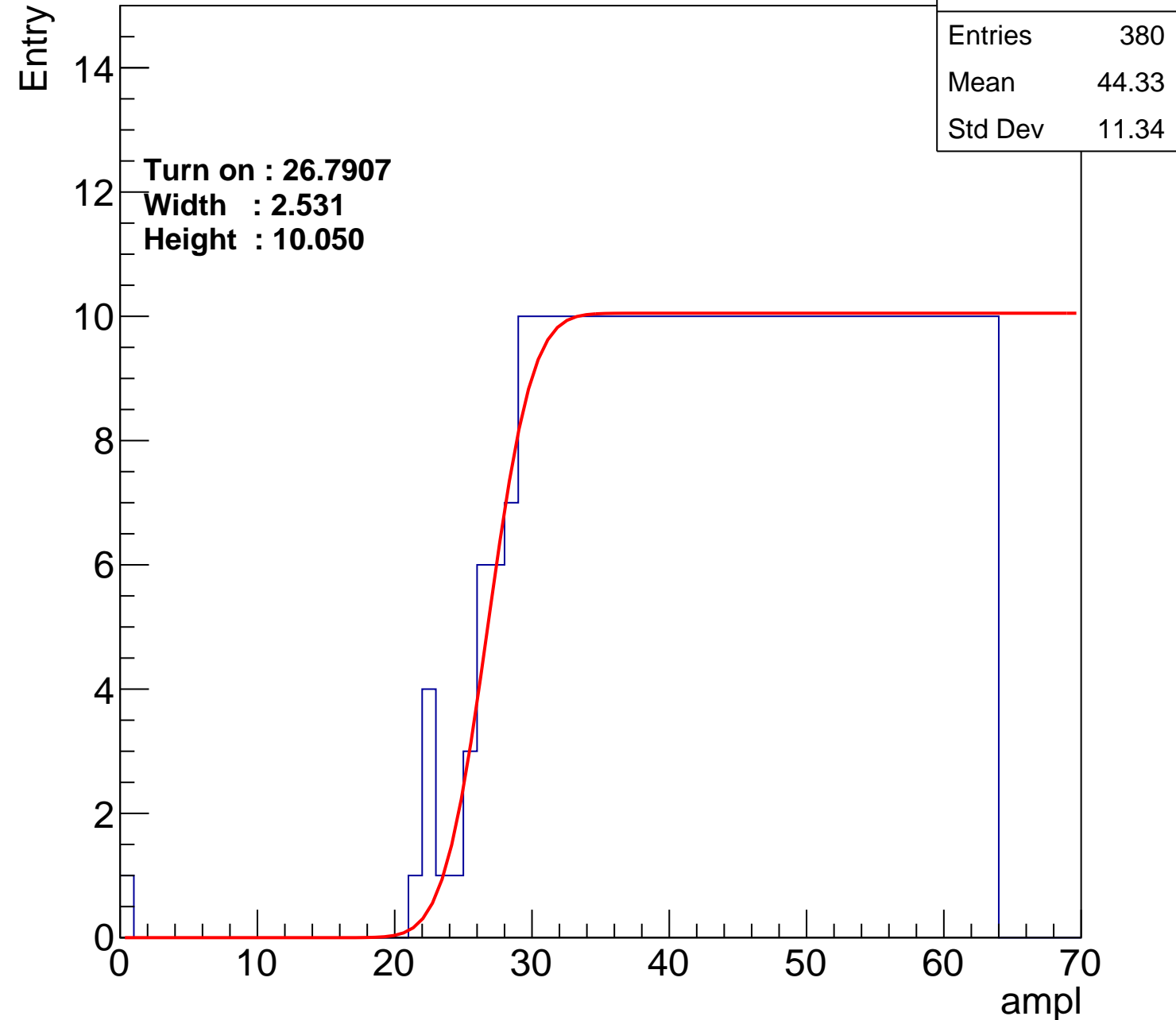
**Width : 2.531**

**Height : 10.050**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch77

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.57
Std Dev	10.87

Turn on : 29.1829

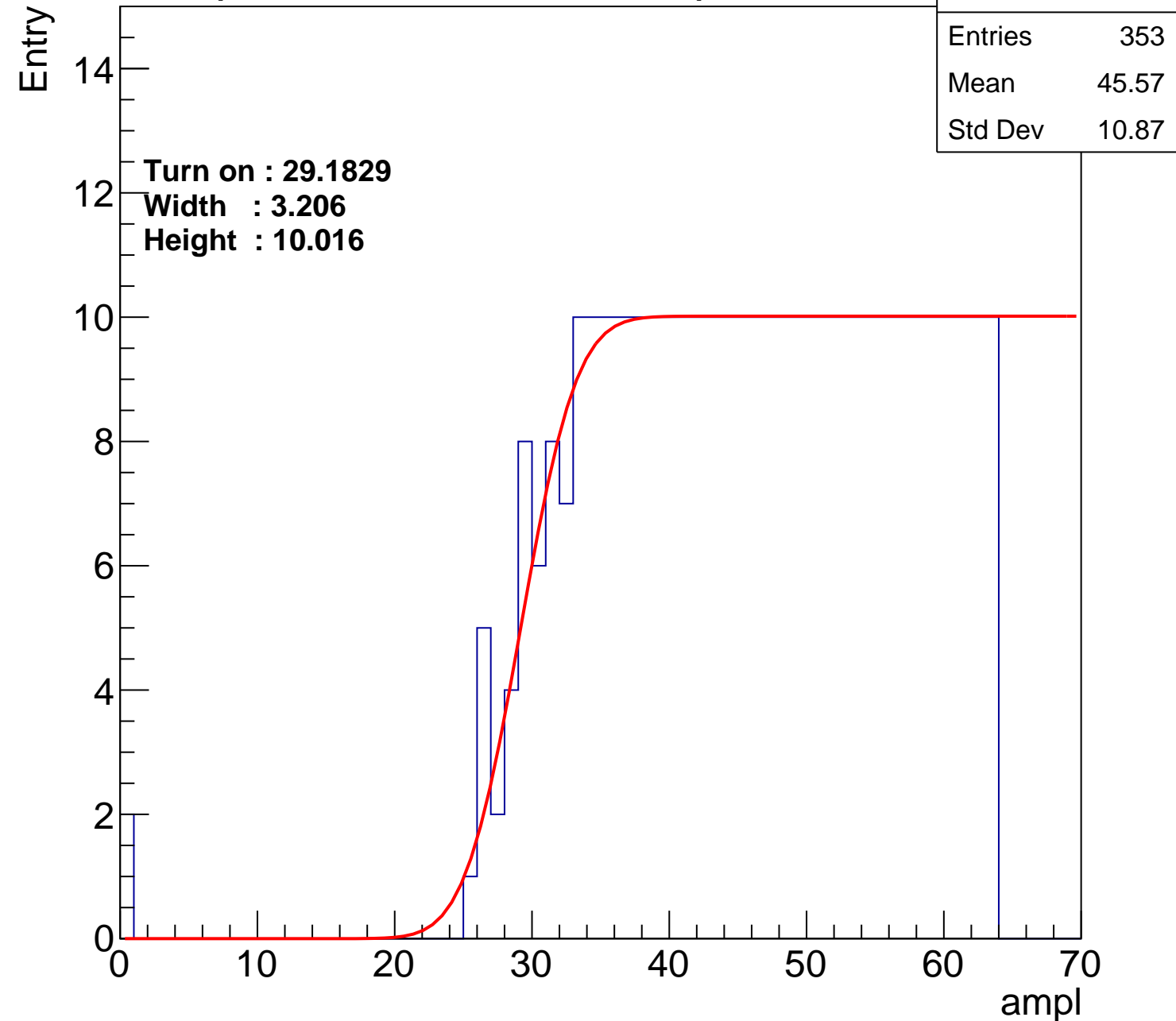
Width : 3.206

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch78

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	343
Mean	45.87
Std Dev	11.14

Turn on : 30.5422

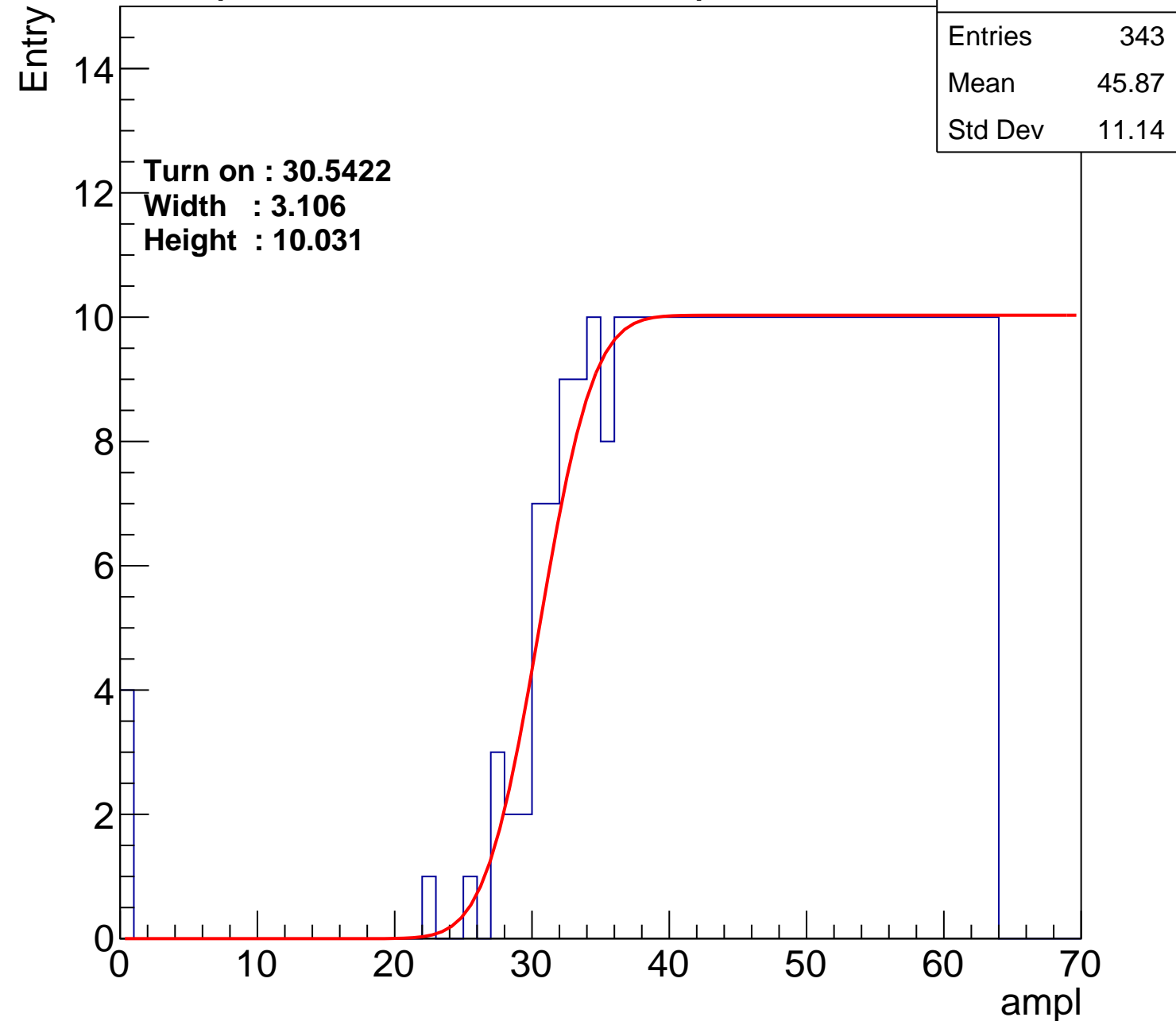
Width : 3.106

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch79

calib\_packv5\_042523\_0143.root, FC#8, port C1

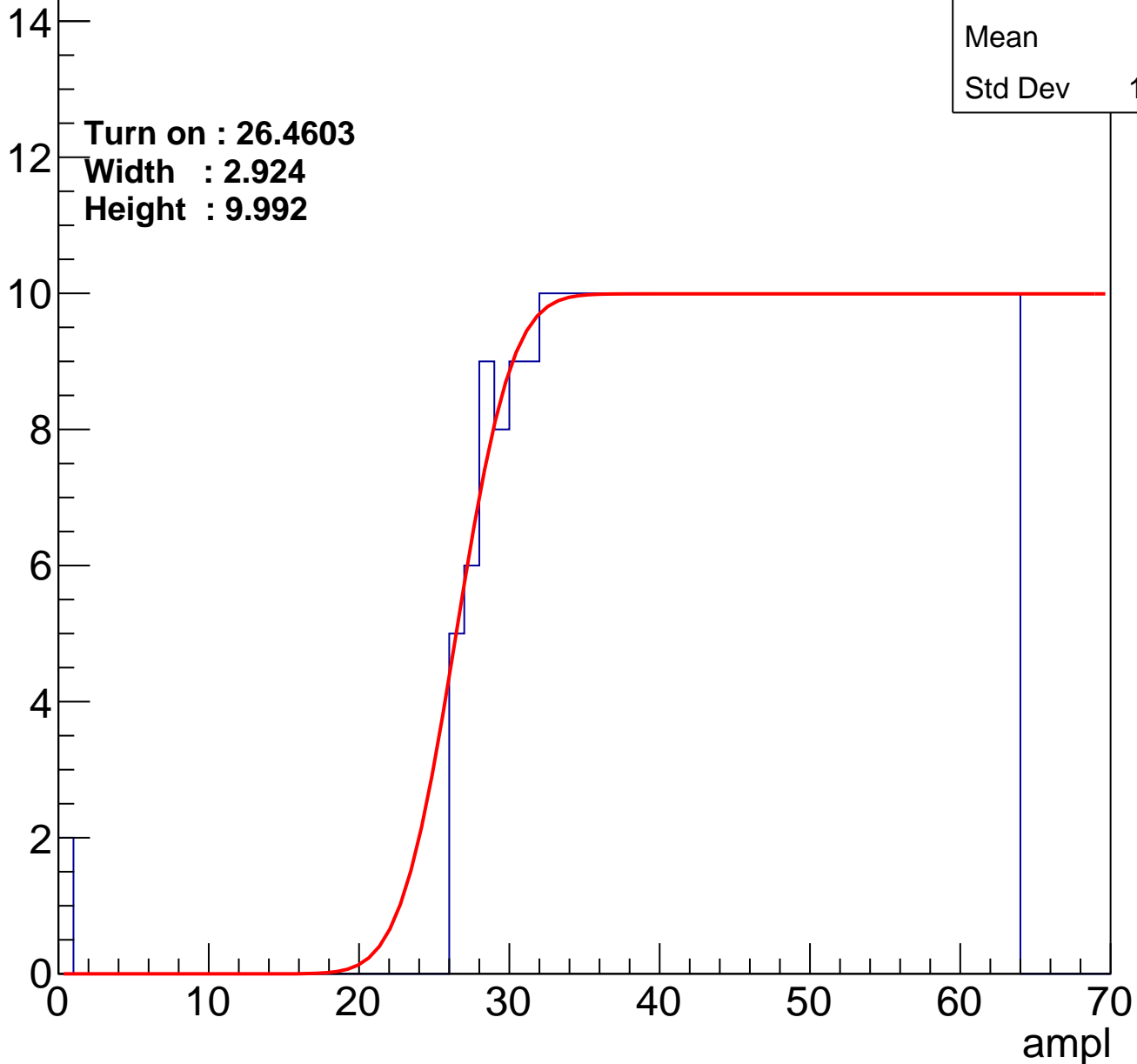
Entries	368
Mean	44.9
Std Dev	11.12

**Turn on : 26.4603**

**Width : 2.924**

**Height : 9.992**

Entry



# B0L002S, U24-ch80

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 27.2386

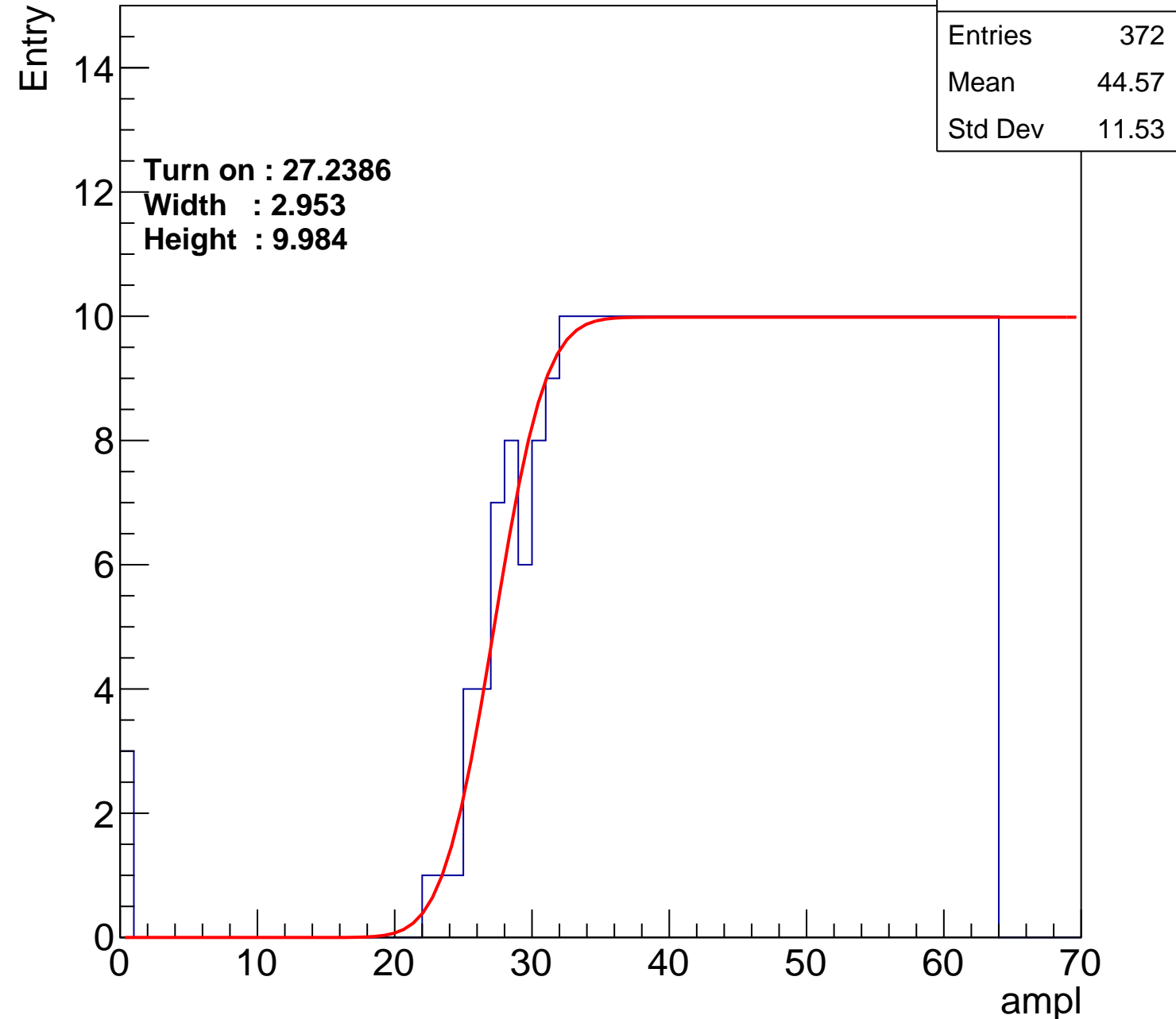
Width : 2.953

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch81

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	348
Mean	45.65
Std Dev	11.21

Turn on : 30.3567

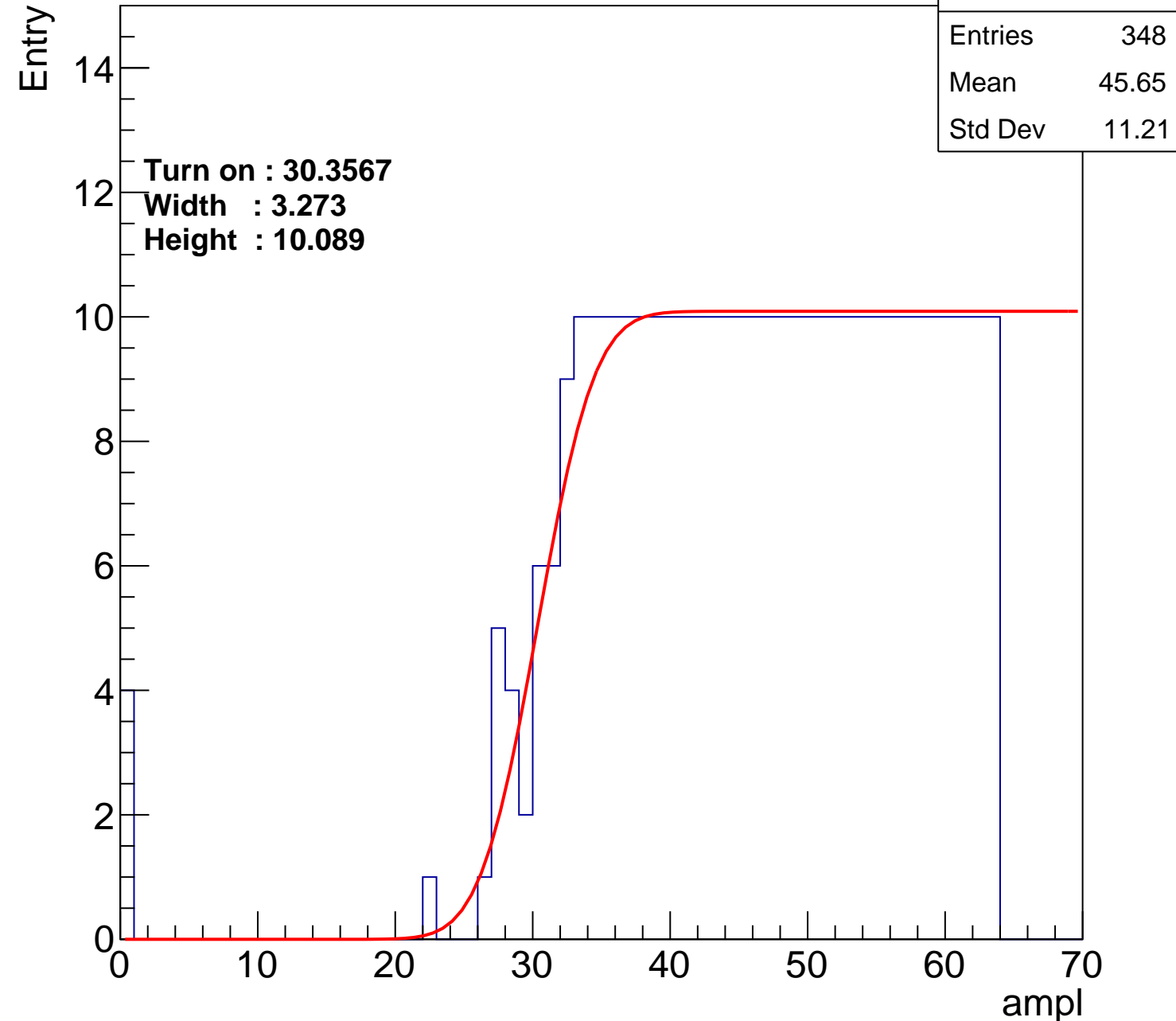
Width : 3.273

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch82

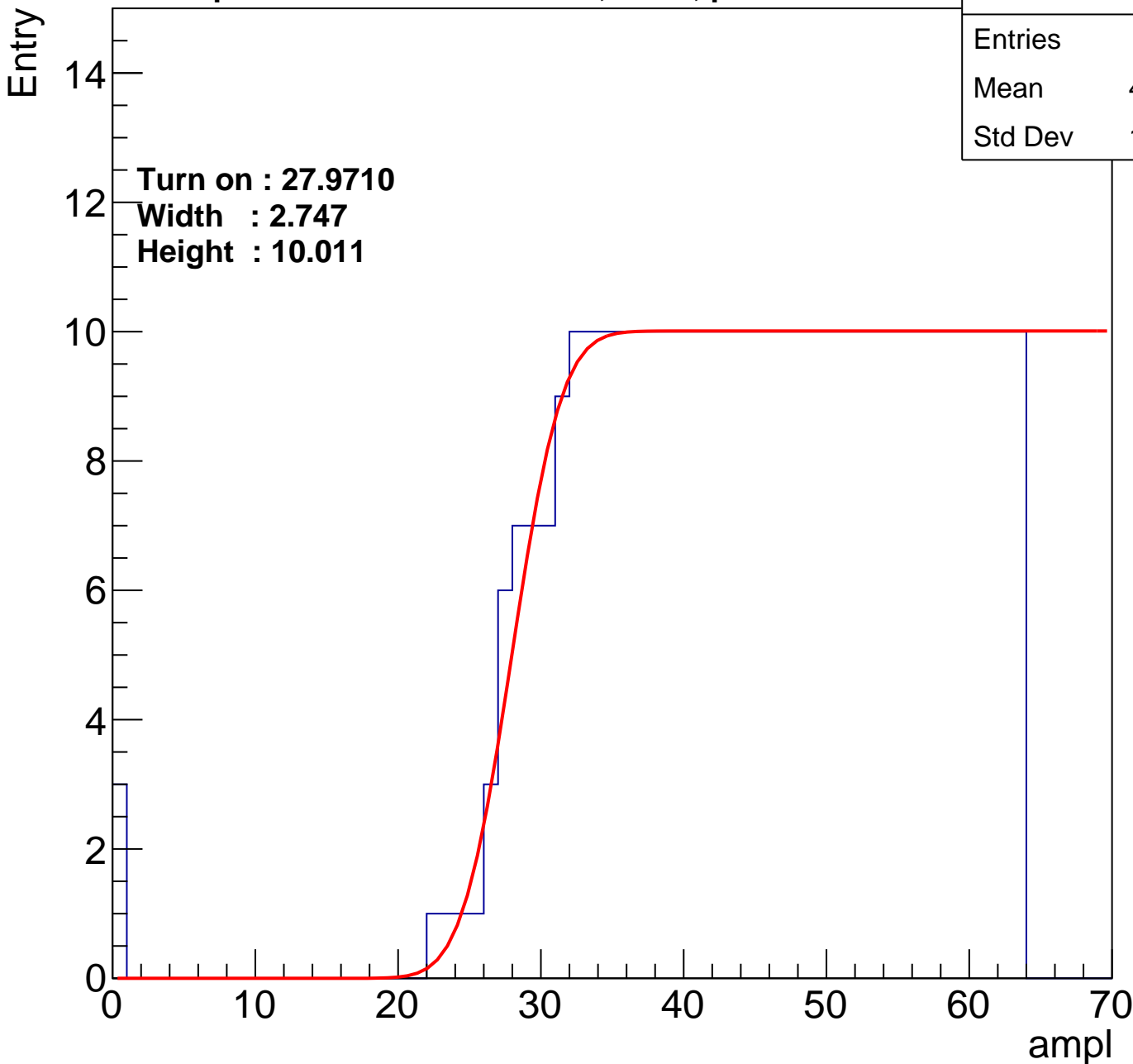
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	366
Mean	44.87
Std Dev	11.38

**Turn on : 27.9710**

**Width : 2.747**

**Height : 10.011**



# B0L002S, U24-ch83

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.51
Std Dev	12.01

Turn on : 27.4720

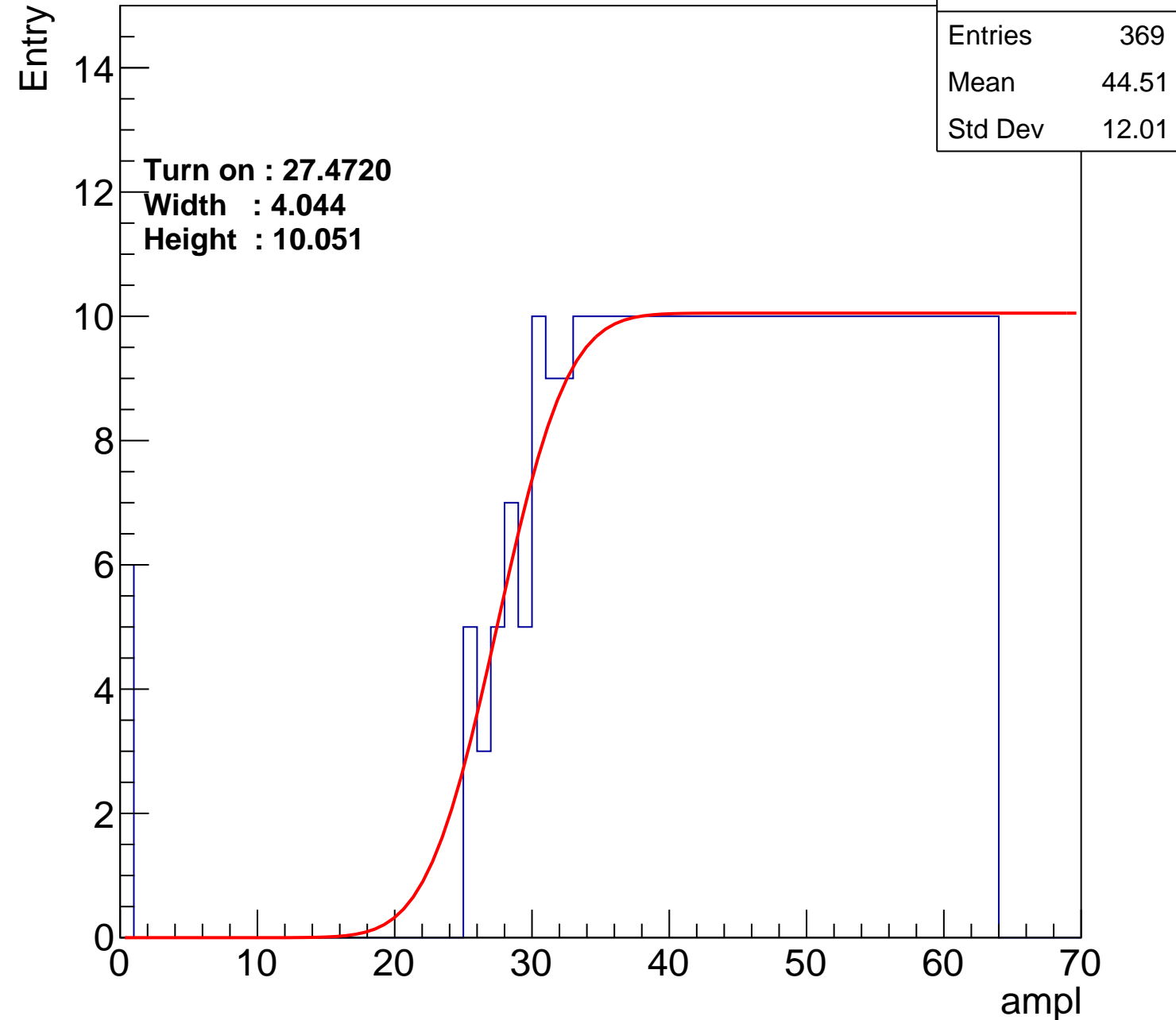
Width : 4.044

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch84

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.4962

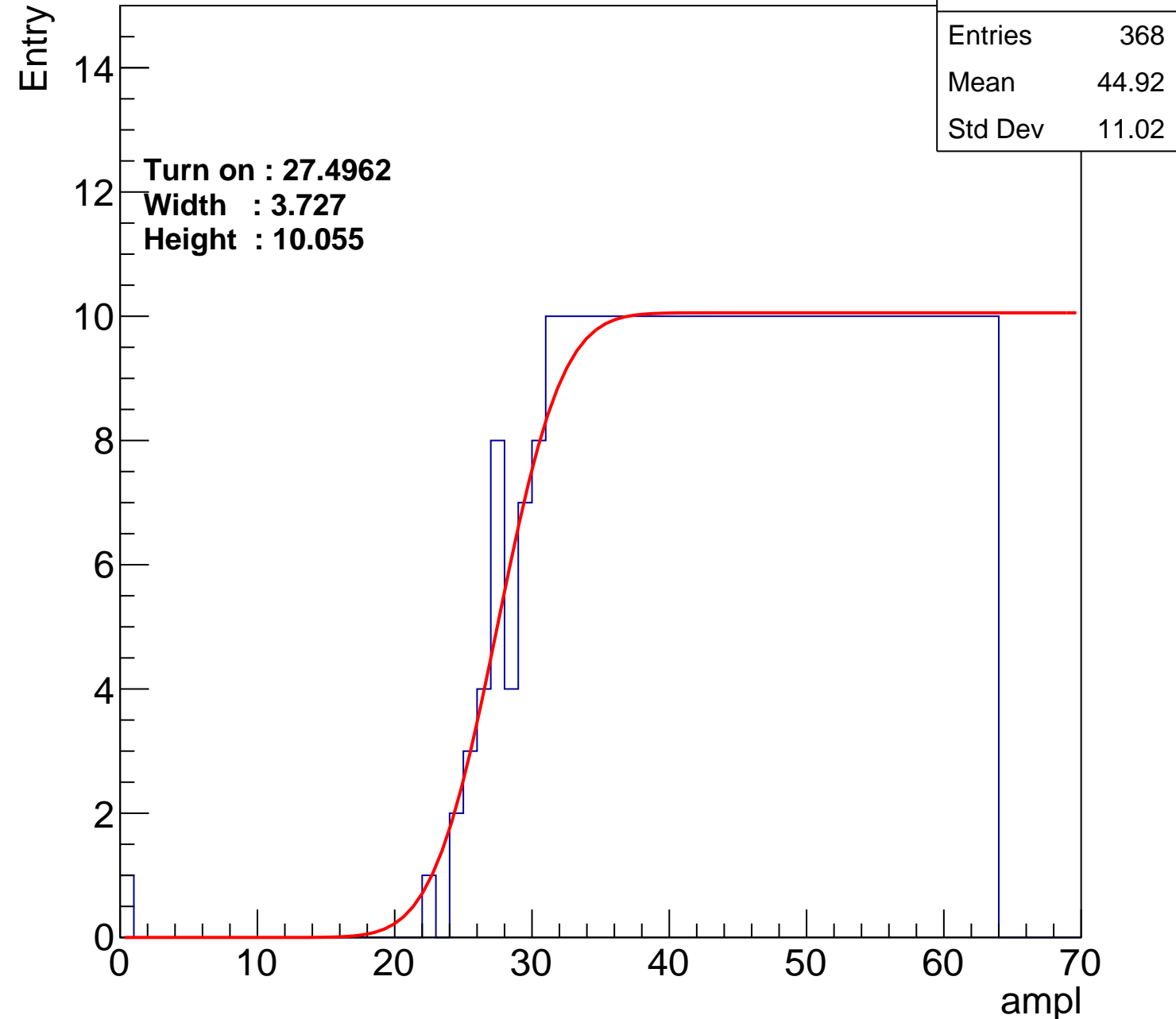
Width : 3.727

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch85

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.7620

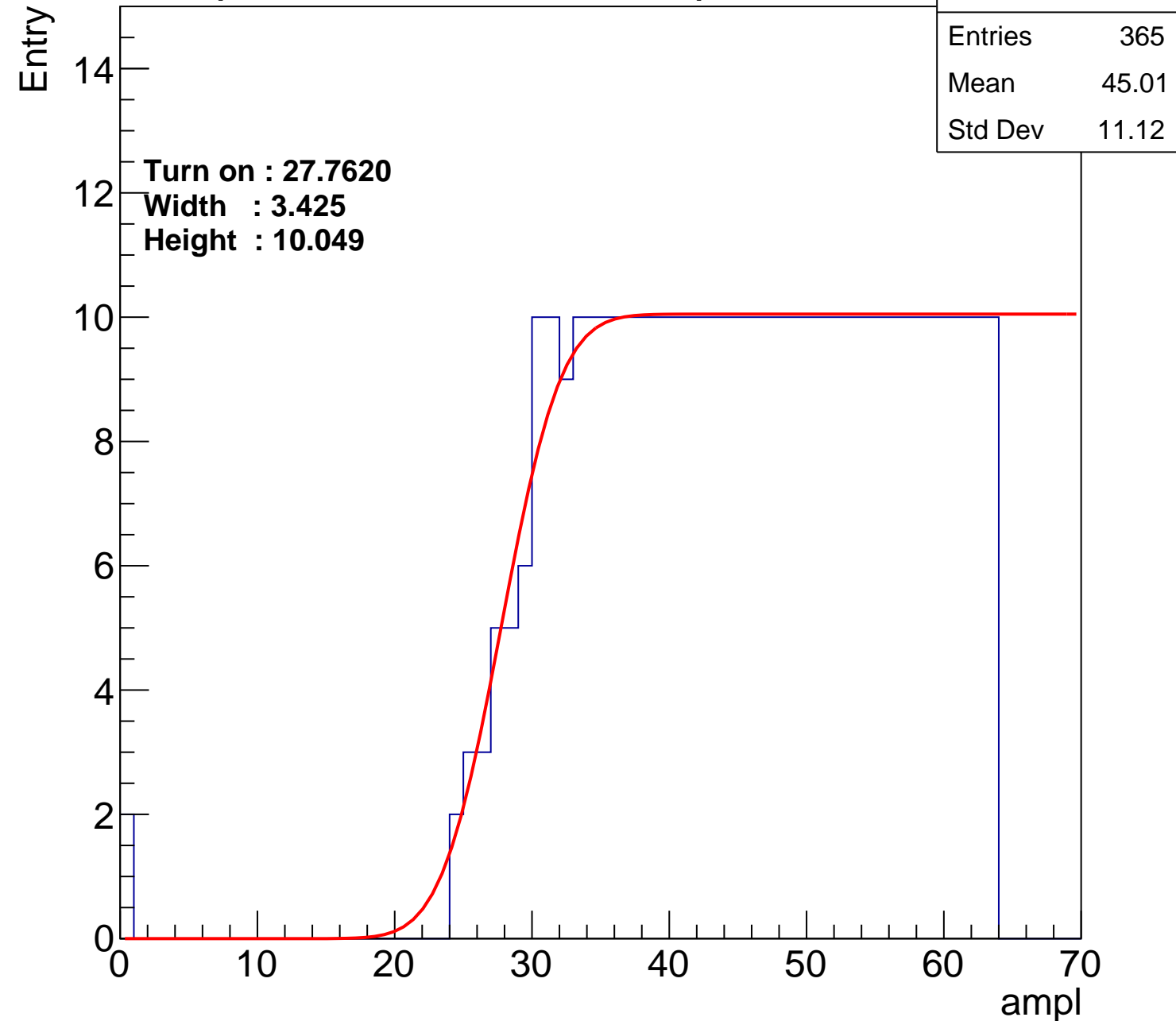
Width : 3.425

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch86

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.69
Std Dev	11.26

**Turn on : 27.3855**

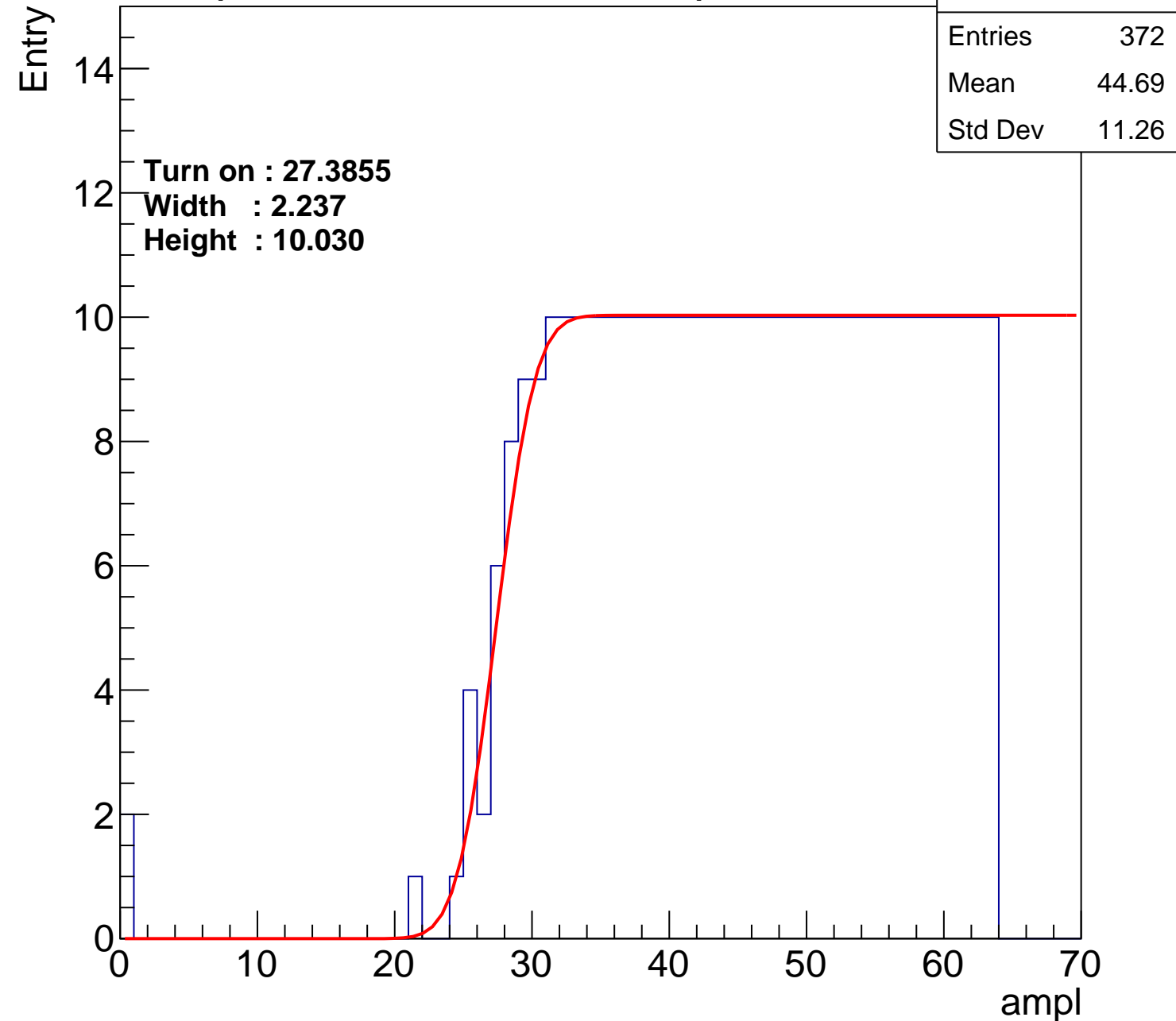
**Width : 2.237**

**Height : 10.030**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch87

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.8
Std Dev	11.17

Turn on : 28.1988

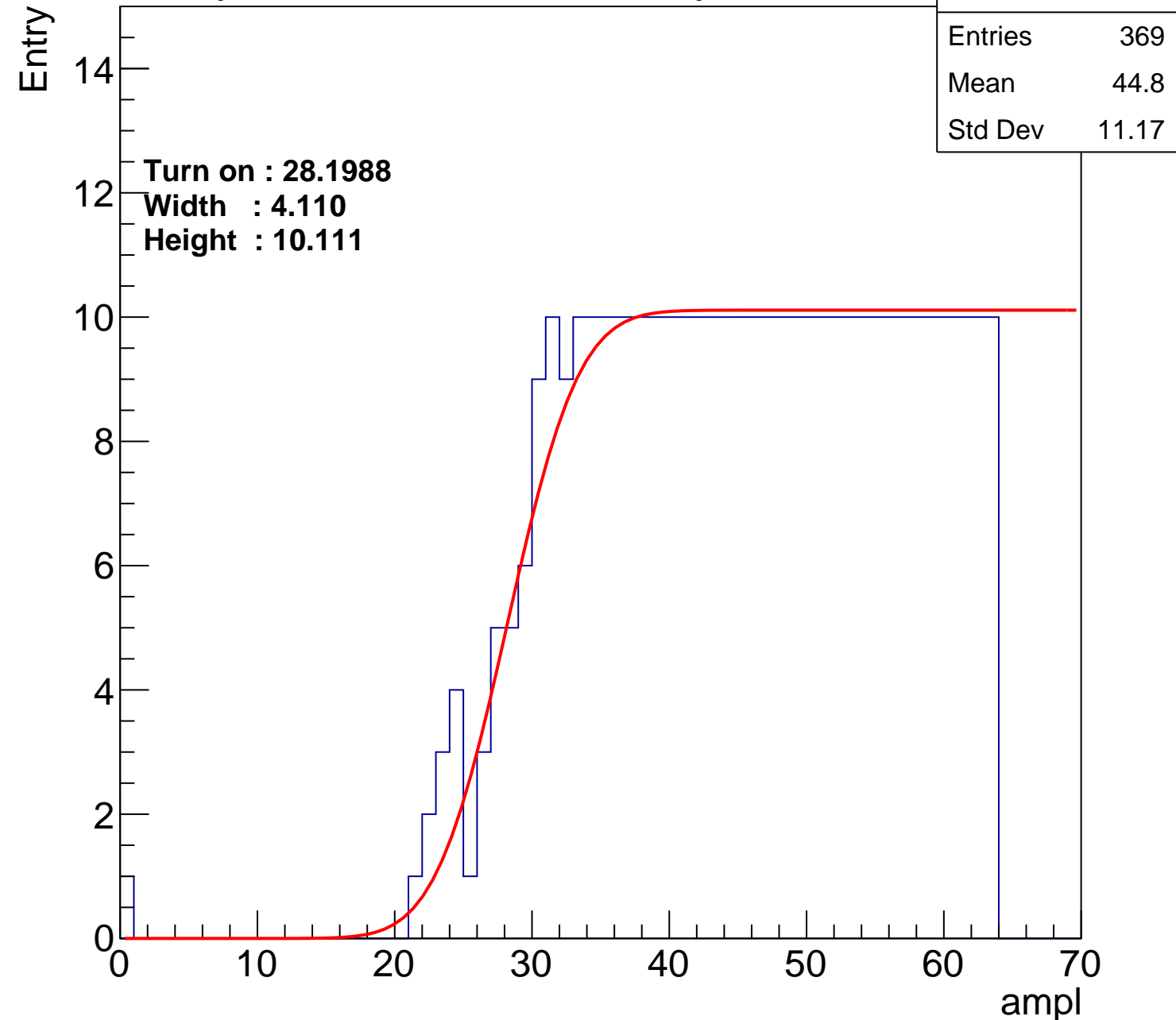
Width : 4.110

Height : 10.111

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch88

calib\_packv5\_042523\_0143.root, FC#8, port C1

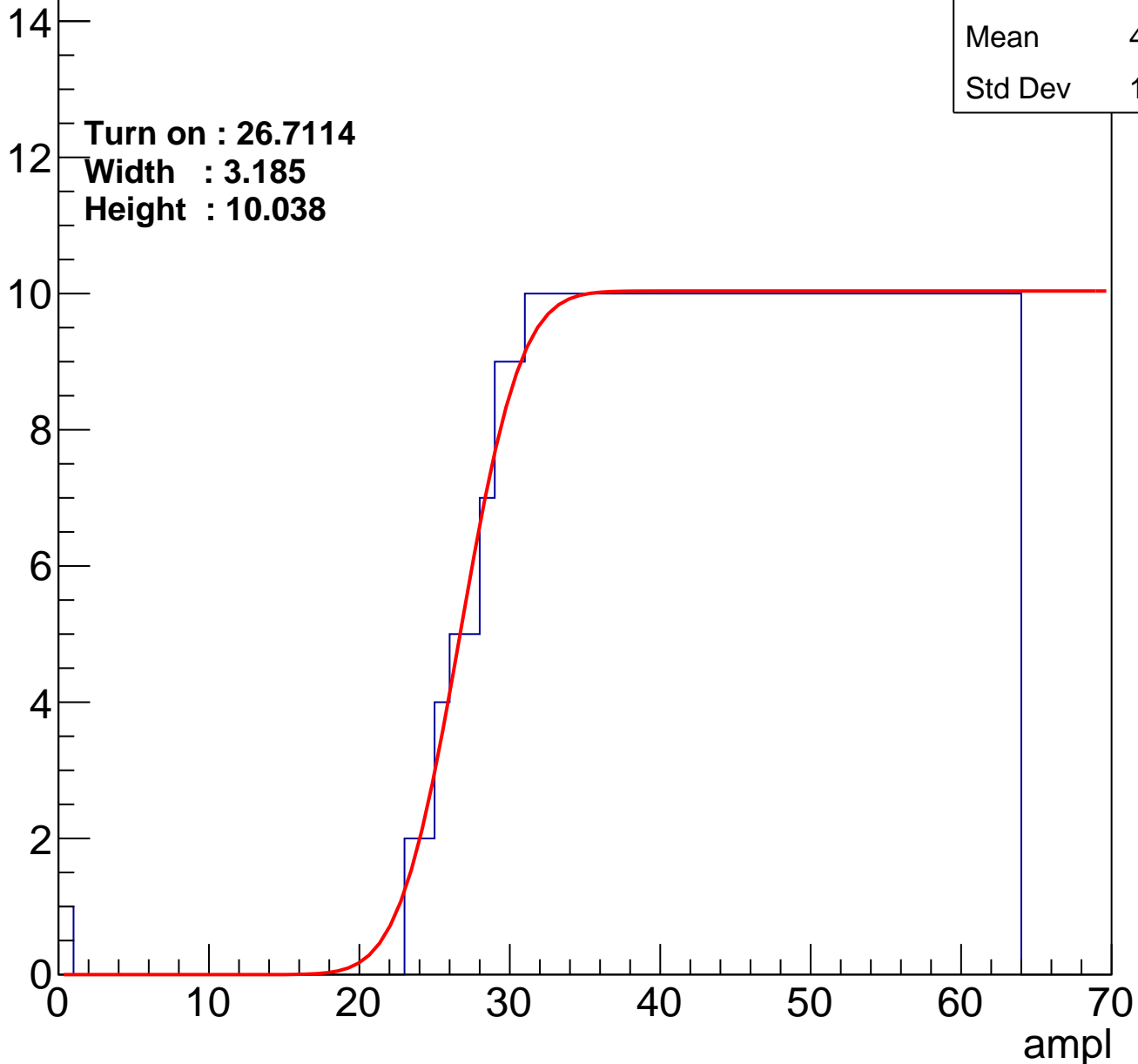
Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.7114

Width : 3.185

Height : 10.038

Entry



# B0L002S, U24-ch89

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	340
Mean	46.29
Std Dev	10.29

Turn on : 30.1764

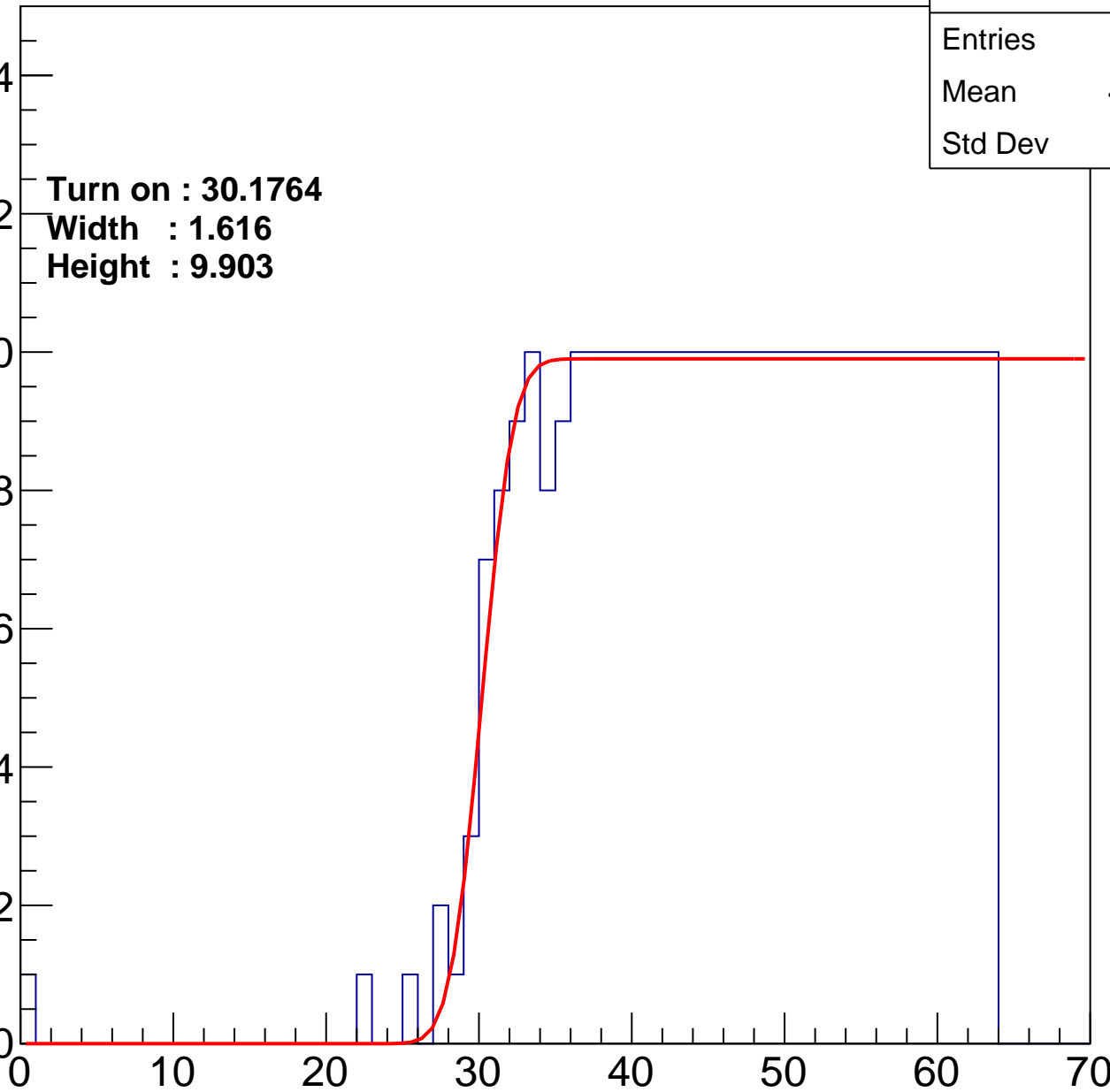
Width : 1.616

Height : 9.903

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch90

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.26
Std Dev	11.39

Turn on : 29.3609

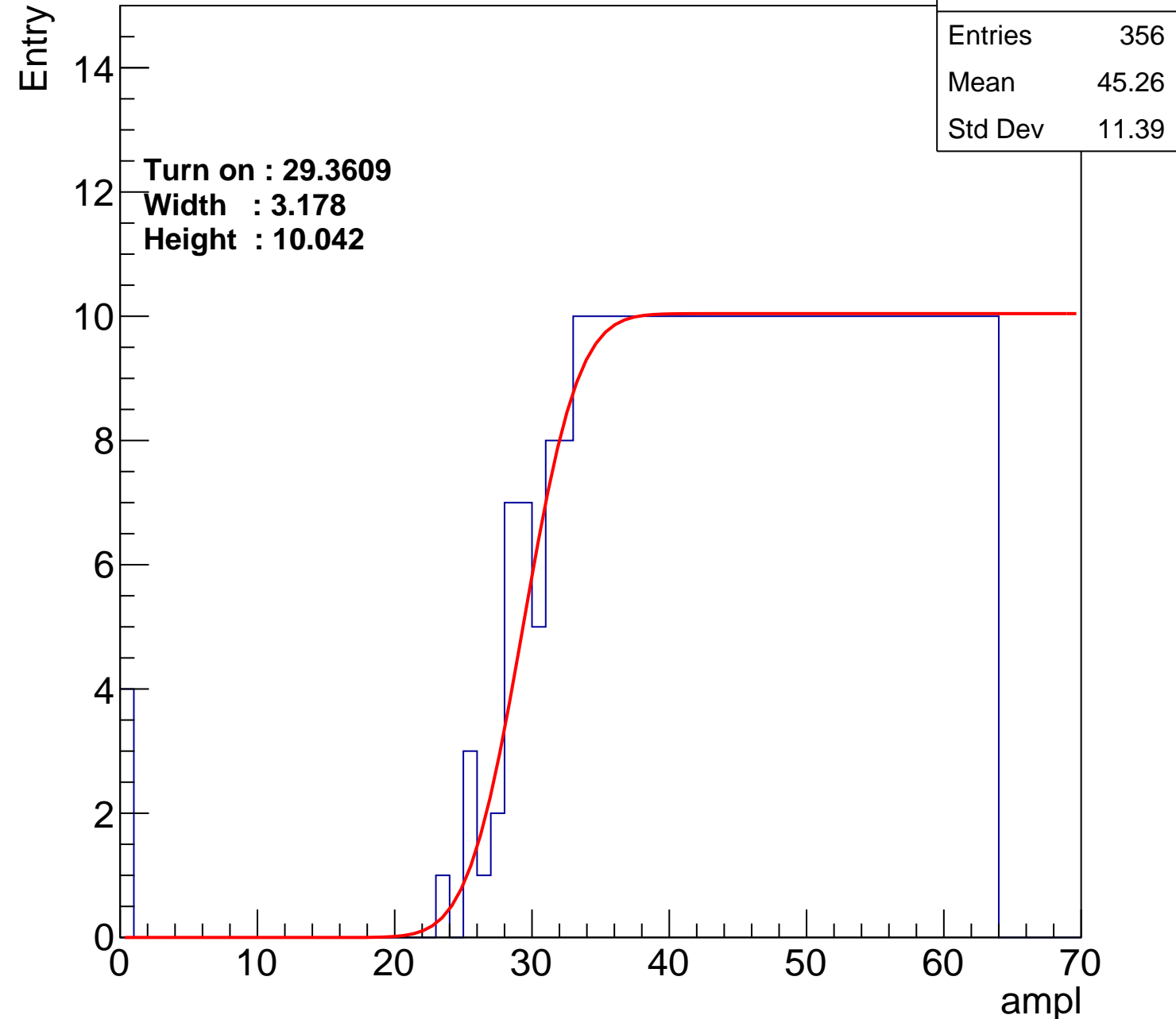
Width : 3.178

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch91

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	386
Mean	44.01
Std Dev	11.67

**Turn on : 25.9777**

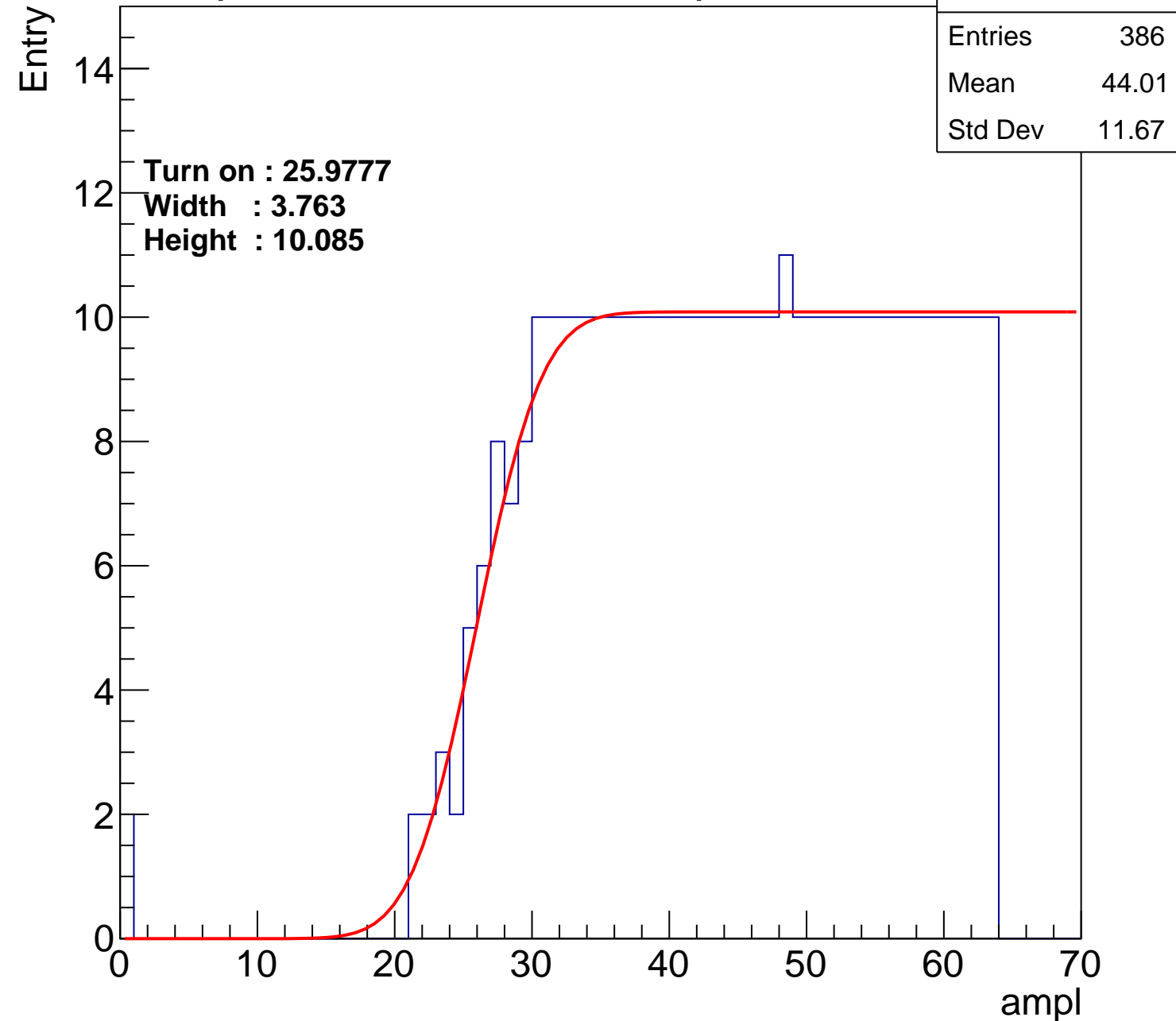
**Width : 3.763**

**Height : 10.085**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch92

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	354
Mean	45.54
Std Dev	10.85

**Turn on : 28.7783**

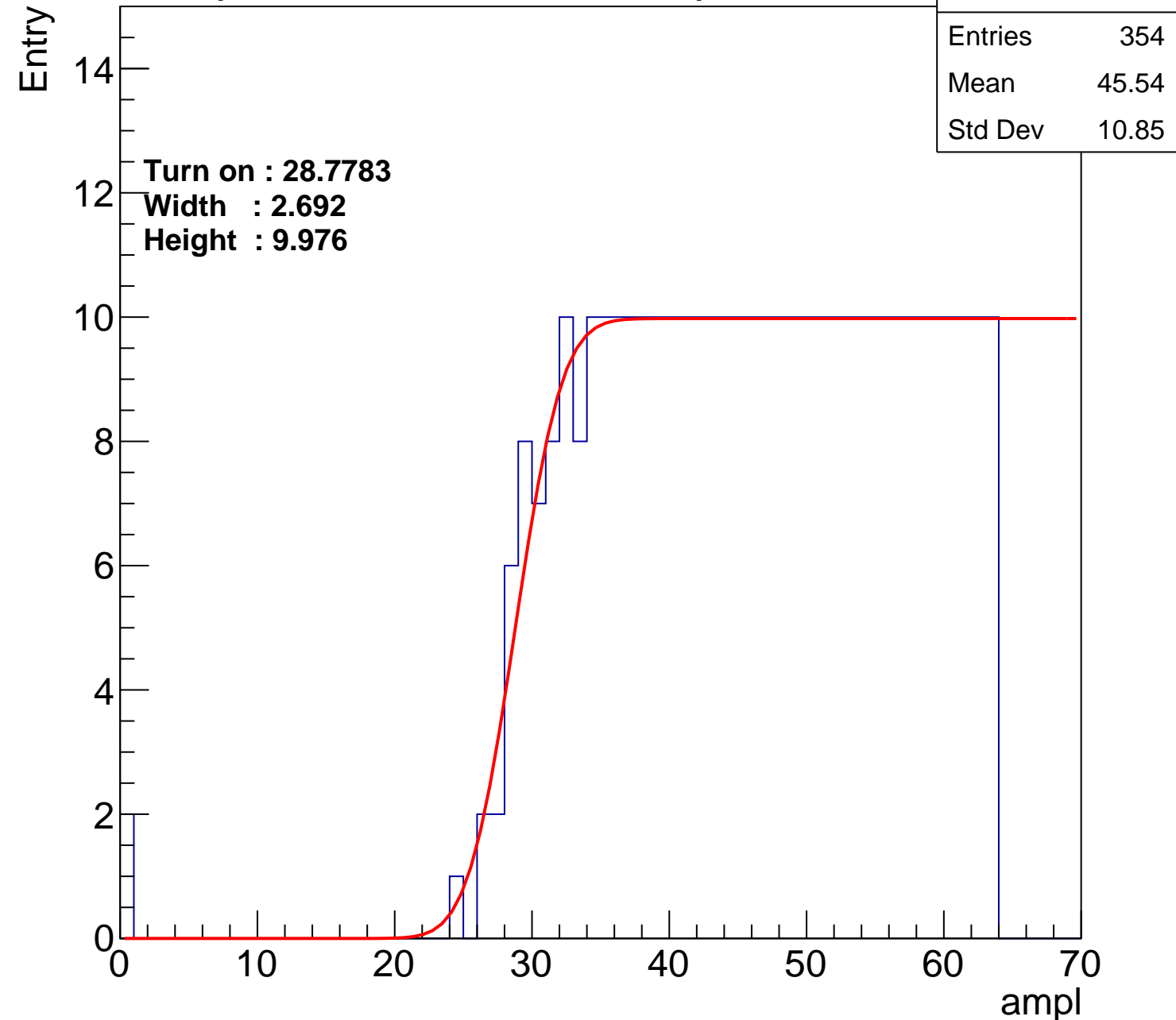
**Width : 2.692**

**Height : 9.976**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch93

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.27
Std Dev	11.02

Turn on : 28.6194

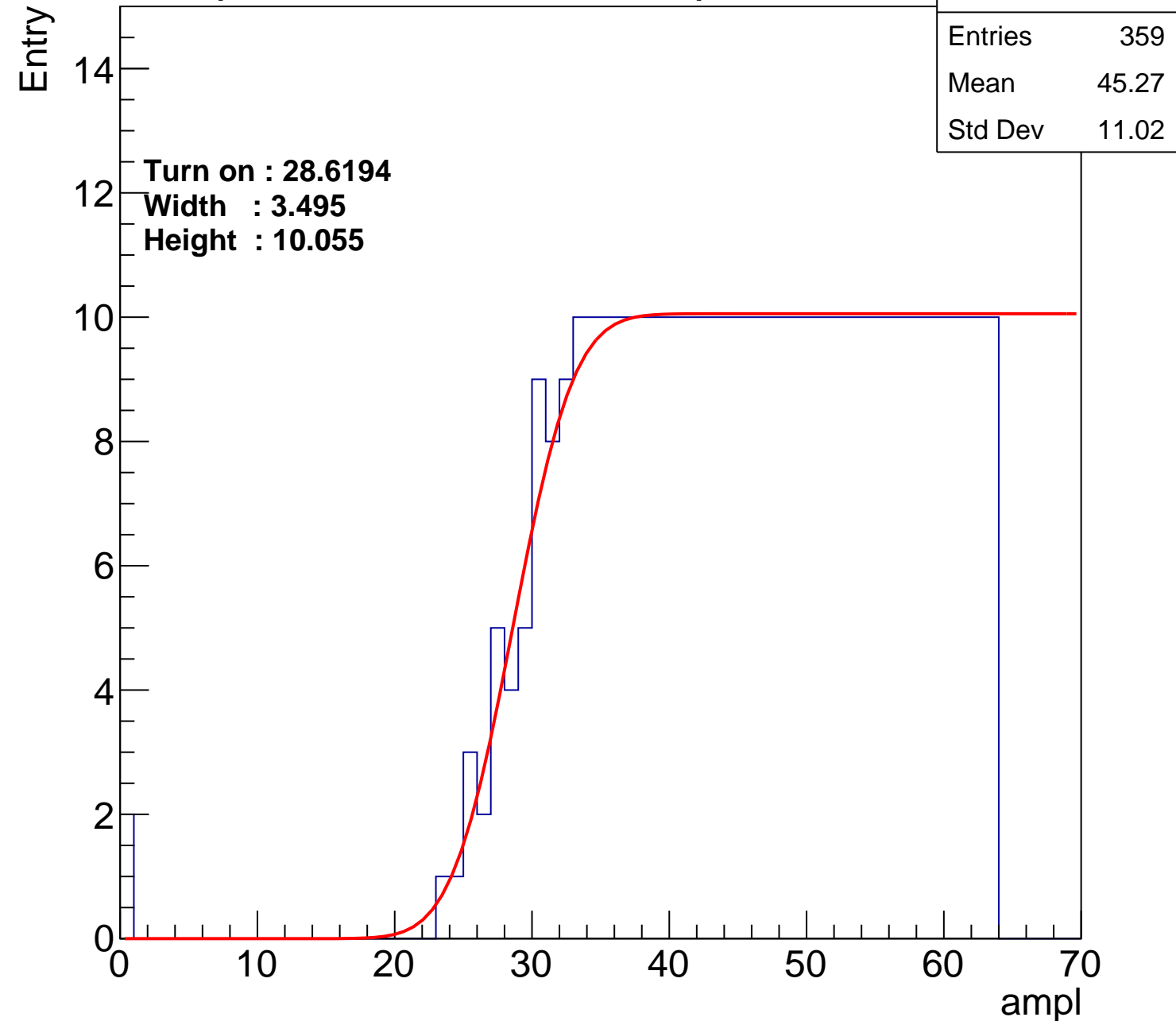
Width : 3.495

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



B0L002S, U24-ch94

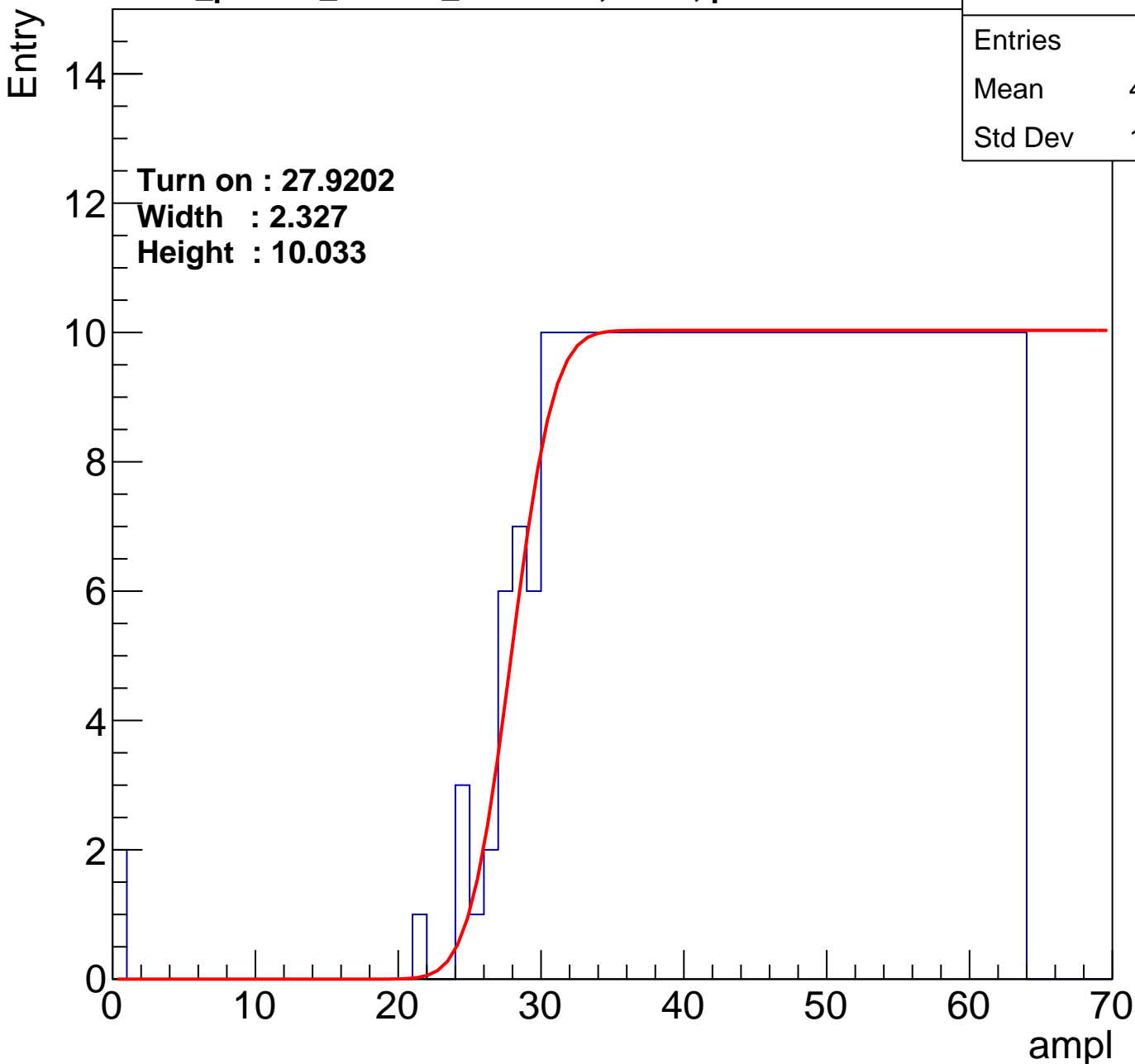
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	368
Mean	44.87
Std Dev	11.19

**Turn on : 27.9202**

**Width : 2.327**

**Height : 10.033**





# B0L002S, U24-ch95

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.22
Std Dev	11.8

Turn on : 29.5600

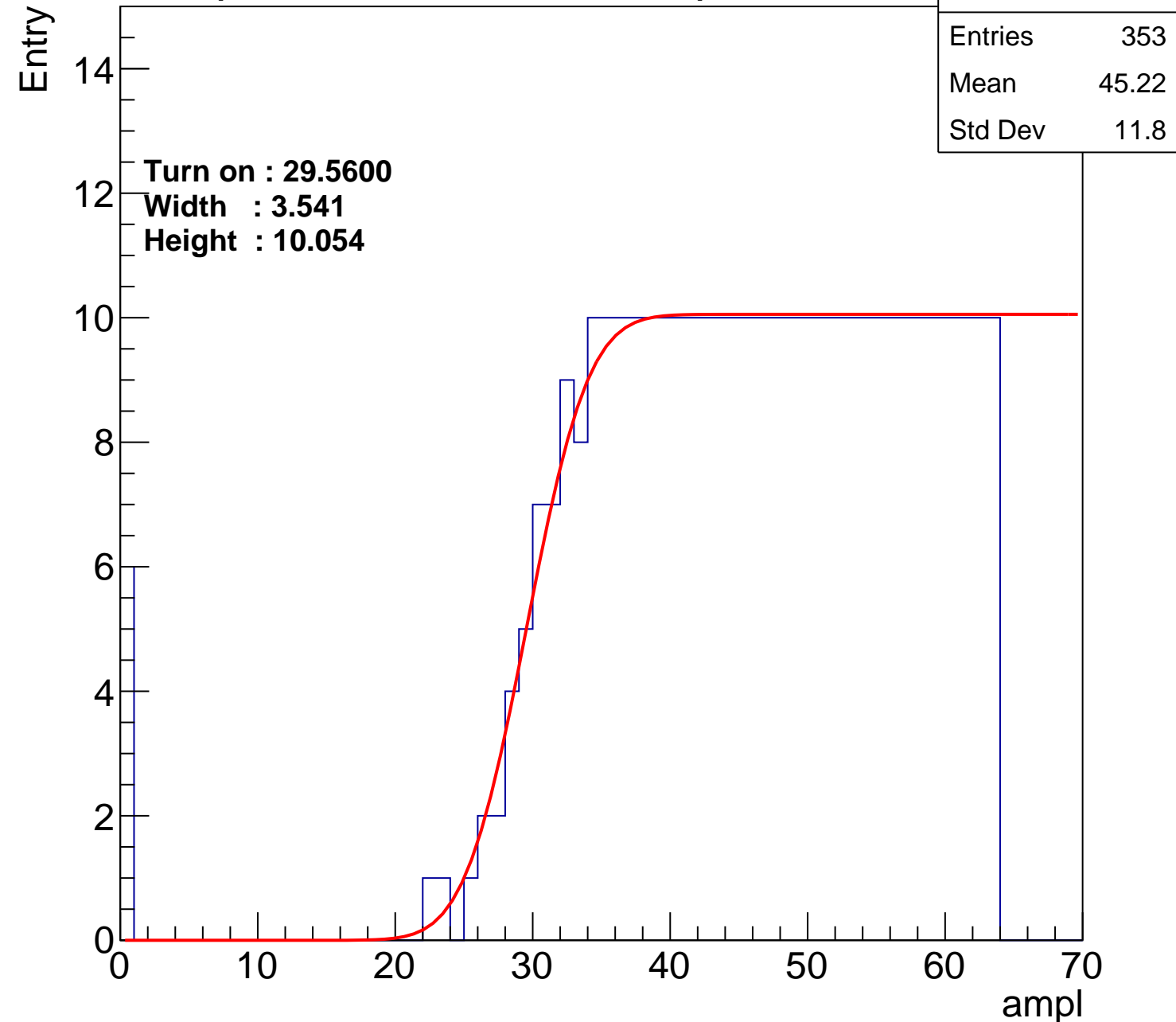
Width : 3.541

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch96

calib\_packv5\_042523\_0143.root, FC#8, port C1

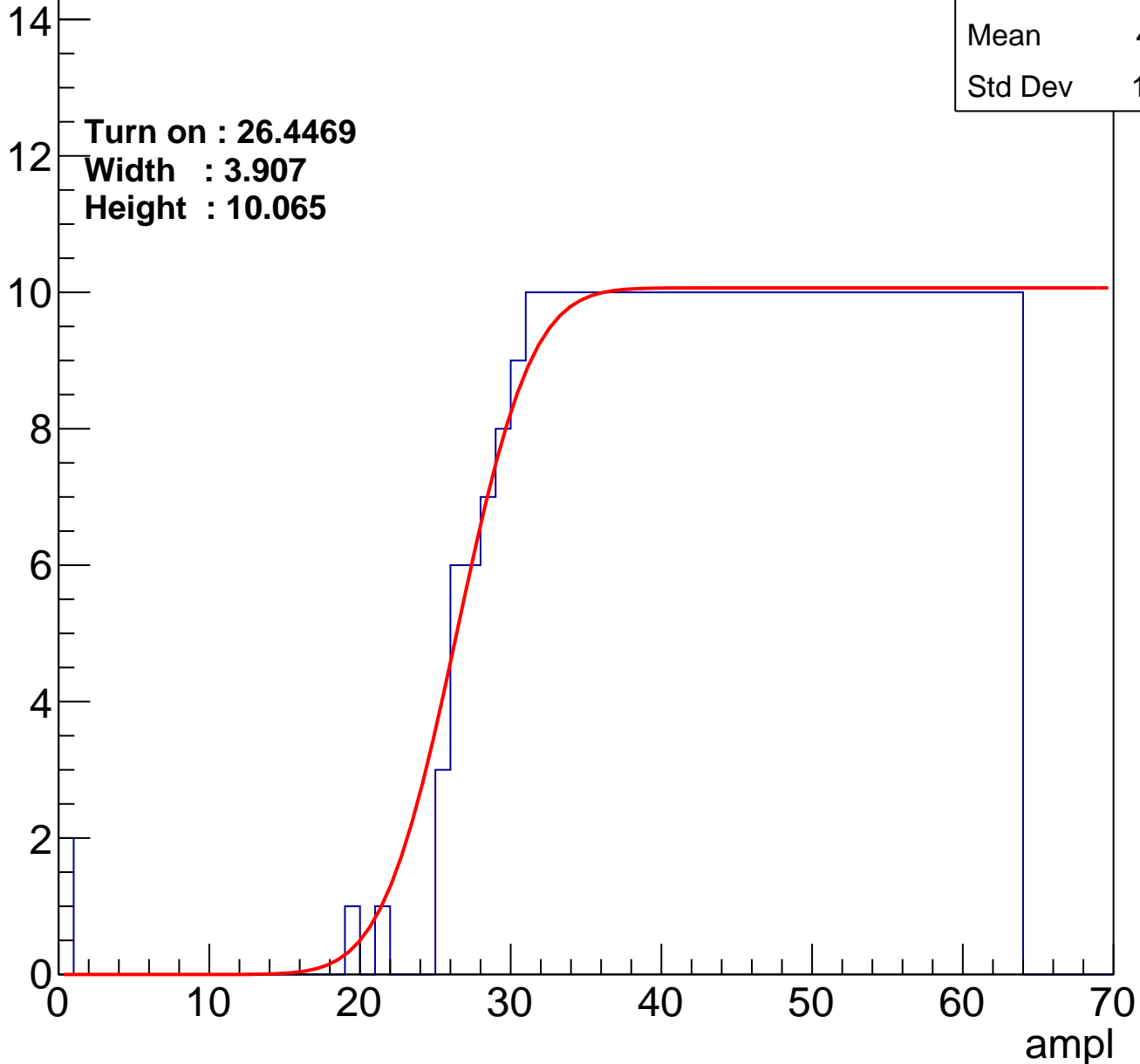
Entries	373
Mean	44.61
Std Dev	11.33

Turn on : 26.4469

Width : 3.907

Height : 10.065

Entry



# B0L002S, U24-ch97

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.18
Std Dev	11.21

Turn on : 28.5854

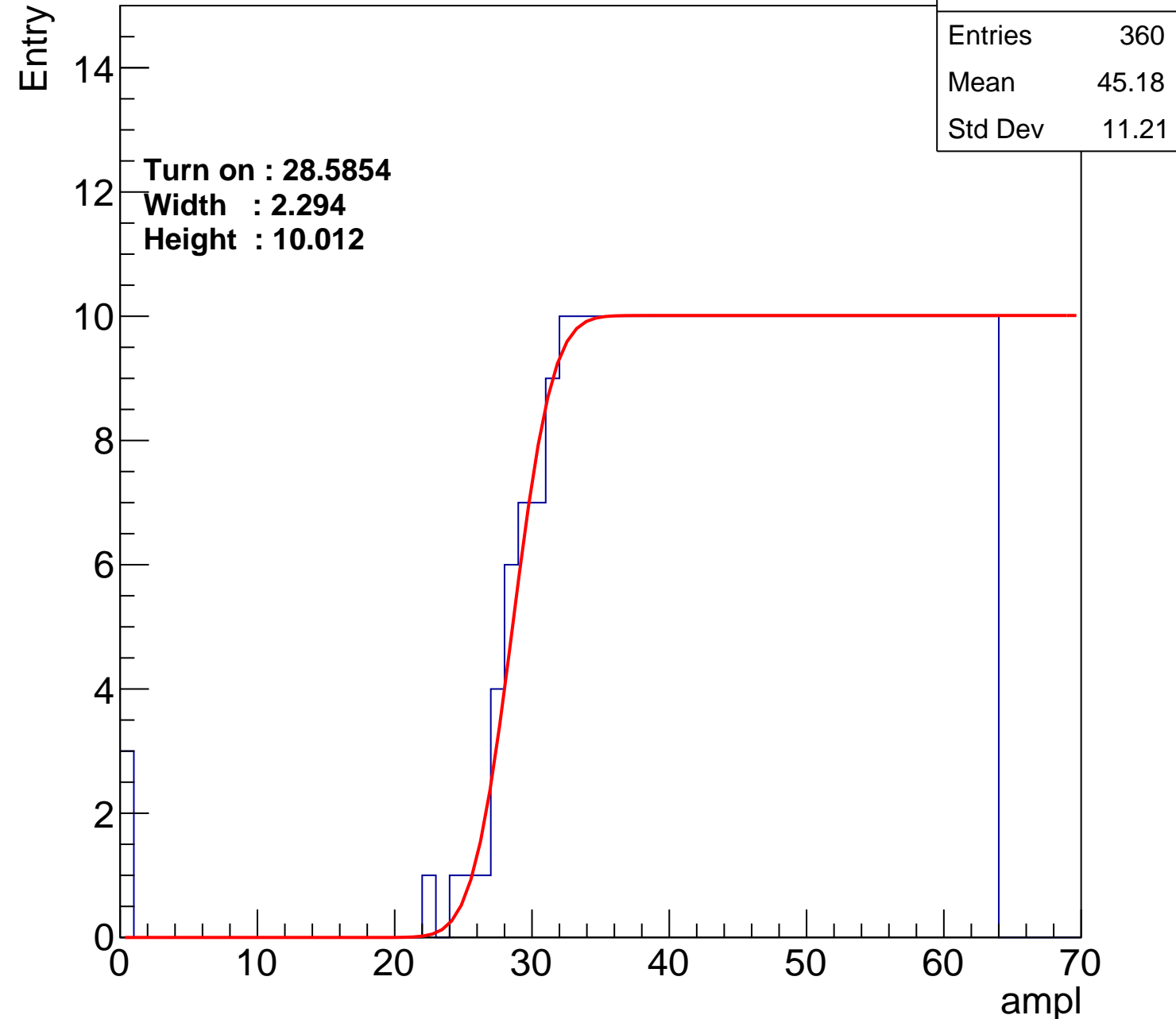
Width : 2.294

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch98

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	397
Mean	43.33
Std Dev	12.16

Turn on : 24.6983

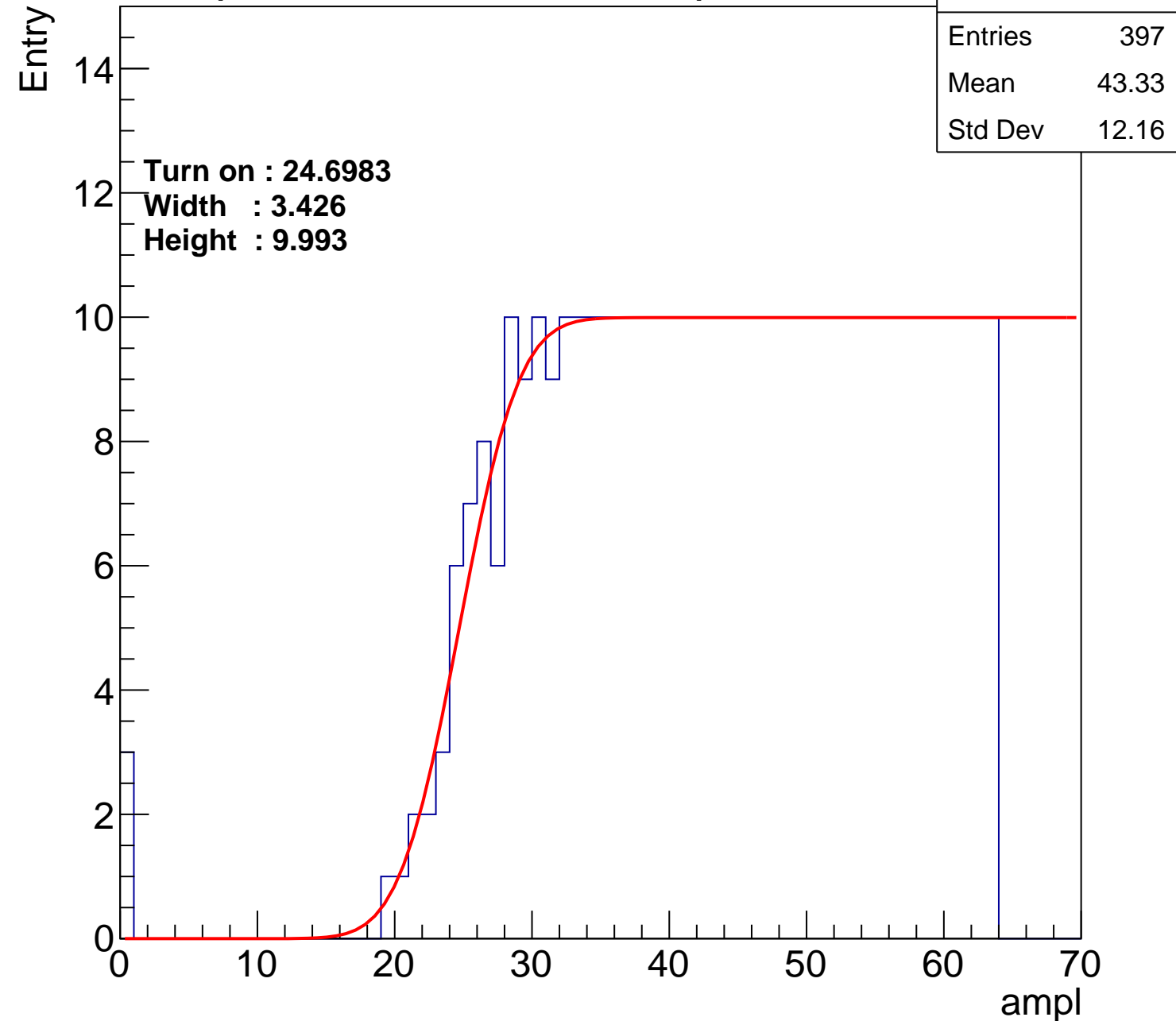
Width : 3.426

Height : 9.993

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch99

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.42
Std Dev	11.3

**Turn on : 29.2195**

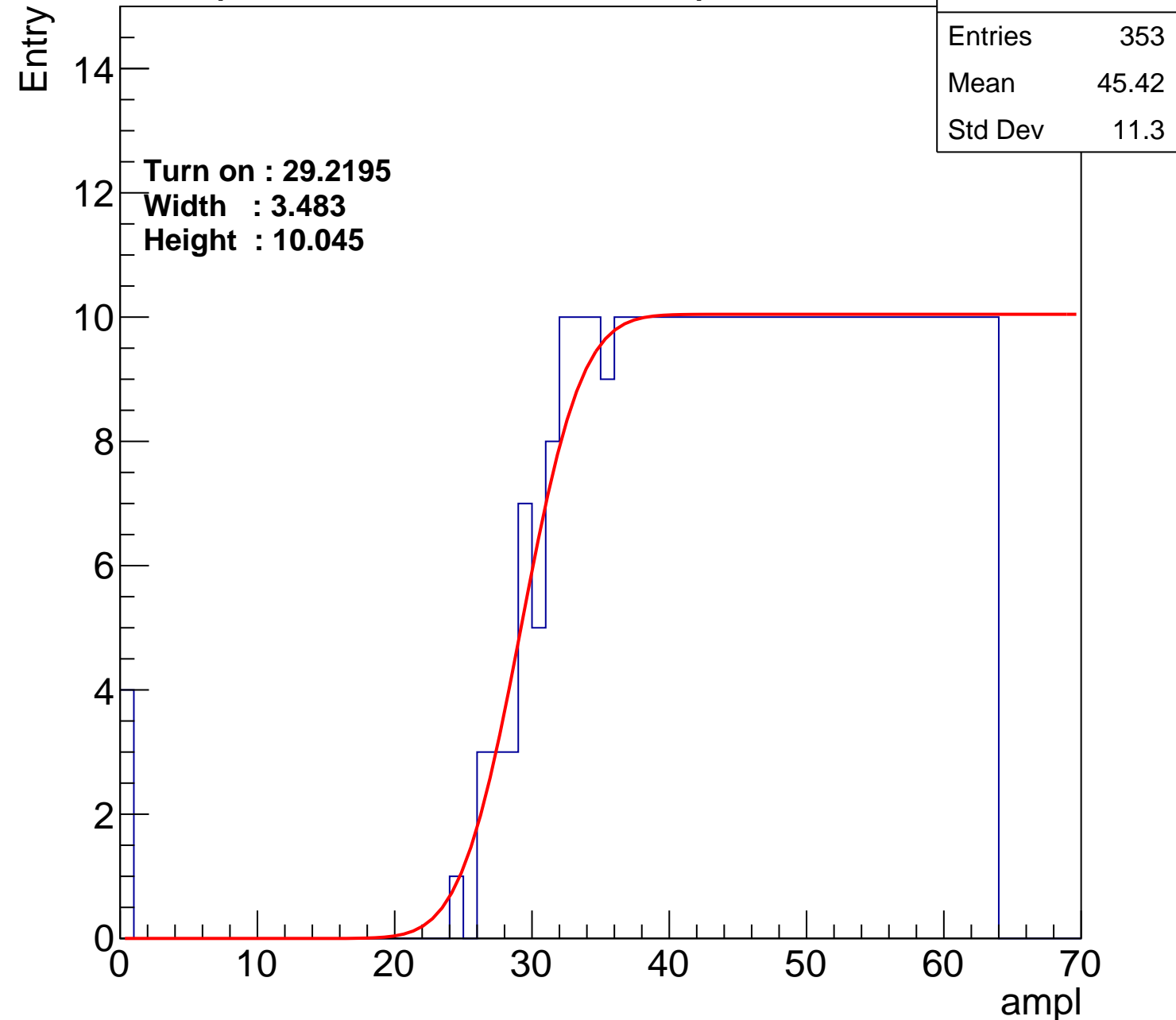
**Width : 3.483**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch100

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.59
Std Dev	10.84

Turn on : 29.0756

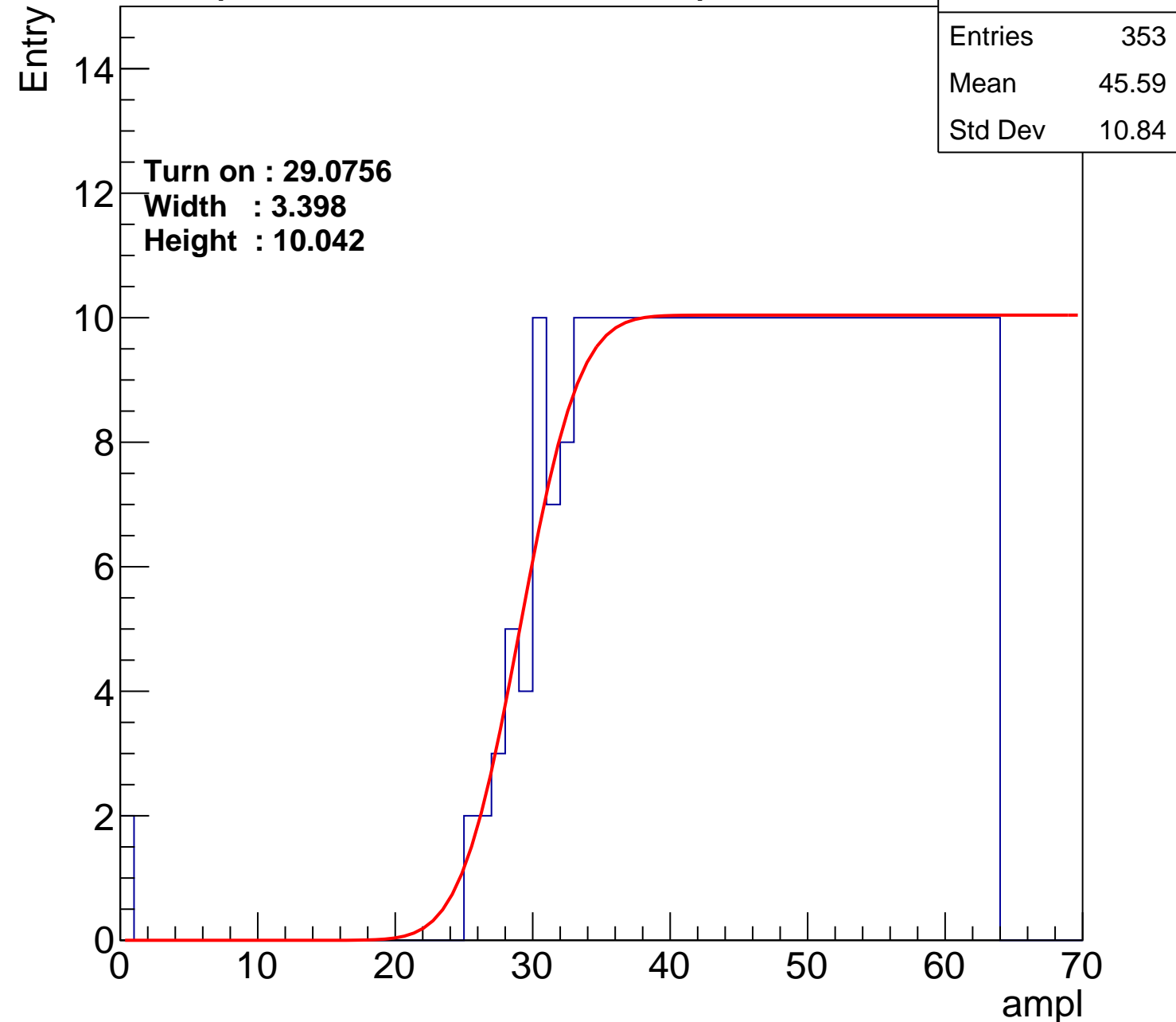
Width : 3.398

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch101

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.39
Std Dev	11.48

Turn on : 27.0607

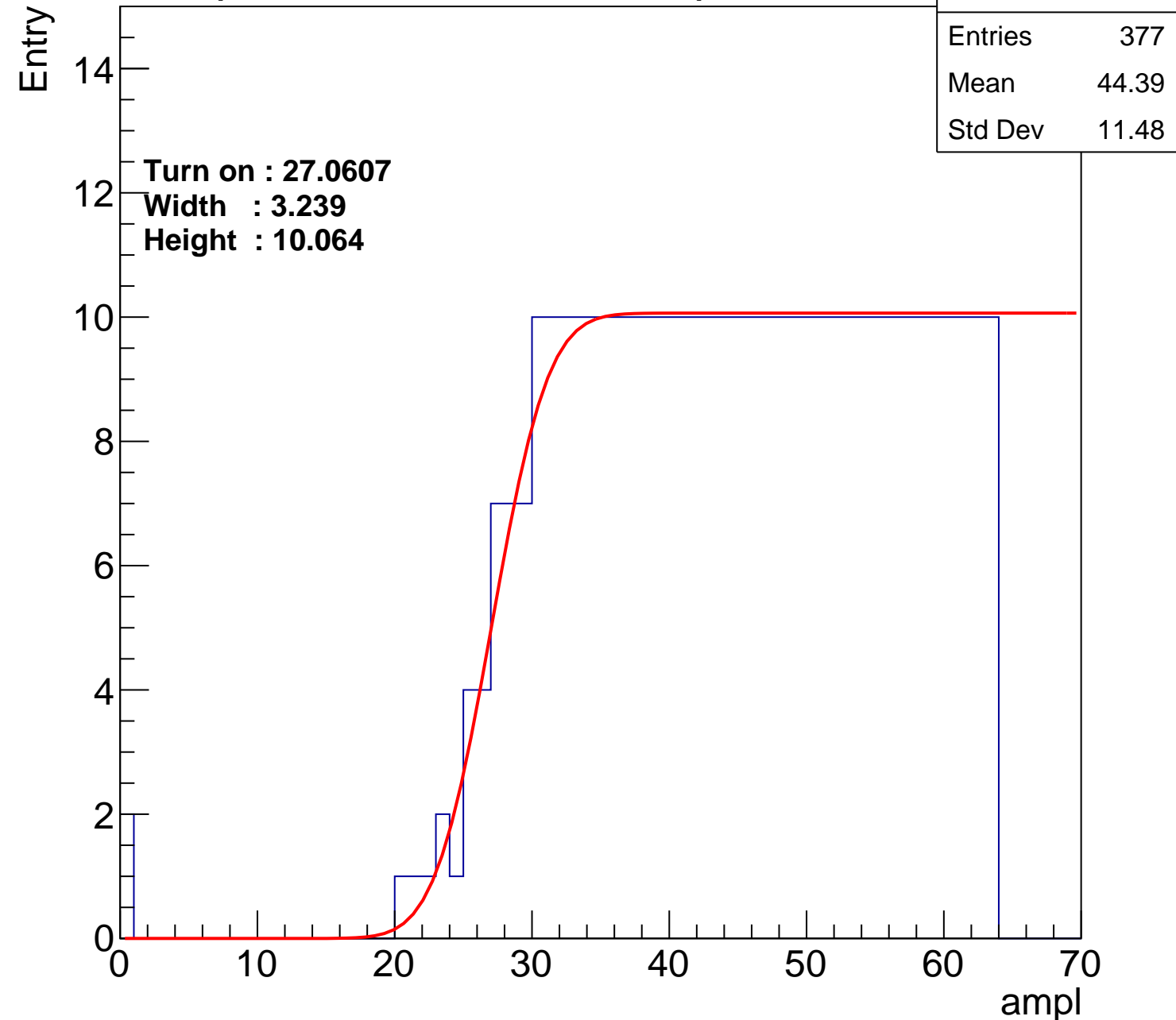
Width : 3.239

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch102

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.14
Std Dev	11.08

Turn on : 28.1841

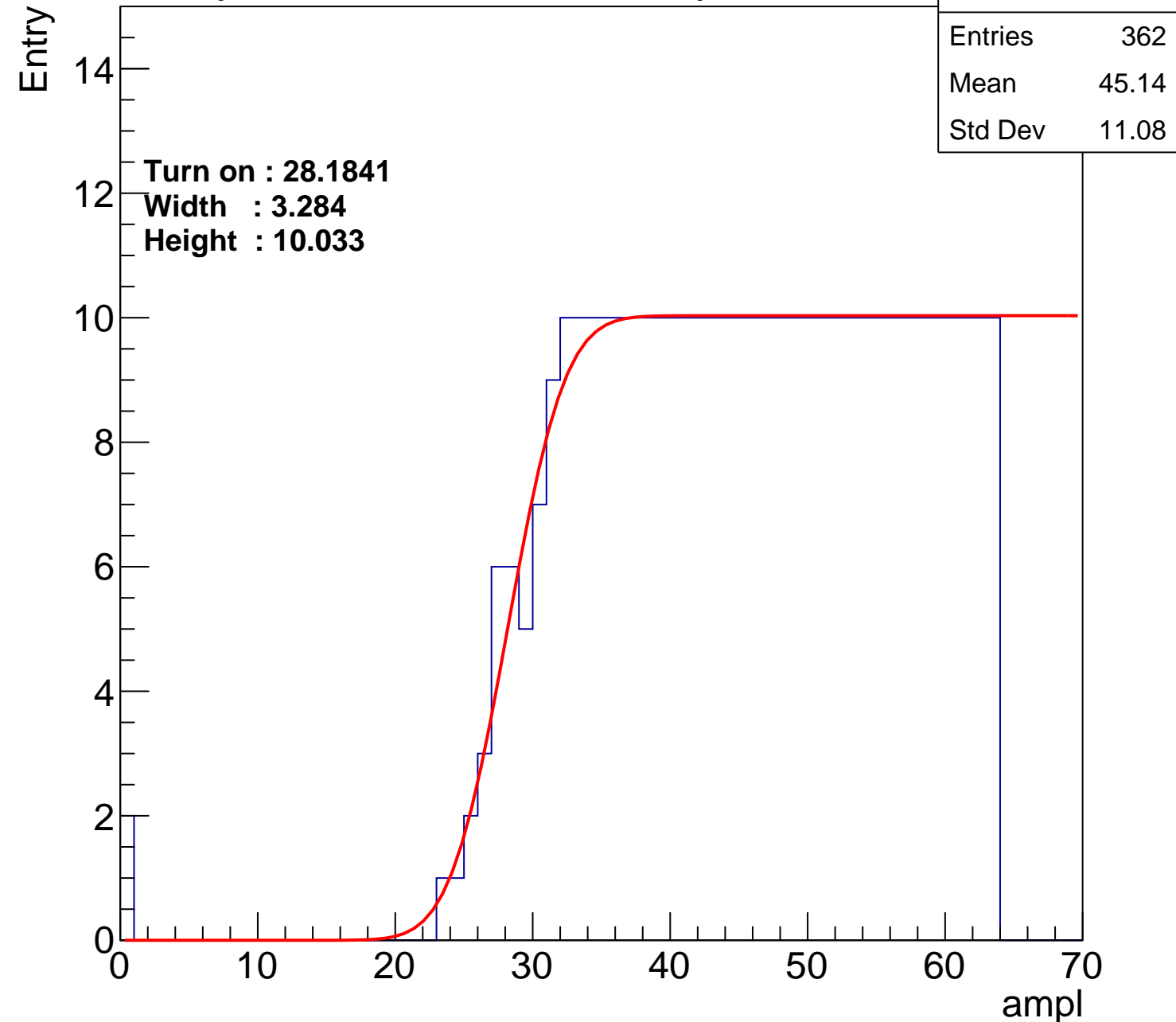
Width : 3.284

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch103

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	354
Mean	45.41
Std Dev	11.26

**Turn on : 29.3241**

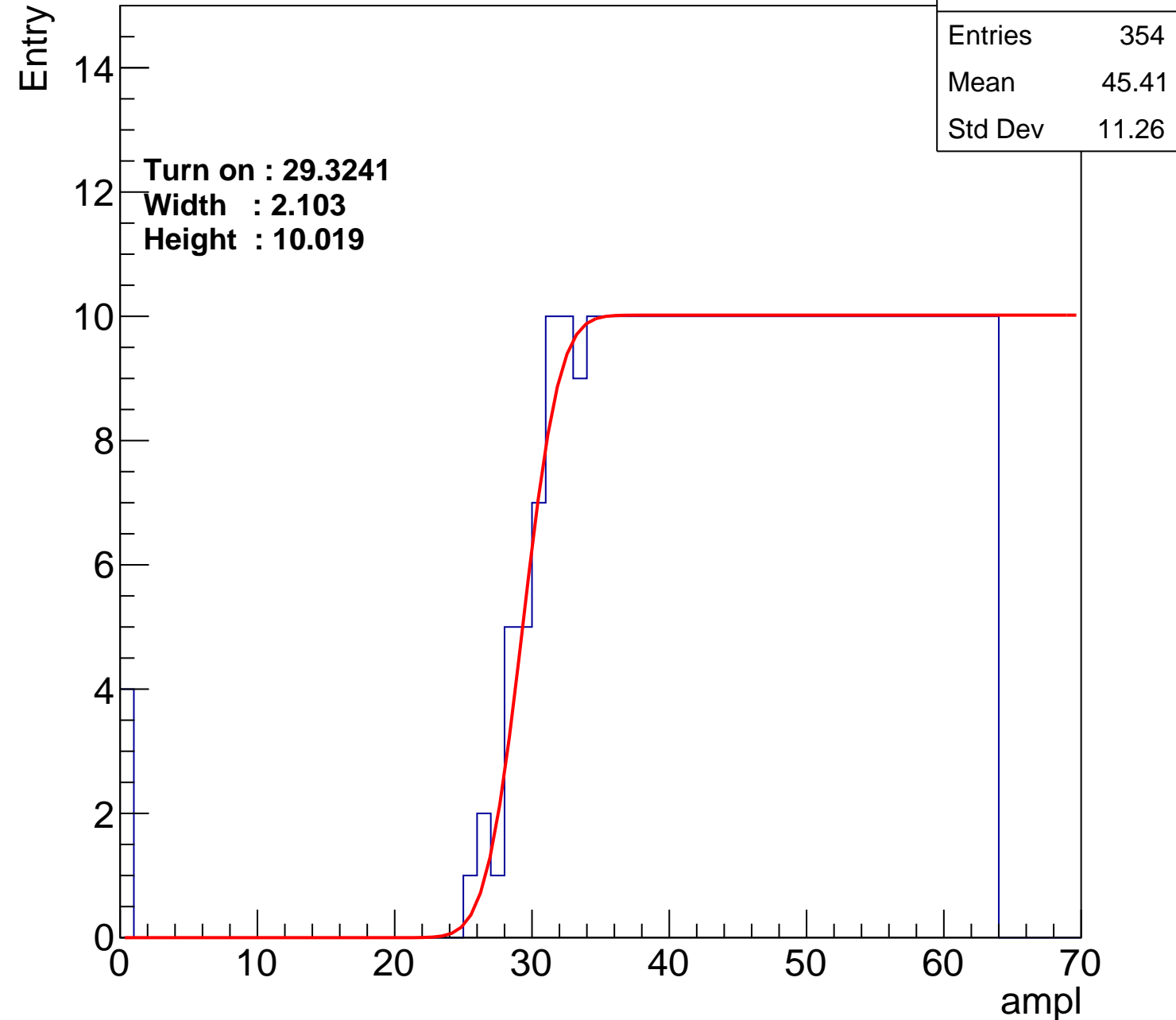
**Width : 2.103**

**Height : 10.019**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch104

calib\_packv5\_042523\_0143.root, FC#8, port C1

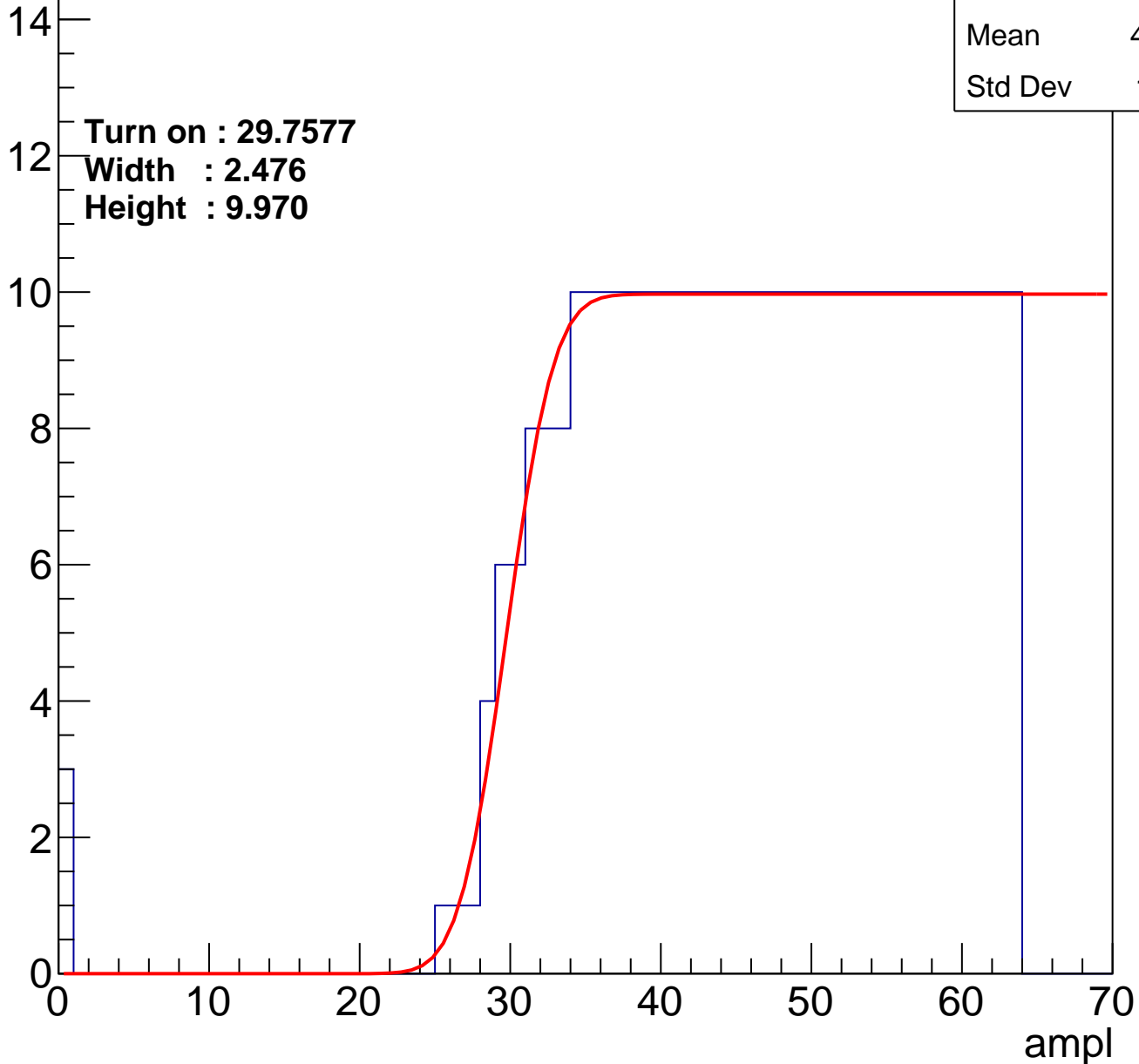
Entries	346
Mean	45.84
Std Dev	10.91

**Turn on : 29.7577**

**Width : 2.476**

**Height : 9.970**

Entry



# B0L002S, U24-ch105

calib\_packv5\_042523\_0143.root, FC#8, port C1

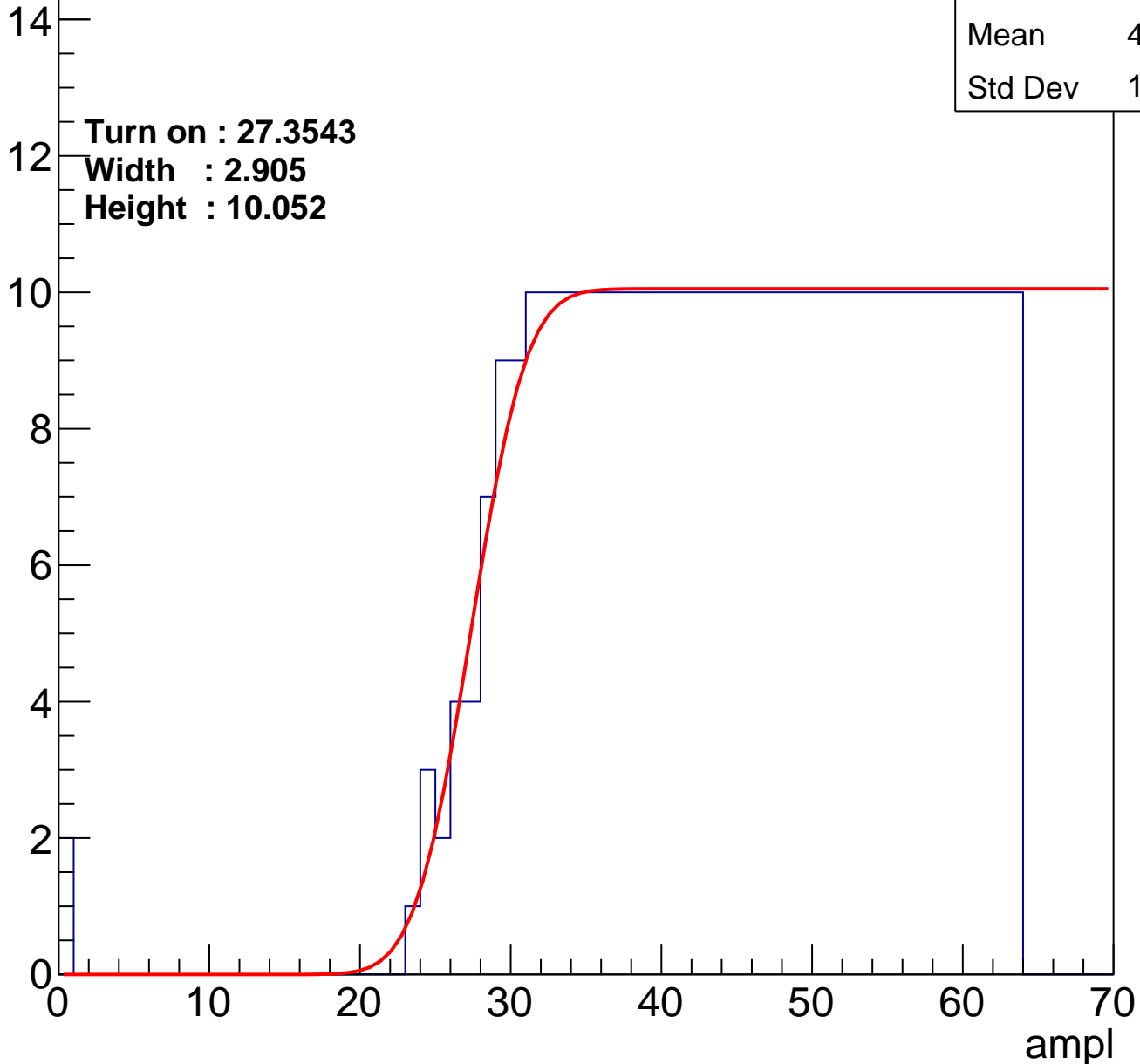
Entries	371
Mean	44.73
Std Dev	11.25

Turn on : 27.3543

Width : 2.905

Height : 10.052

Entry



# B0L002S, U24-ch106

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.26
Std Dev	12

**Turn on : 27.1587**

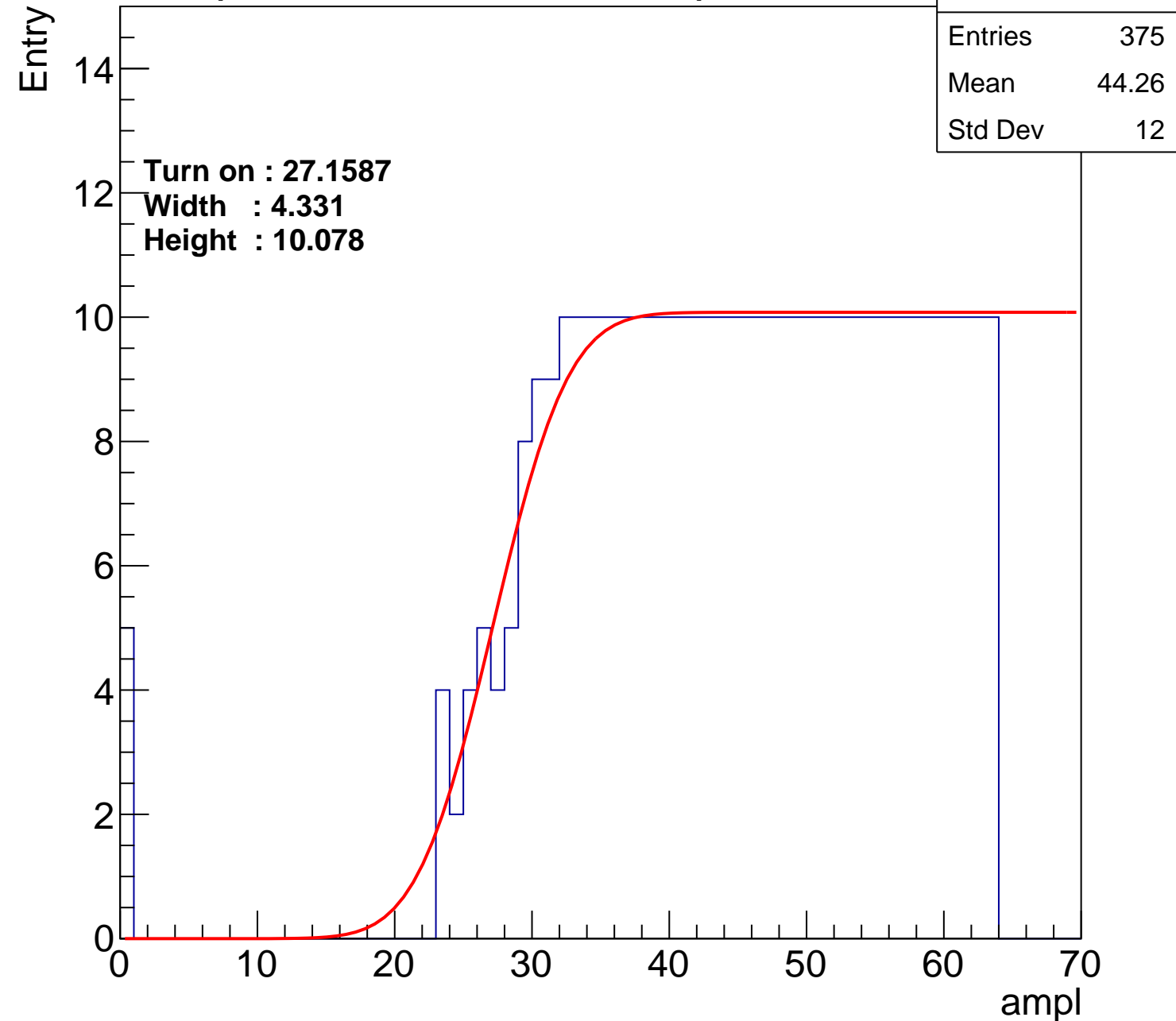
**Width : 4.331**

**Height : 10.078**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch107

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.01
Std Dev	11.3

Turn on : 27.8043

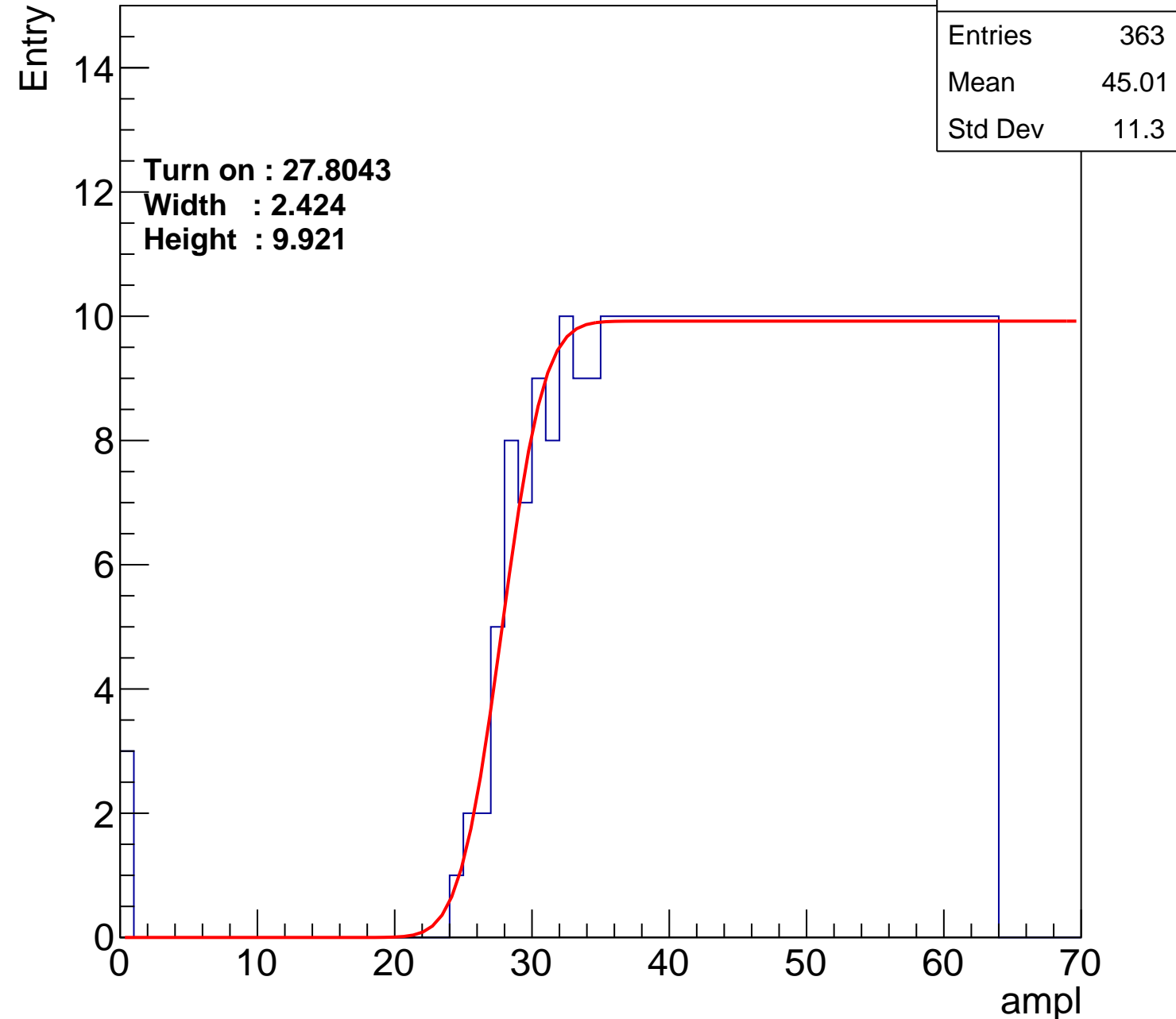
Width : 2.424

Height : 9.921

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch108

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 25.6172

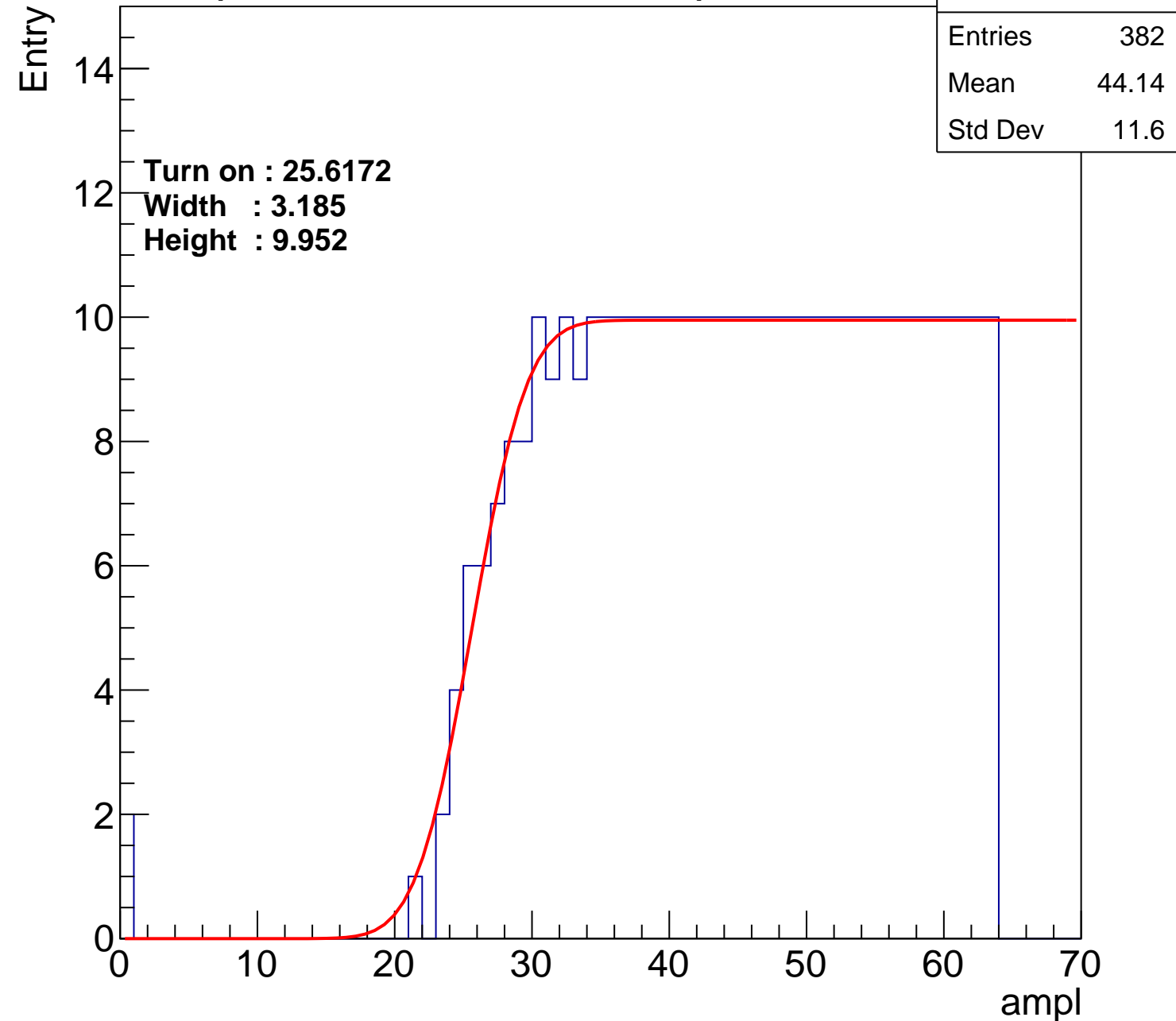
Width : 3.185

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch109

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.3816

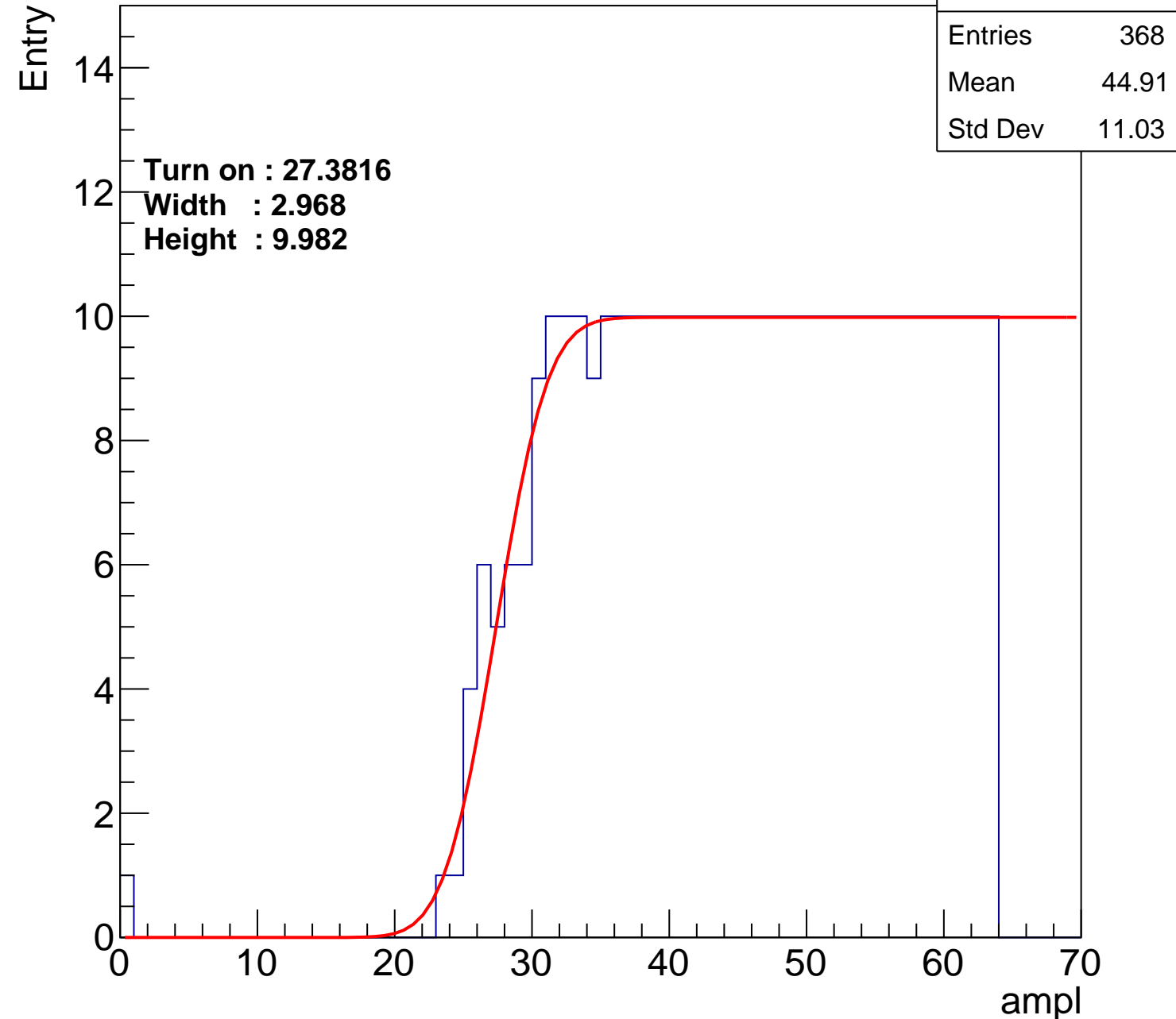
Width : 2.968

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch110

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.45
Std Dev	11.56

**Turn on : 26.9318**

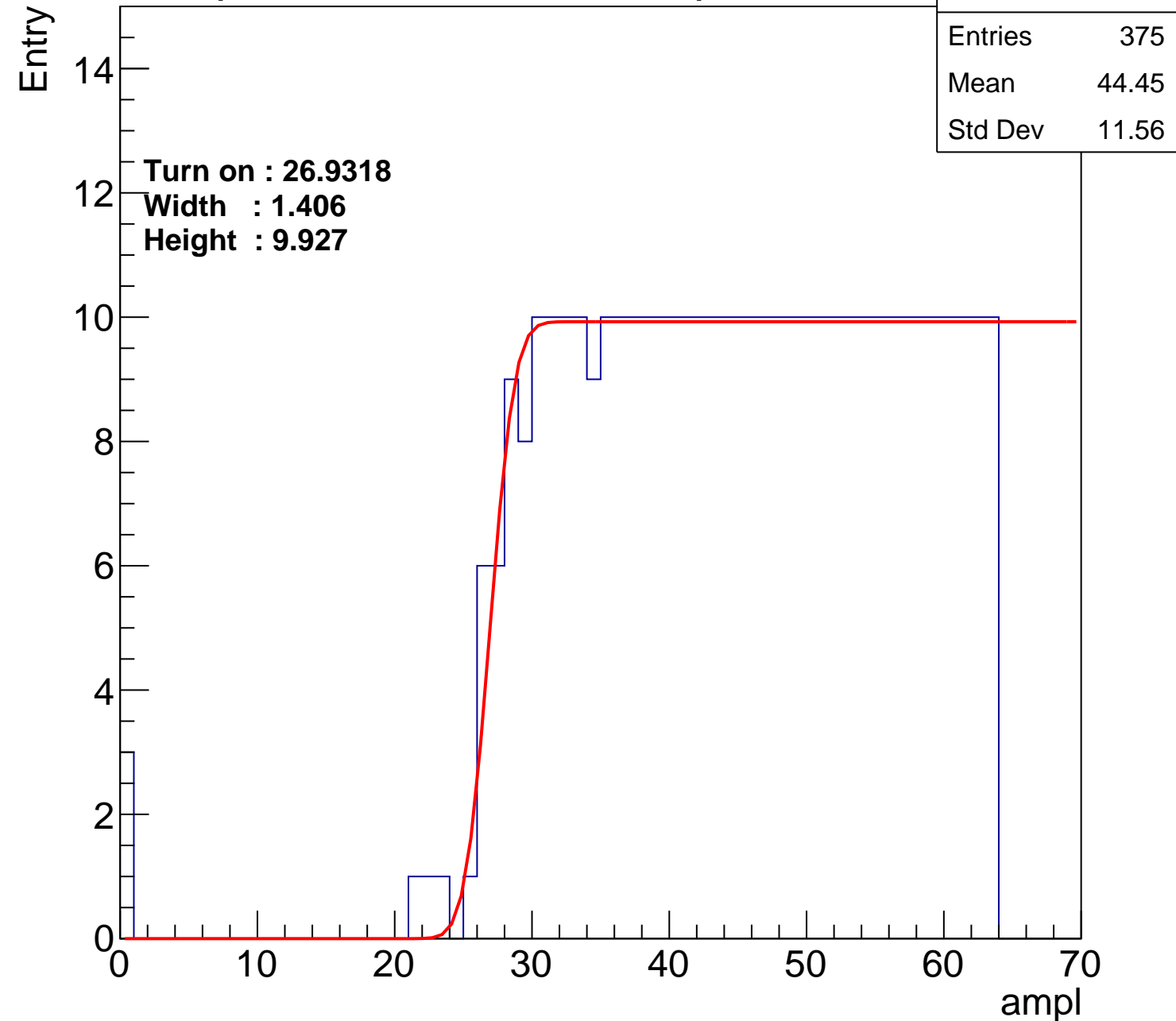
**Width : 1.406**

**Height : 9.927**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch111

calib\_packv5\_042523\_0143.root, FC#8, port C1

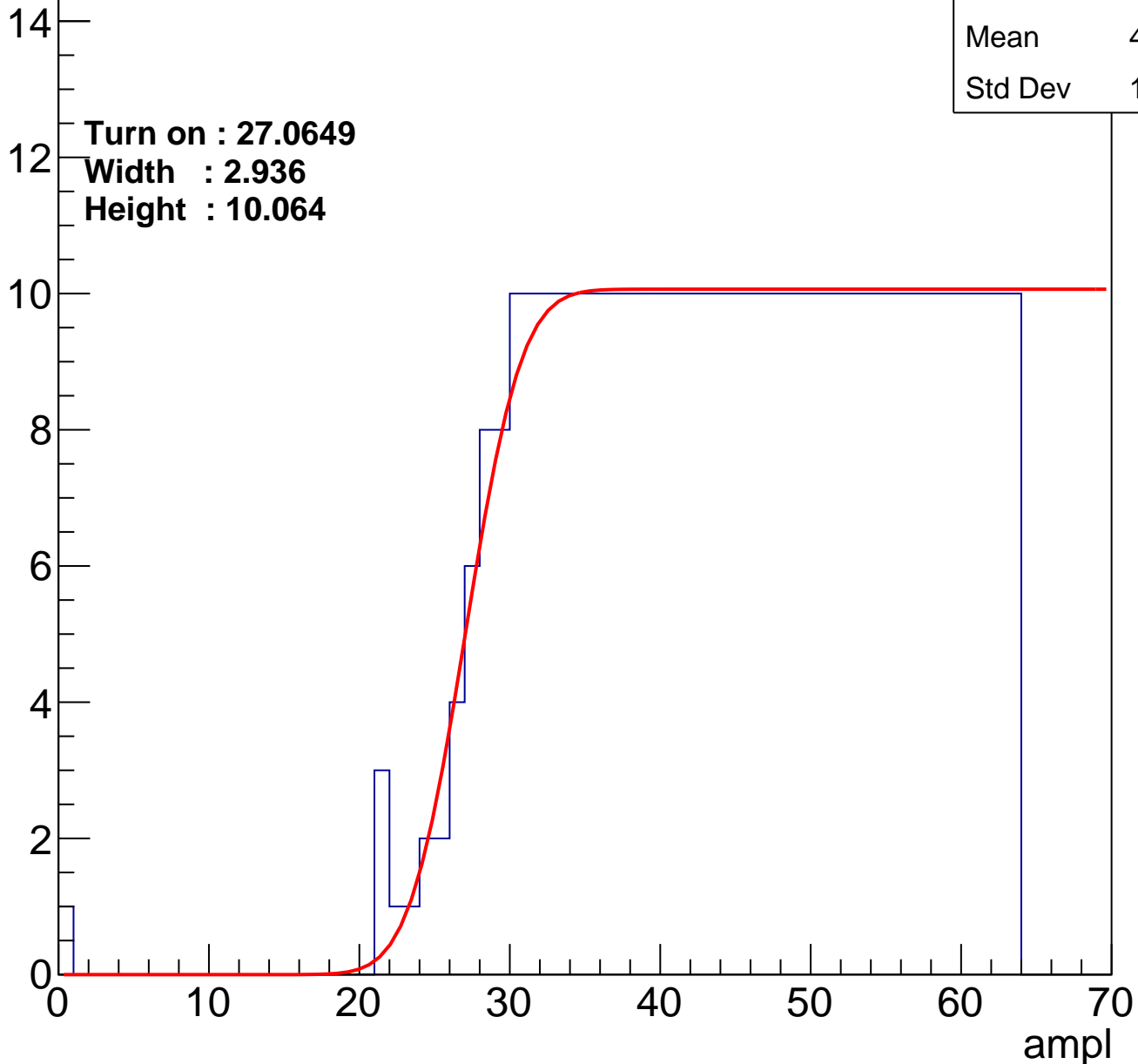
Entries	376
Mean	44.52
Std Dev	11.25

Turn on : 27.0649

Width : 2.936

Height : 10.064

Entry



# B0L002S, U24-ch112

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.87
Std Dev	11.39

**Turn on : 28.2878**

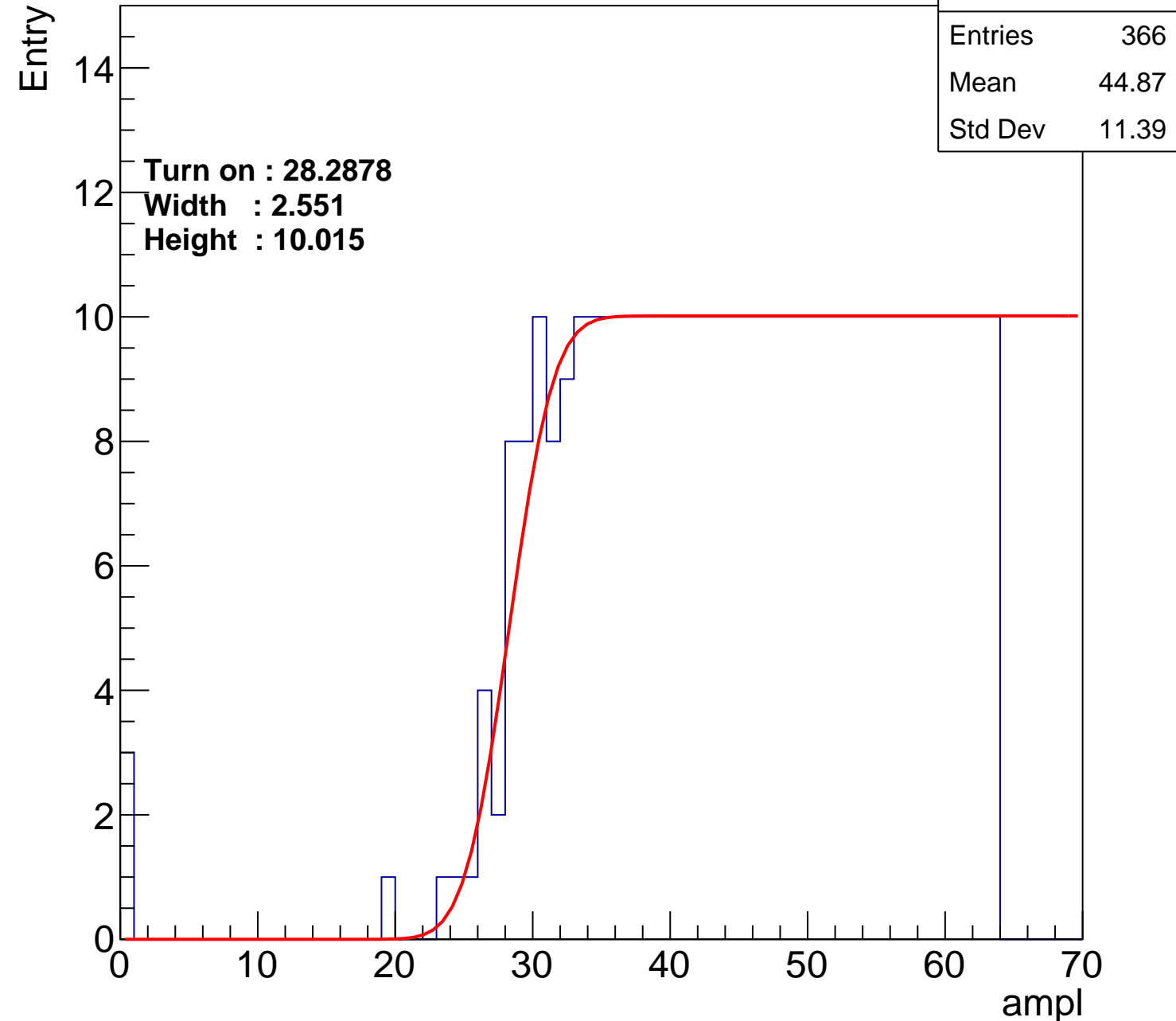
**Width : 2.551**

**Height : 10.015**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch113

calib\_packv5\_042523\_0143.root, FC#8, port C1

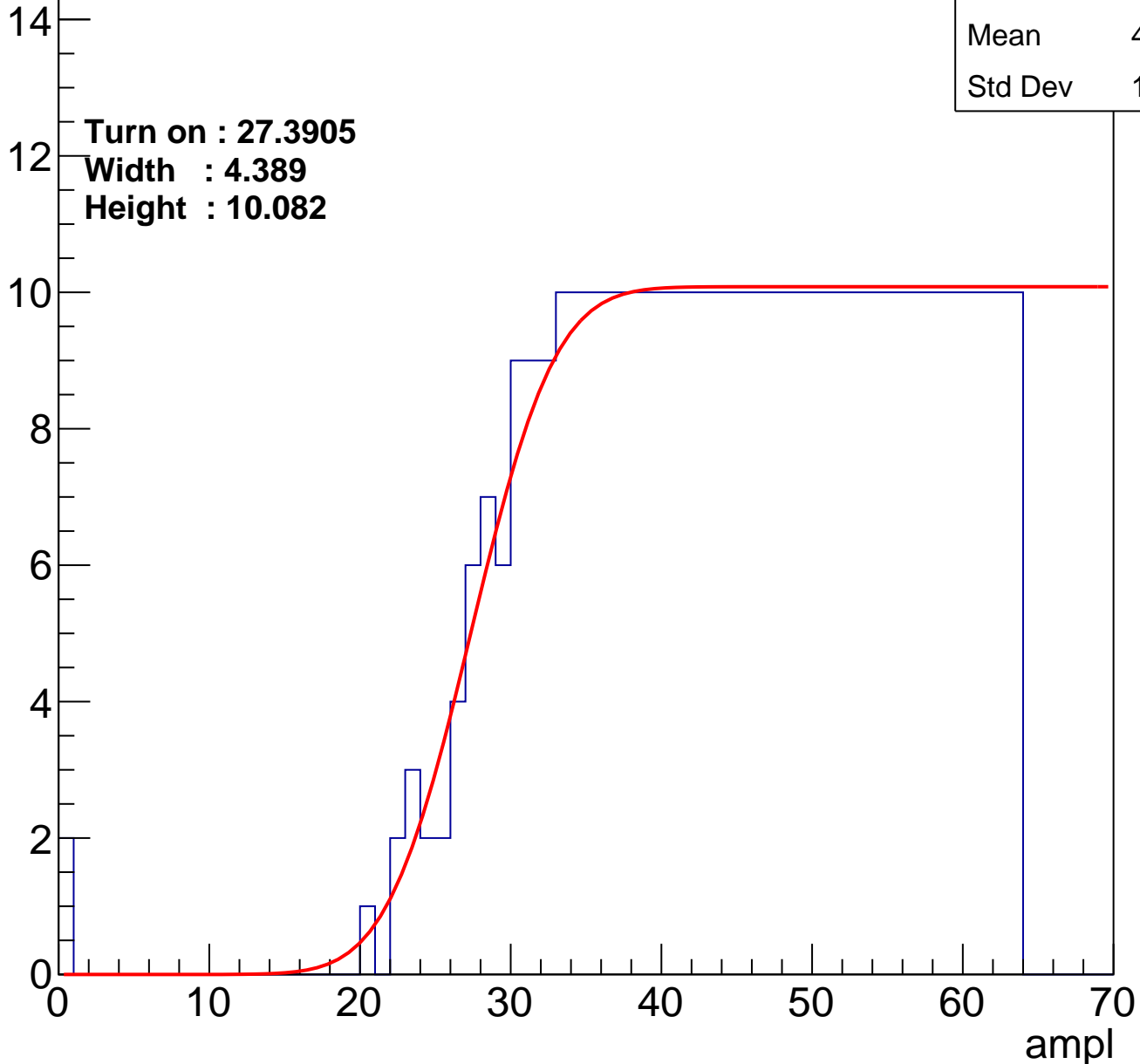
Entries	372
Mean	44.58
Std Dev	11.44

Turn on : 27.3905

Width : 4.389

Height : 10.082

Entry



# B0L002S, U24-ch114

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.7893

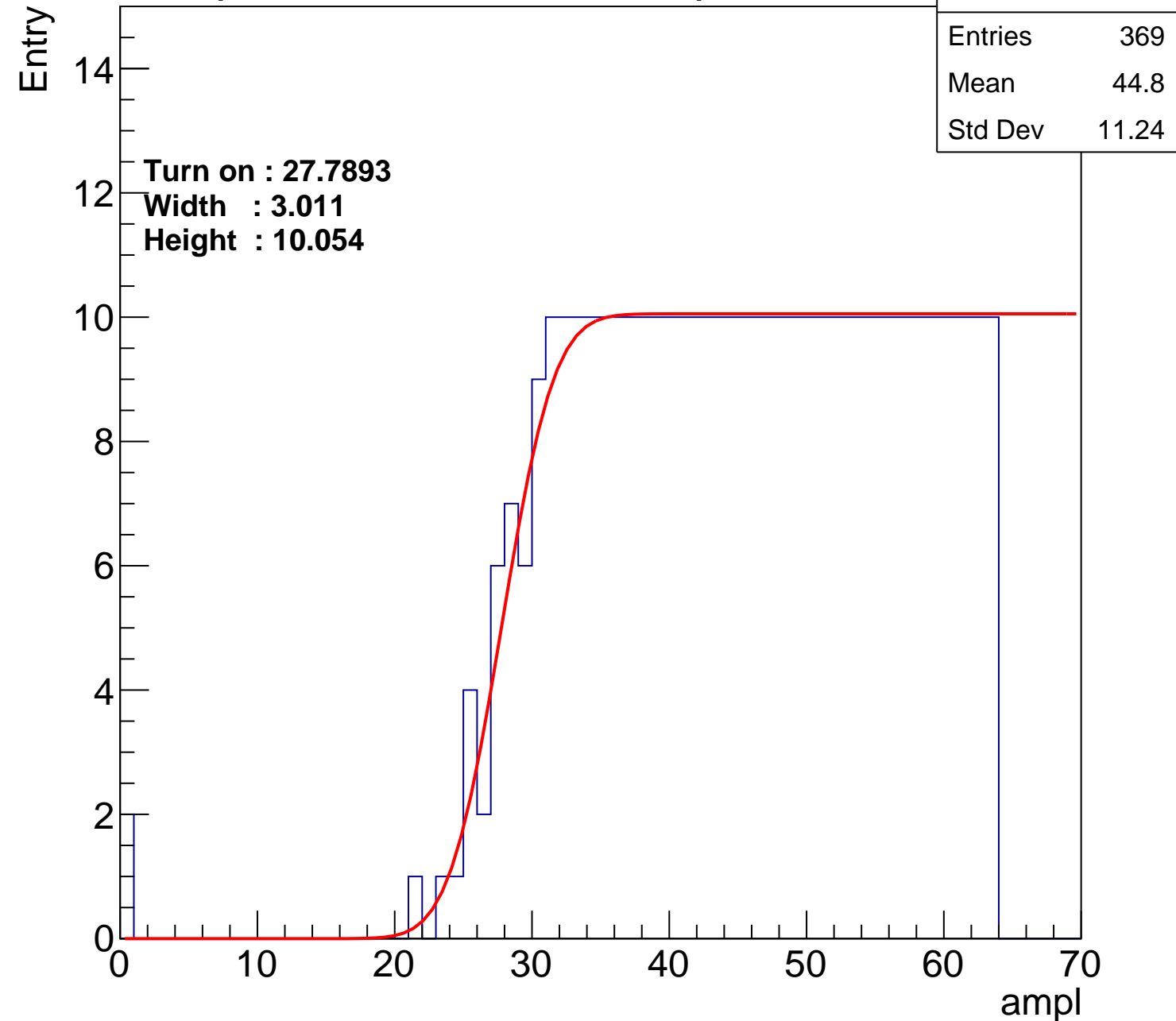
Width : 3.011

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch115

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.25
Std Dev	11.04

Turn on : 28.2333

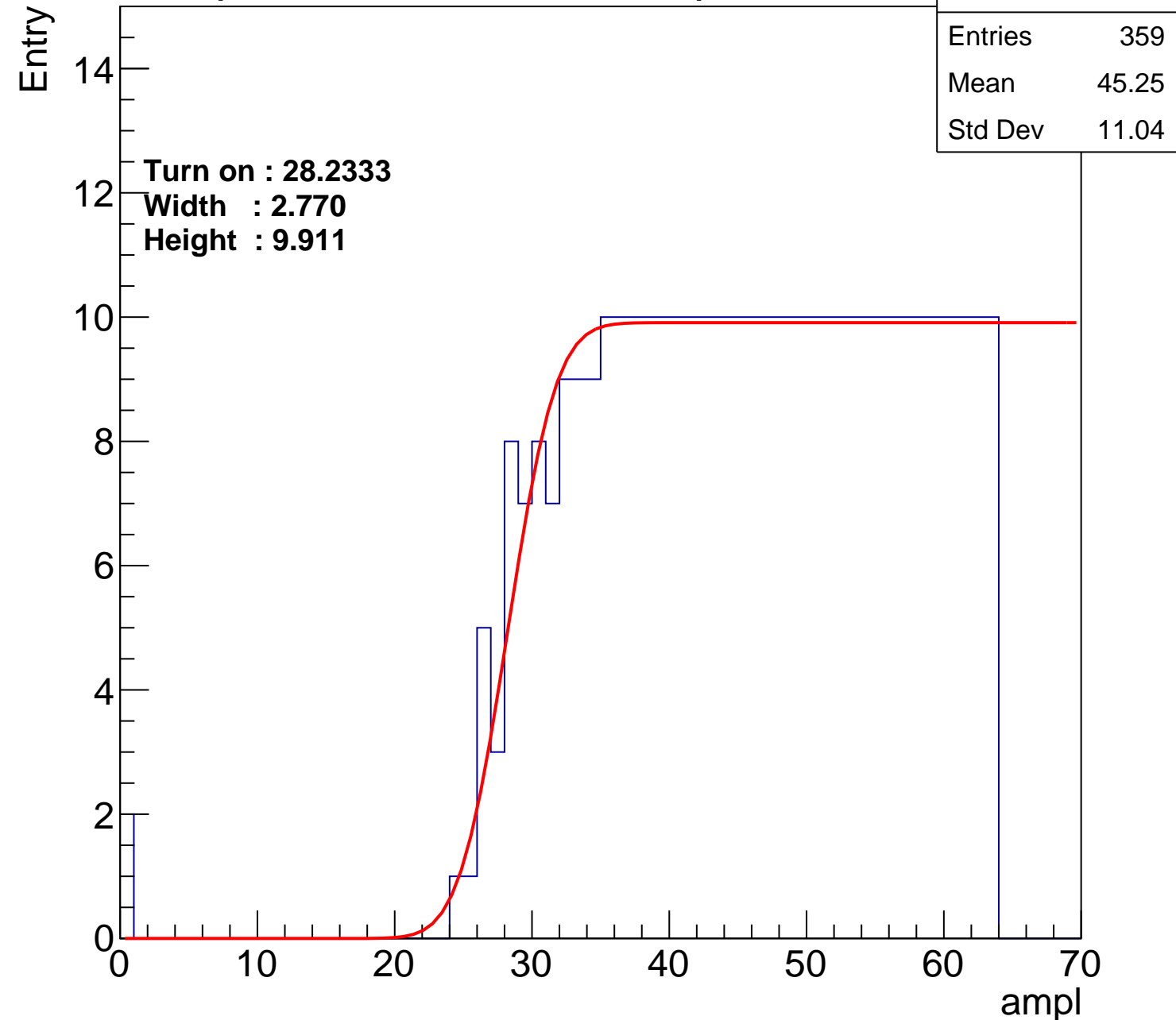
Width : 2.770

Height : 9.911

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch116

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.5674

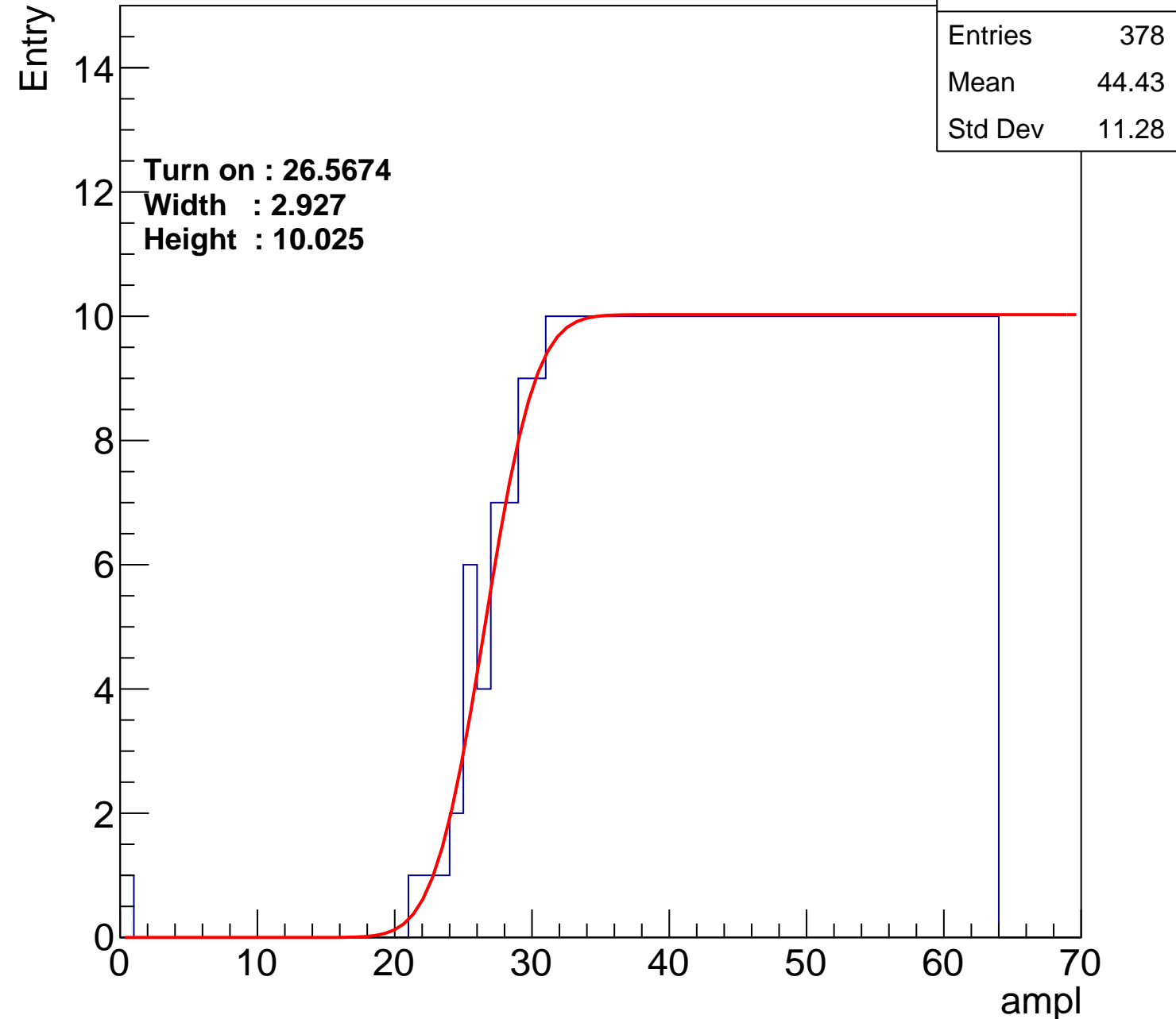
Width : 2.927

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch117

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	348
Mean	45.68
Std Dev	11.17

**Turn on : 29.8940**

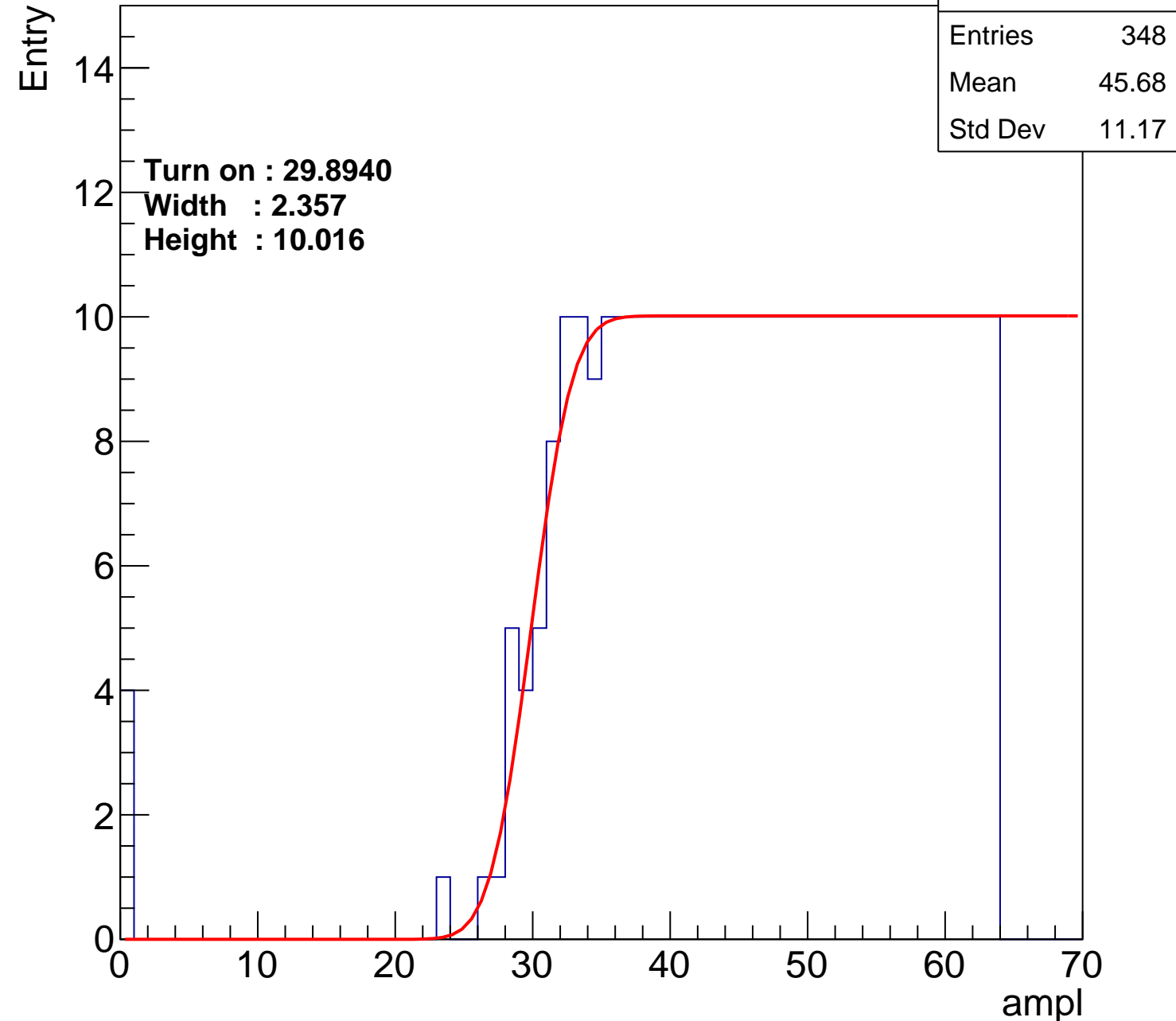
**Width : 2.357**

**Height : 10.016**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch118

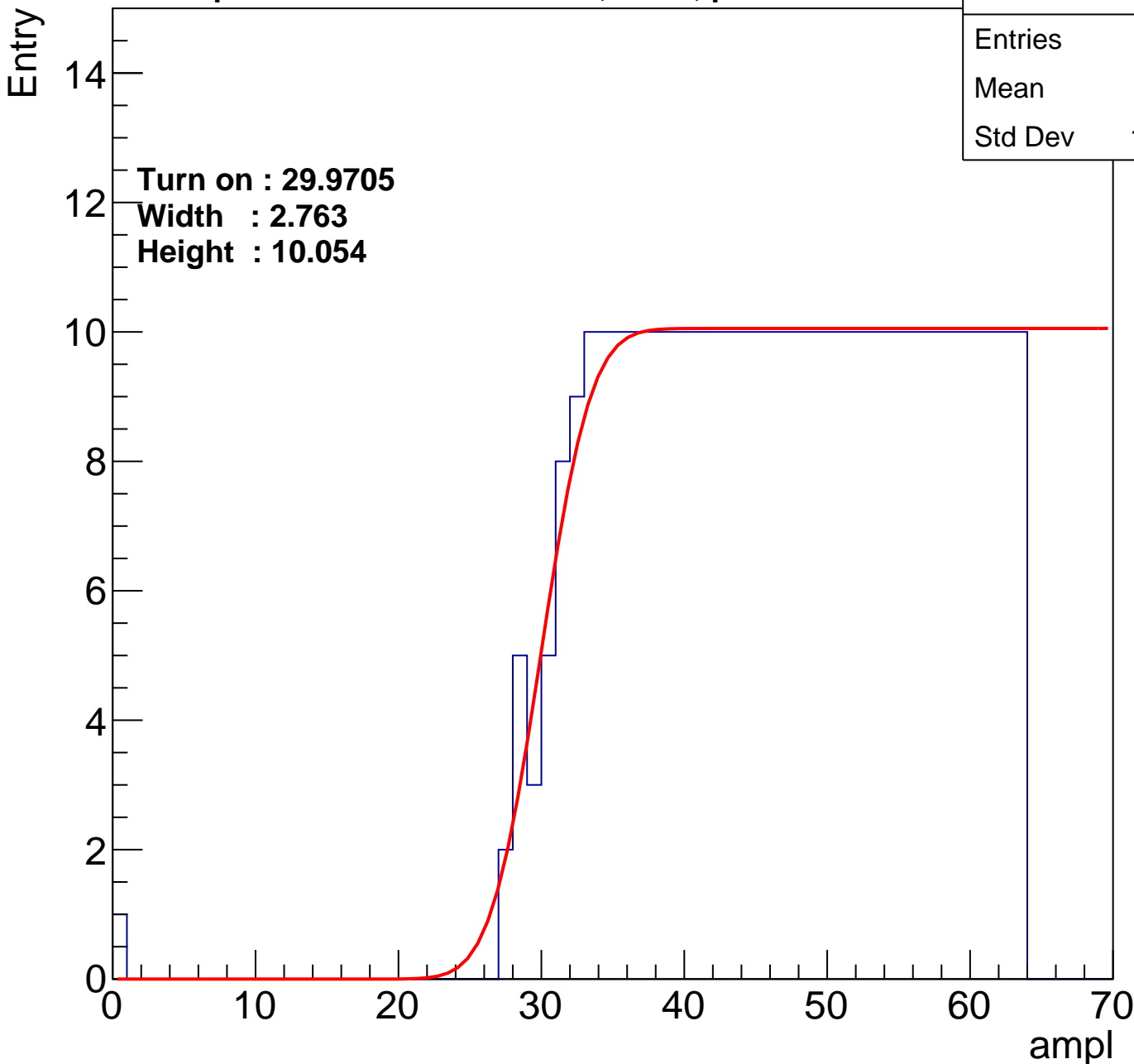
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	343
Mean	46.2
Std Dev	10.27

**Turn on : 29.9705**

**Width : 2.763**

**Height : 10.054**





# B0L002S, U24-ch119

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.27
Std Dev	10.98

**Turn on : 28.4589**

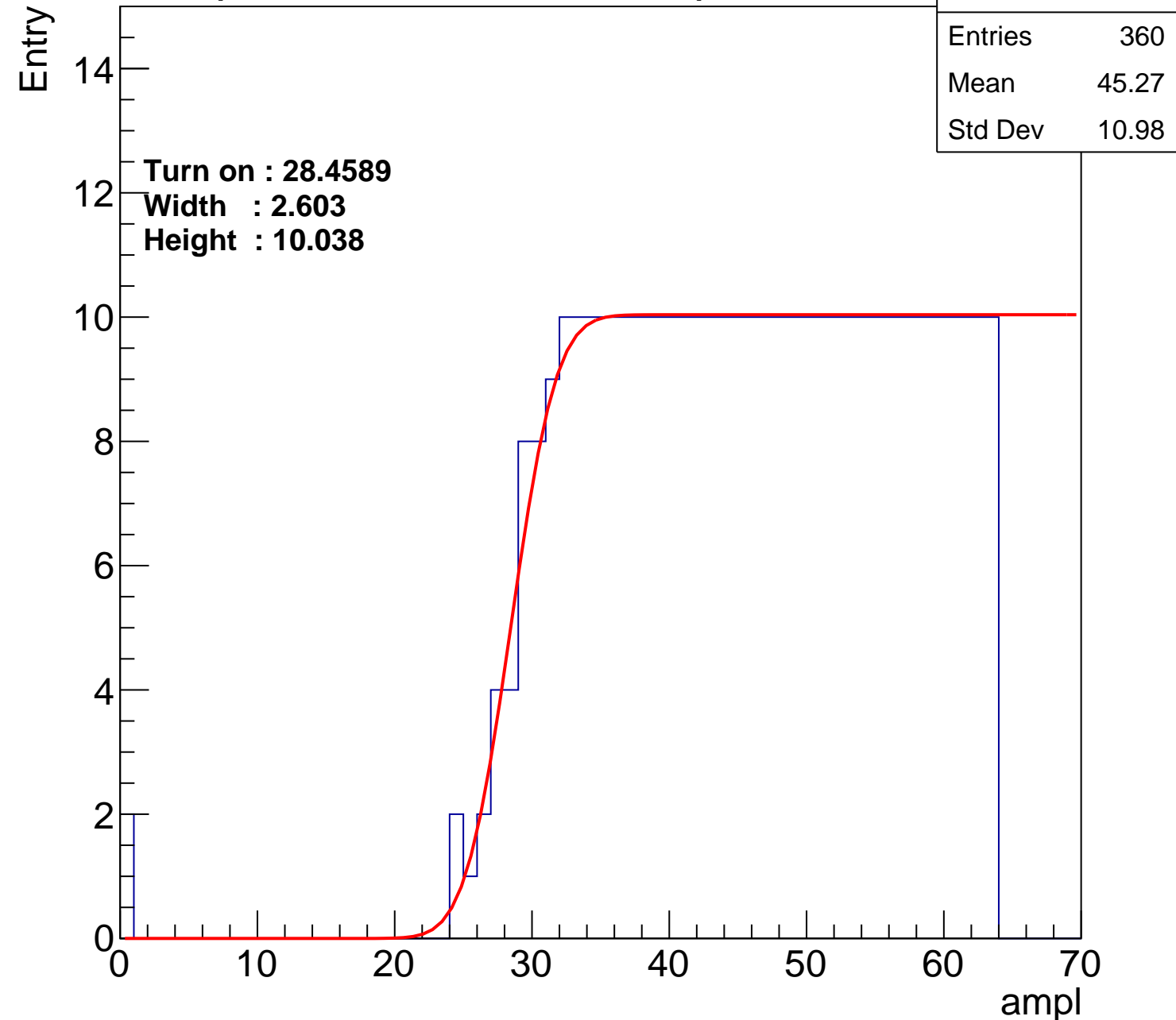
**Width : 2.603**

**Height : 10.038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch120

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.13
Std Dev	11.09

Turn on : 28.5488

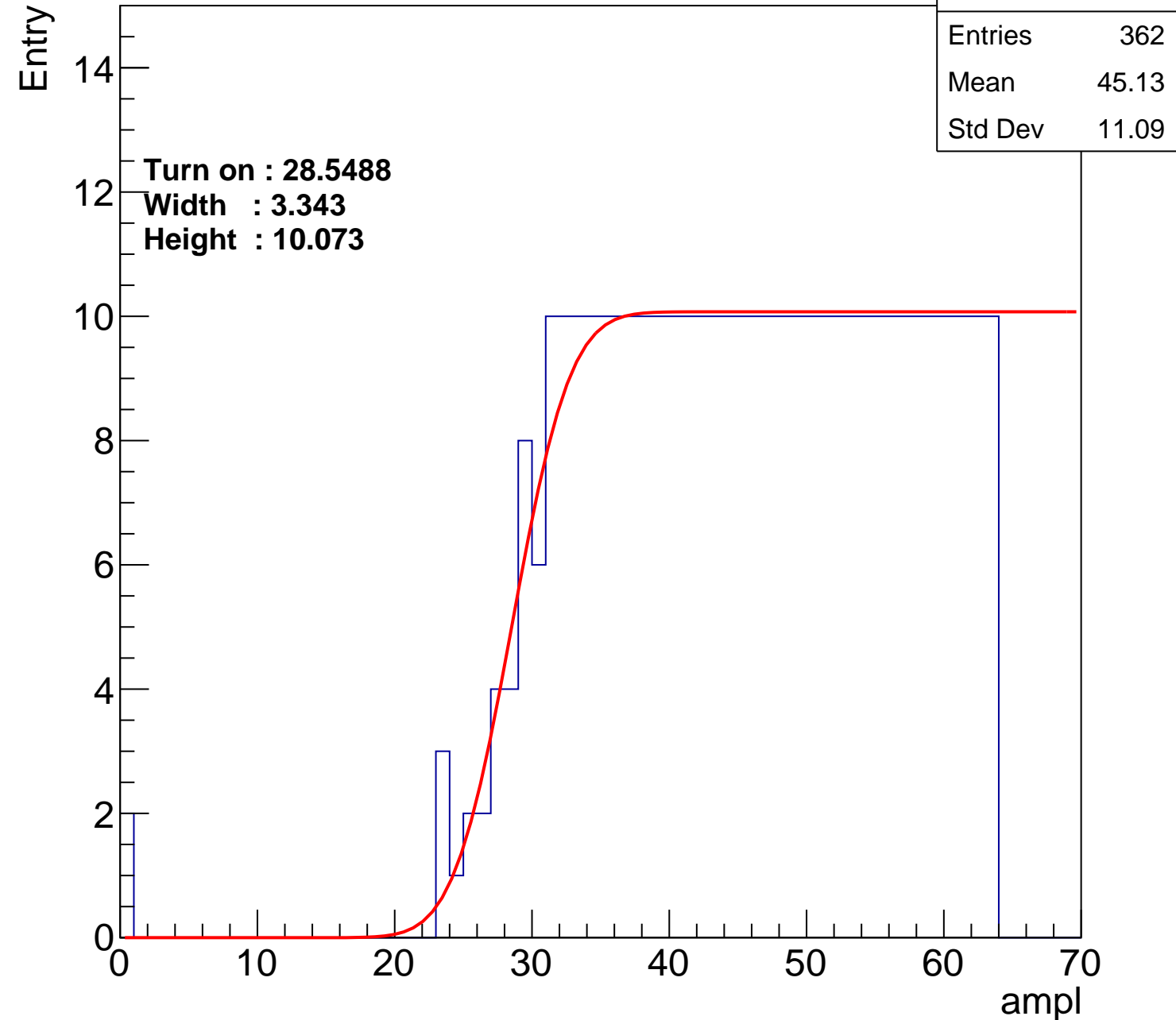
Width : 3.343

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch121

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.63
Std Dev	11.03

Turn on : 29.9198

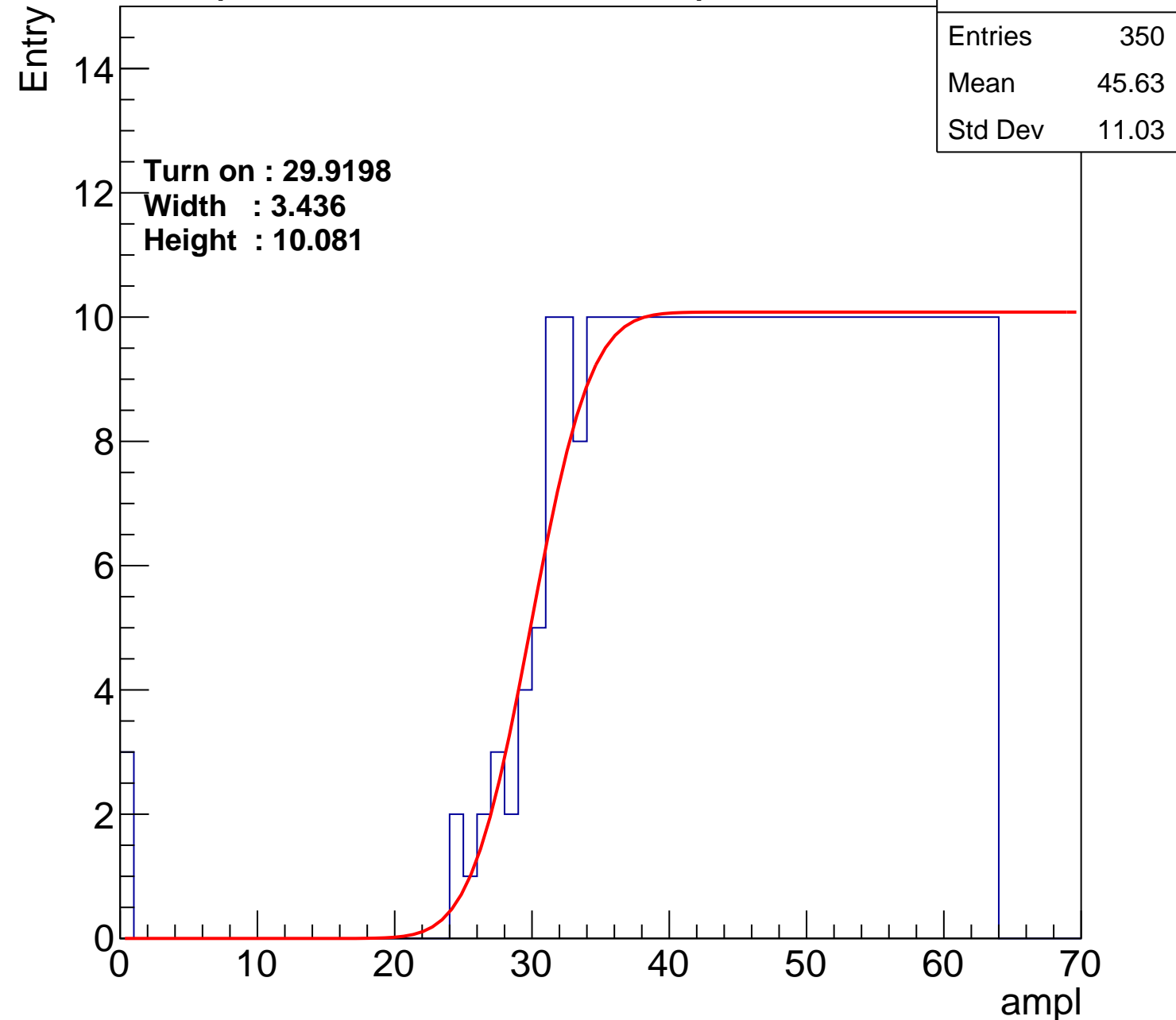
Width : 3.436

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch122

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.7
Std Dev	11.21

Turn on : 27.2504

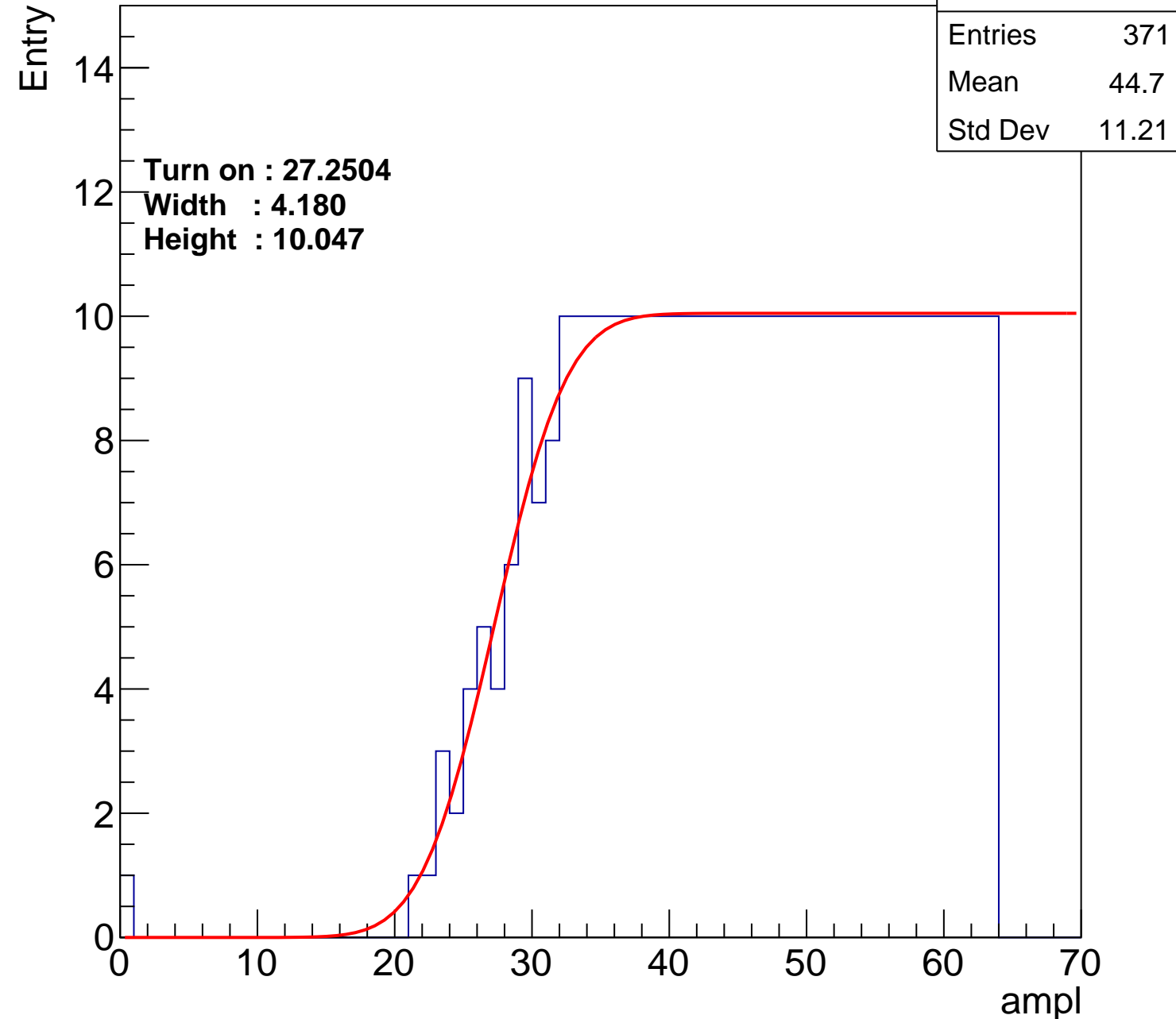
Width : 4.180

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch123

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.77
Std Dev	11.43

**Turn on : 27.6445**

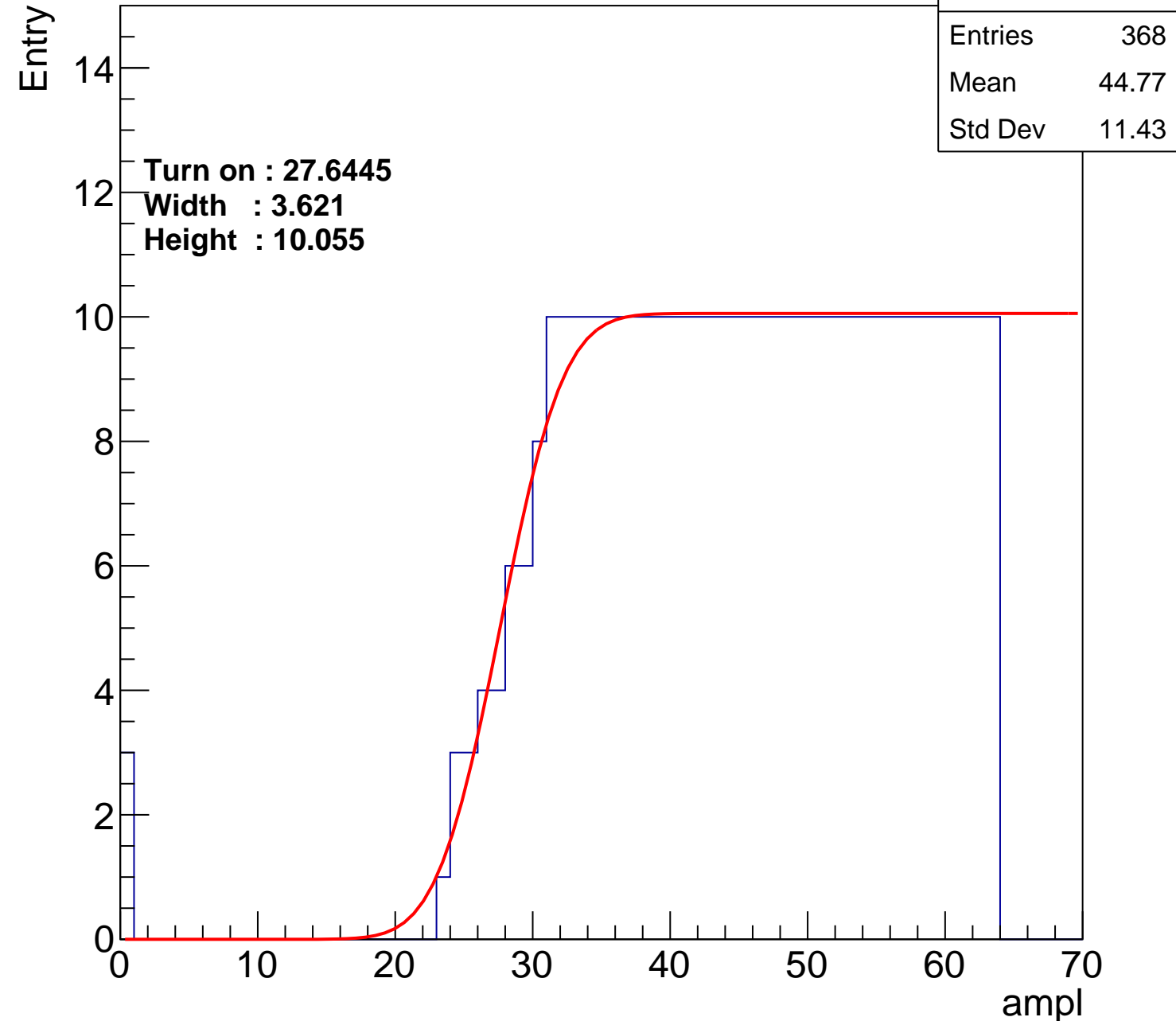
**Width : 3.621**

**Height : 10.055**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch124

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.77
Std Dev	10.59

Turn on : 29.1416

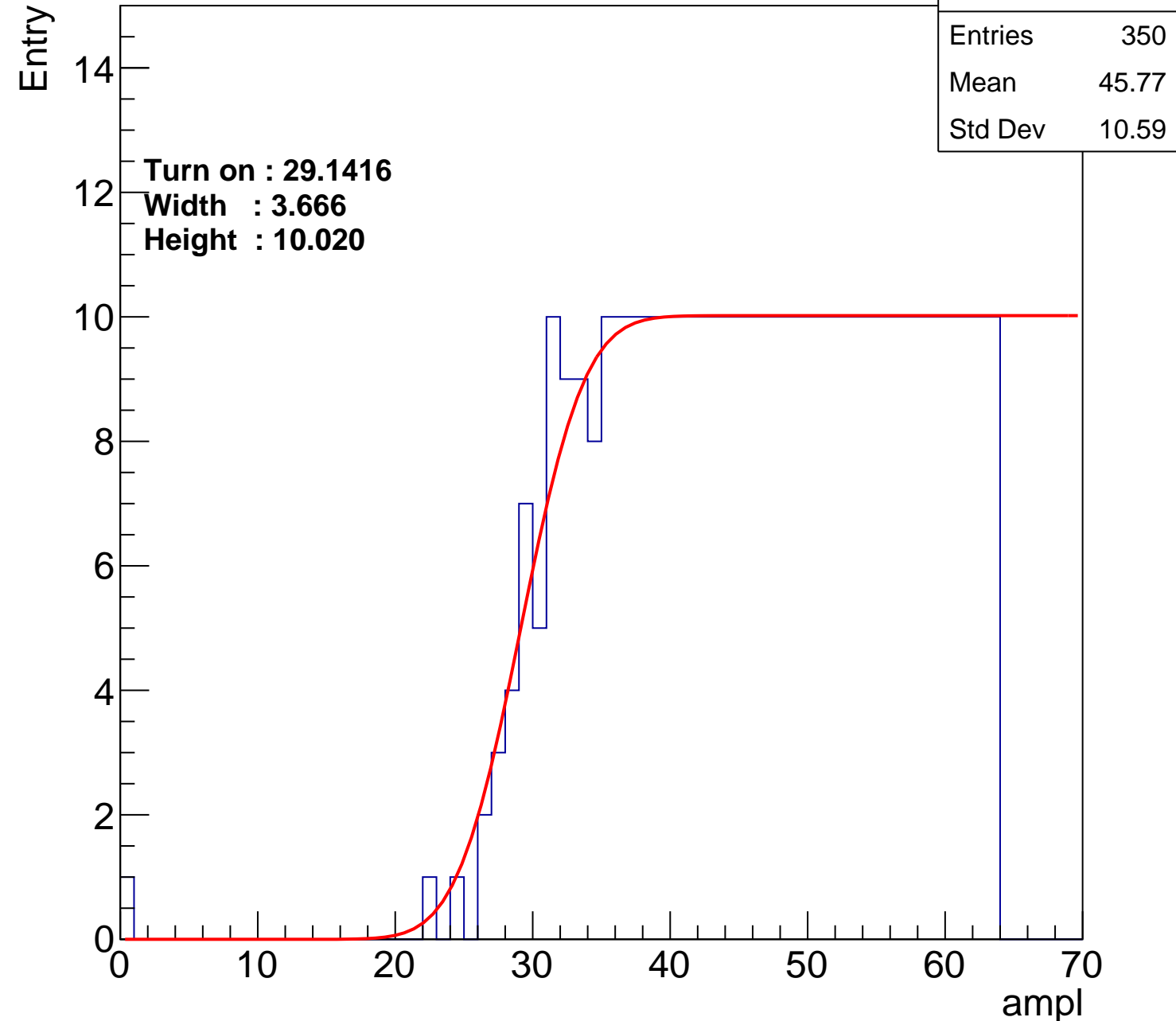
Width : 3.666

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch125

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45
Std Dev	11.28

**Turn on : 28.2368**

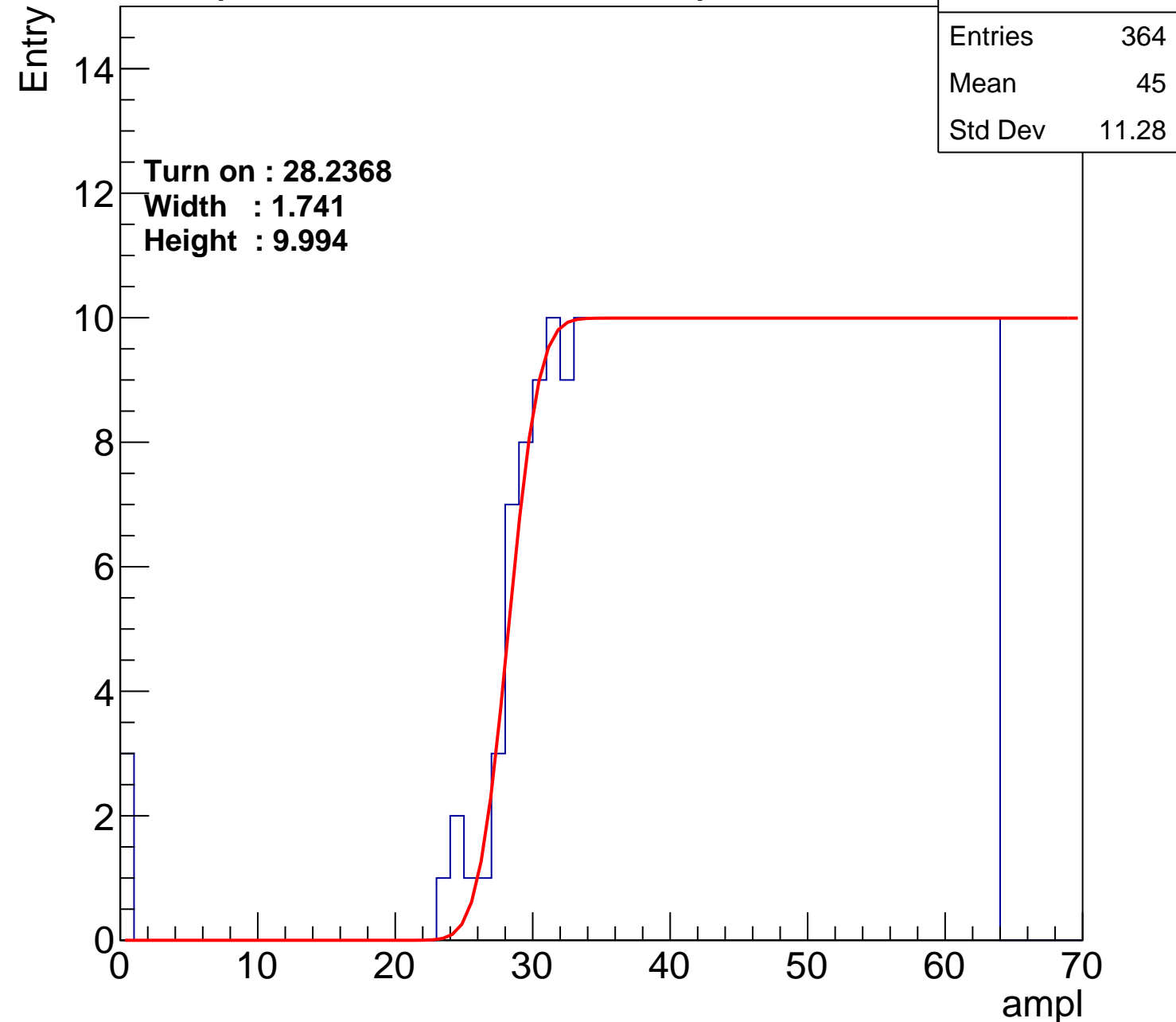
**Width : 1.741**

**Height : 9.994**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch126

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.47
Std Dev	10.78

**Turn on : 28.8287**

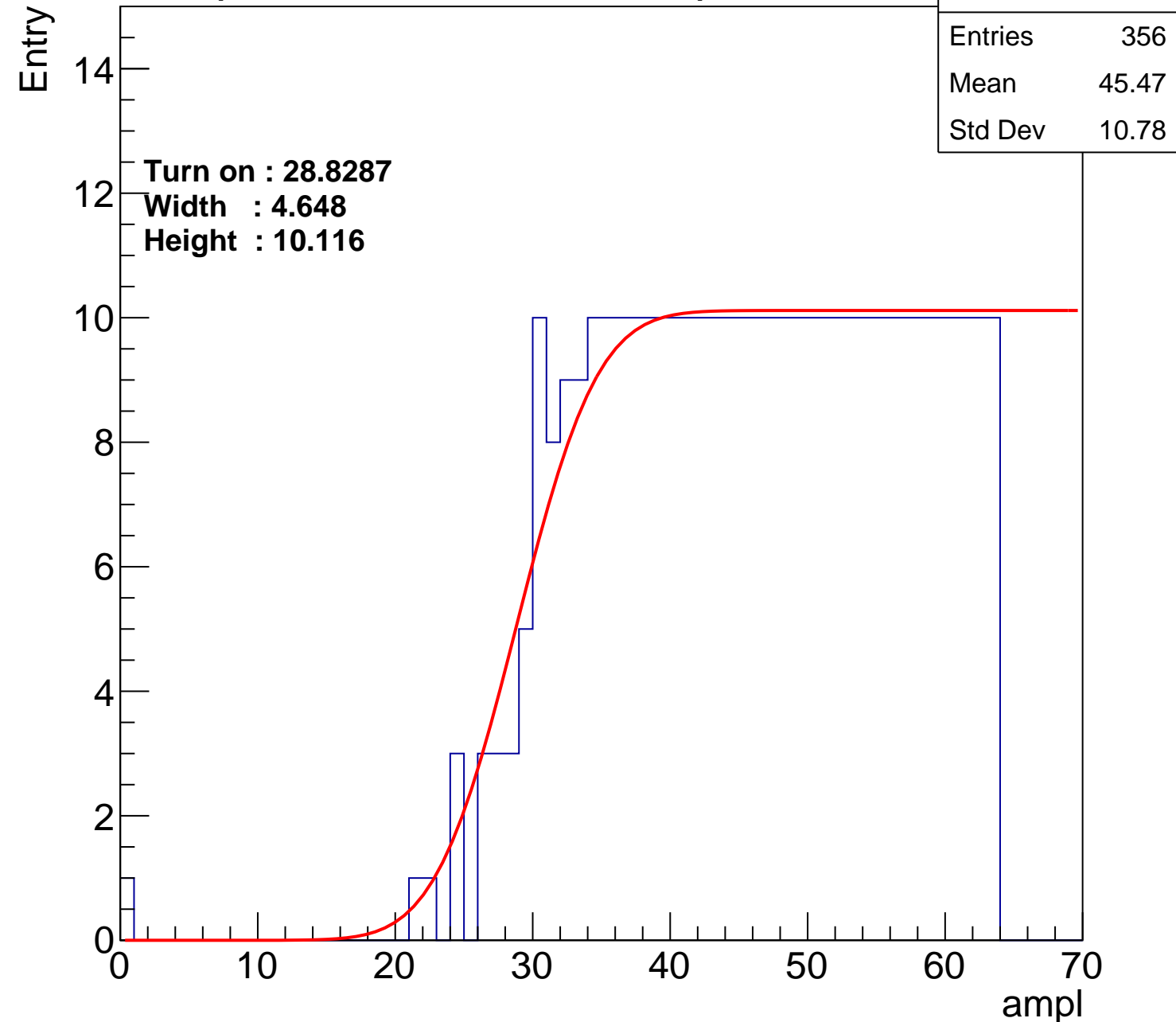
**Width : 4.648**

**Height : 10.116**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U24-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.7171

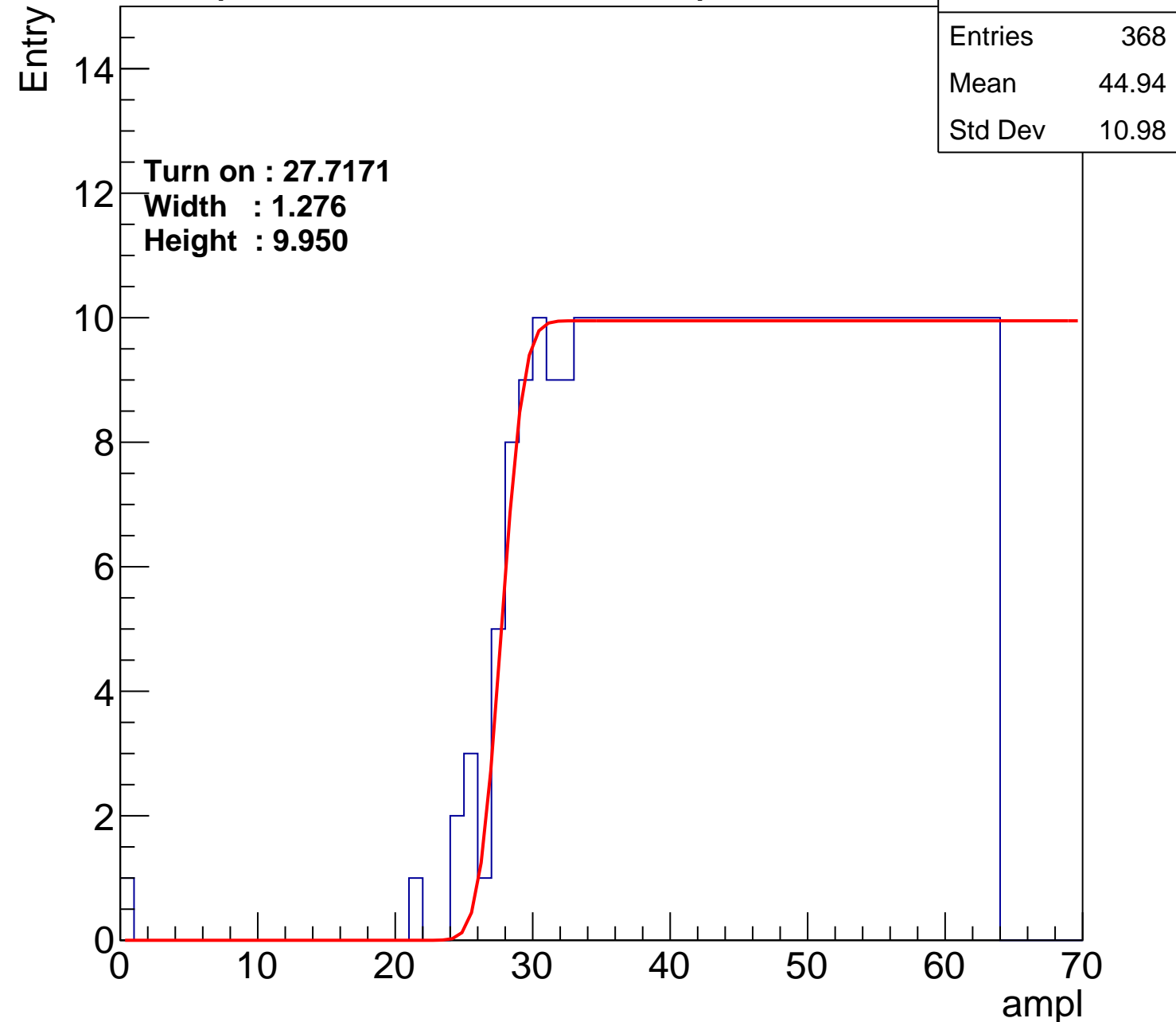
Width : 1.276

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U24-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.7171

Width : 1.276

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

