

B1L103S, U23-ch0

calib_packv5_042523_0143.root, FC#7, port C2

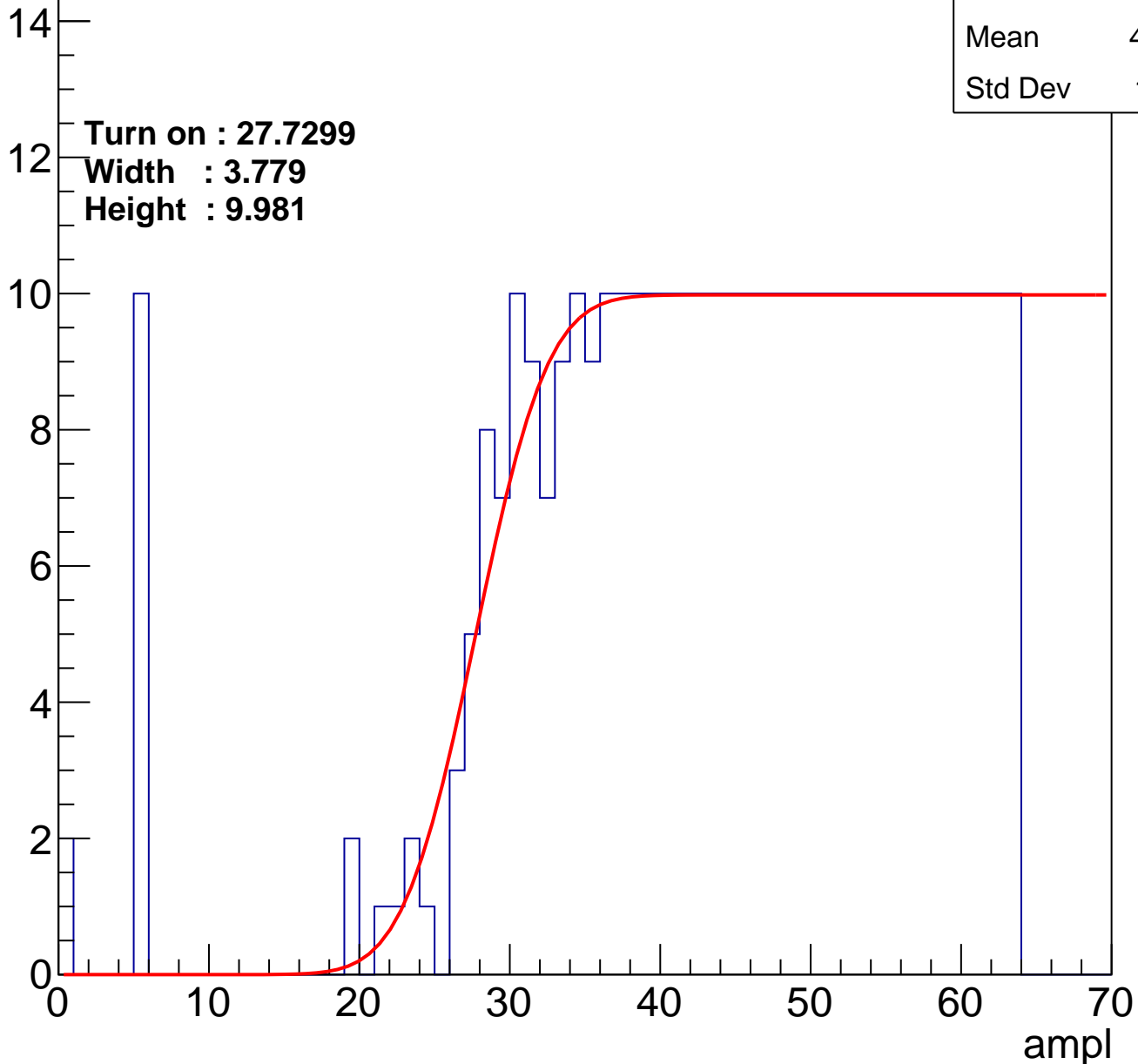
Entries	376
Mean	43.77
Std Dev	12.91

Turn on : 27.7299

Width : 3.779

Height : 9.981

Entry



B1L103S, U23-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 25.5585

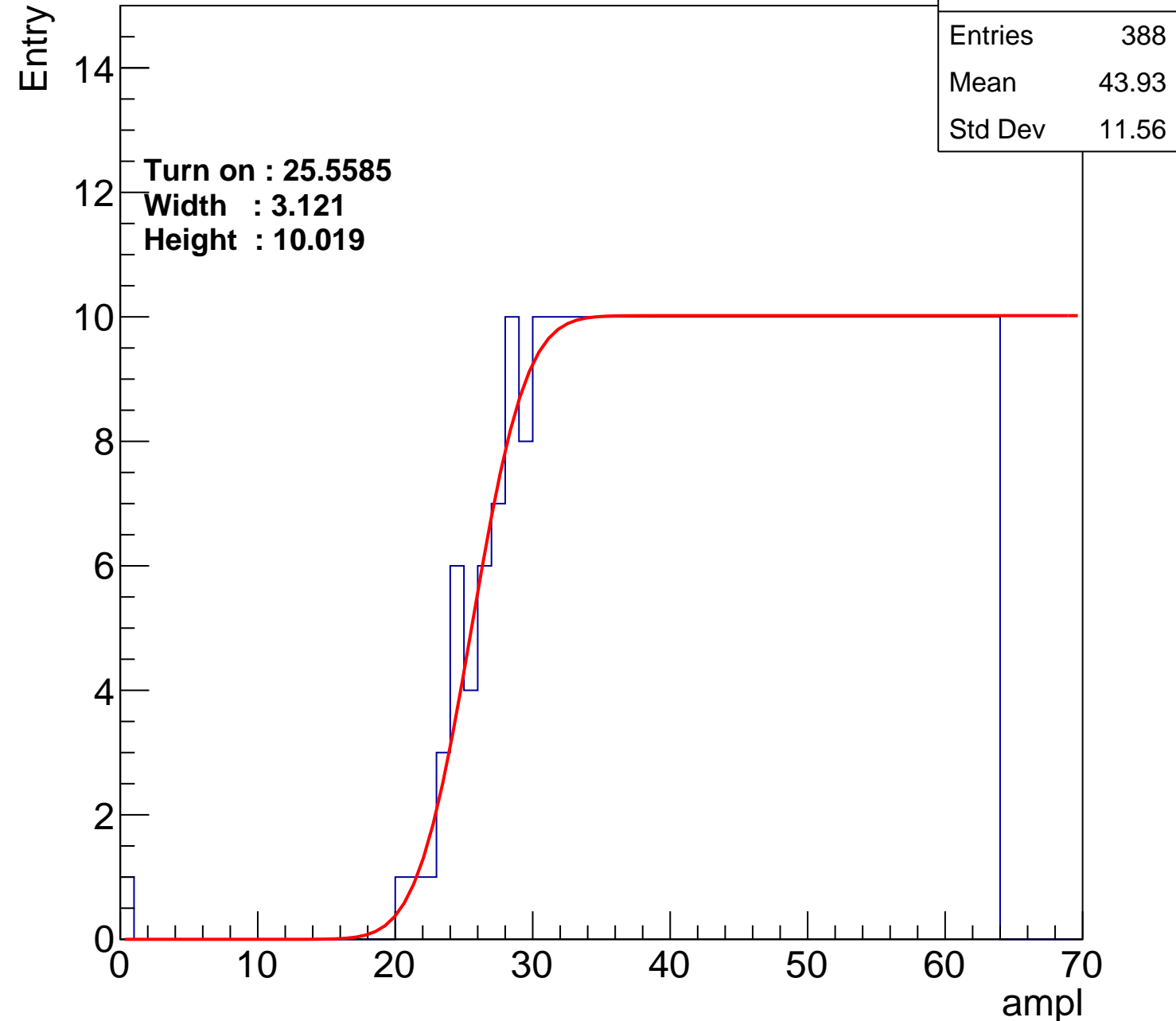
Width : 3.121

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.97
Std Dev	11.95

Turn on : 25.7278

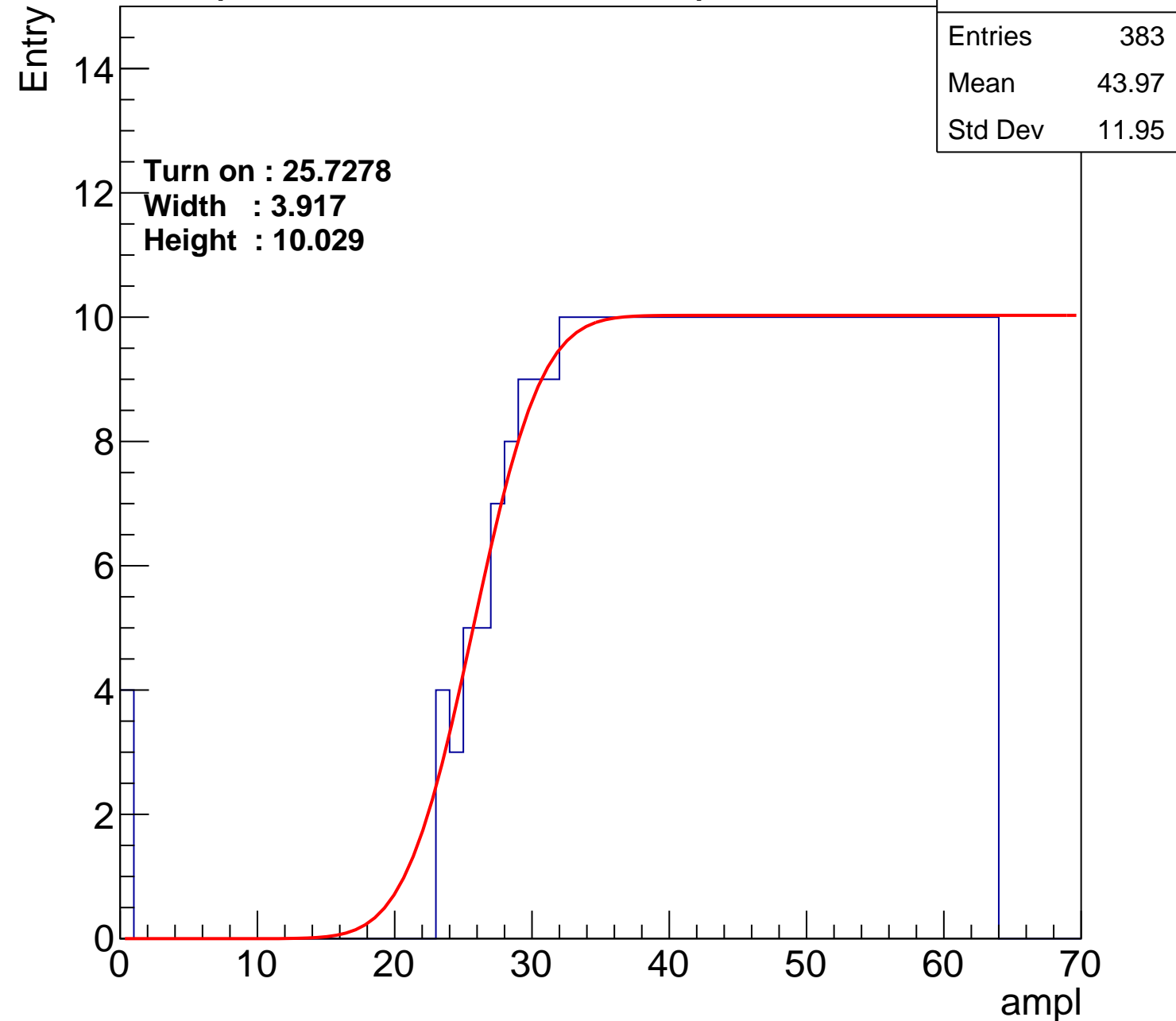
Width : 3.917

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.86
Std Dev	11.4

Turn on : 27.7072

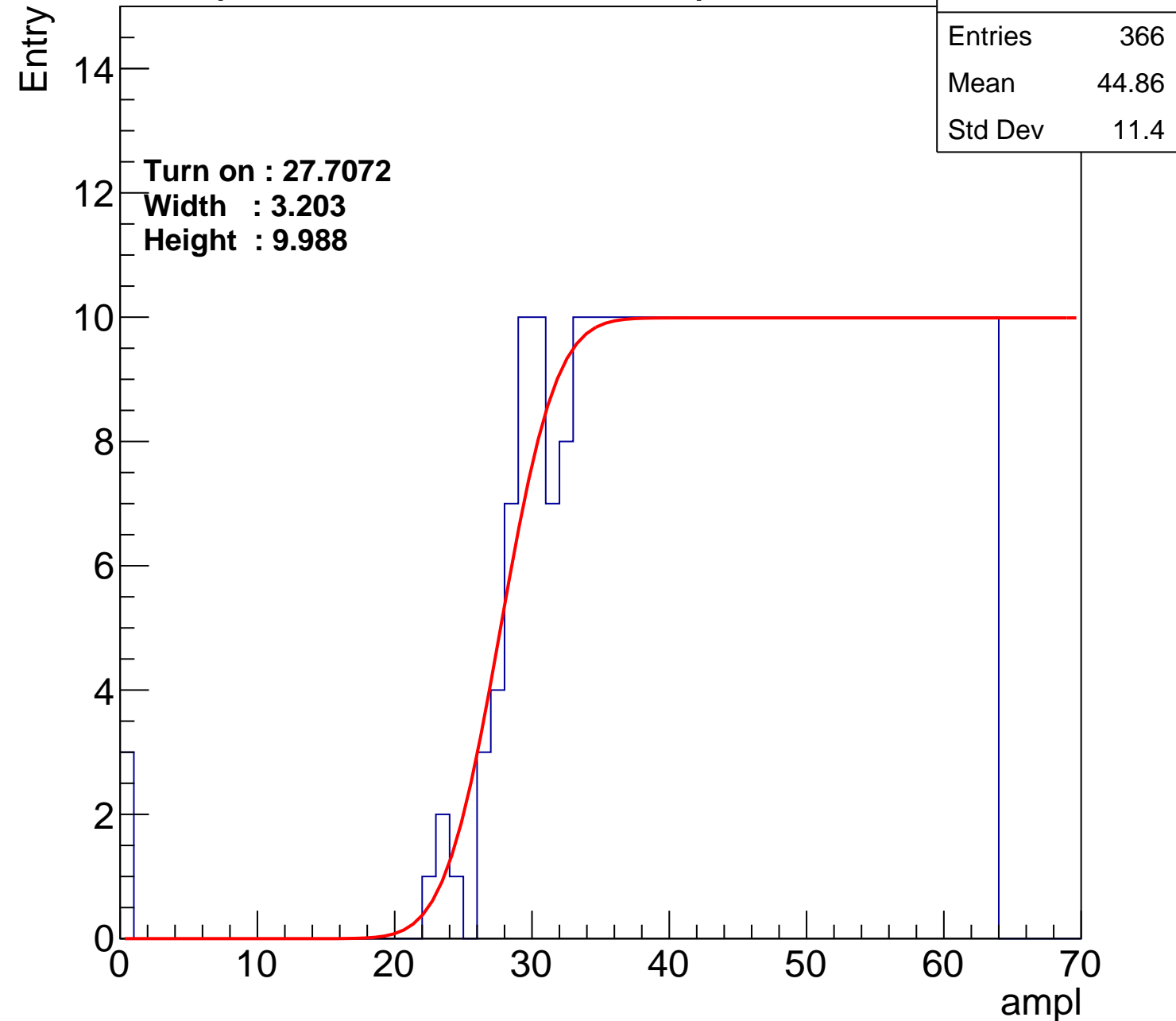
Width : 3.203

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch4

calib_packv5_042523_0143.root, FC#7, port C2

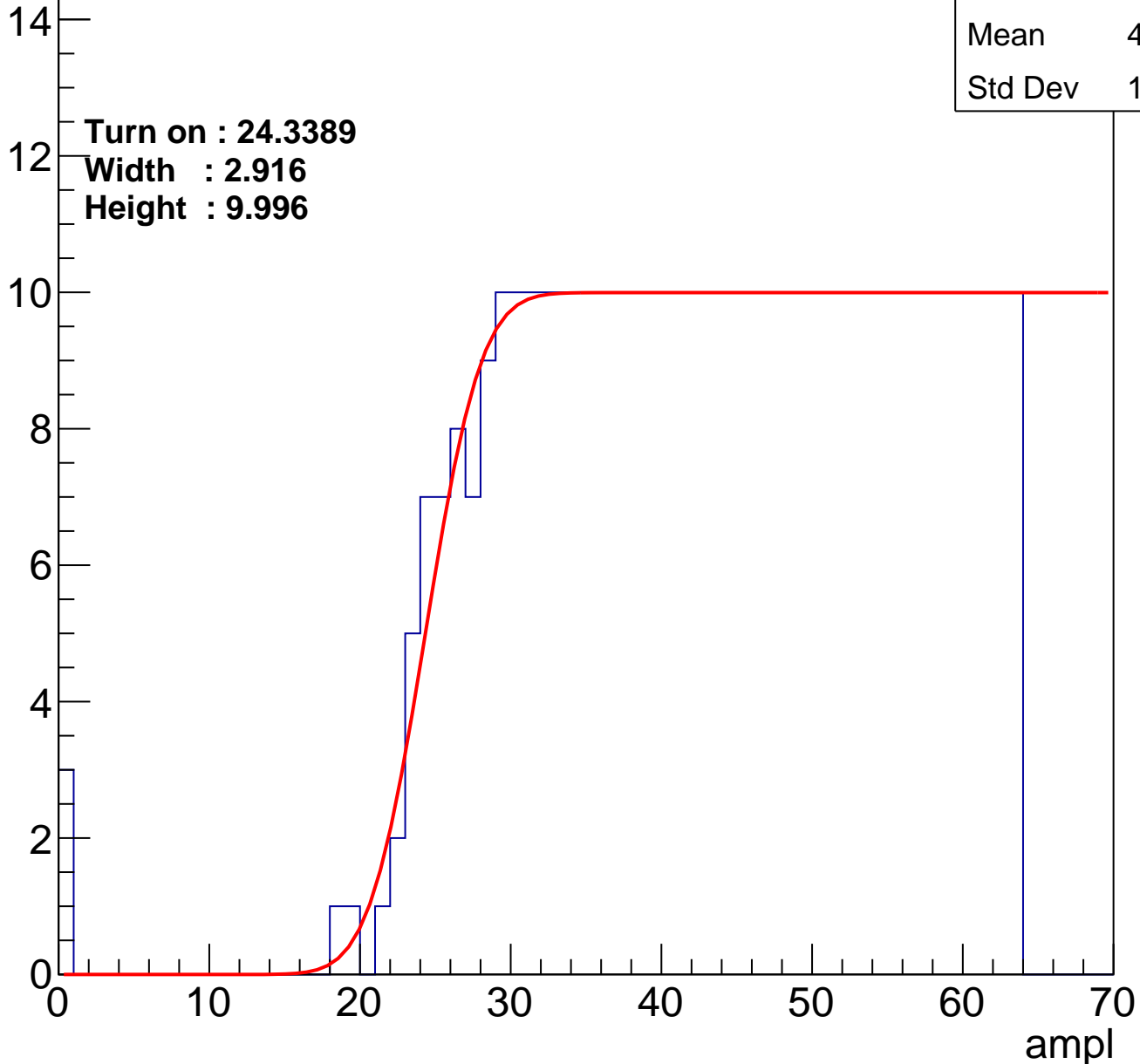
Entries	401
Mean	43.16
Std Dev	12.22

Turn on : 24.3389

Width : 2.916

Height : 9.996

Entry



B1L103S, U23-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.52
Std Dev	11.23

Turn on : 26.5537

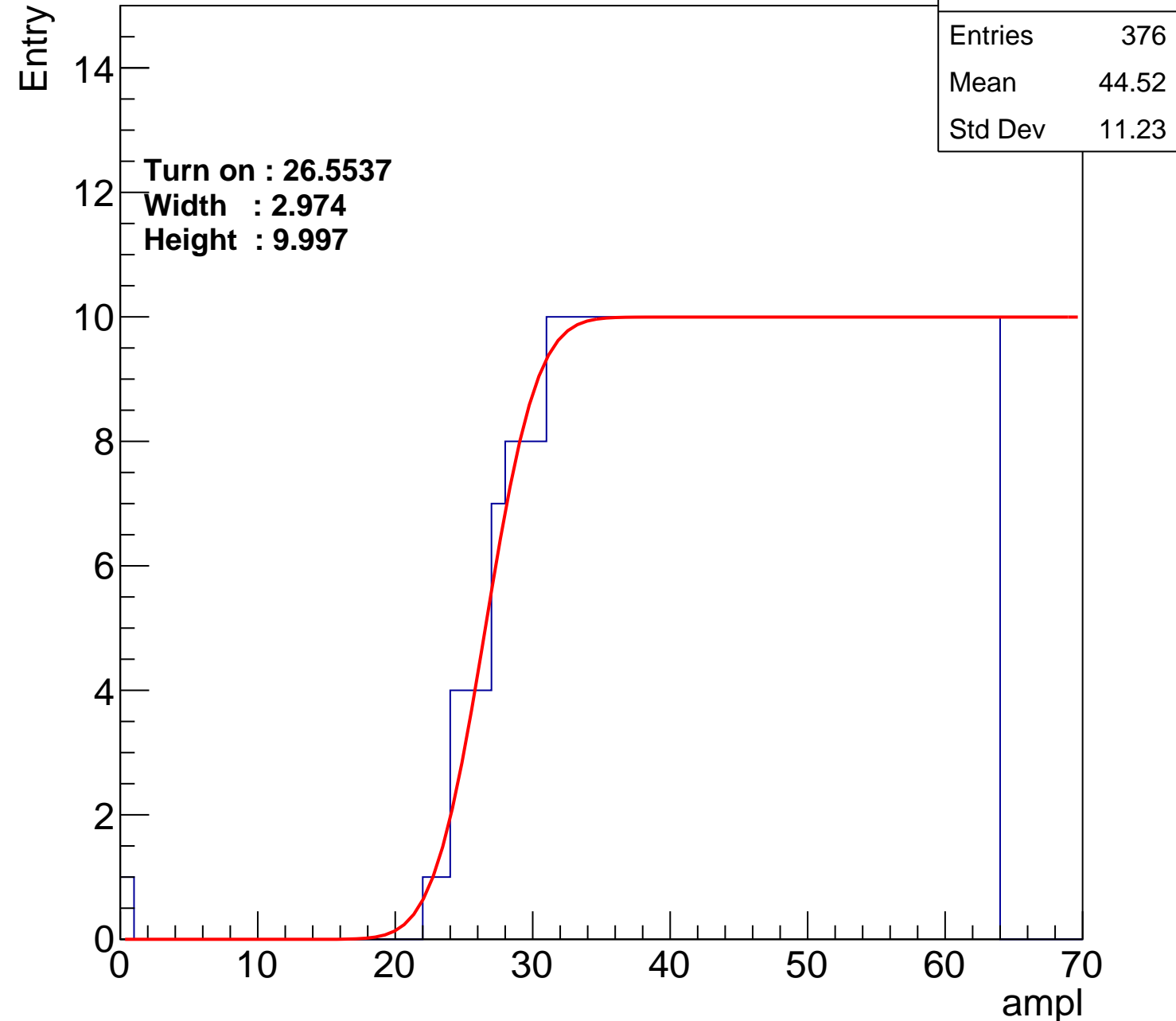
Width : 2.974

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.6119

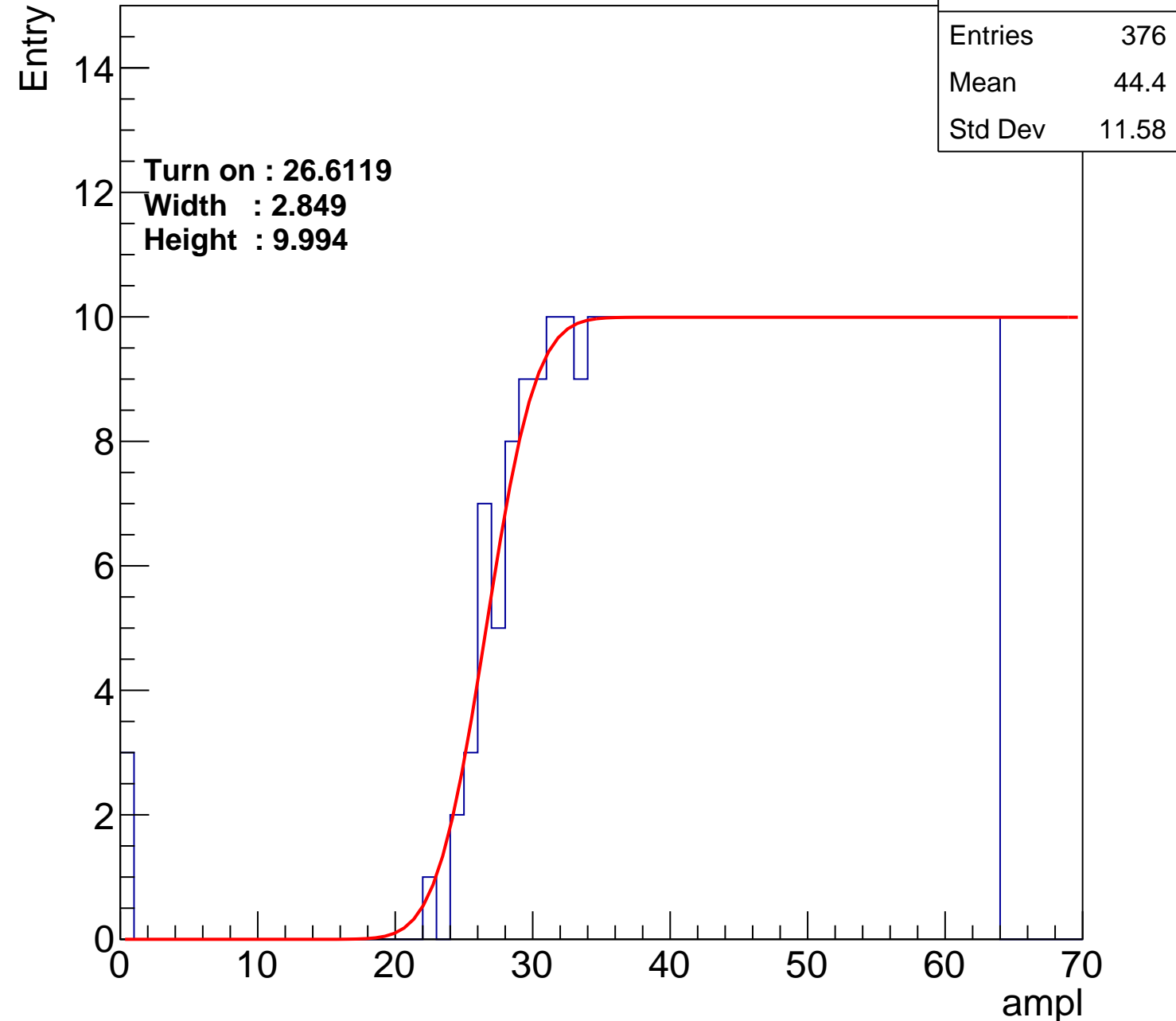
Width : 2.849

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	340
Mean	46.14
Std Dev	10.77

Turn on : 30.2959

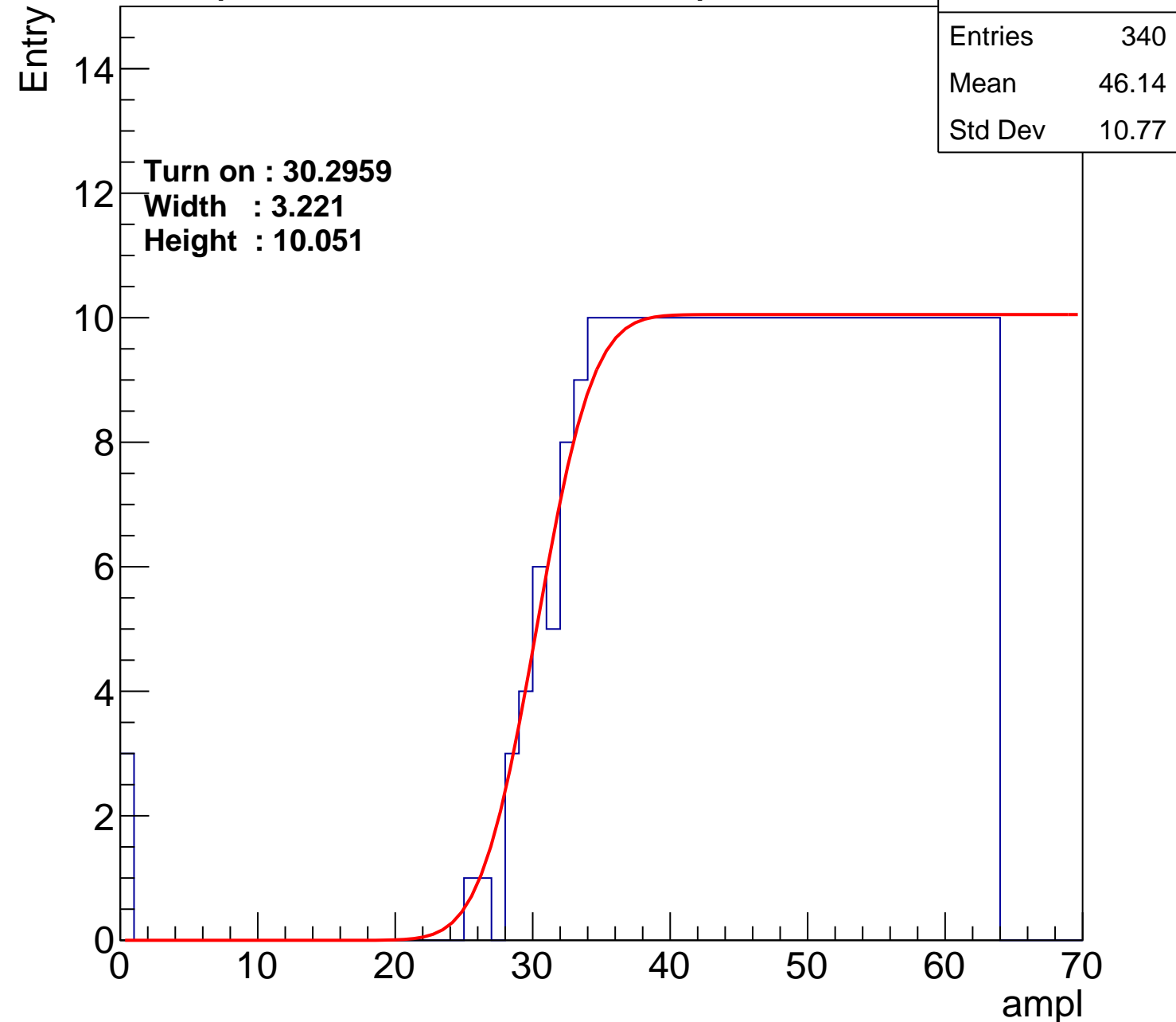
Width : 3.221

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 27.0631

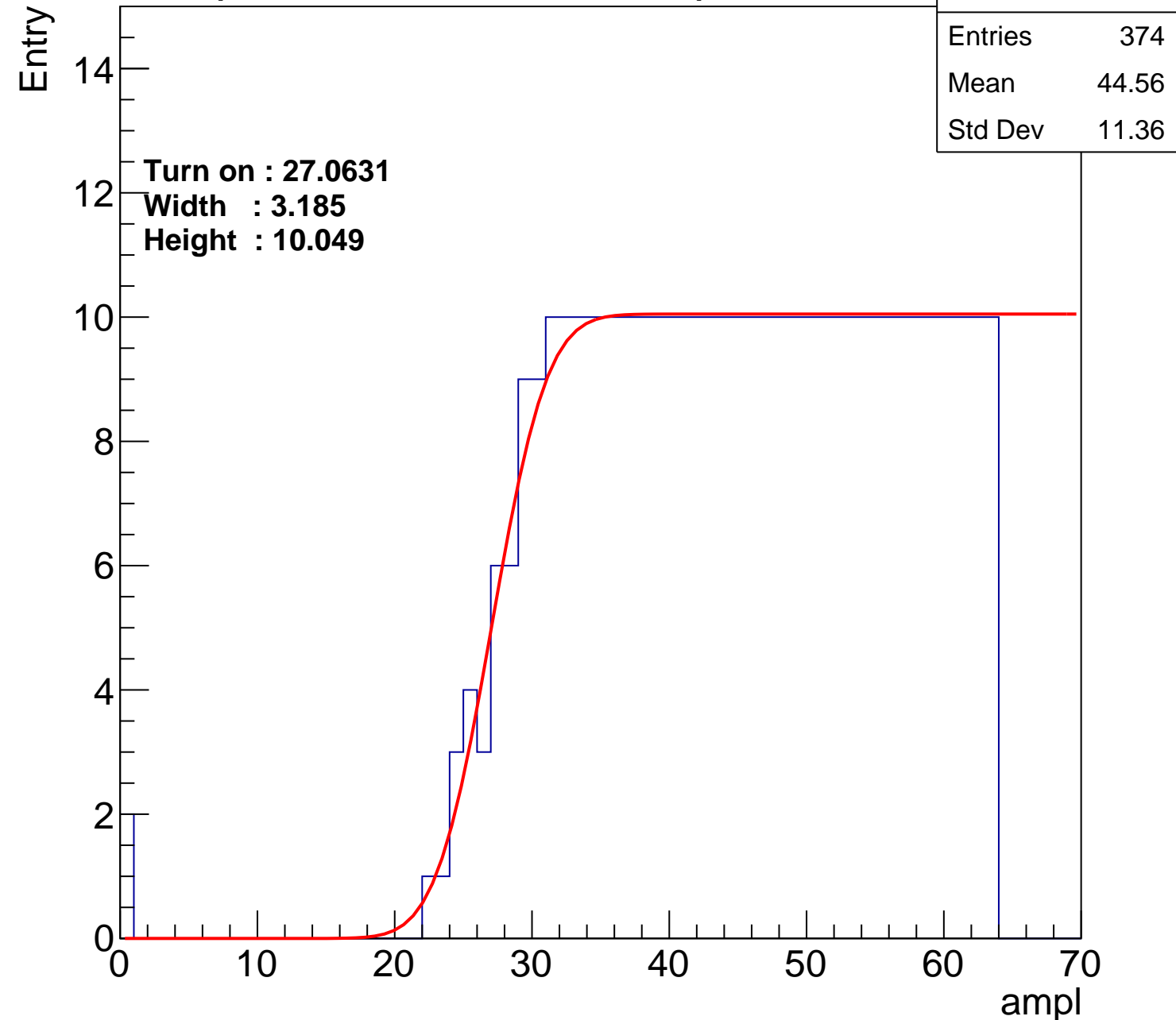
Width : 3.185

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	43.95
Std Dev	12.24

Turn on : 26.6024

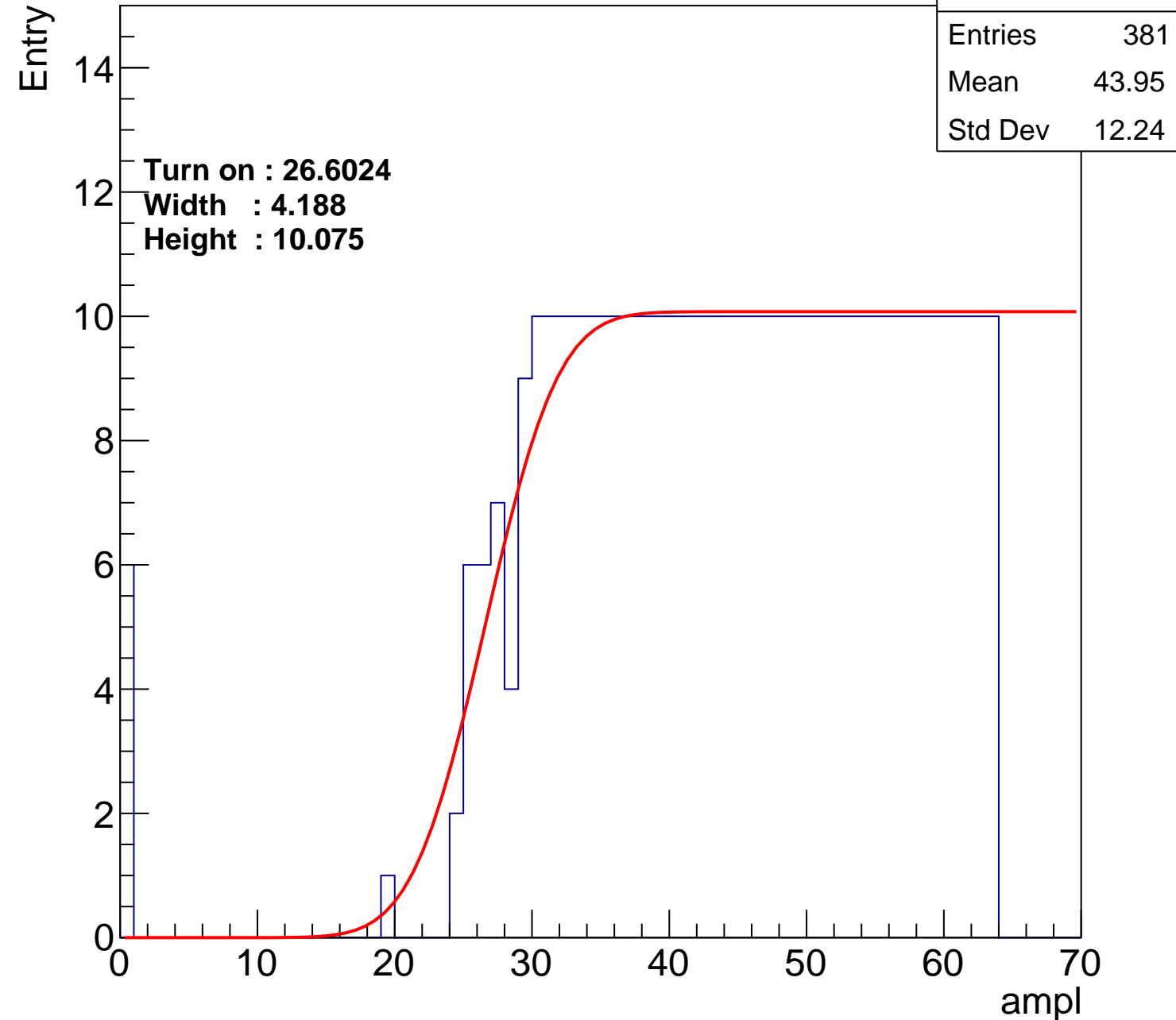
Width : 4.188

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.9991

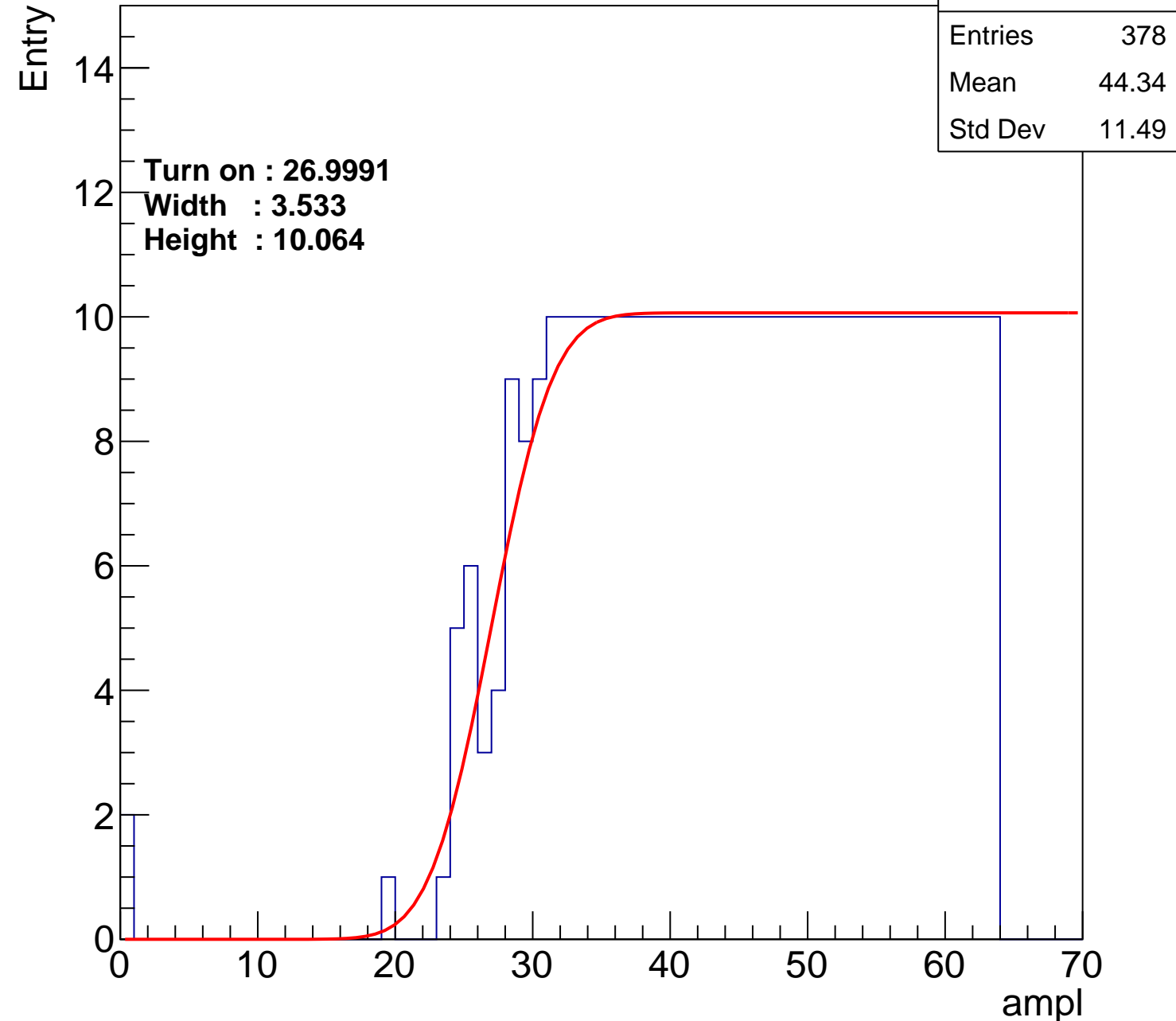
Width : 3.533

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch11

calib_packv5_042523_0143.root, FC#7, port C2

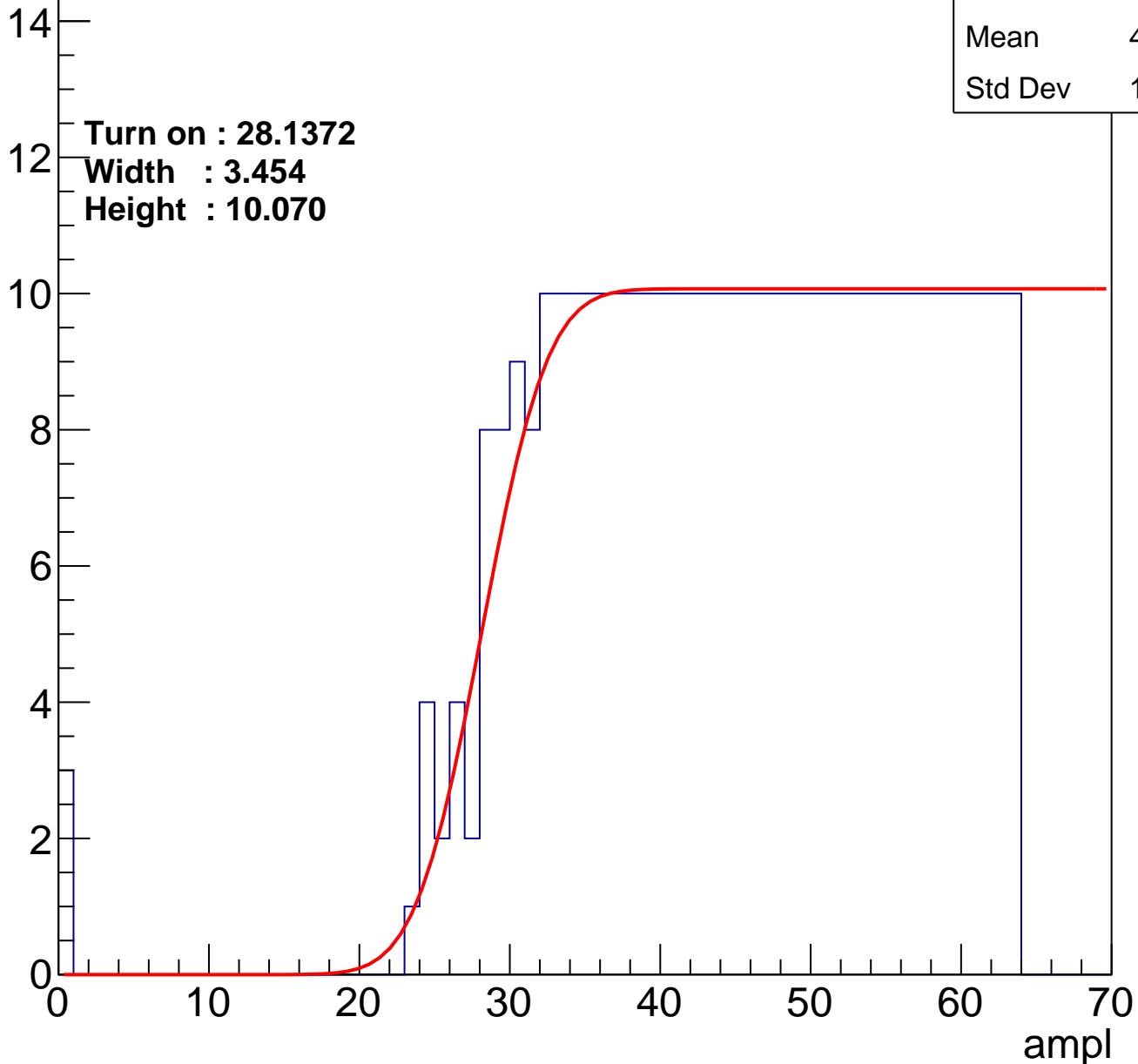
Entries	369
Mean	44.72
Std Dev	11.45

Turn on : 28.1372

Width : 3.454

Height : 10.070

Entry



B1L103S, U23-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.37
Std Dev	11.72

Turn on : 27.2619

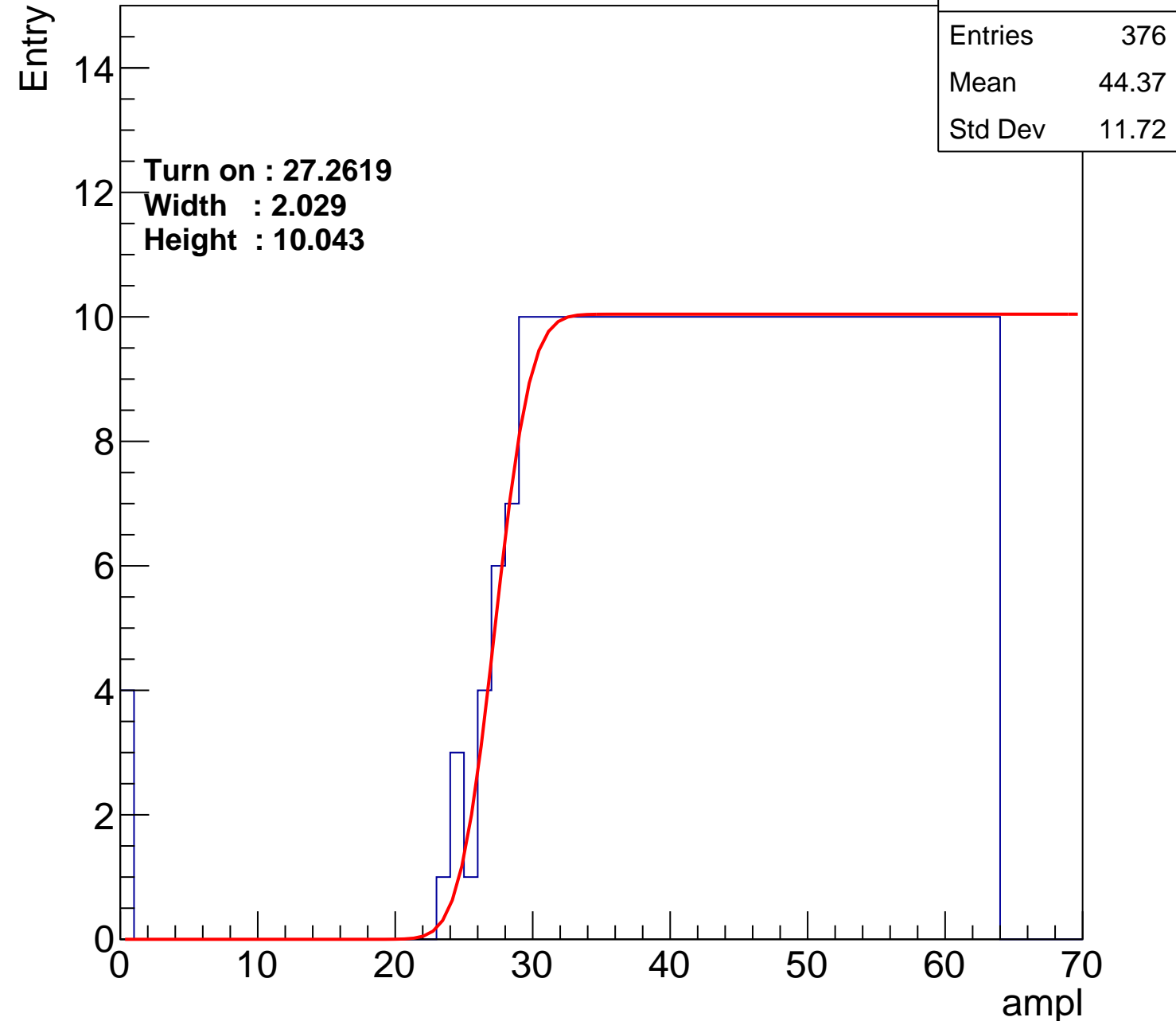
Width : 2.029

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch13

calib_packv5_042523_0143.root, FC#7, port C2

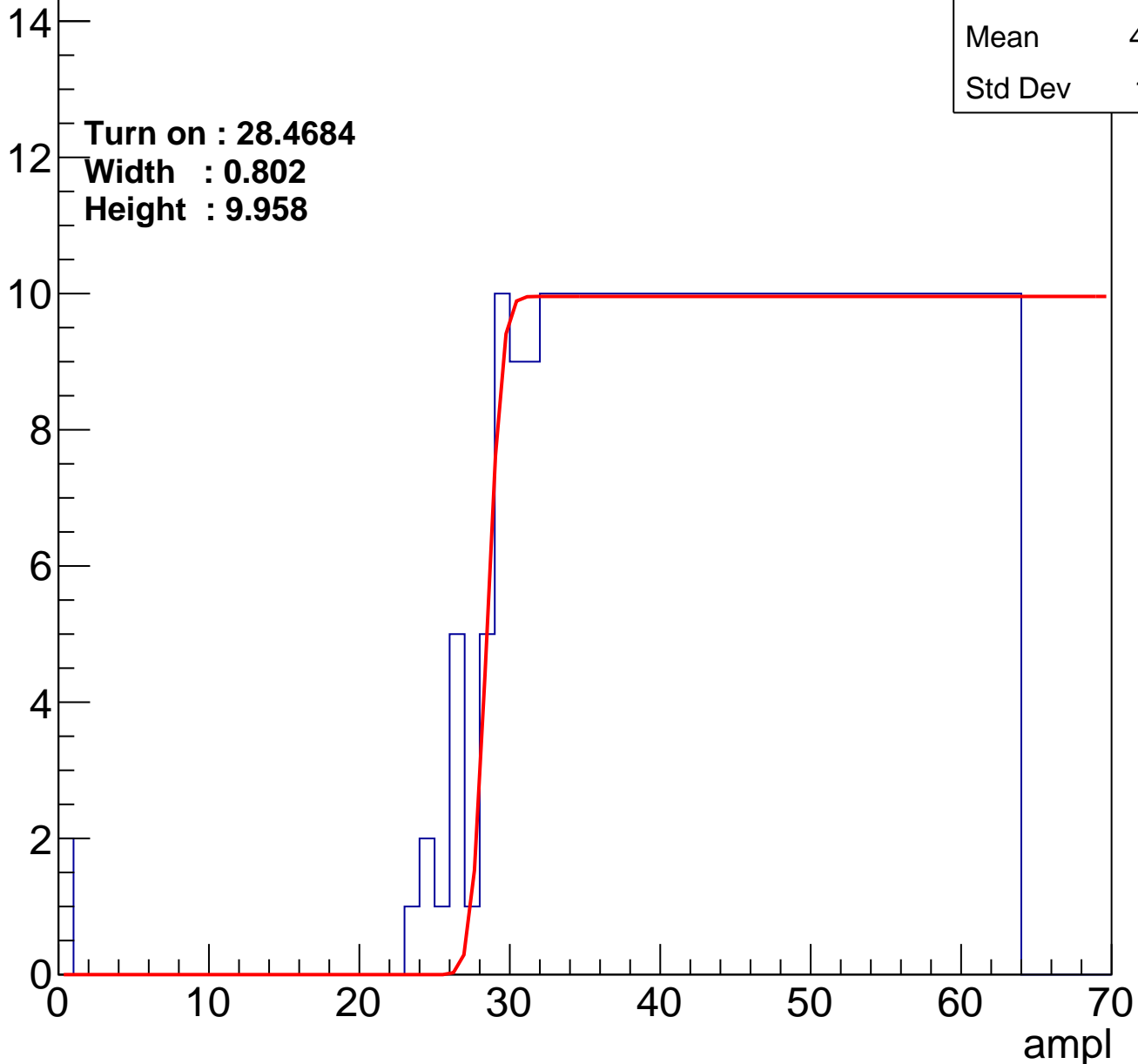
Entries	365
Mean	45.02
Std Dev	11.11

Turn on : 28.4684

Width : 0.802

Height : 9.958

Entry



B1L103S, U23-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.58
Std Dev	11.39

Turn on : 27.2486

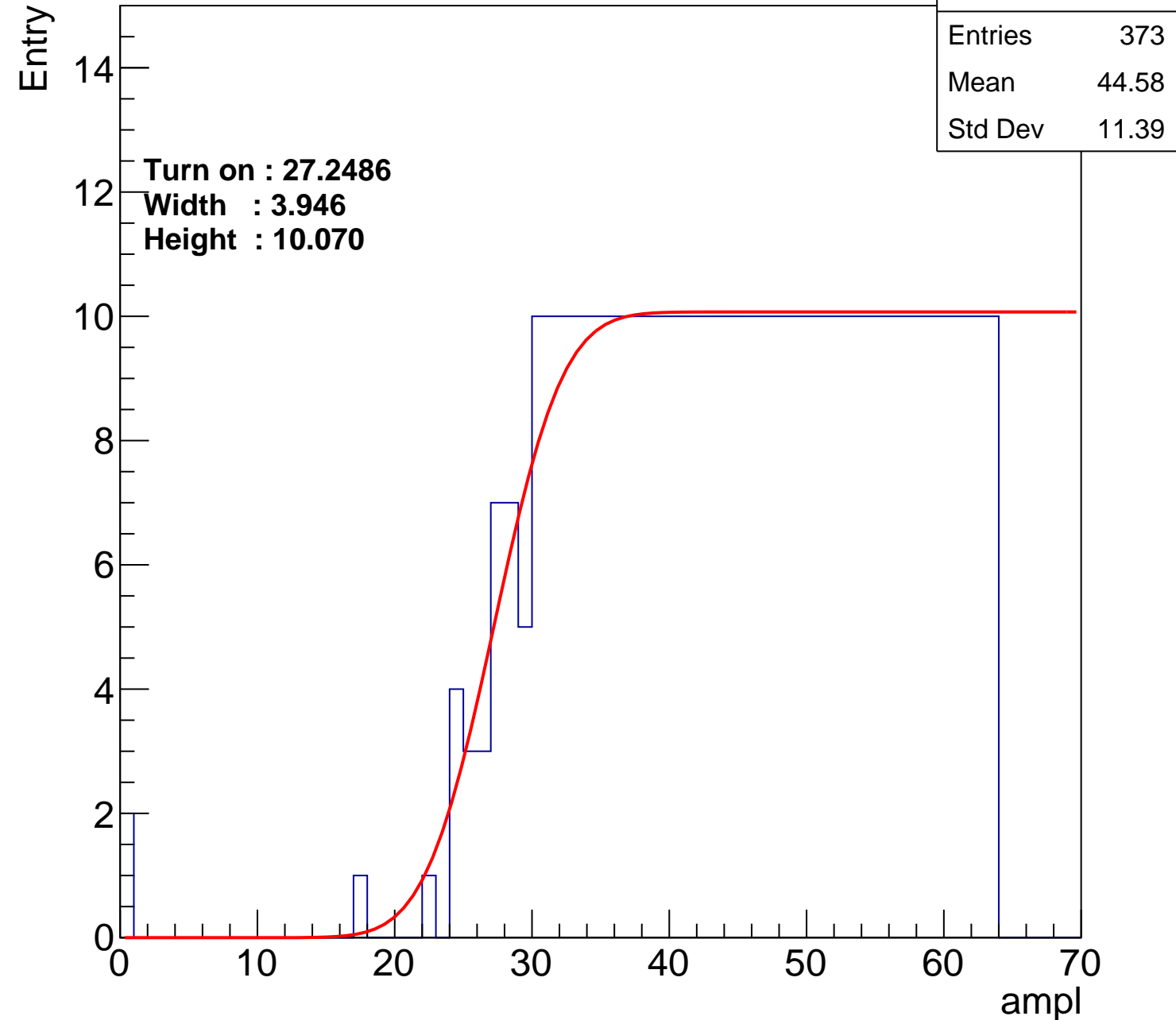
Width : 3.946

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.37
Std Dev	10.76

Turn on : 28.5857

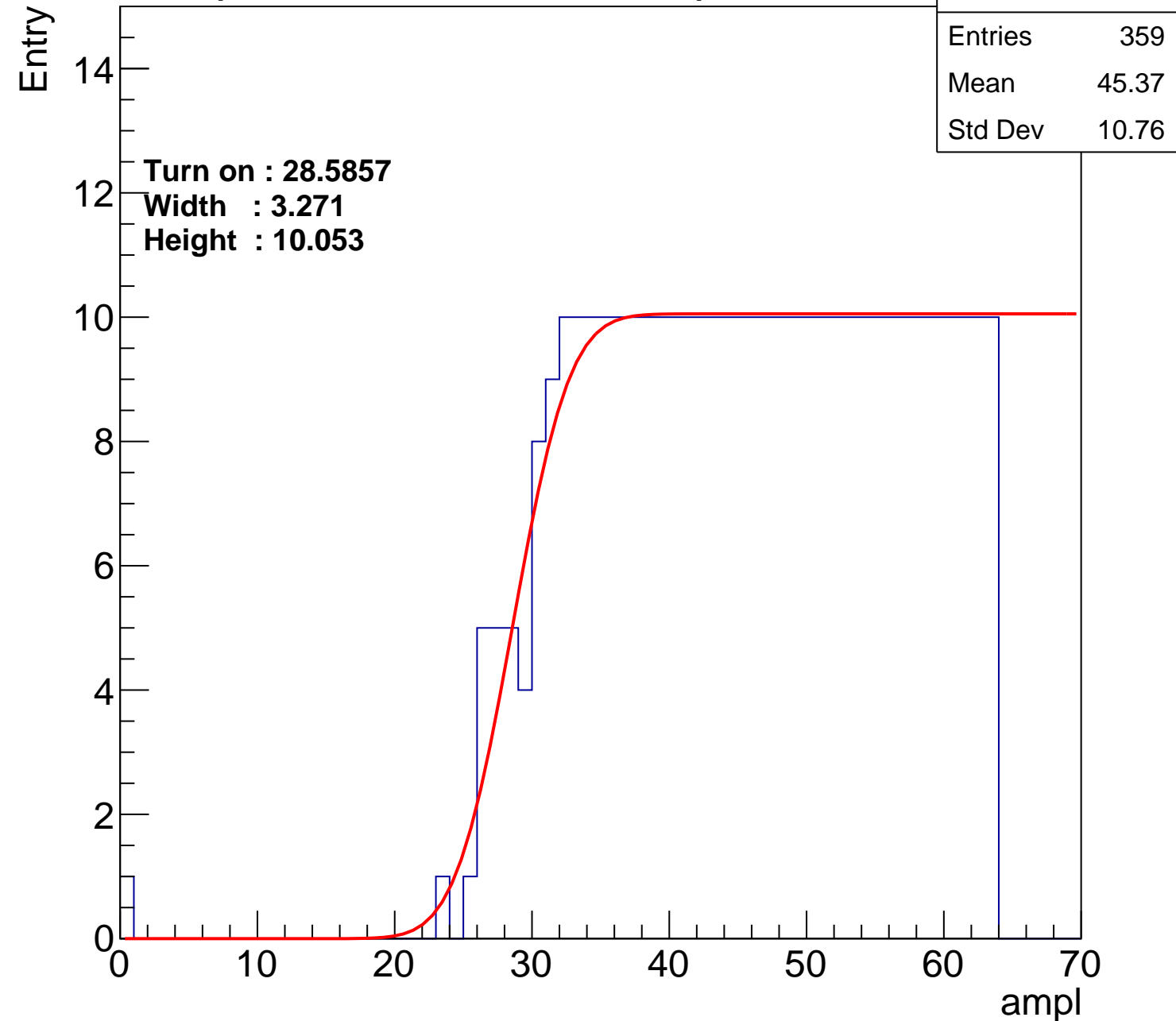
Width : 3.271

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch16

calib_packv5_042523_0143.root, FC#7, port C2

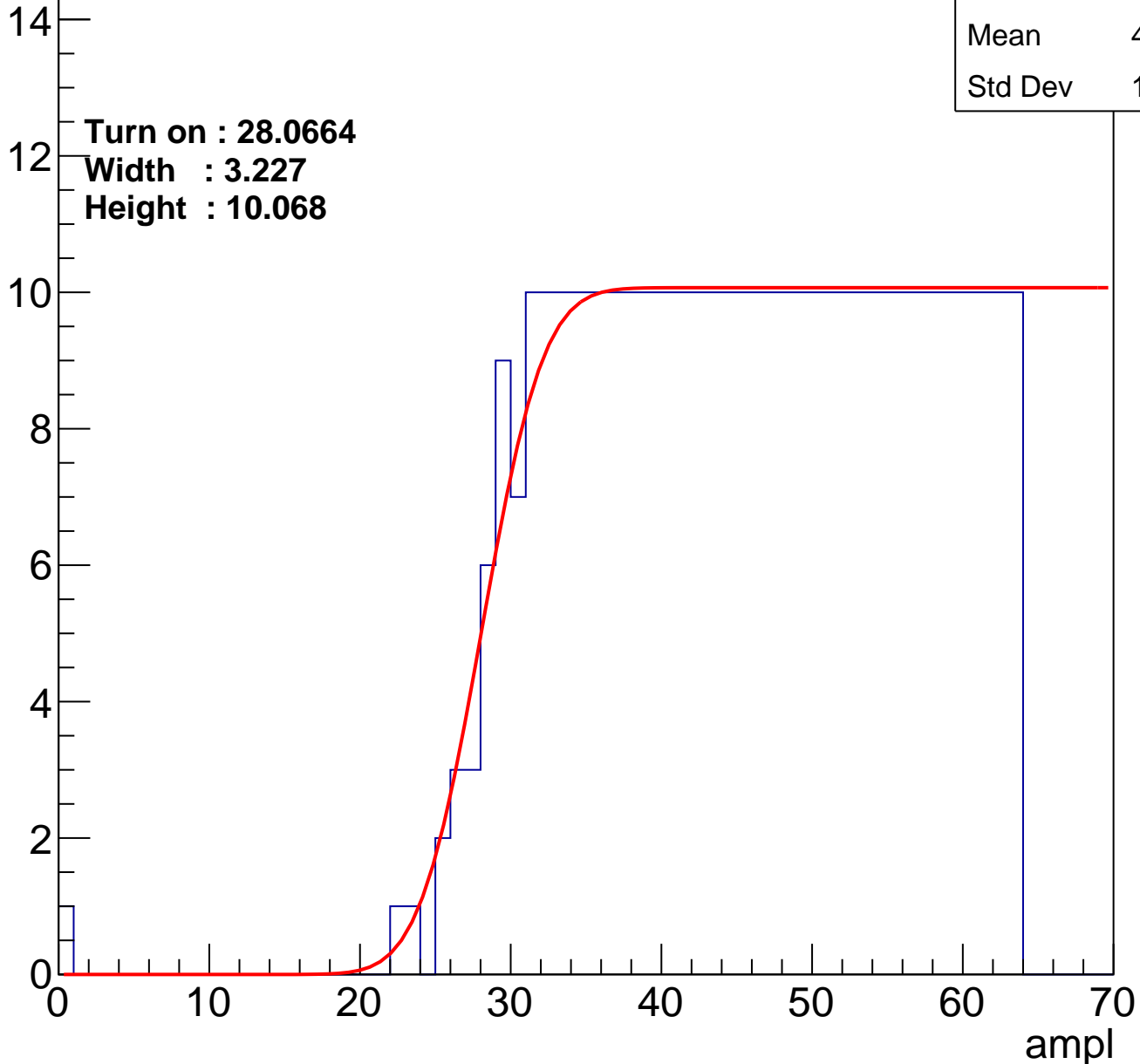
Entries	363
Mean	45.19
Std Dev	10.85

Turn on : 28.0664

Width : 3.227

Height : 10.068

Entry



B1L103S, U23-ch17

calib_packv5_042523_0143.root, FC#7, port C2

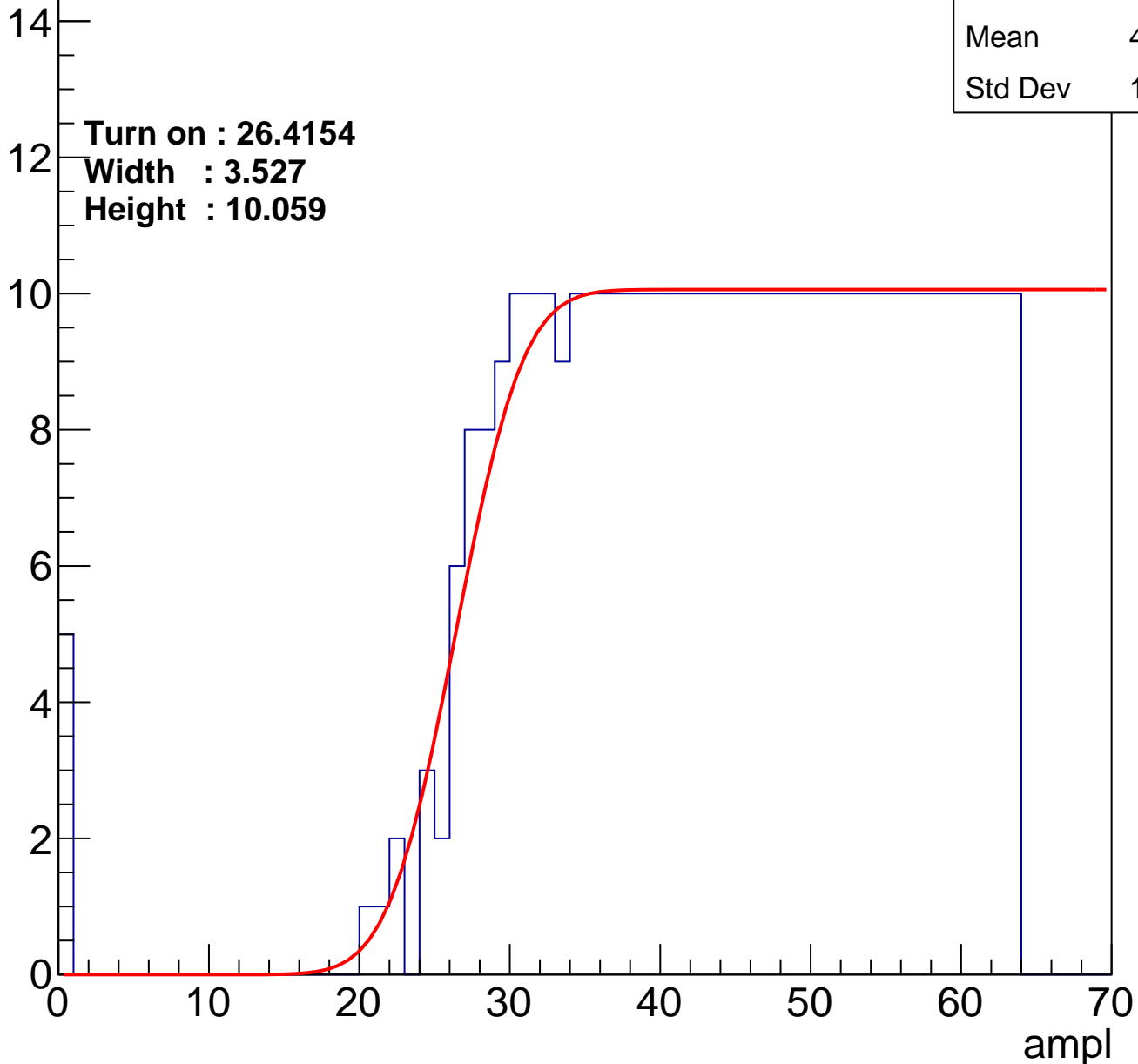
Entries	384
Mean	43.86
Std Dev	12.15

Turn on : 26.4154

Width : 3.527

Height : 10.059

Entry



B1L103S, U23-ch18

calib_packv5_042523_0143.root, FC#7, port C2

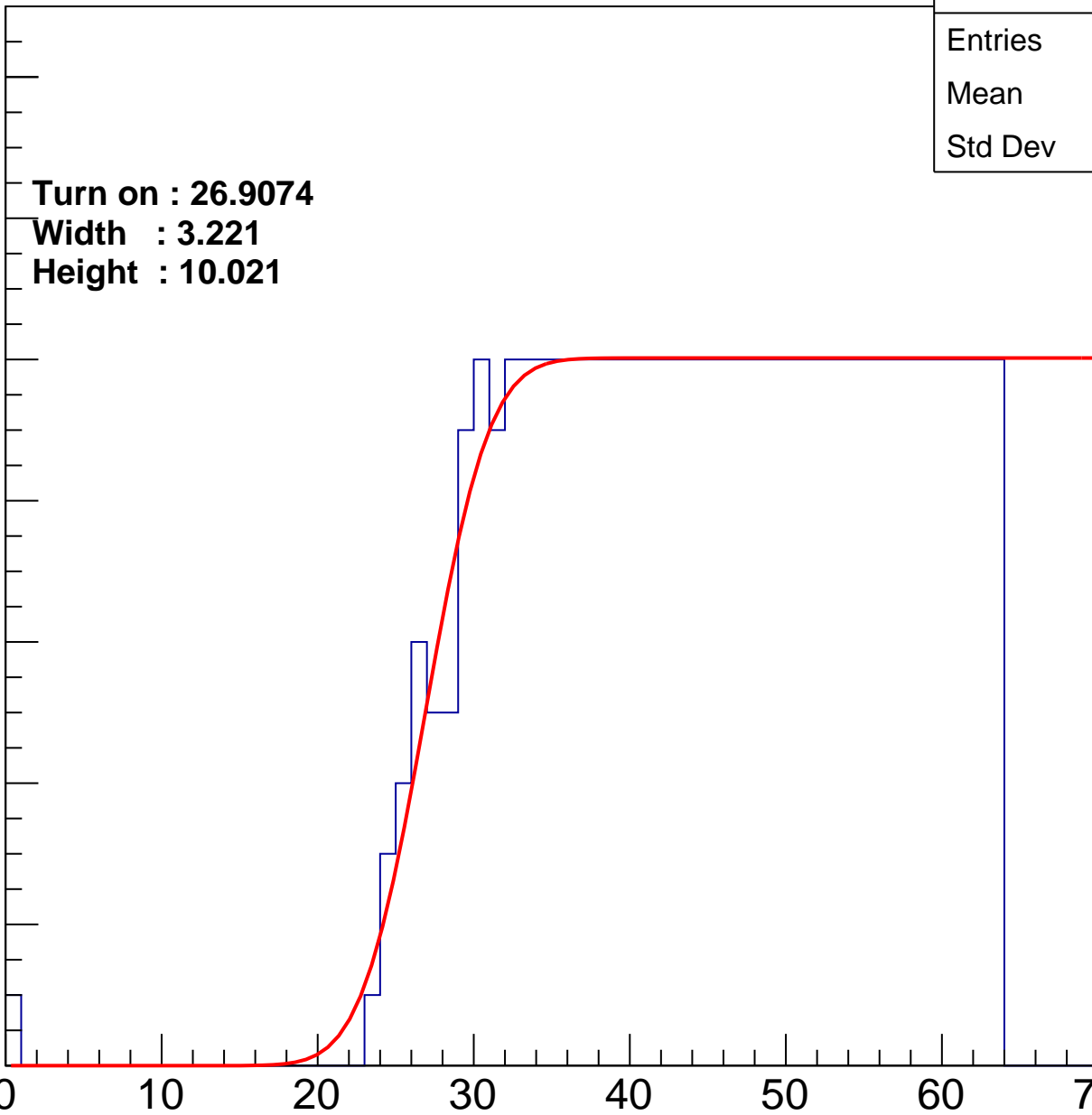
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9074
Width : 3.221
Height : 10.021

Entries	373
Mean	44.68
Std Dev	11.13

ampl



B1L103S, U23-ch19

calib_packv5_042523_0143.root, FC#7, port C2

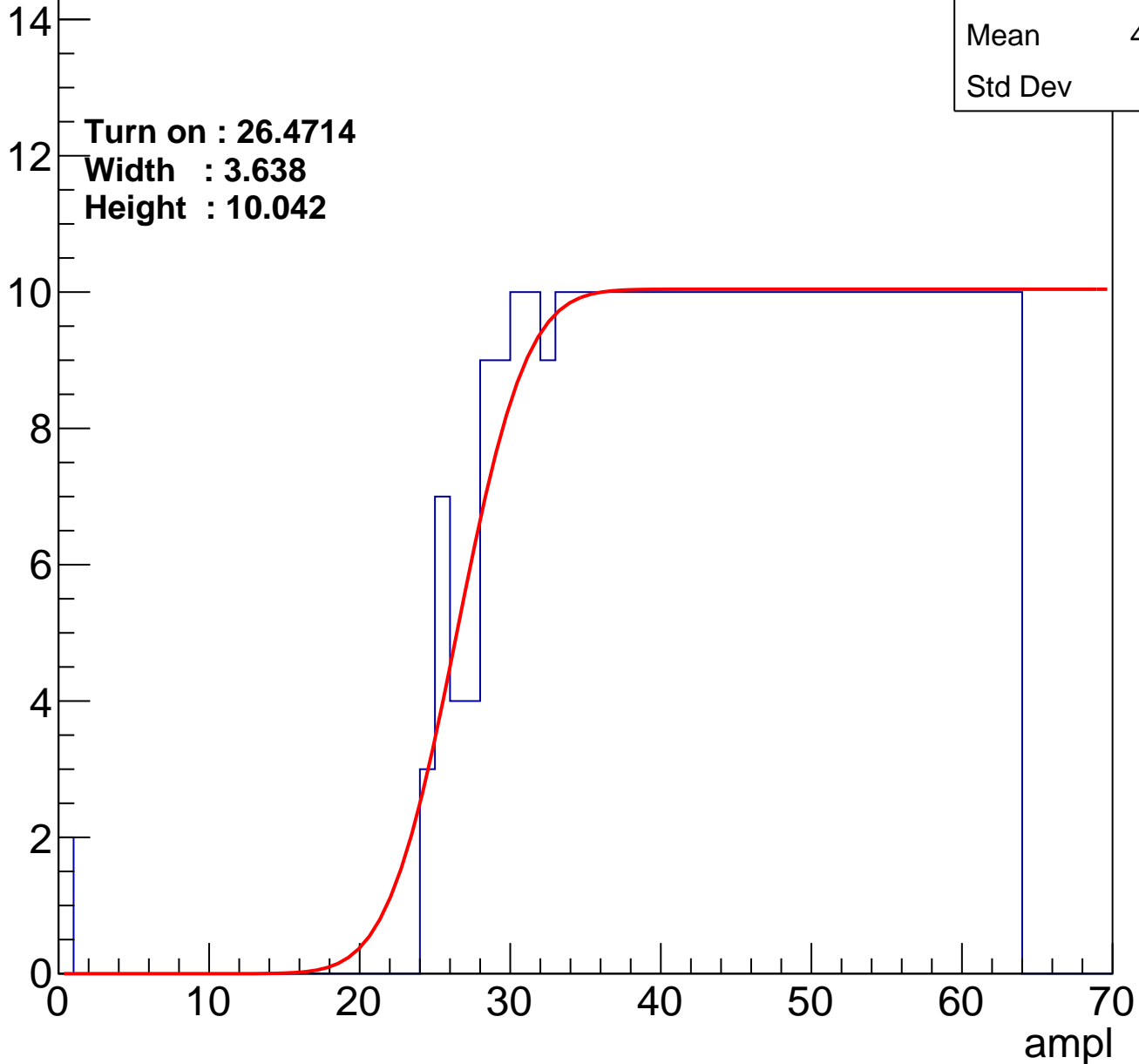
Entries	377
Mean	44.43
Std Dev	11.4

Turn on : 26.4714

Width : 3.638

Height : 10.042

Entry



B1L103S, U23-ch20

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.97
Std Dev	11.59

Turn on : 25.6874

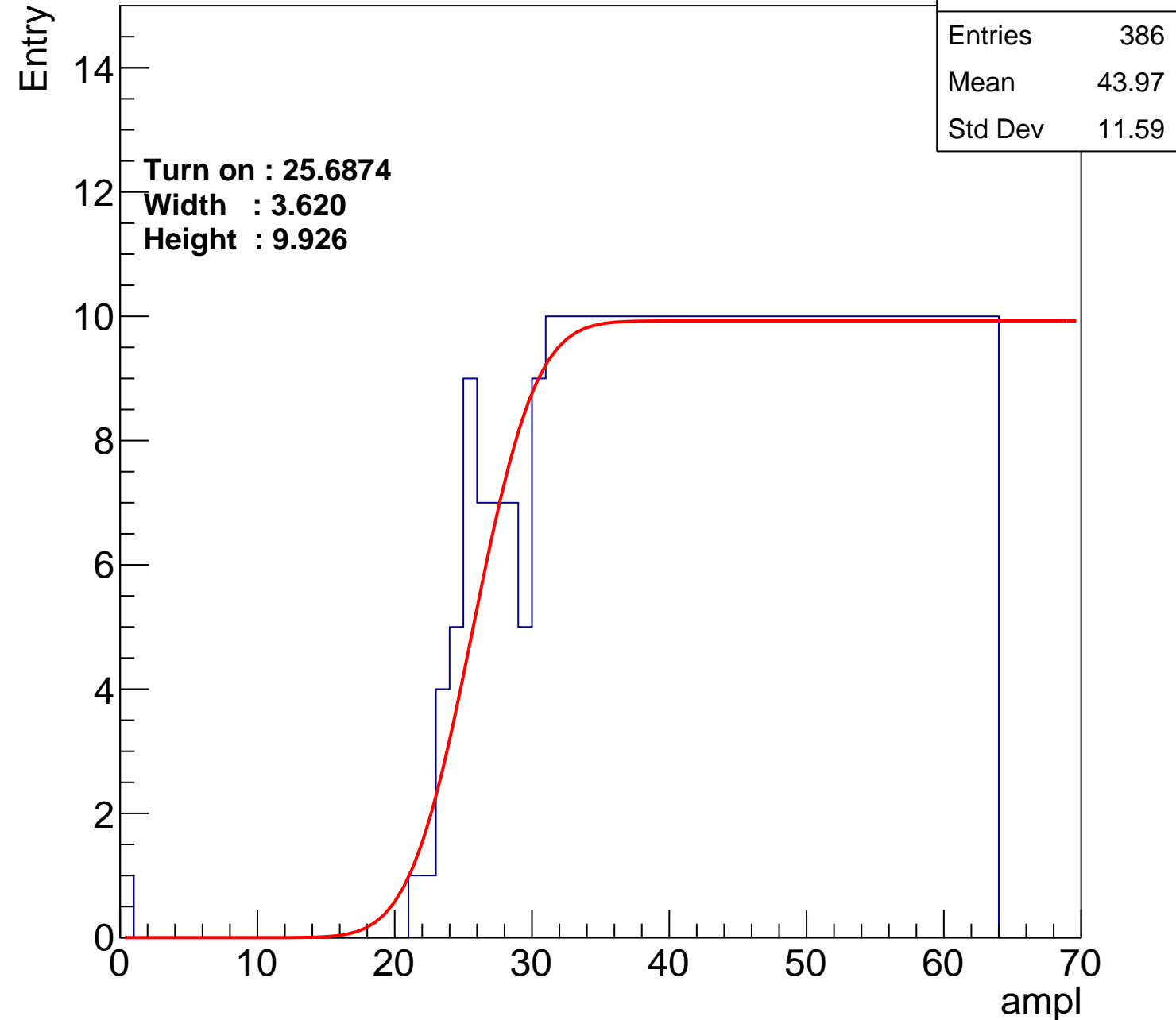
Width : 3.620

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.4621

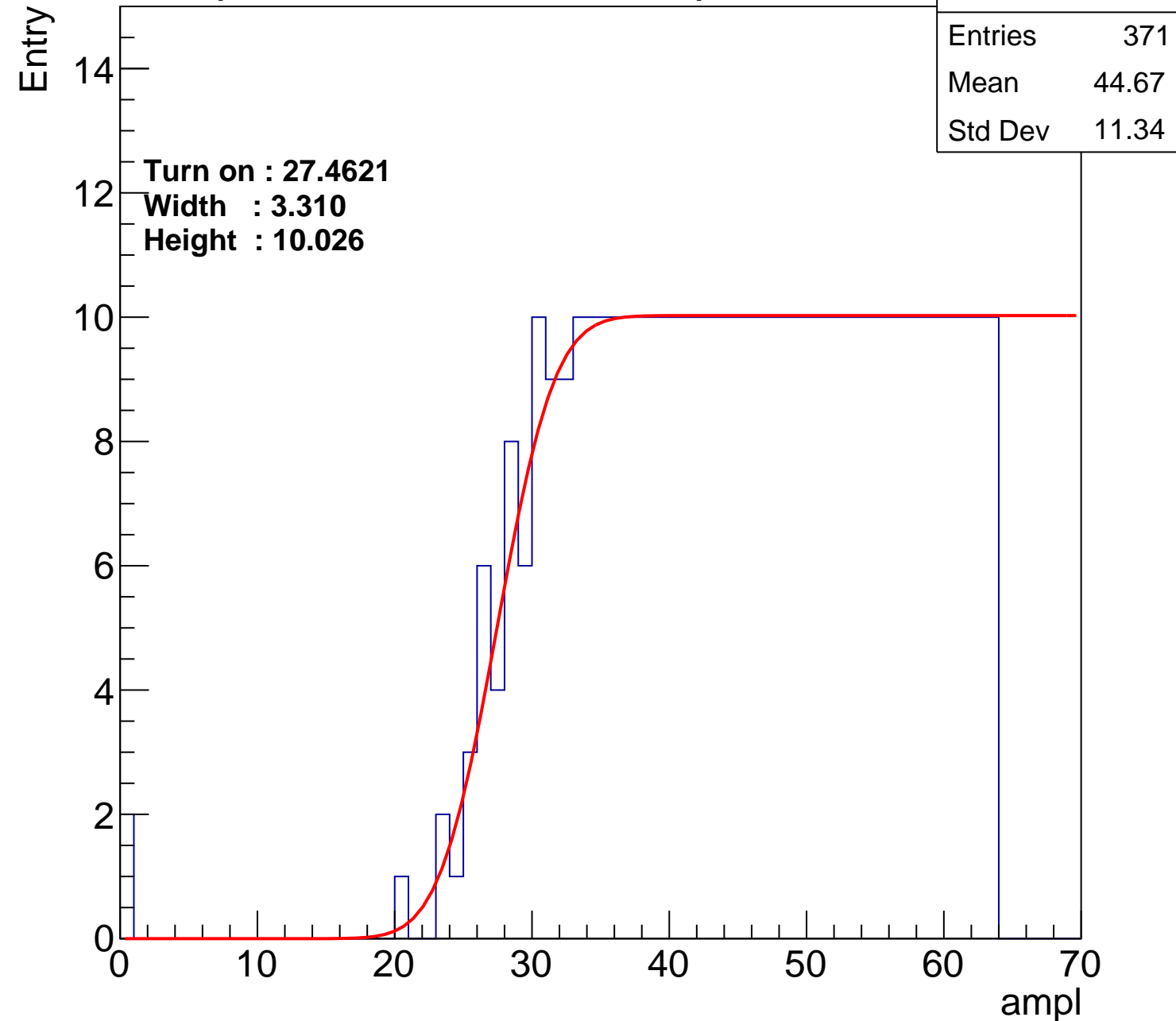
Width : 3.310

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.33
Std Dev	11.89

Turn on : 24.0929

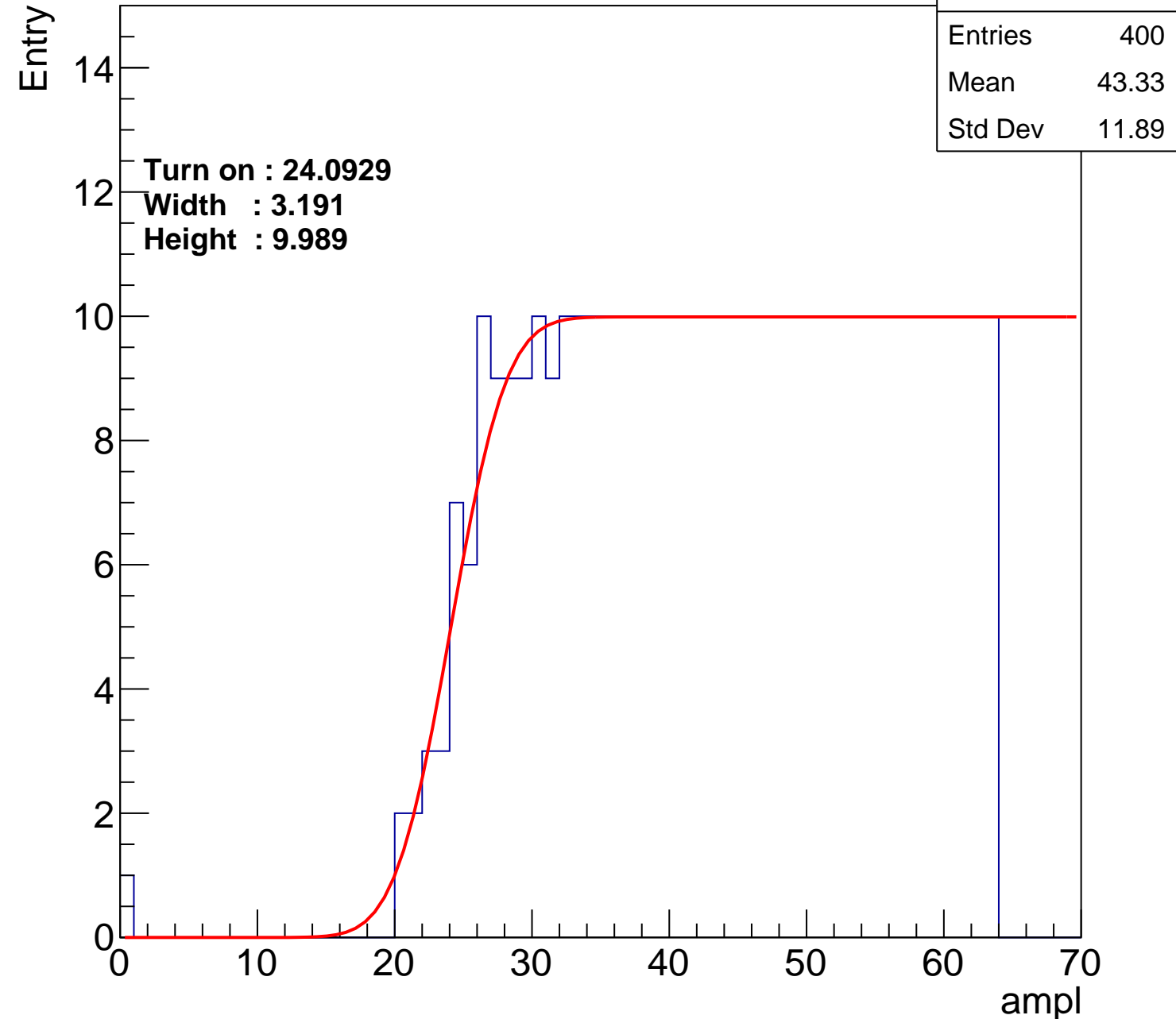
Width : 3.191

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.8
Std Dev	11.3

Turn on : 28.5282

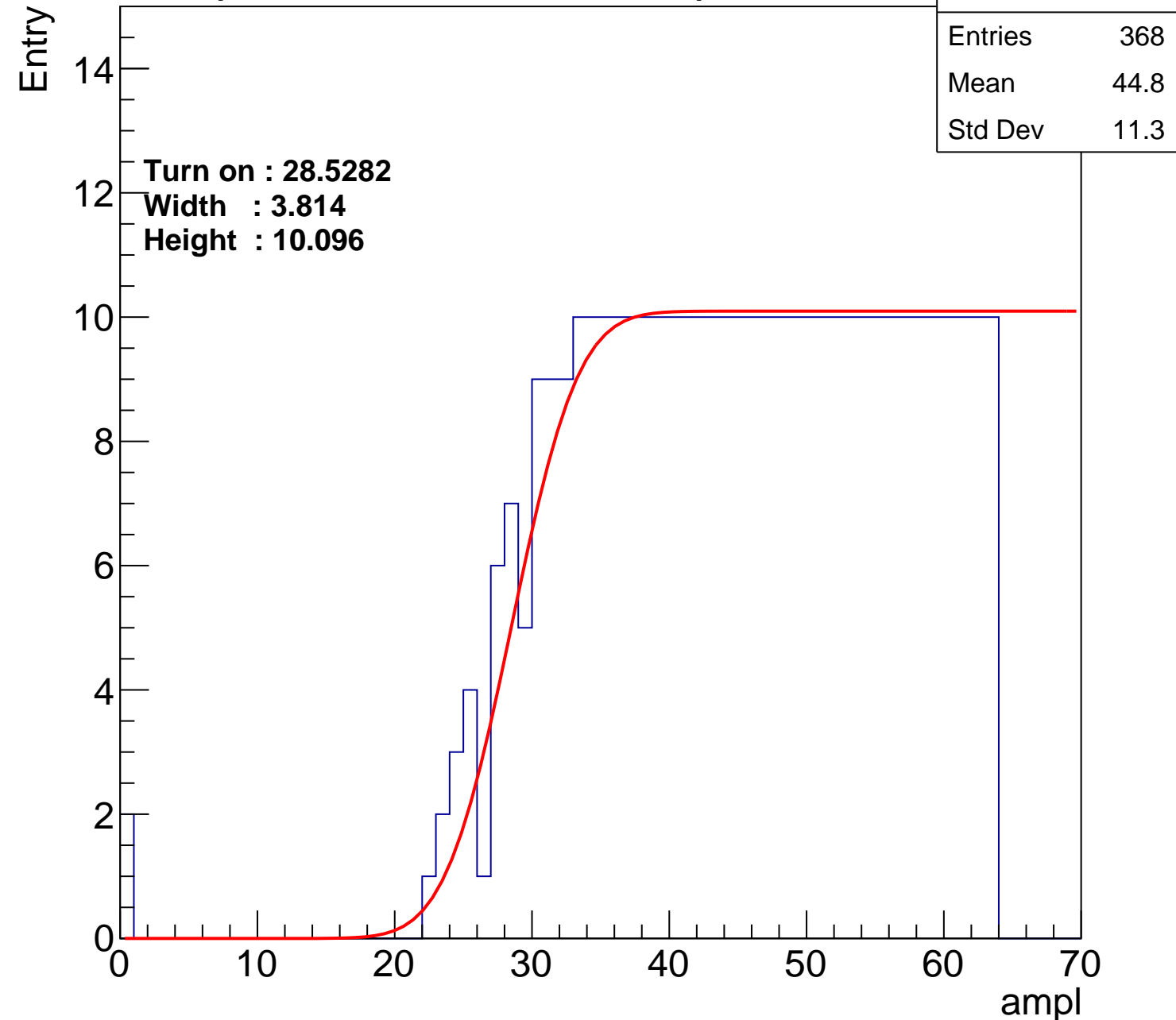
Width : 3.814

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch24

calib_packv5_042523_0143.root, FC#7, port C2

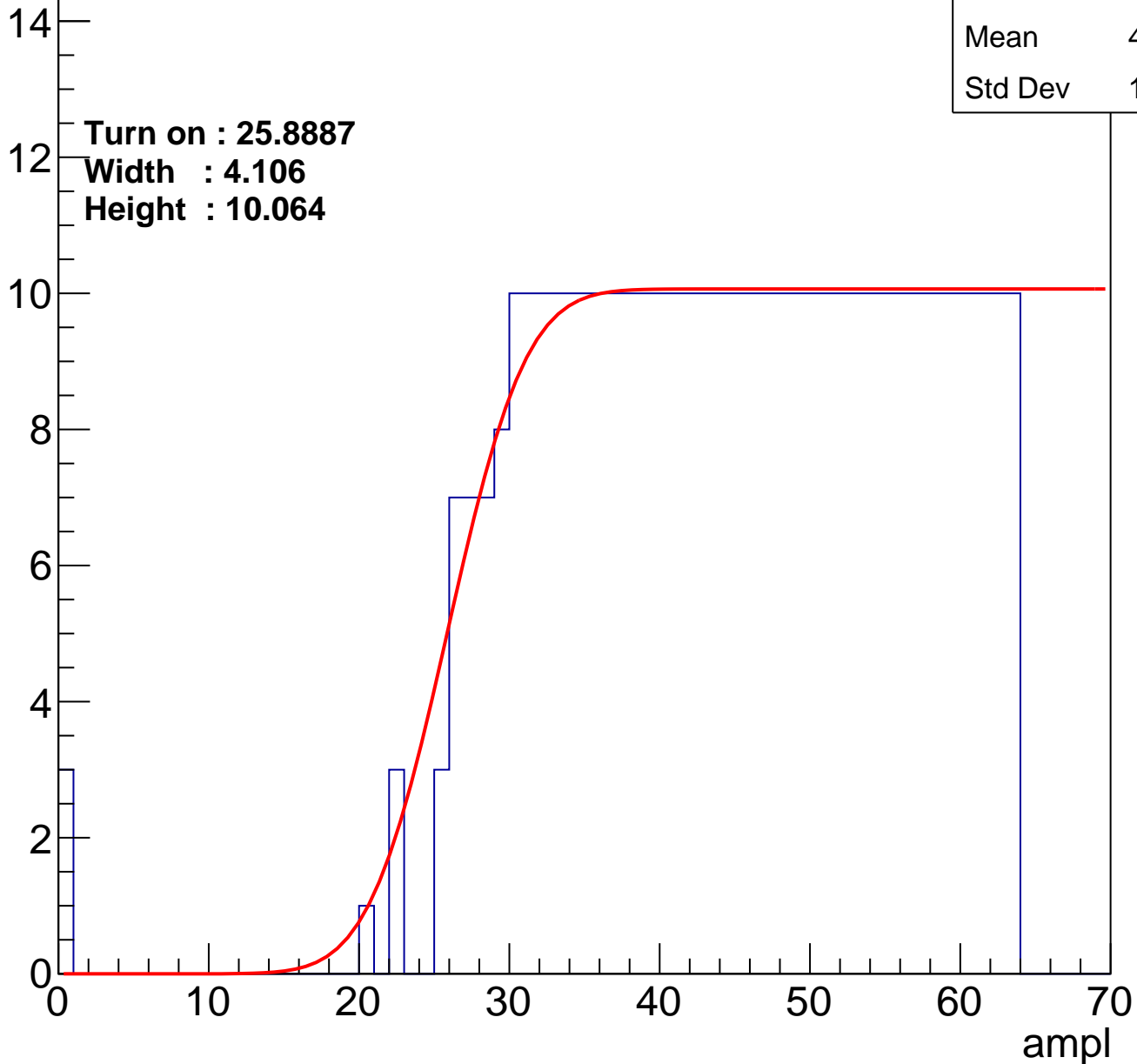
Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 25.8887

Width : 4.106

Height : 10.064

Entry



B1L103S, U23-ch25

calib_packv5_042523_0143.root, FC#7, port C2

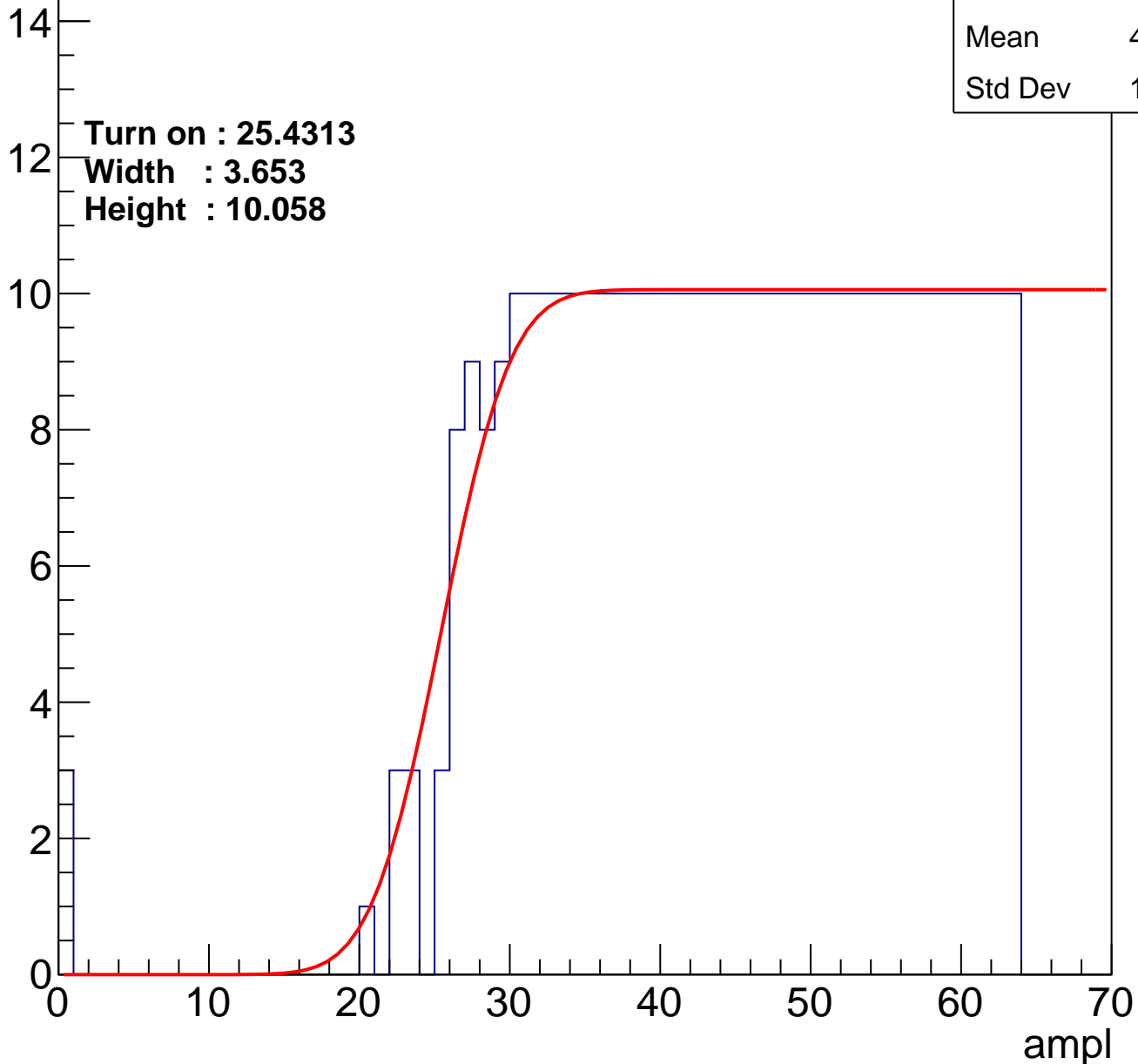
Entries	387
Mean	43.87
Std Dev	11.85

Turn on : 25.4313

Width : 3.653

Height : 10.058

Entry



B1L103S, U23-ch26

calib_packv5_042523_0143.root, FC#7, port C2

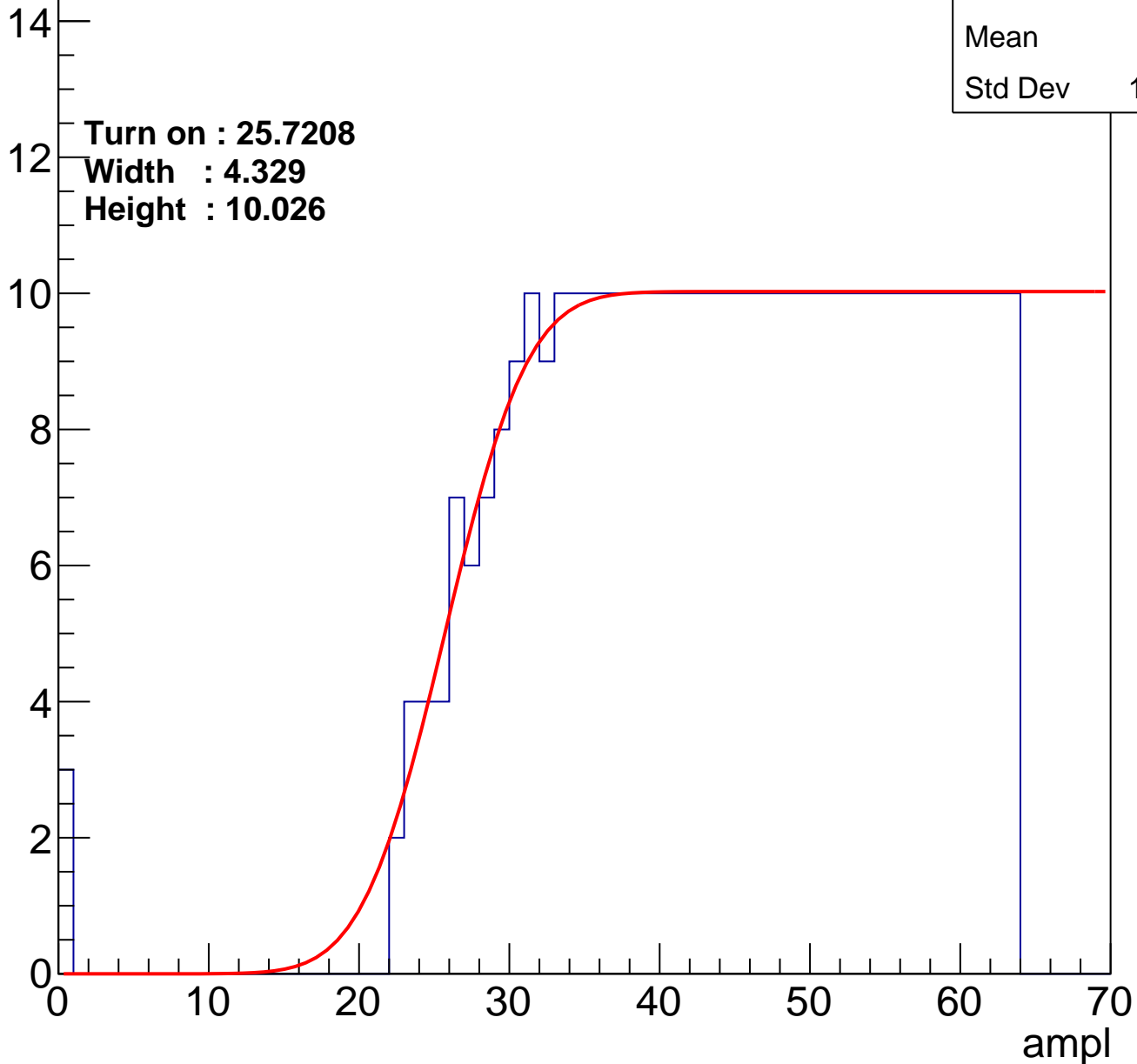
Entries	383
Mean	44
Std Dev	11.84

Turn on : 25.7208

Width : 4.329

Height : 10.026

Entry



B1L103S, U23-ch27

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.8801

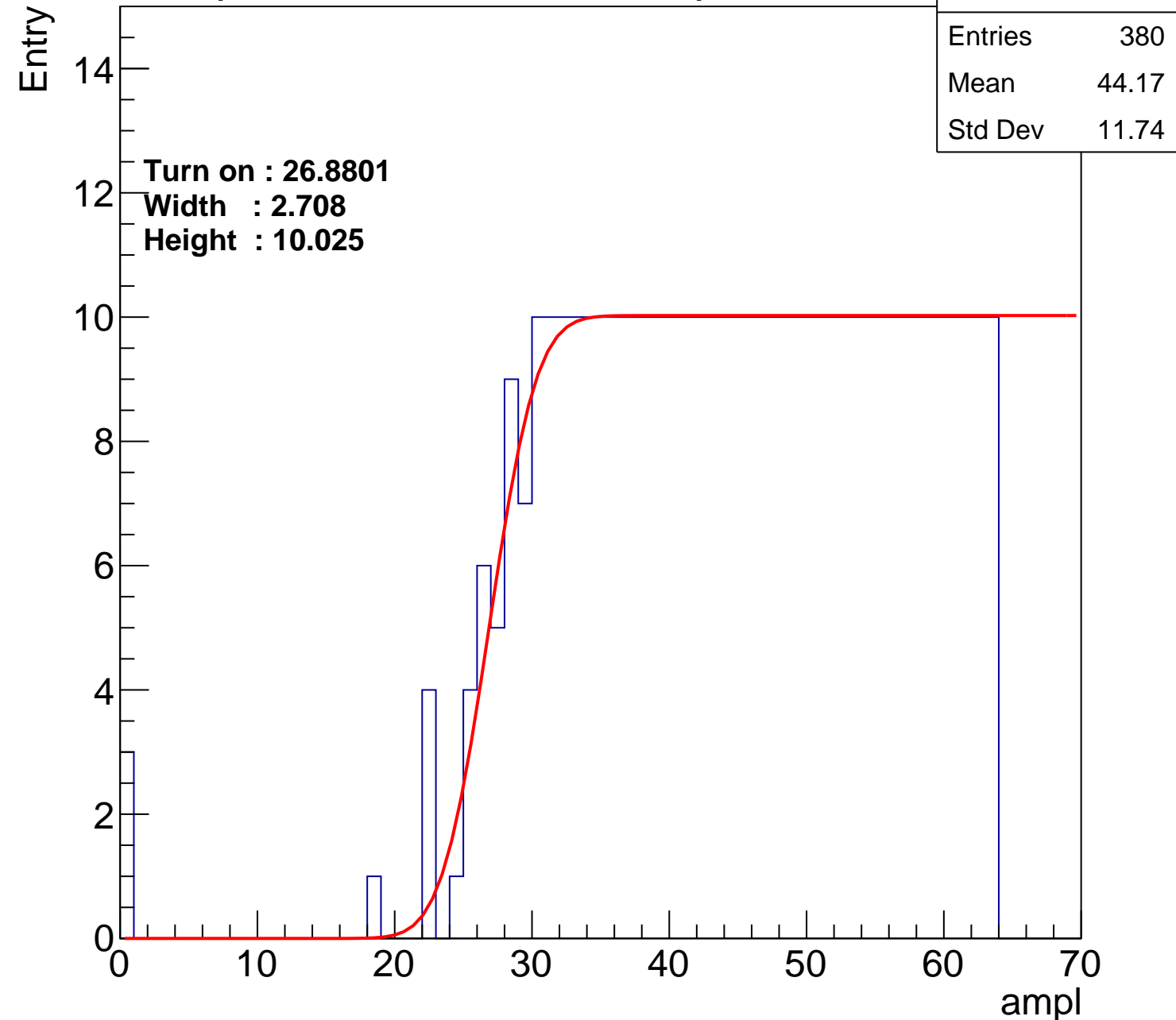
Width : 2.708

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 26.2439

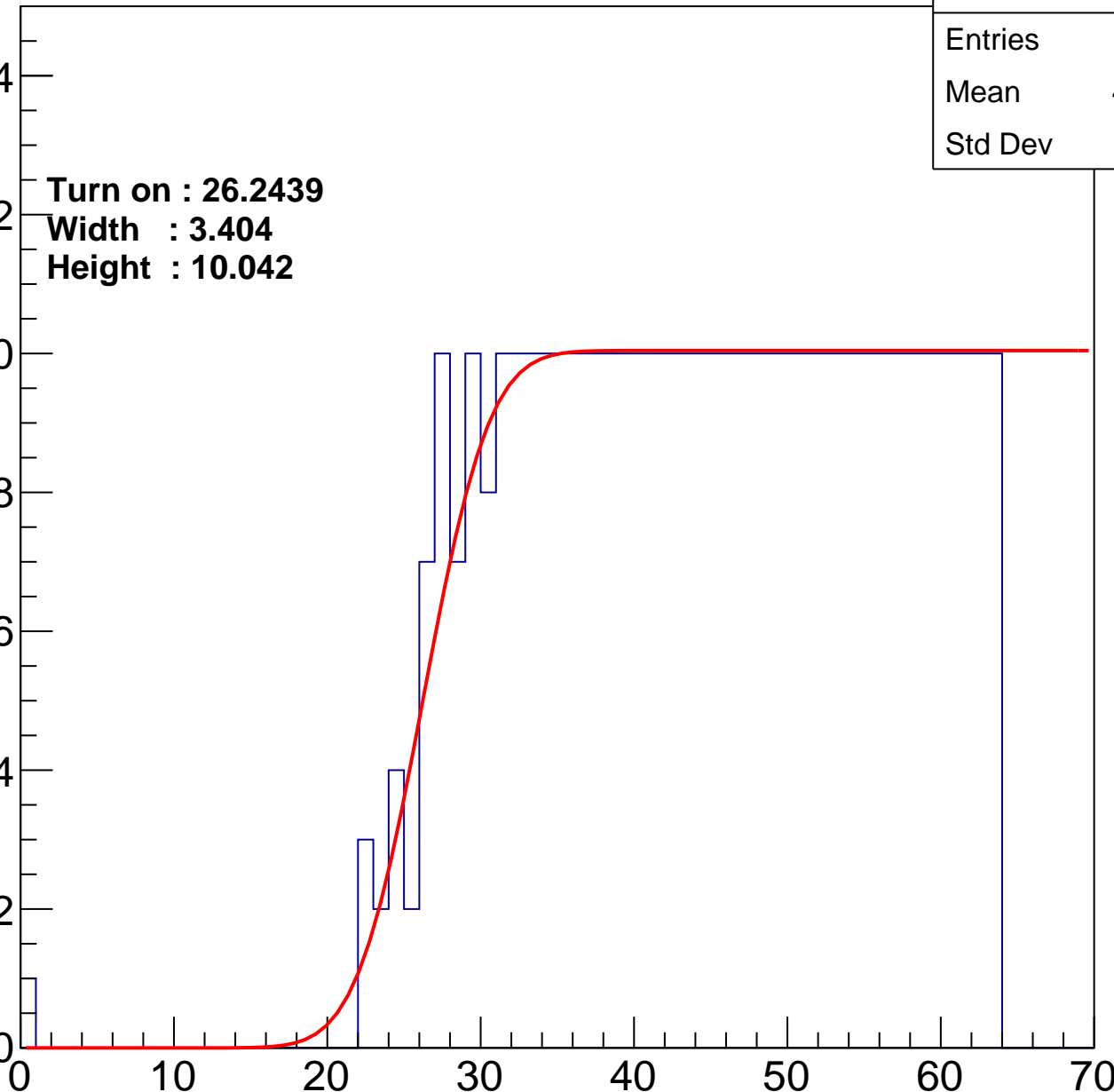
Width : 3.404

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.32
Std Dev	12.28

Turn on : 28.2650

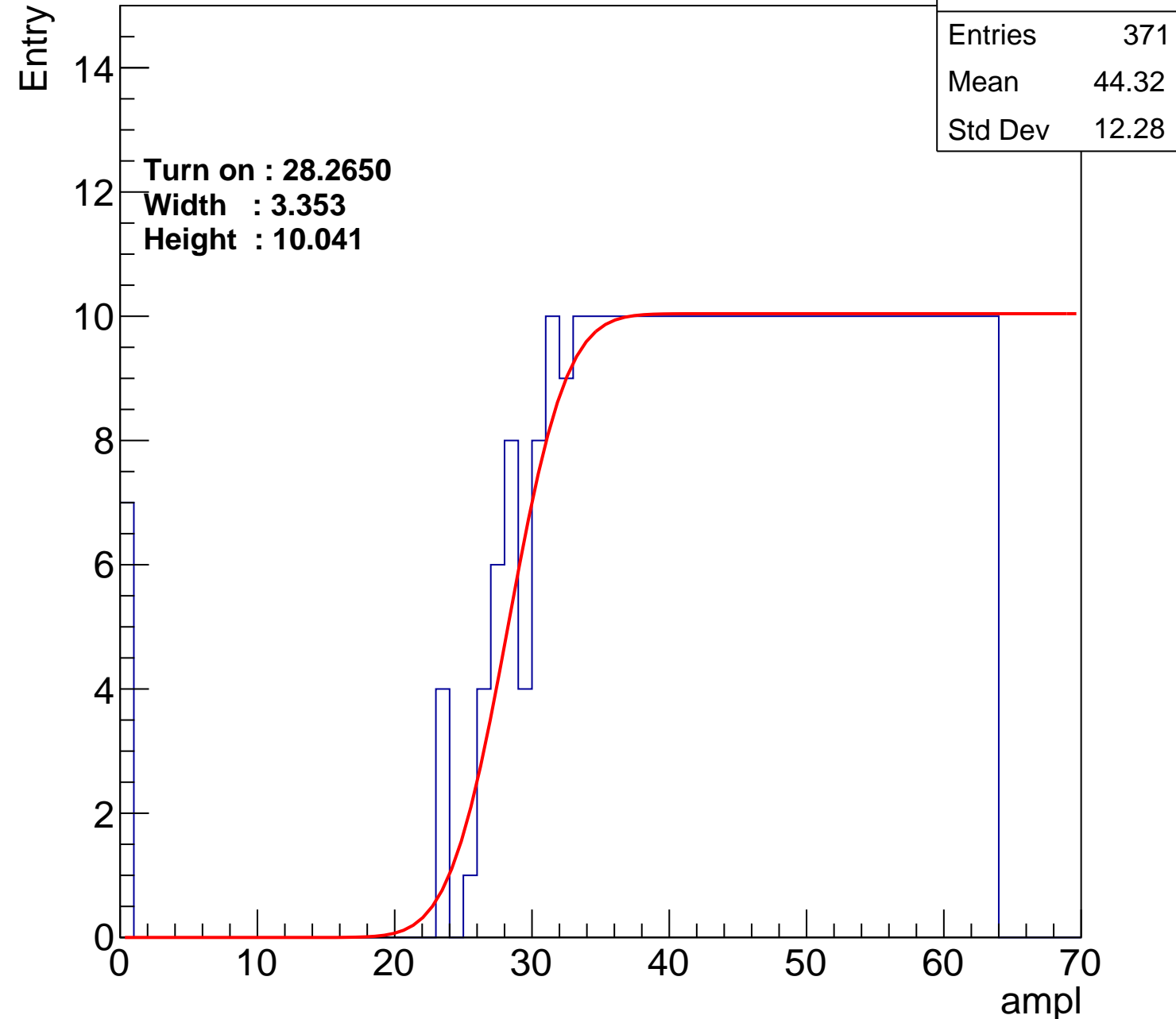
Width : 3.353

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 26.1617

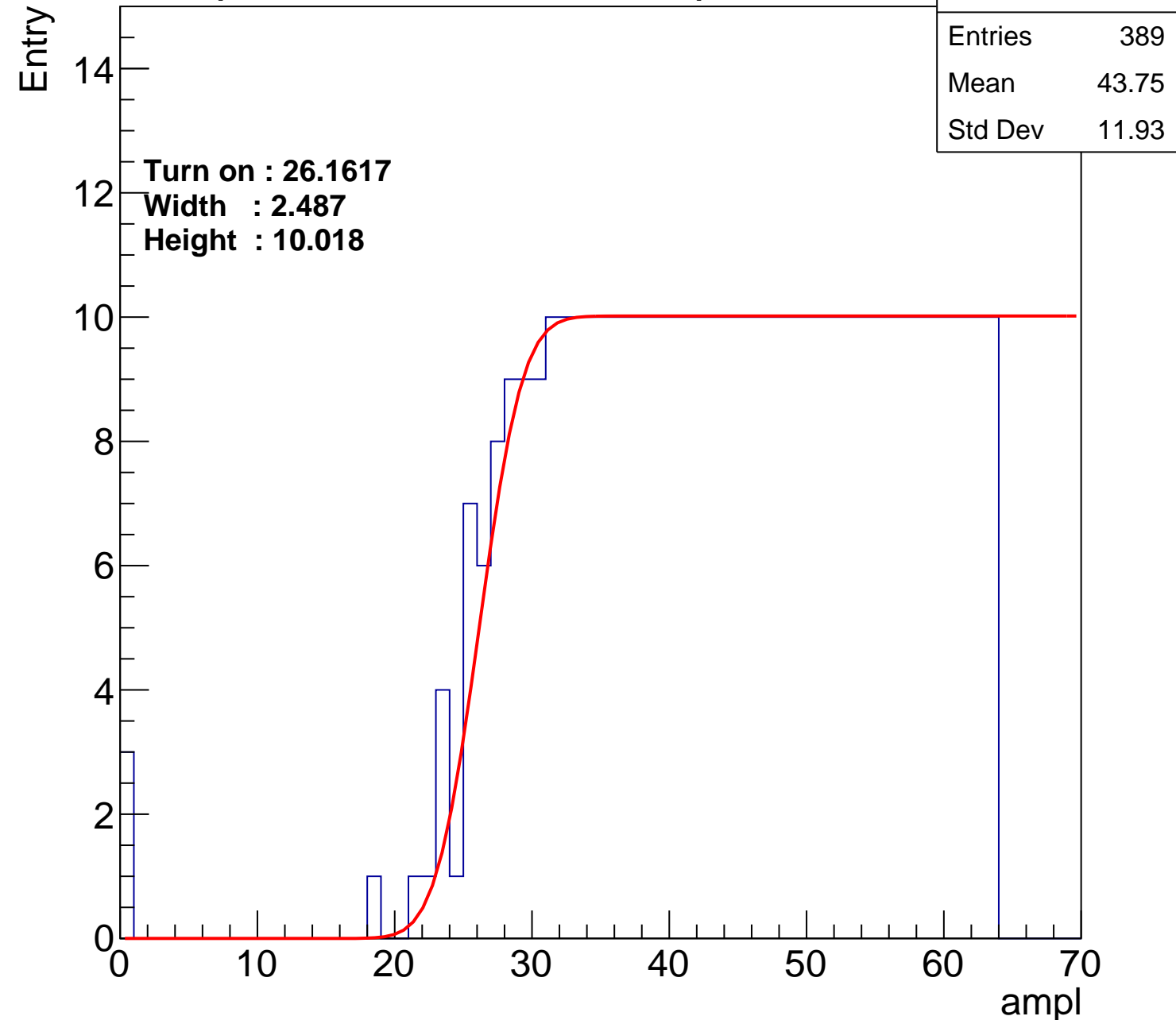
Width : 2.487

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch31

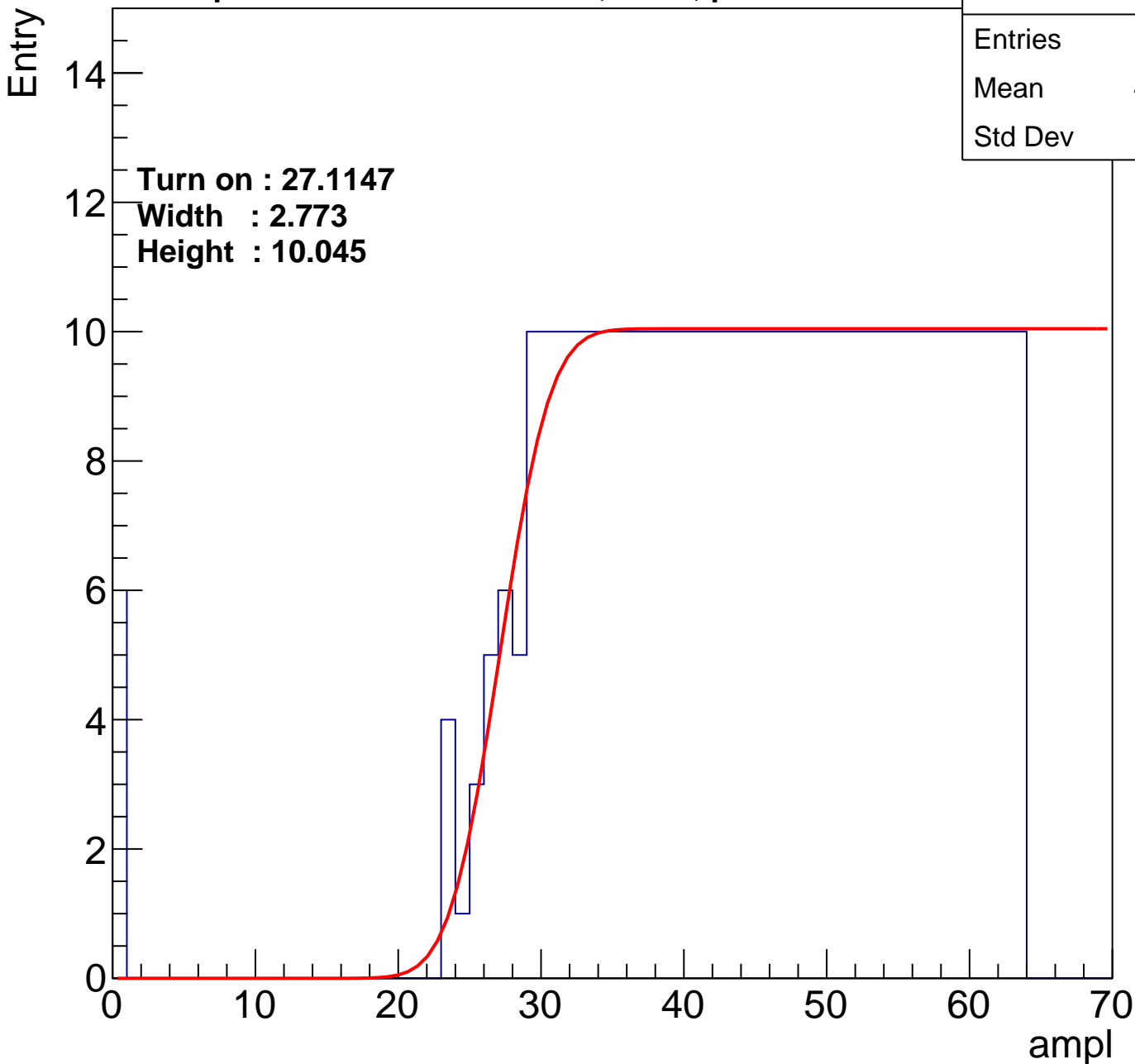
calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.01
Std Dev	12.2

Turn on : 27.1147

Width : 2.773

Height : 10.045



B1L103S, U23-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.8
Std Dev	11.1

Turn on : 27.6900

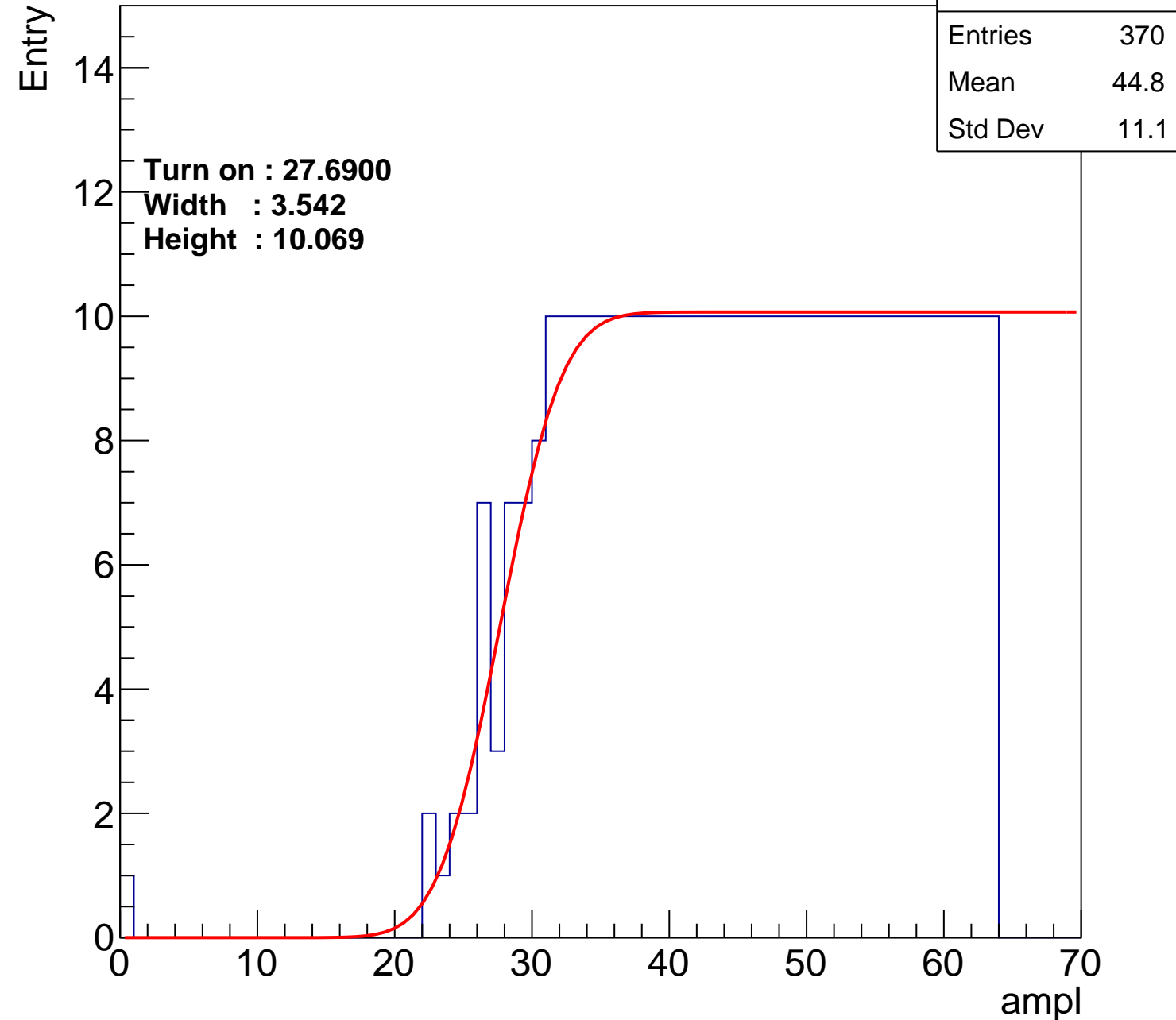
Width : 3.542

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch33

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.79
Std Dev	11.39

Turn on : 27.5778

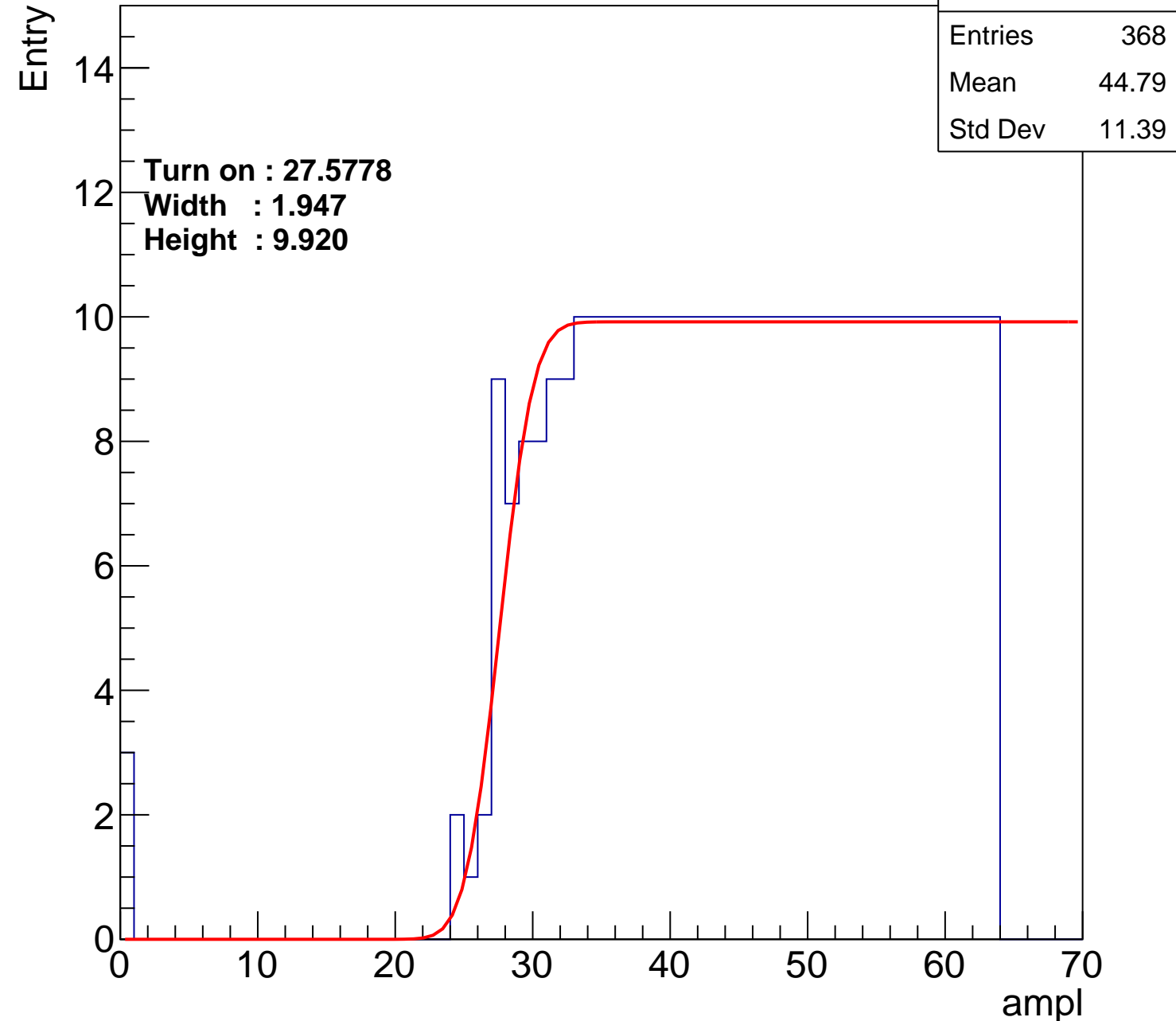
Width : 1.947

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch34

calib_packv5_042523_0143.root, FC#7, port C2

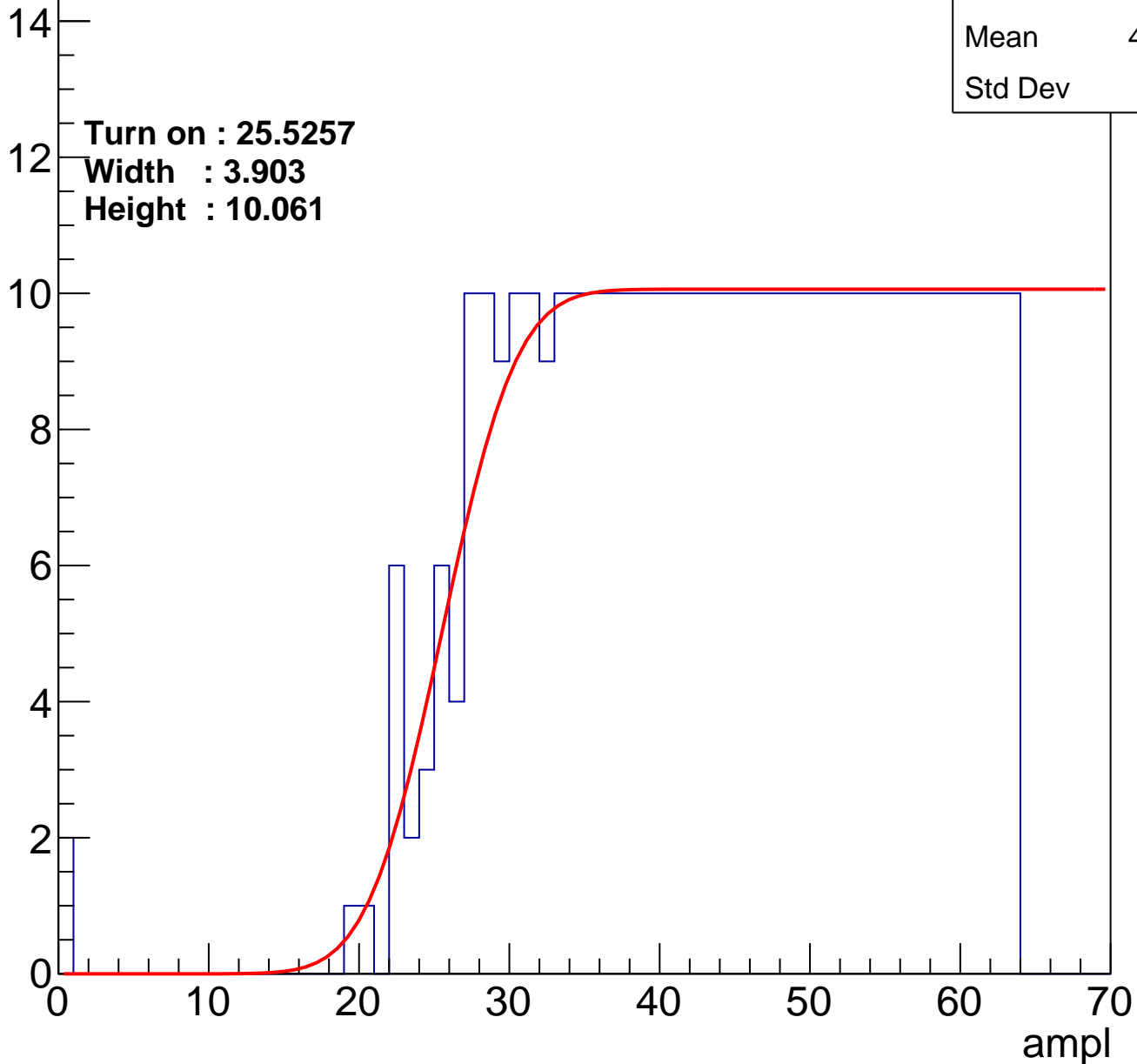
Entries	393
Mean	43.59
Std Dev	11.9

Turn on : 25.5257

Width : 3.903

Height : 10.061

Entry



B1L103S, U23-ch35

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.69
Std Dev	11.77

Turn on : 27.7616

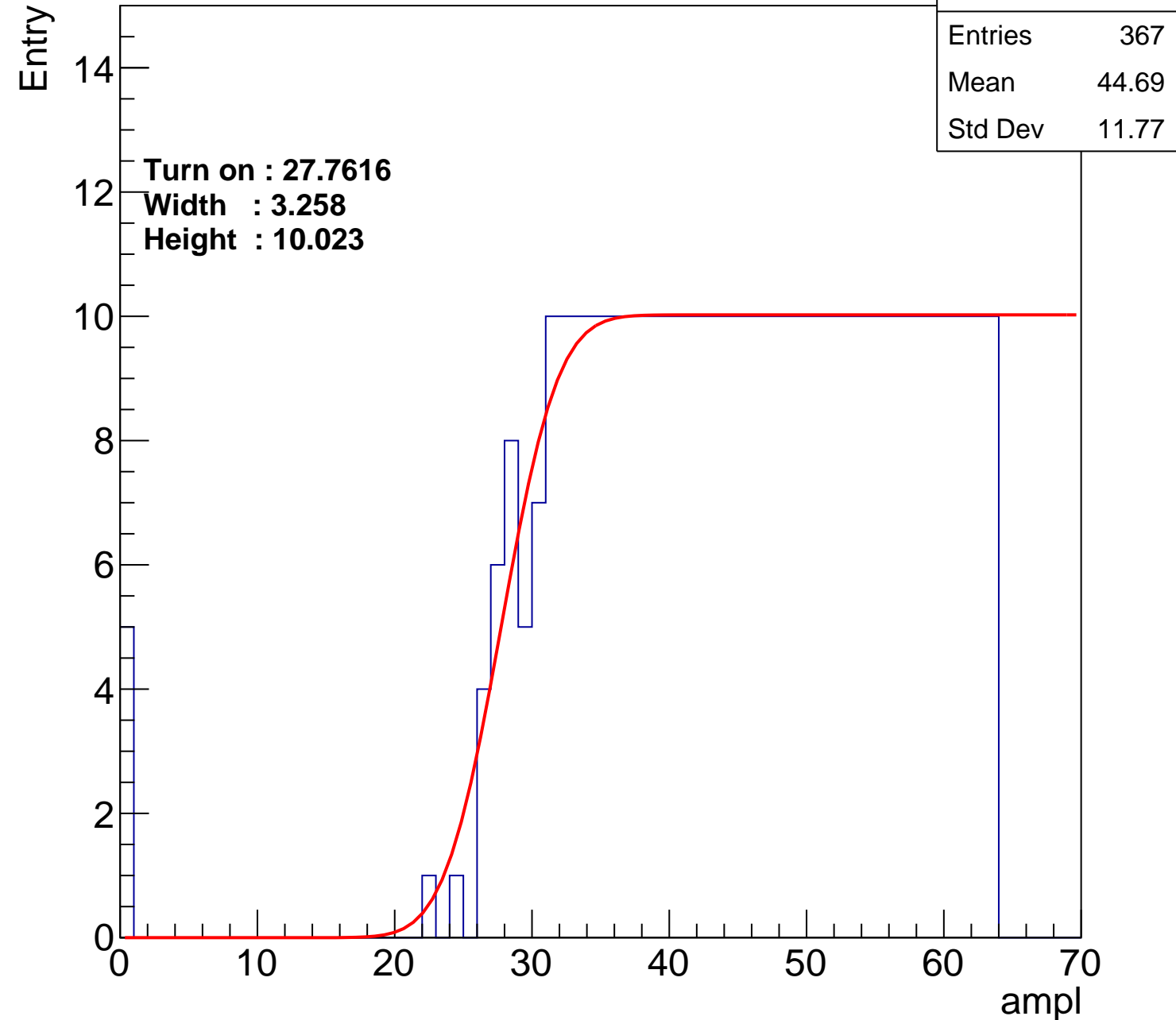
Width : 3.258

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch36

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.54
Std Dev	11.38

Turn on : 26.9120

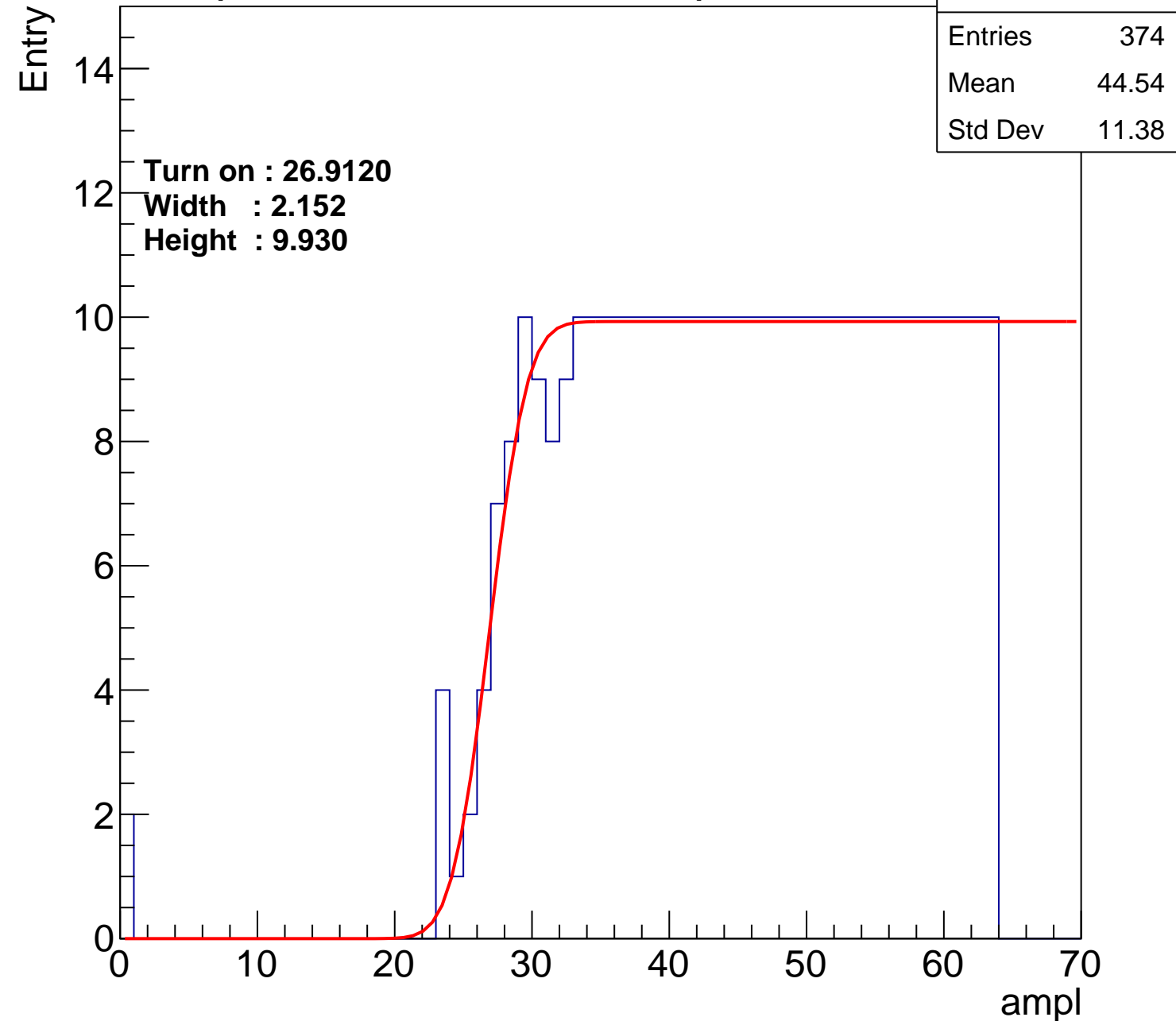
Width : 2.152

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch37

calib_packv5_042523_0143.root, FC#7, port C2

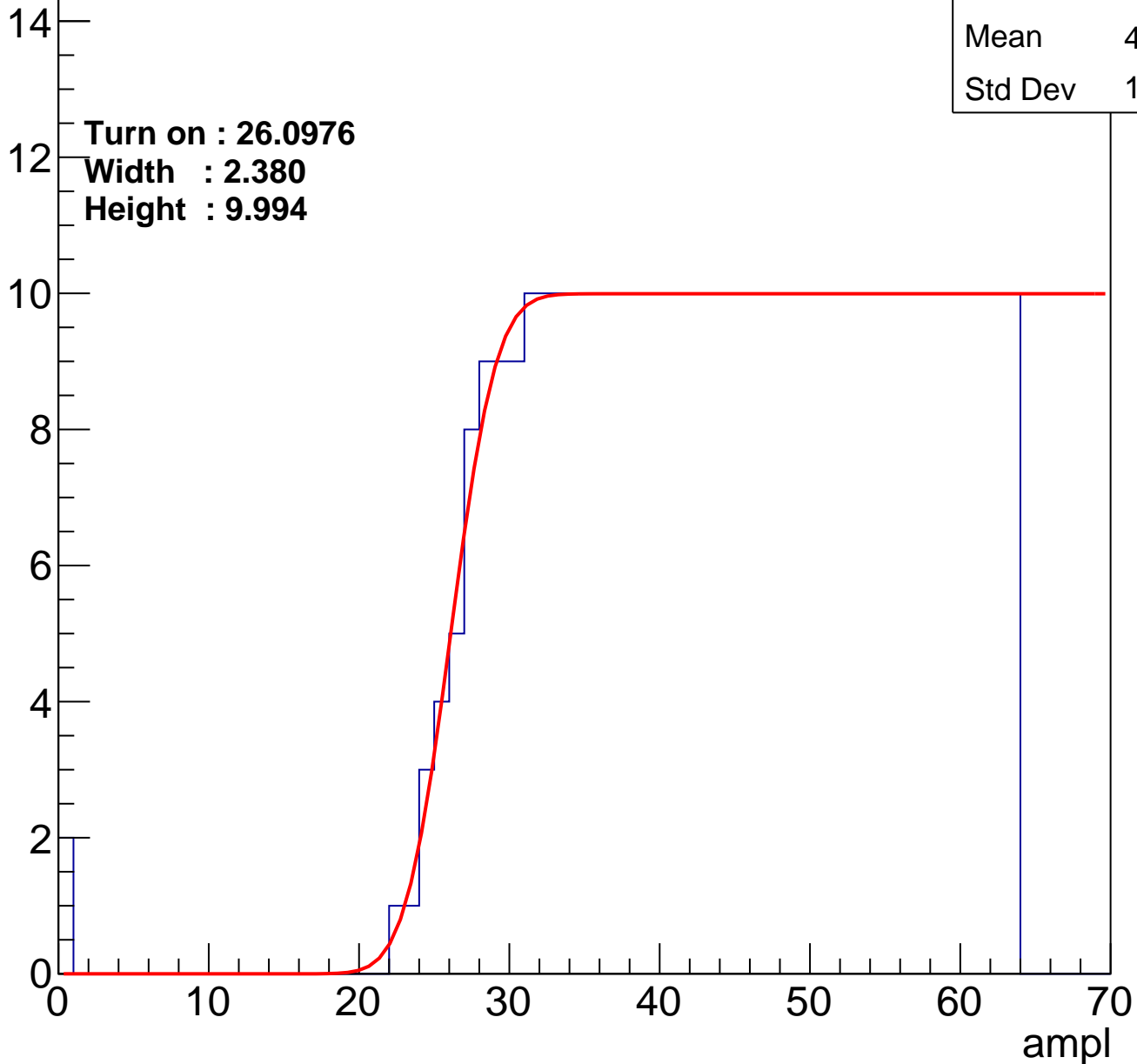
Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.0976

Width : 2.380

Height : 9.994

Entry



B1L103S, U23-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.93
Std Dev	11.19

Turn on : 27.7819

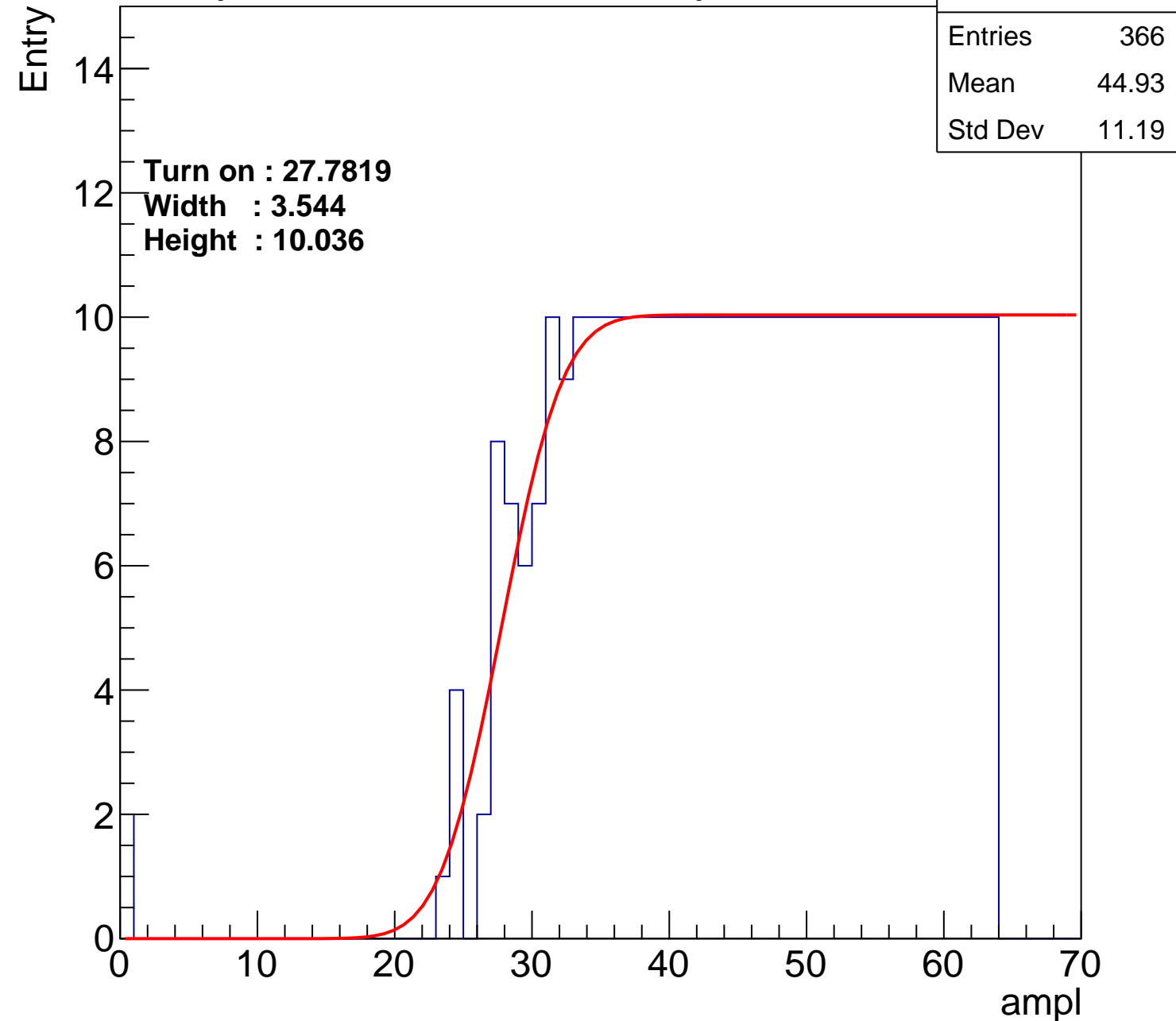
Width : 3.544

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 26.6490

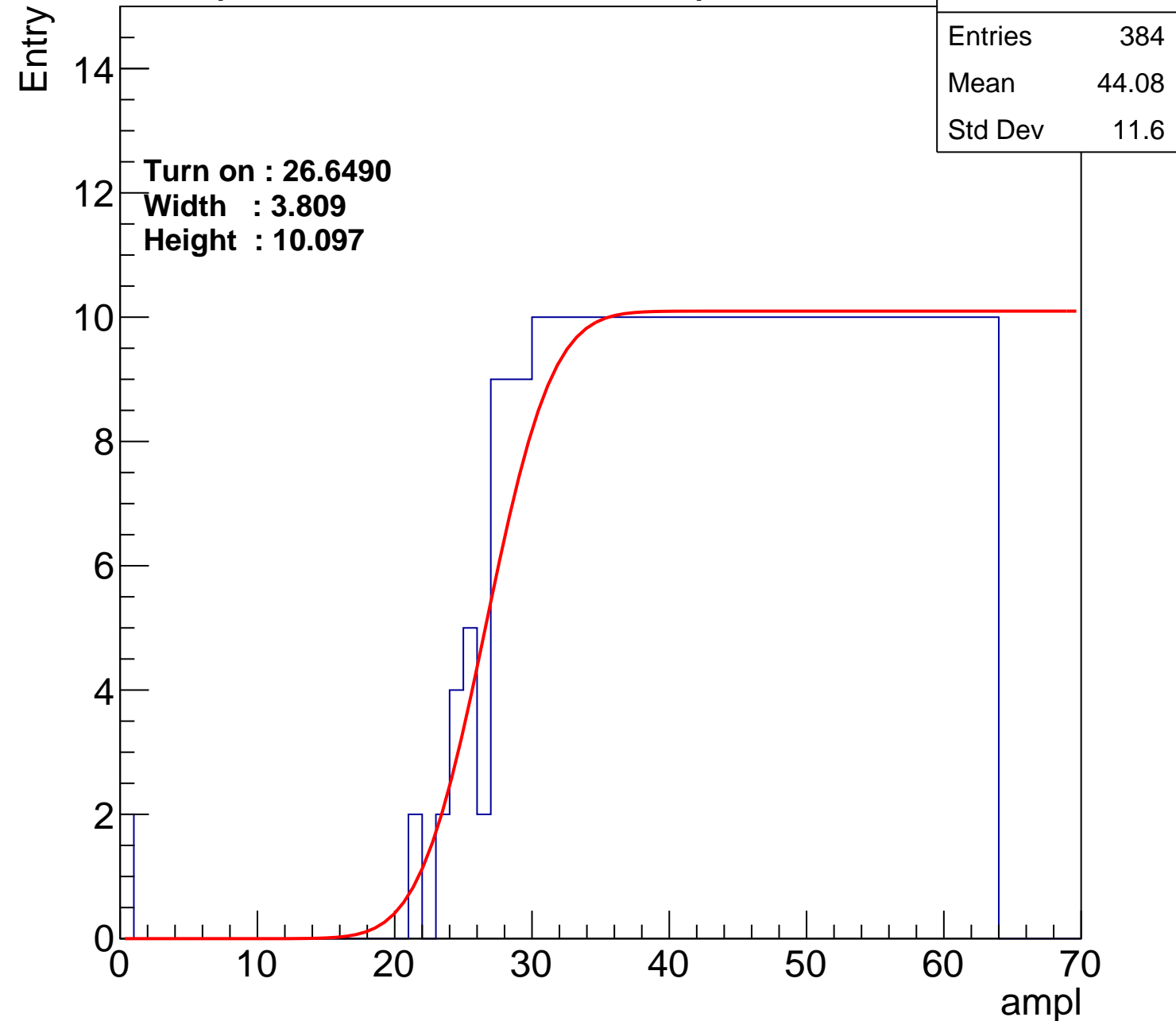
Width : 3.809

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch40

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.18
Std Dev	11.64

Turn on : 27.2664

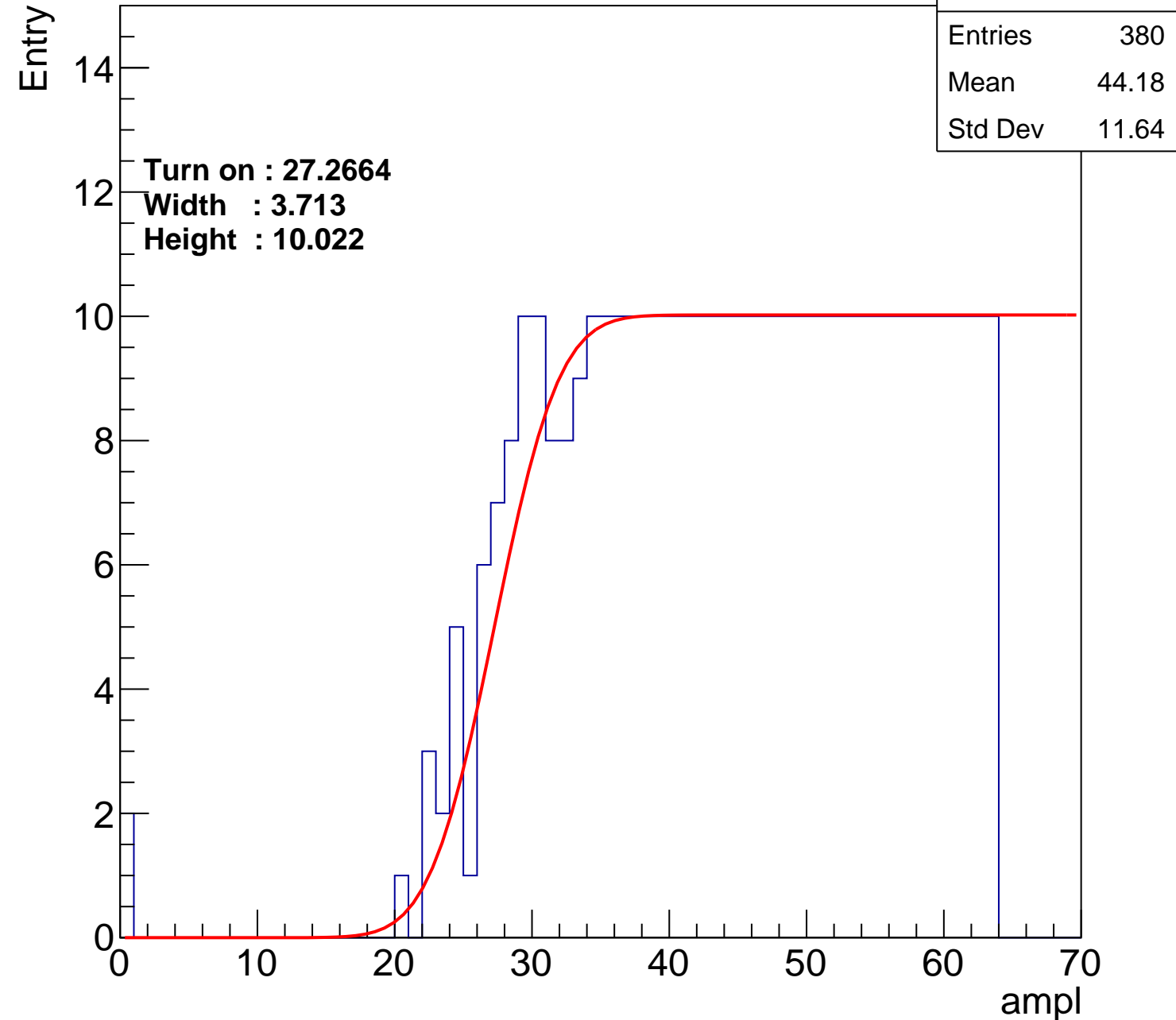
Width : 3.713

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.42
Std Dev	11.6

Turn on : 26.9173

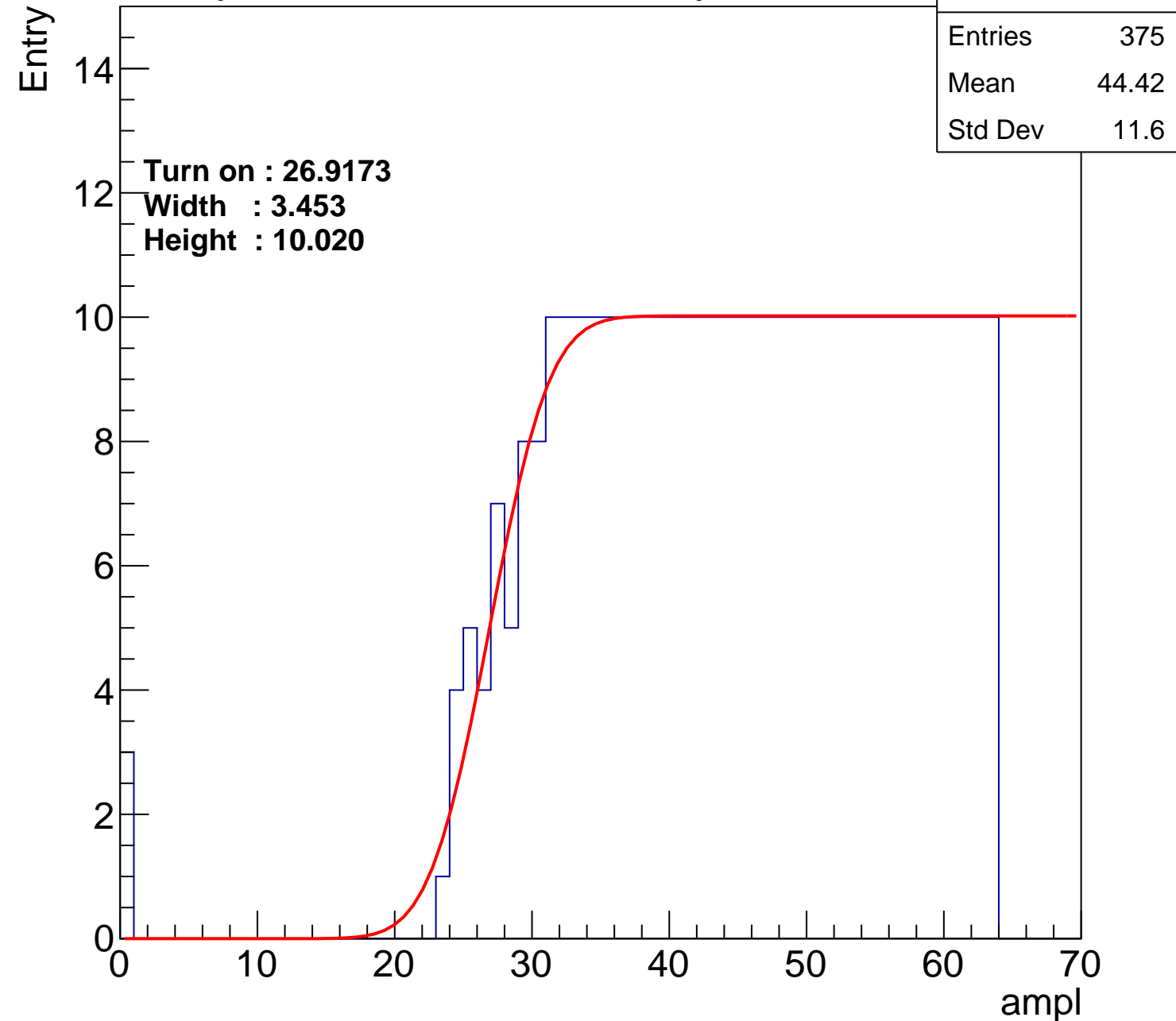
Width : 3.453

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.92
Std Dev	11.75

Turn on : 26.5400

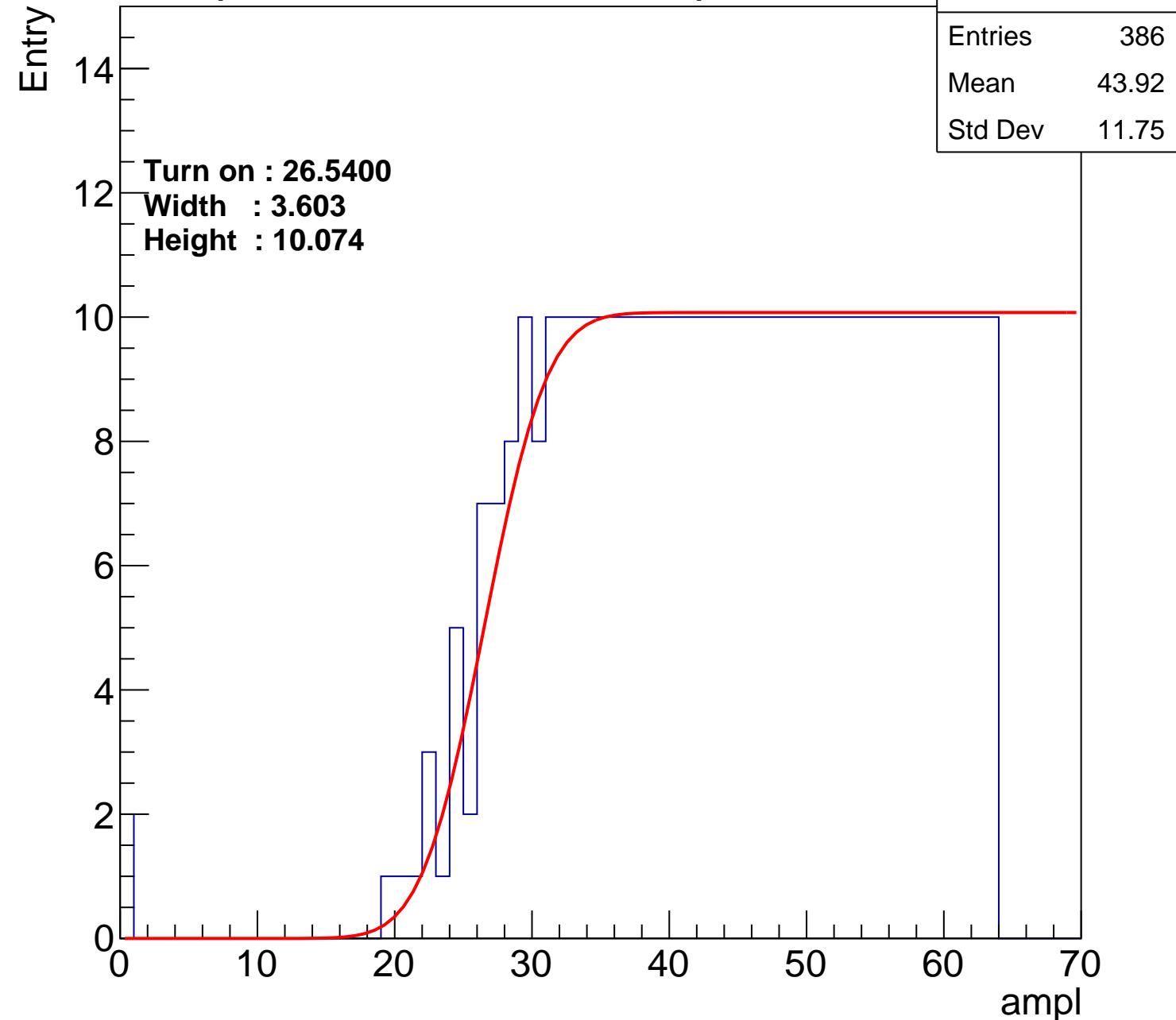
Width : 3.603

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.58
Std Dev	12.16

Turn on : 25.9772

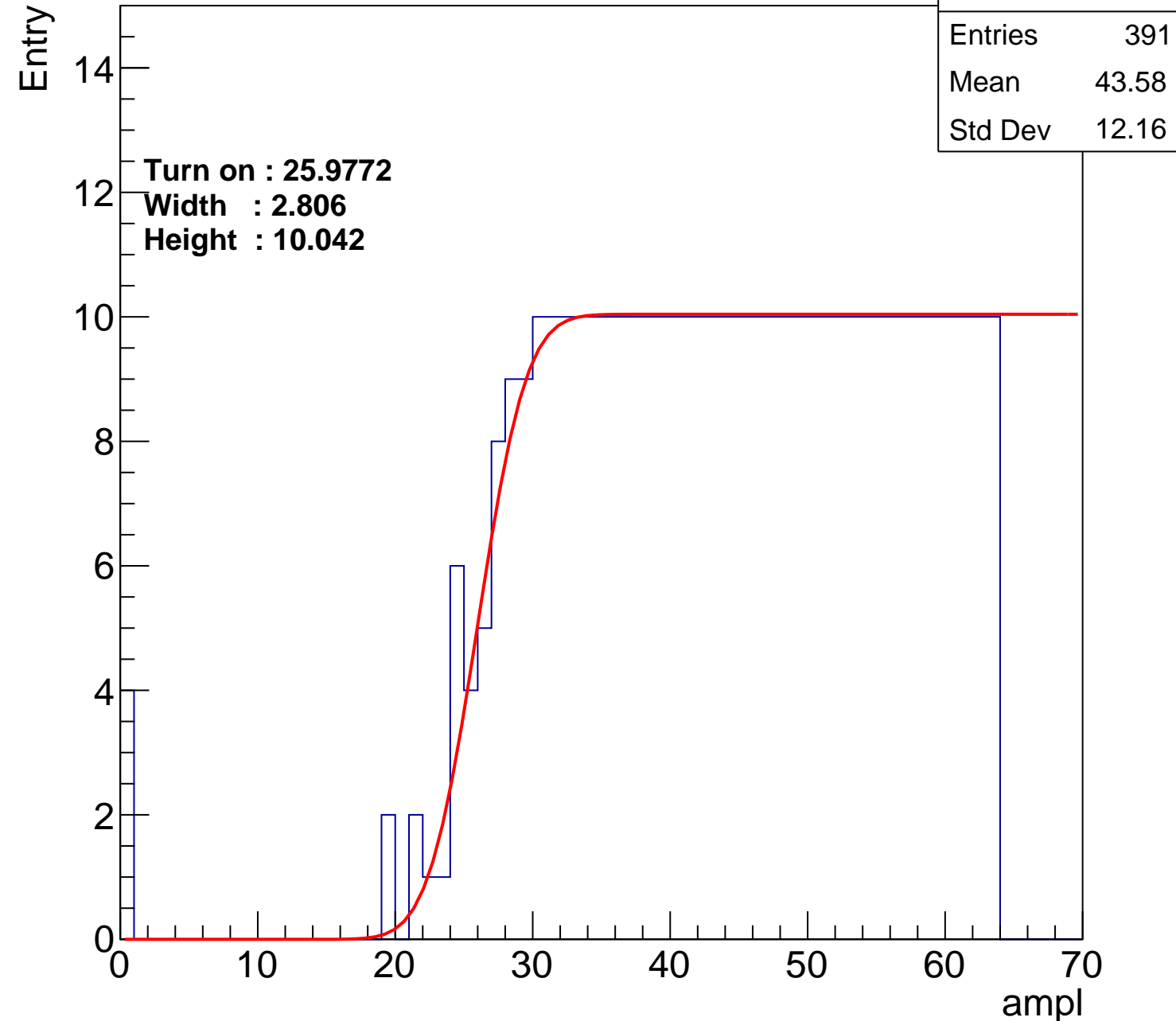
Width : 2.806

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch44

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.23
Std Dev	11.56

Turn on : 26.5465

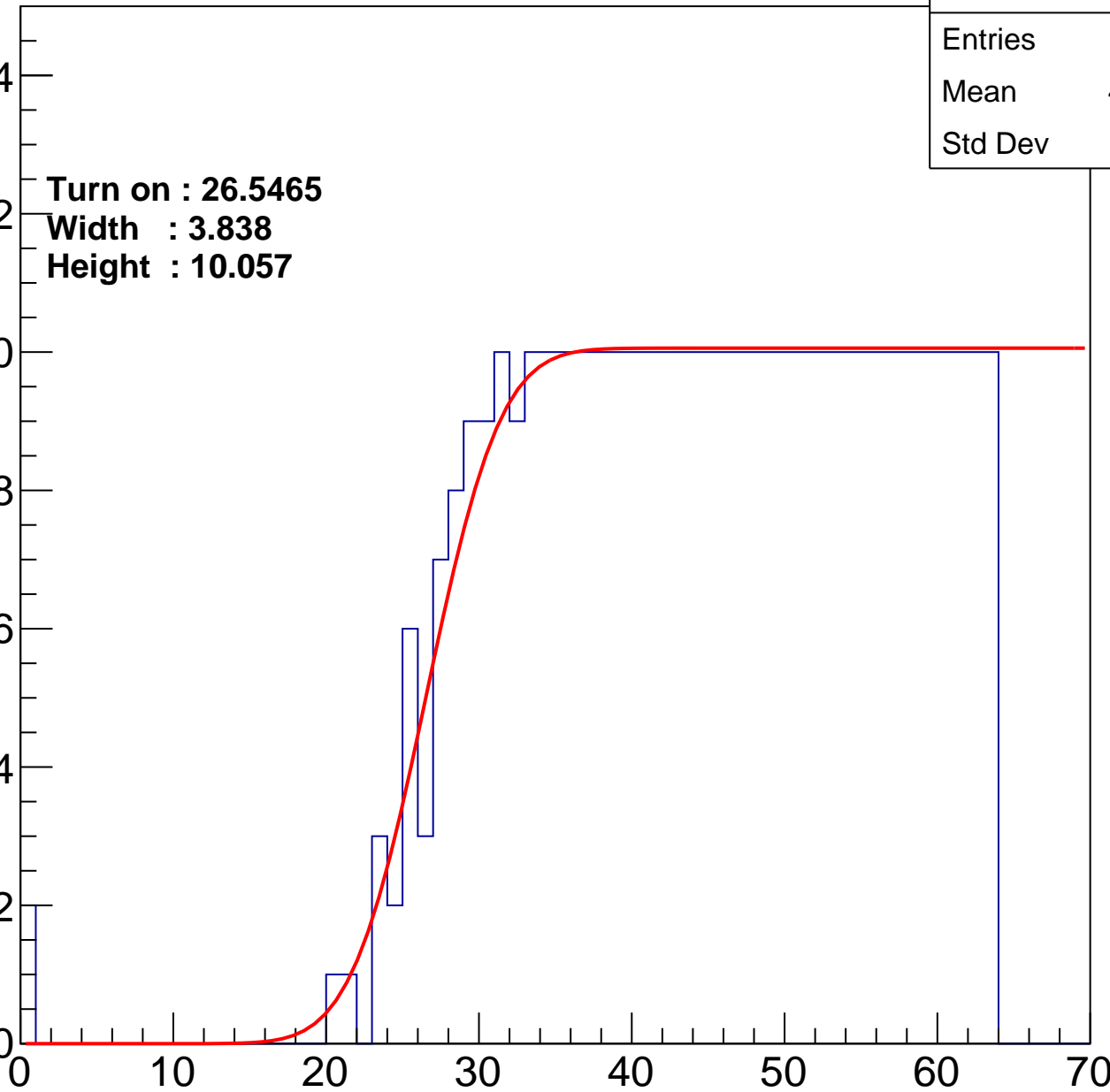
Width : 3.838

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

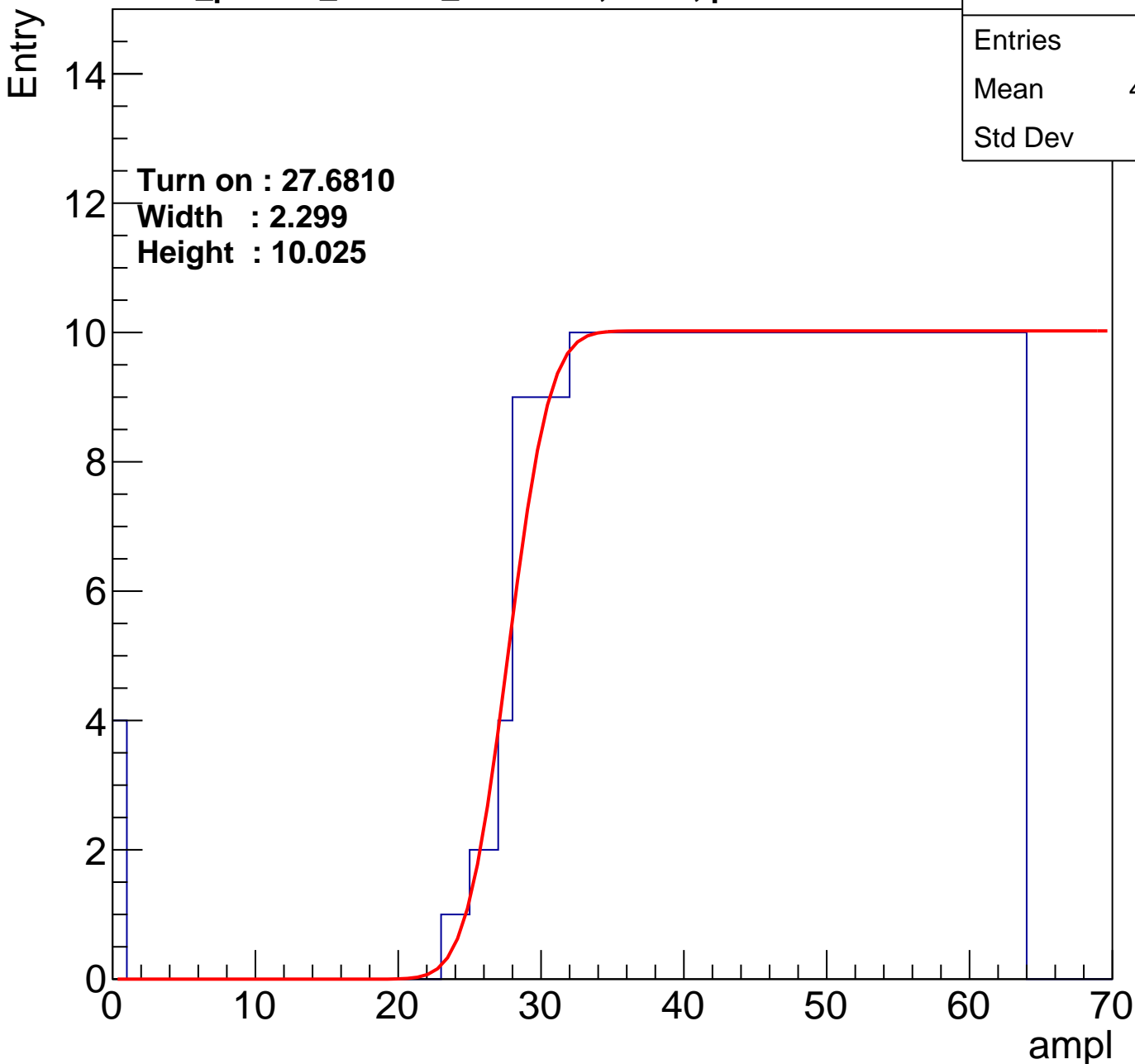


calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.65
Std Dev	11.6

Height : 10.025



B1L103S, U23-ch46

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 28.2548

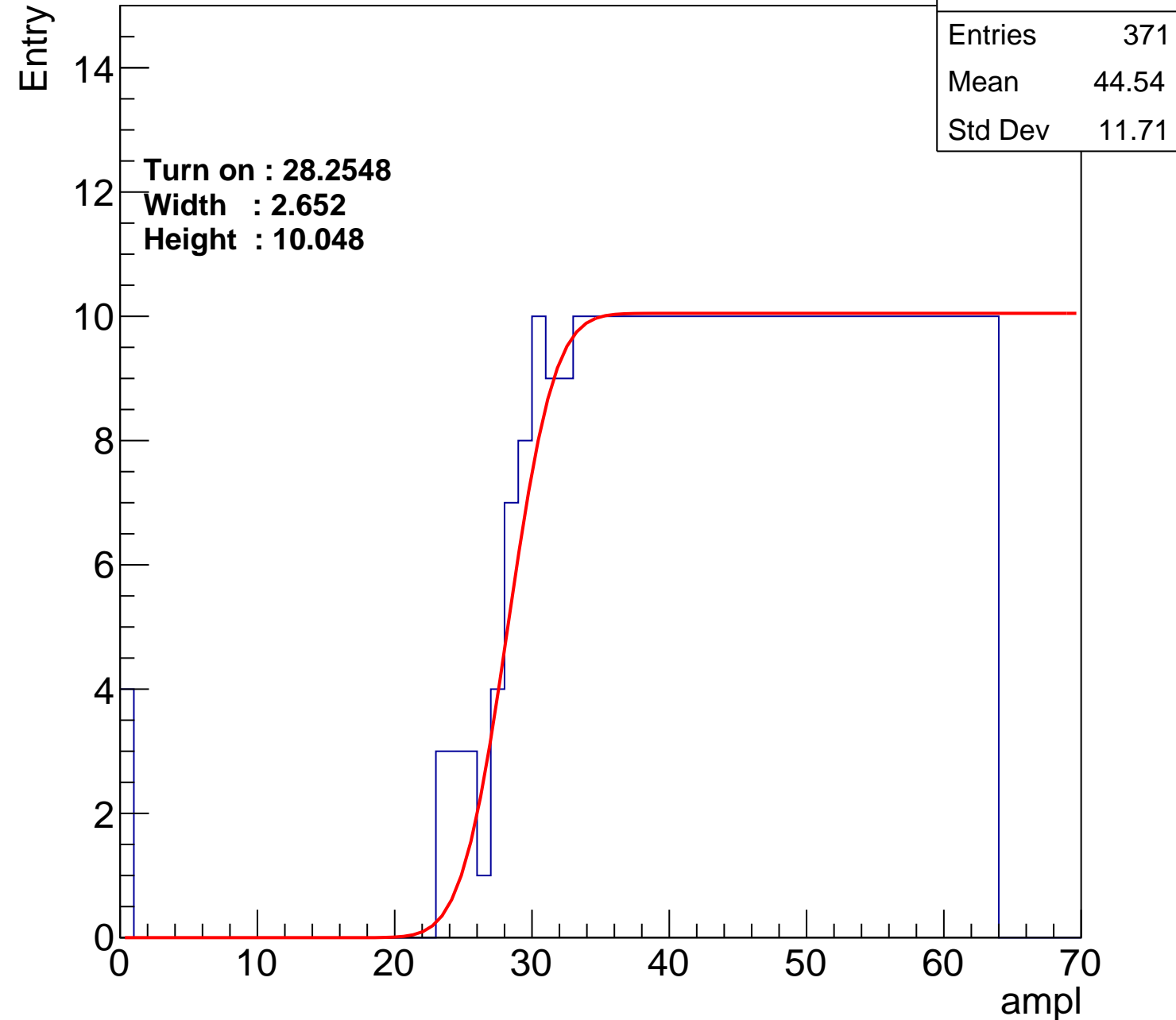
Width : 2.652

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch47

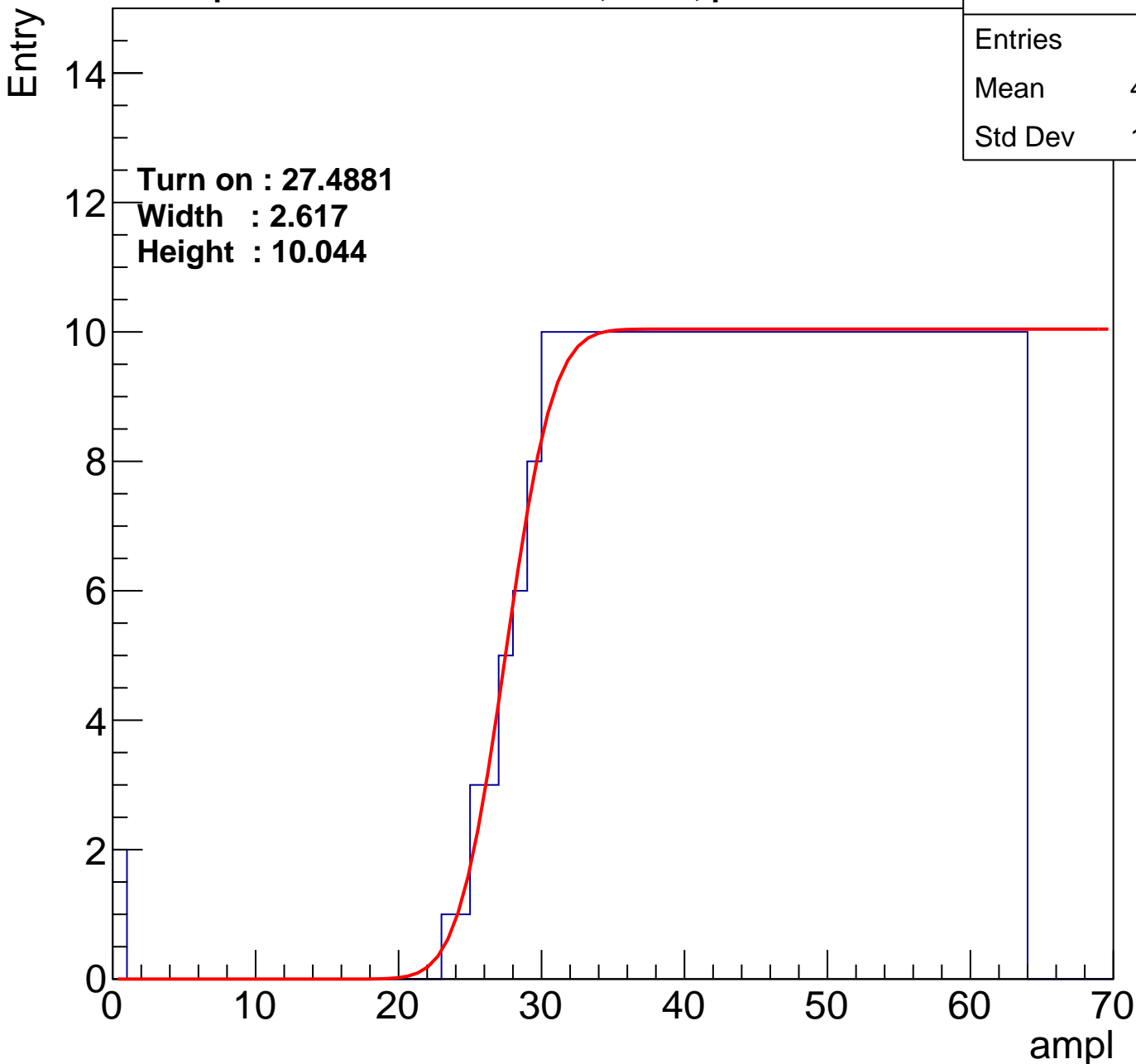
calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 27.4881

Width : 2.617

Height : 10.044



B1L103S, U23-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.77
Std Dev	11.67

Turn on : 25.6313

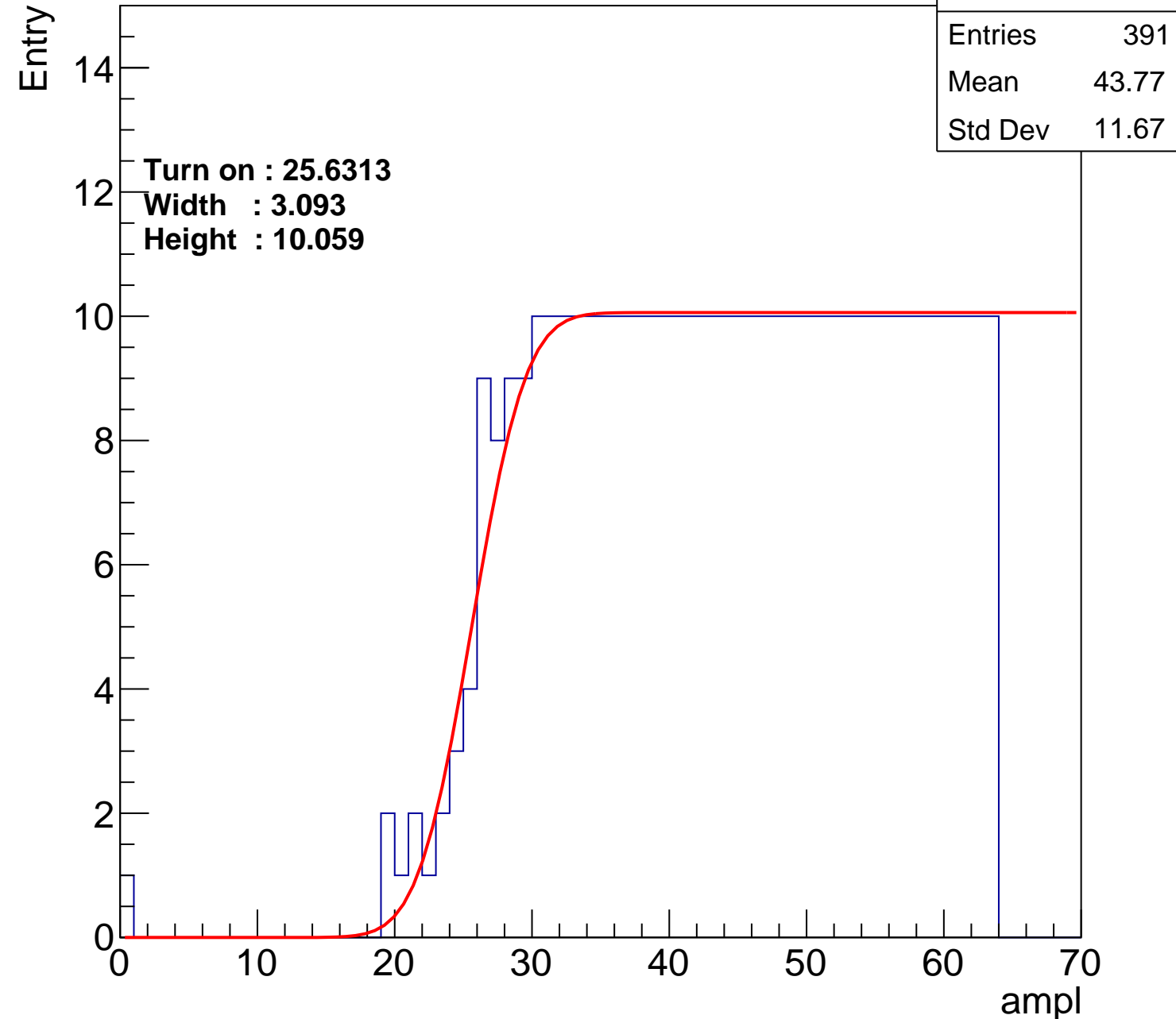
Width : 3.093

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch49

calib_packv5_042523_0143.root, FC#7, port C2

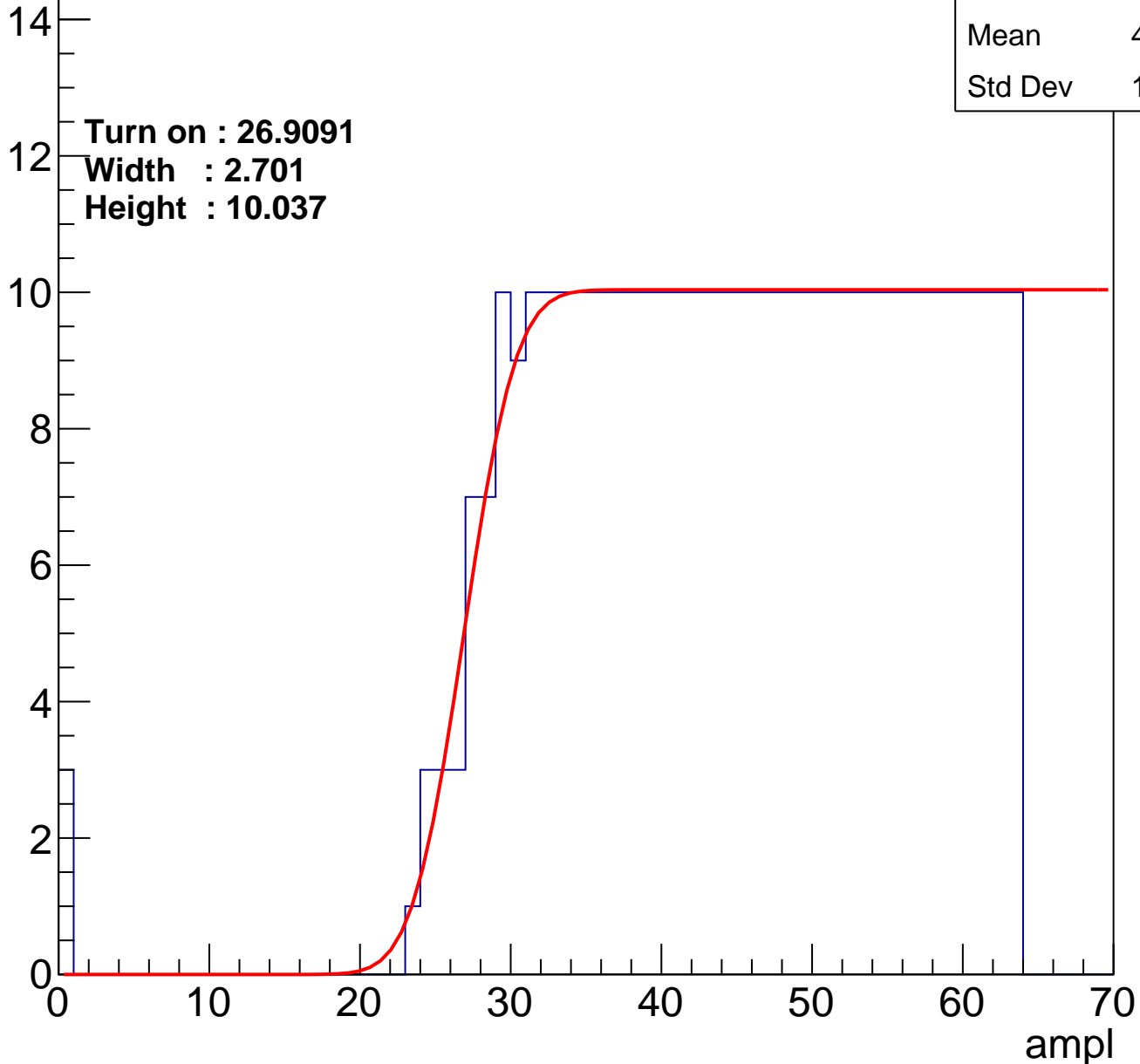
Entries	376
Mean	44.42
Std Dev	11.55

Turn on : 26.9091

Width : 2.701

Height : 10.037

Entry



B1L103S, U23-ch50

calib_packv5_042523_0143.root, FC#7, port C2

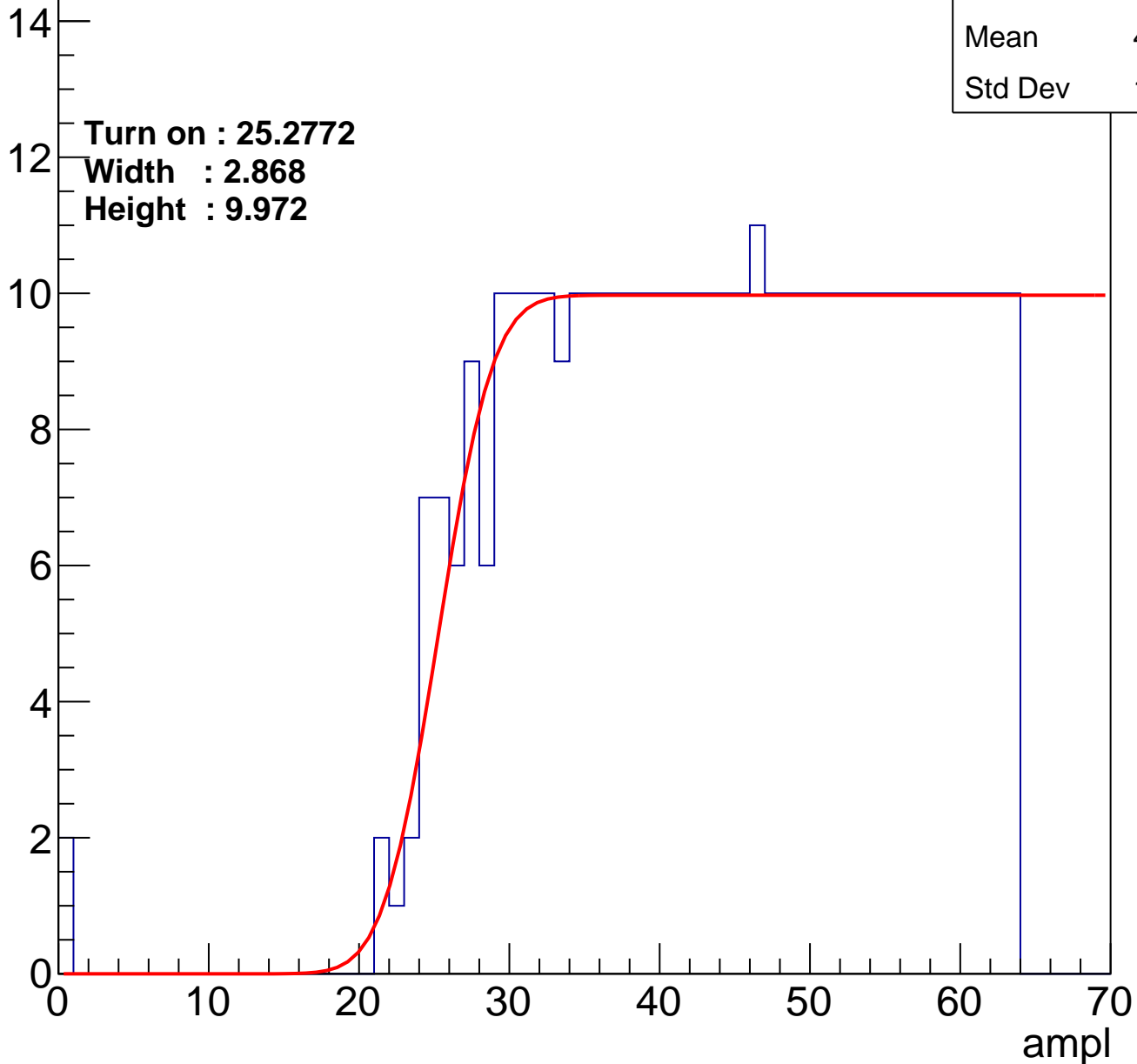
Entries	392
Mean	43.71
Std Dev	11.81

Turn on : 25.2772

Width : 2.868

Height : 9.972

Entry



B1L103S, U23-ch51

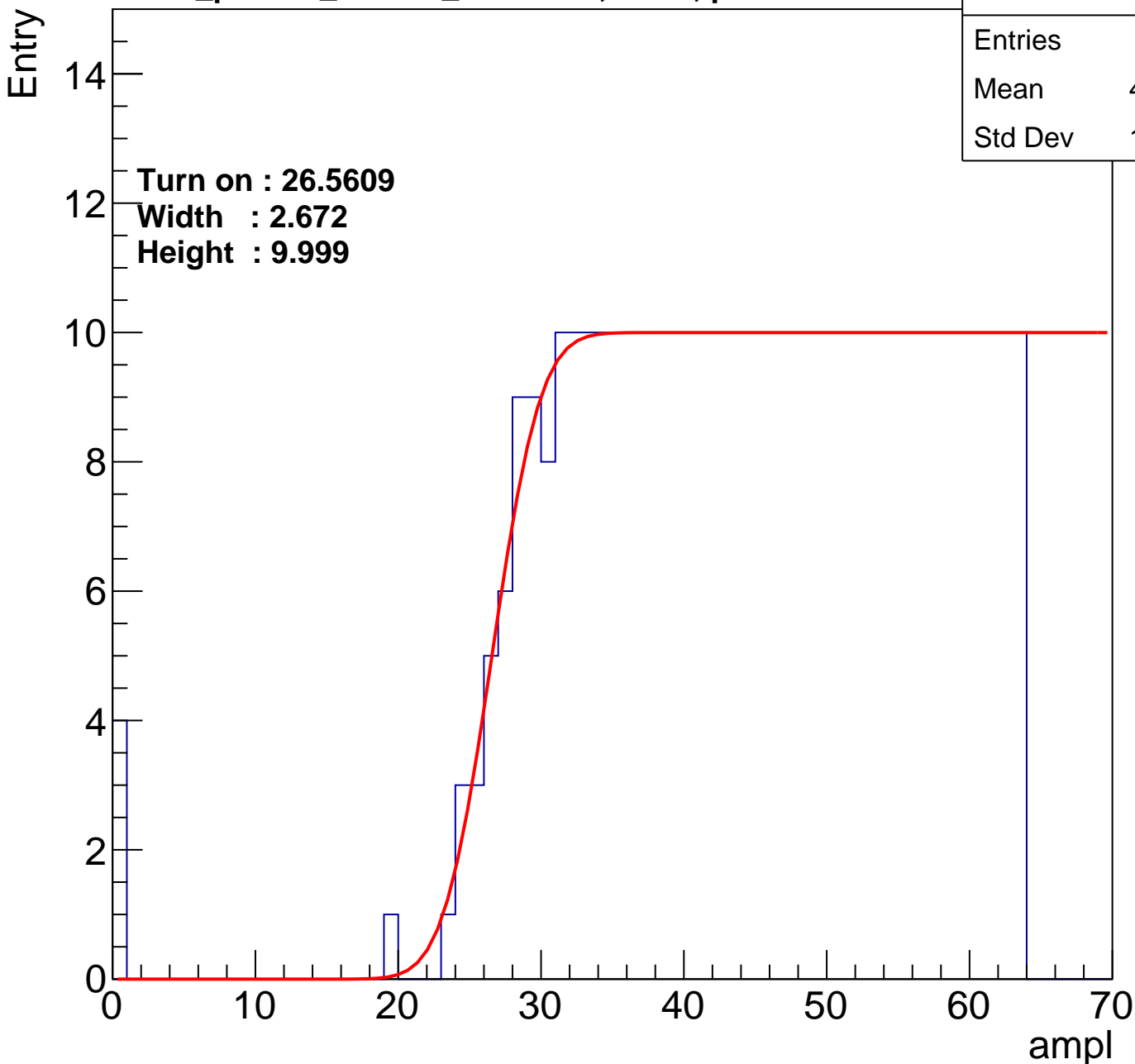
calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.18
Std Dev	11.85

Turn on : 26.5609

Width : 2.672

Height : 9.999



B1L103S, U23-ch52

calib_packv5_042523_0143.root, FC#7, port C2

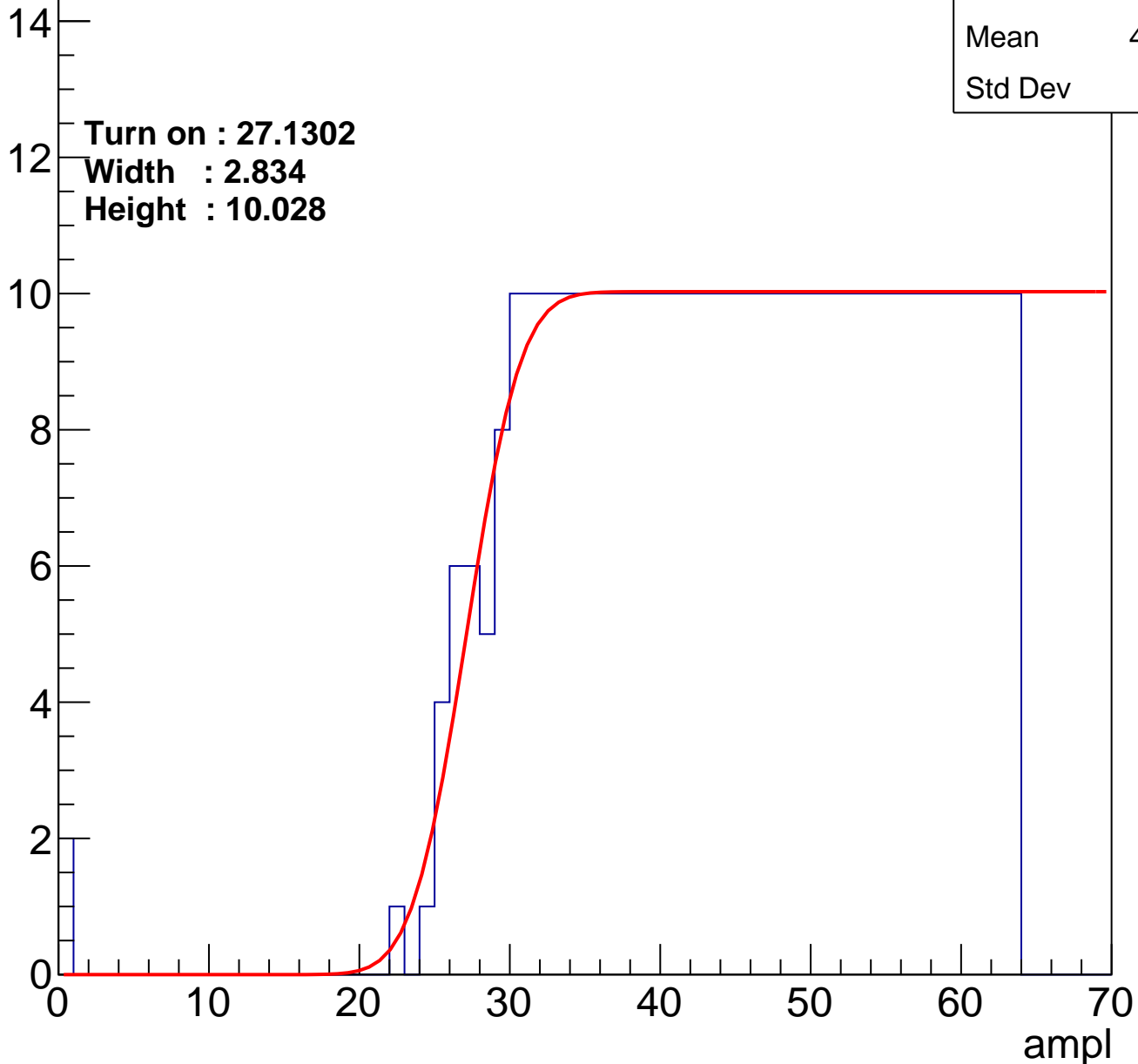
Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.1302

Width : 2.834

Height : 10.028

Entry



B1L103S, U23-ch53

calib_packv5_042523_0143.root, FC#7, port C2

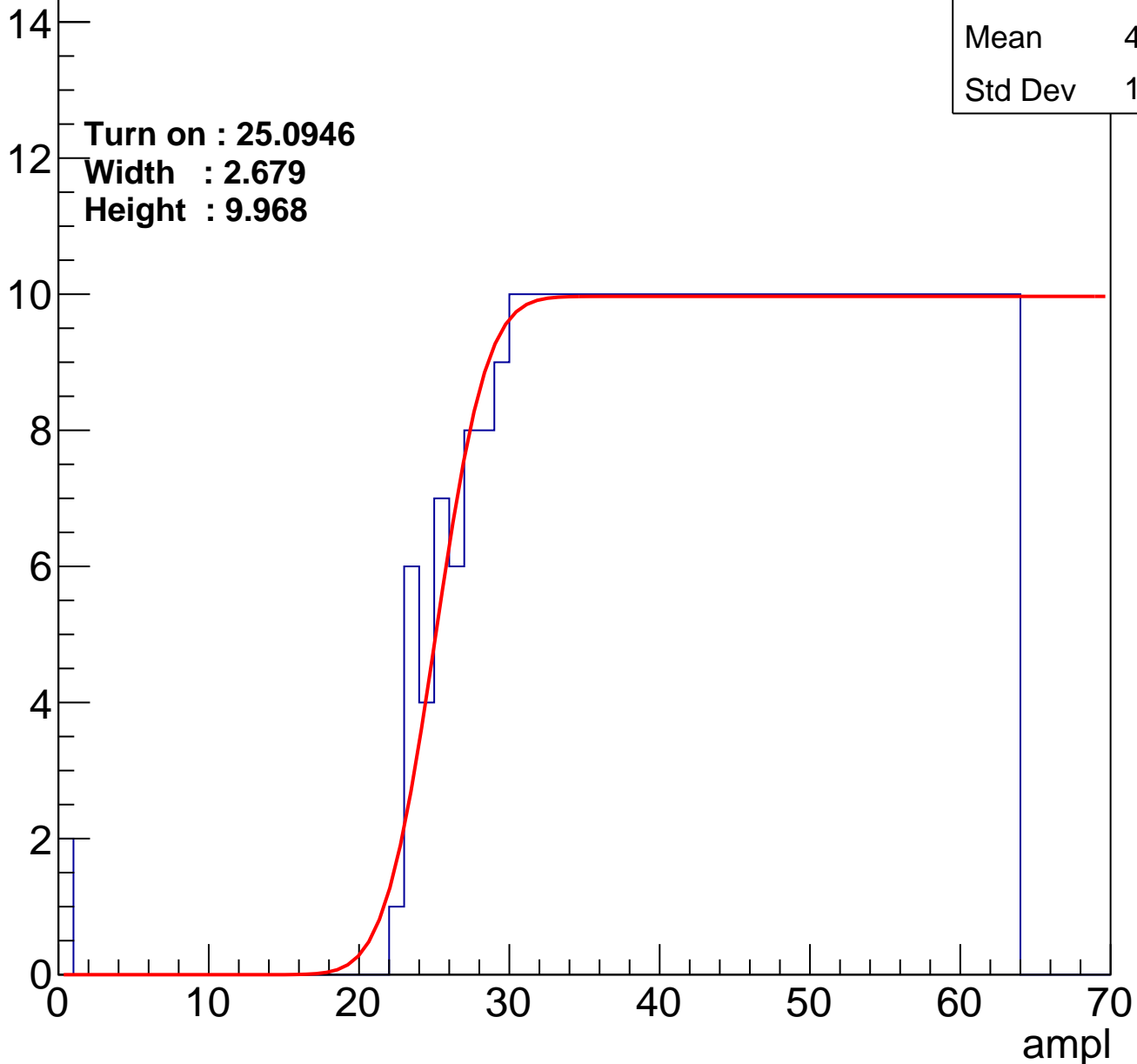
Entry

Entries	391
Mean	43.73
Std Dev	11.78

Turn on : 25.0946

Width : 2.679

Height : 9.968



B1L103S, U23-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.07
Std Dev	11.81

Turn on : 26.3141

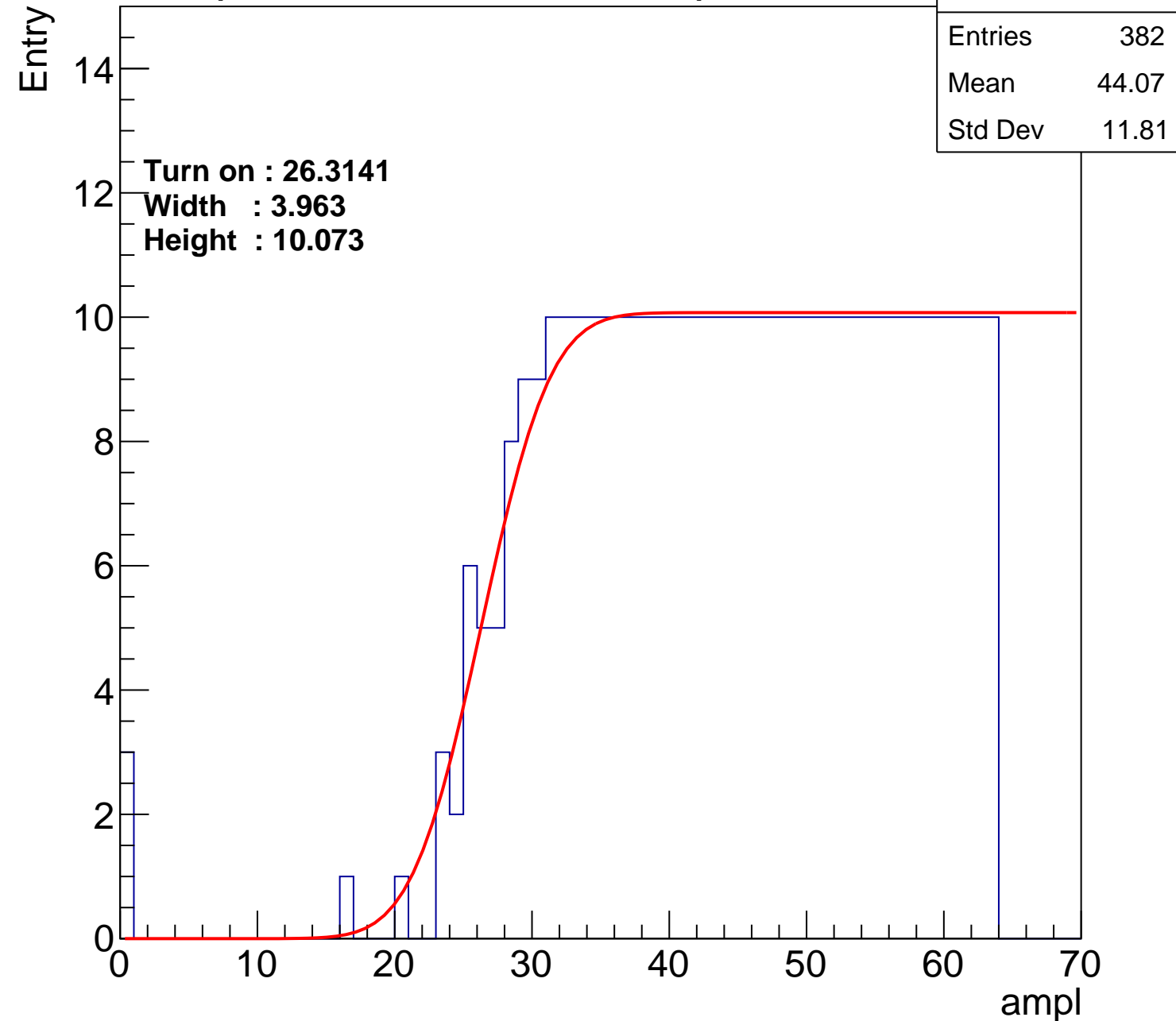
Width : 3.963

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.89
Std Dev	11.54

Turn on : 28.0104

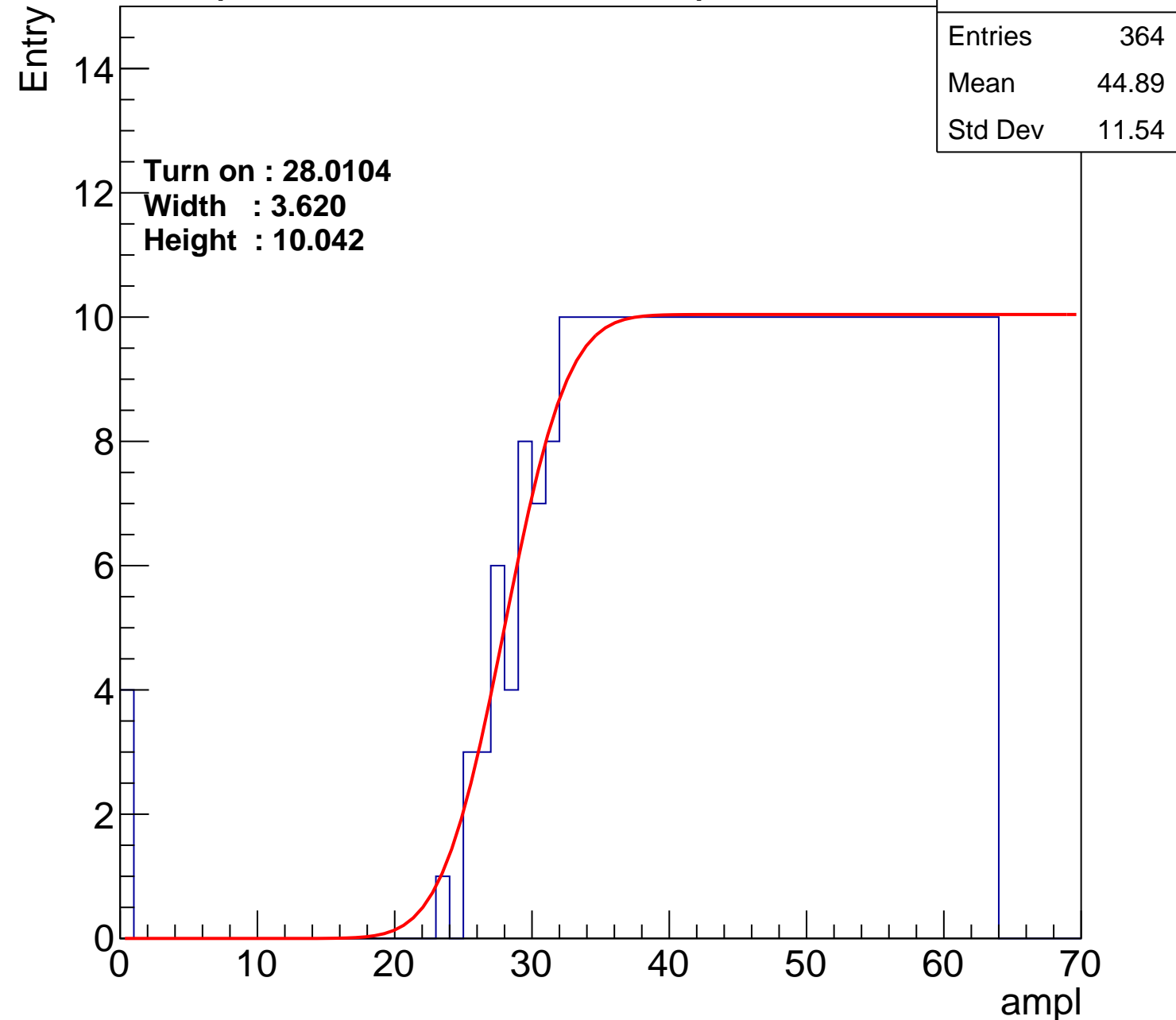
Width : 3.620

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.12
Std Dev	12.45

Turn on : 24.7977

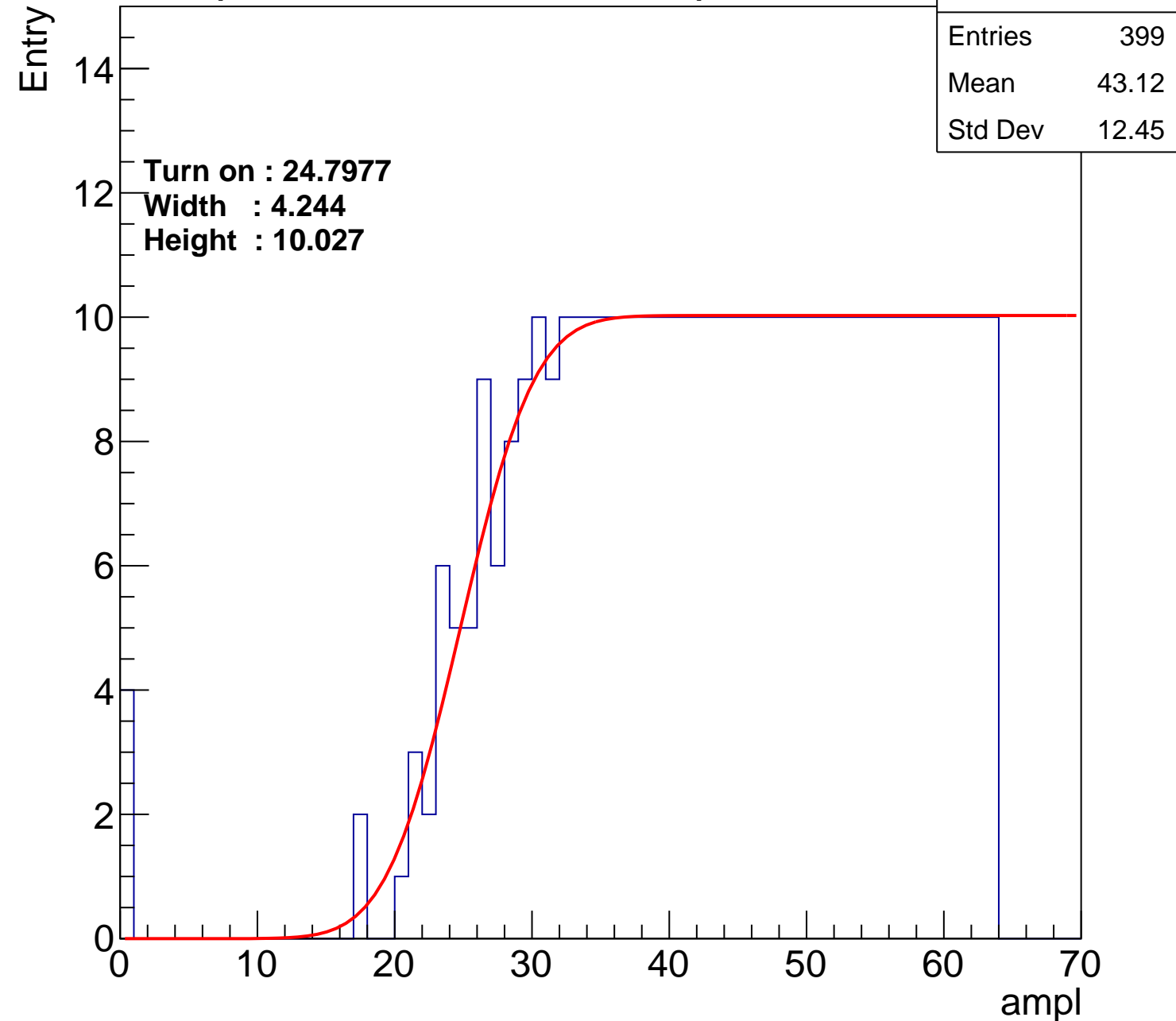
Width : 4.244

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#7, port C2

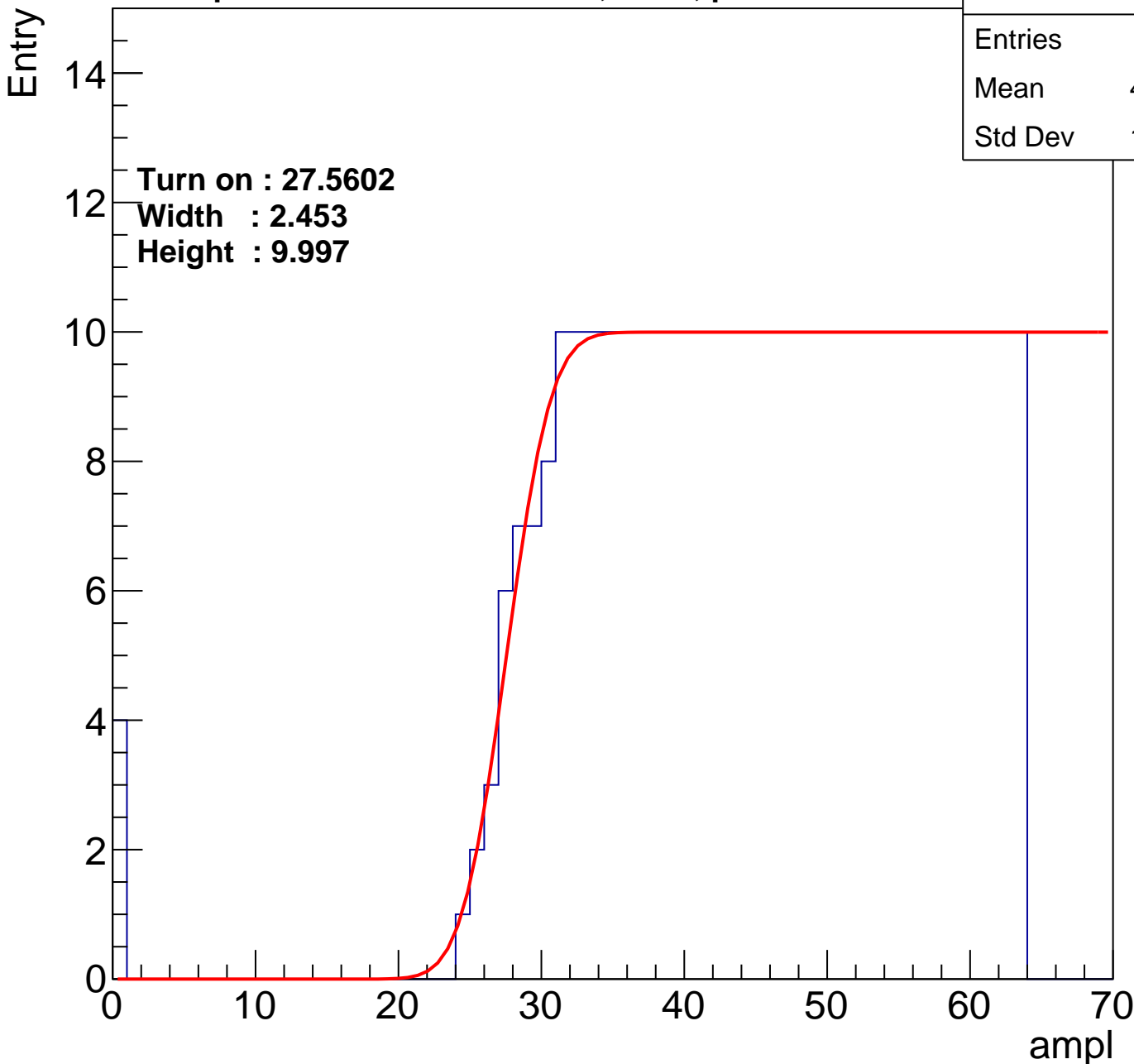
Entries	368
Mean	44.74
Std Dev	11.56

Mean	44.74
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Std Dev	11.56
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Width : 2.453

Height : 9.997



B1L103S, U23-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.8670

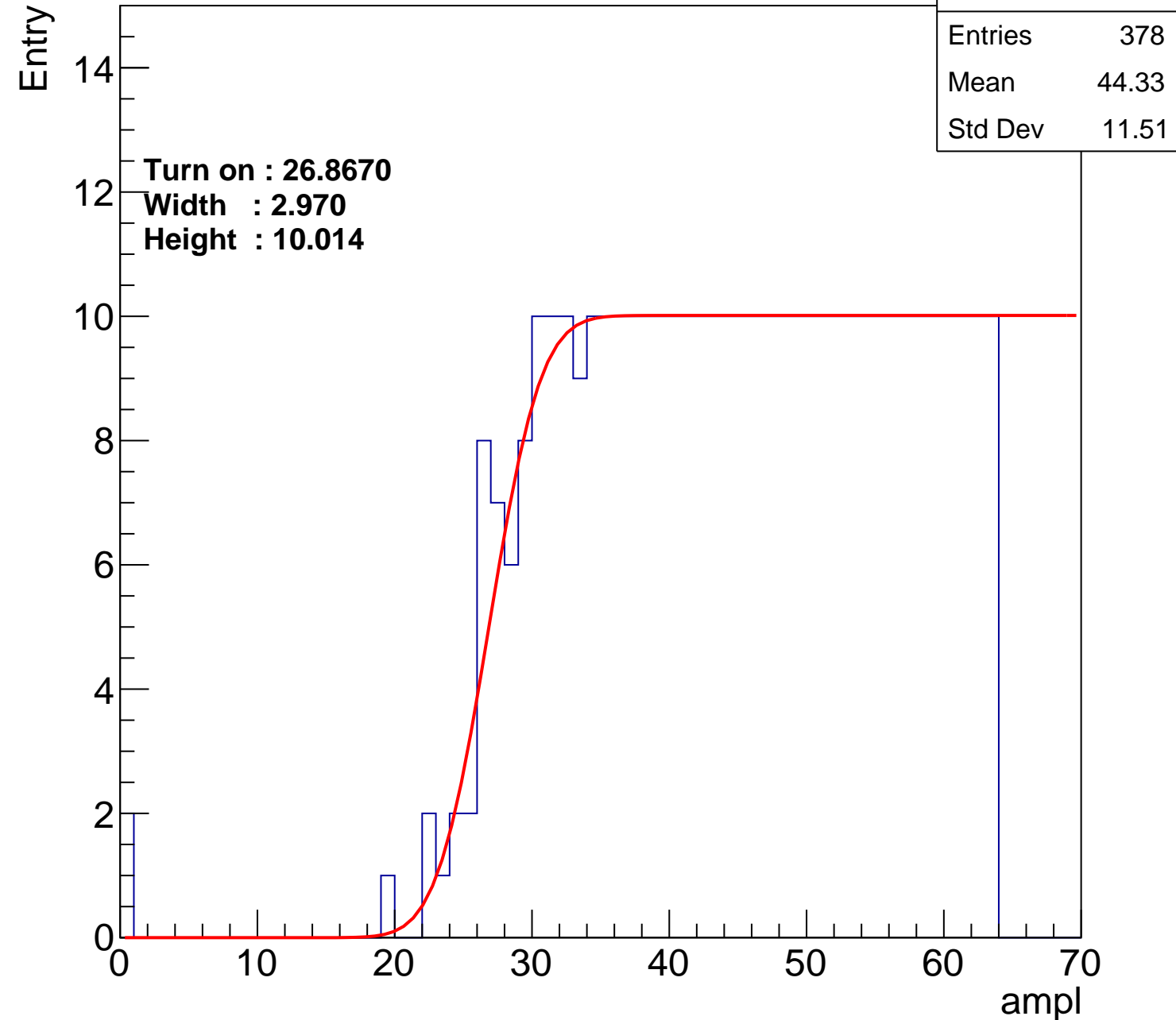
Width : 2.970

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.44
Std Dev	11.71

Turn on : 26.9075

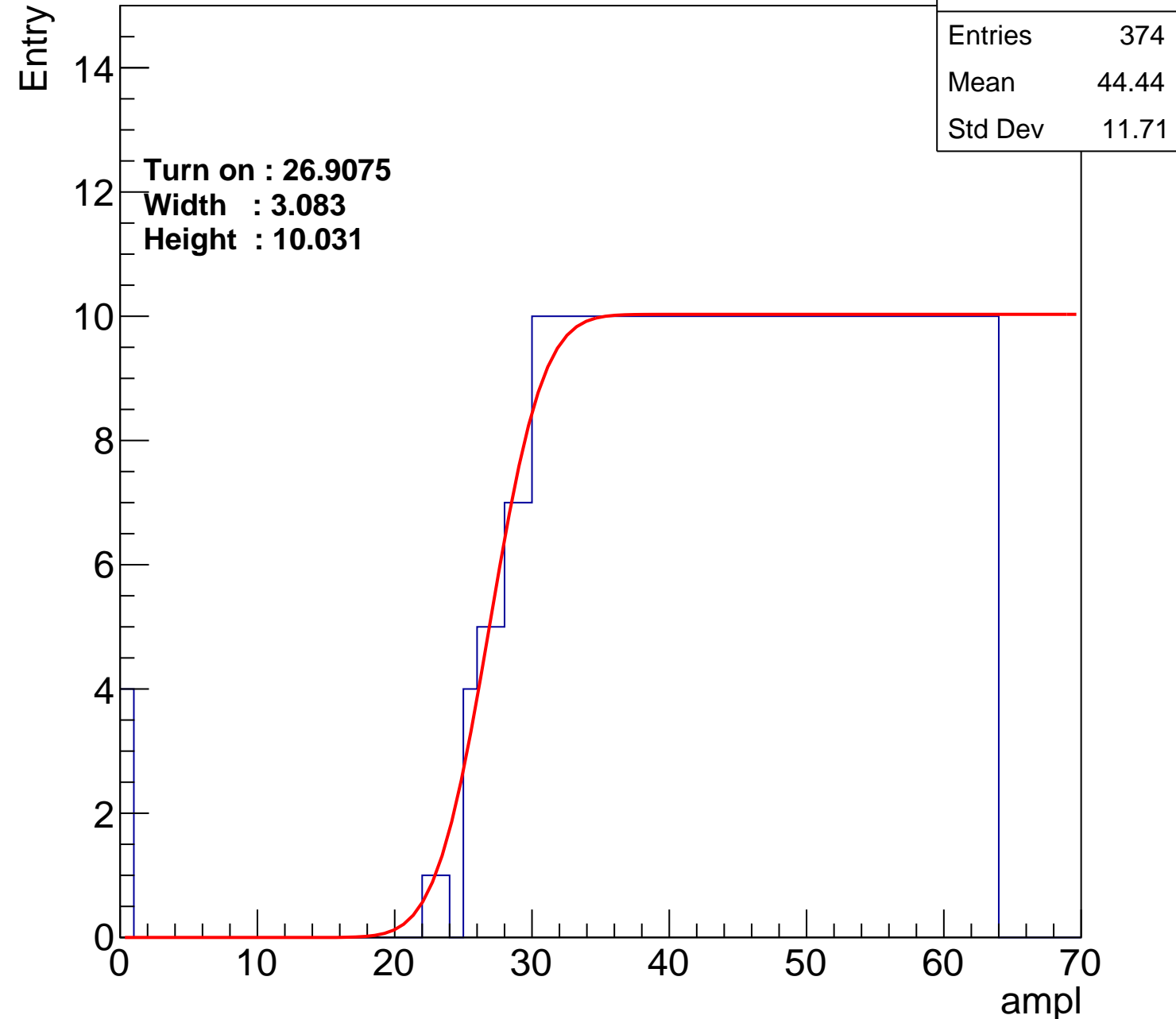
Width : 3.083

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch60

calib_packv5_042523_0143.root, FC#7, port C2

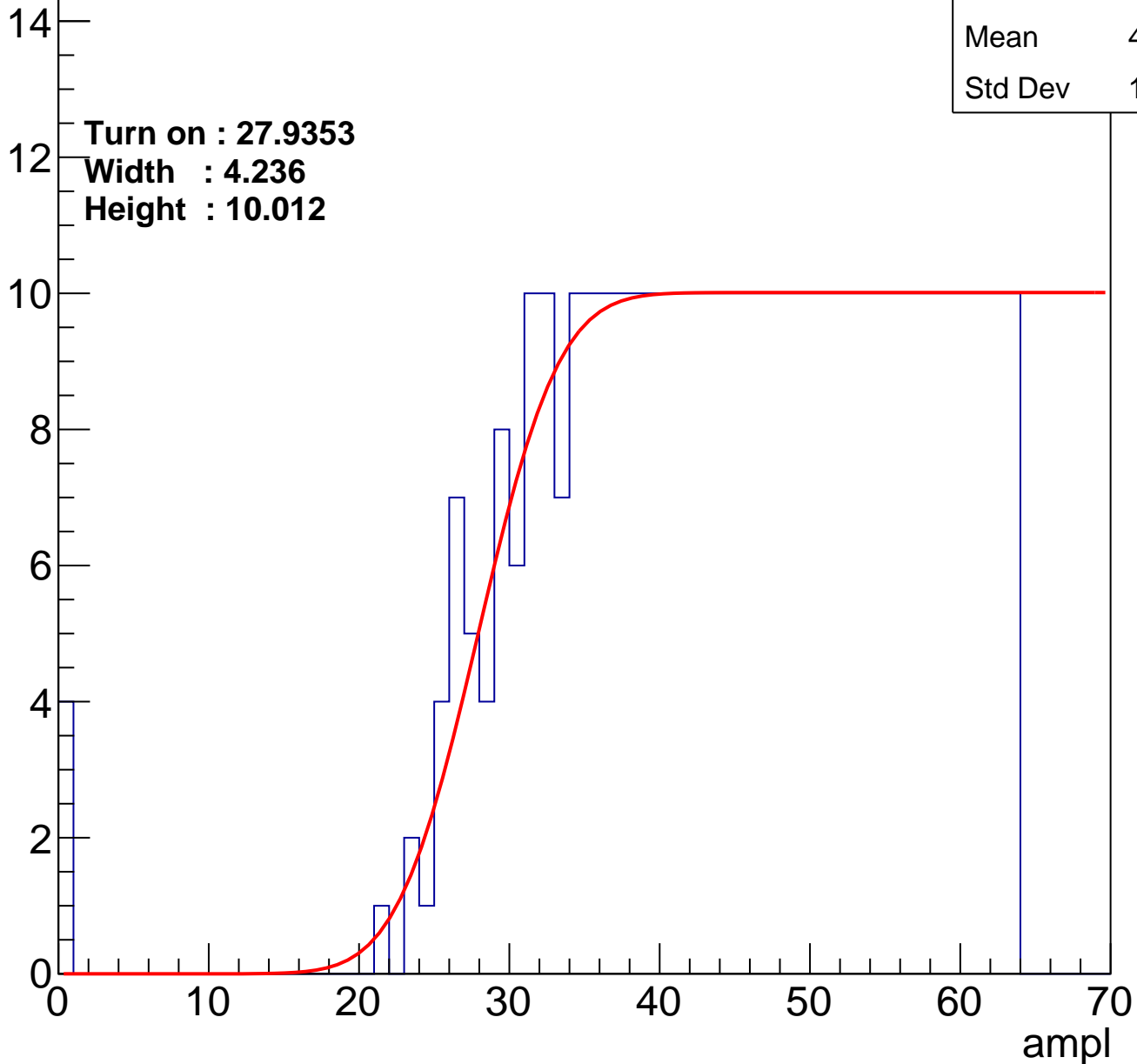
Entries	369
Mean	44.56
Std Dev	11.77

Turn on : 27.9353

Width : 4.236

Height : 10.012

Entry



B1L103S, U23-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	43.84
Std Dev	12.06

Turn on : 26.5863

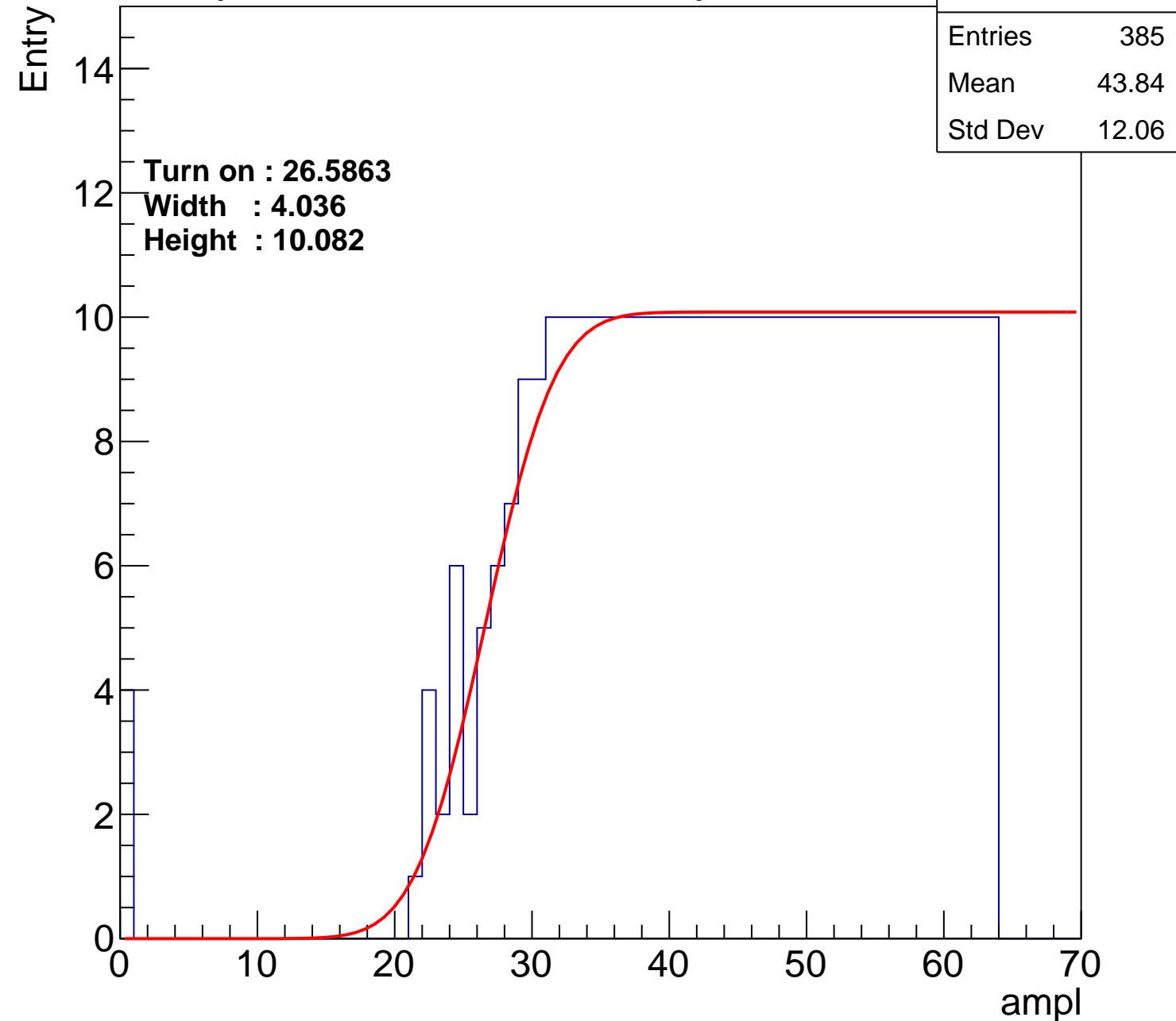
Width : 4.036

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch62

calib_packv5_042523_0143.root, FC#7, port C2

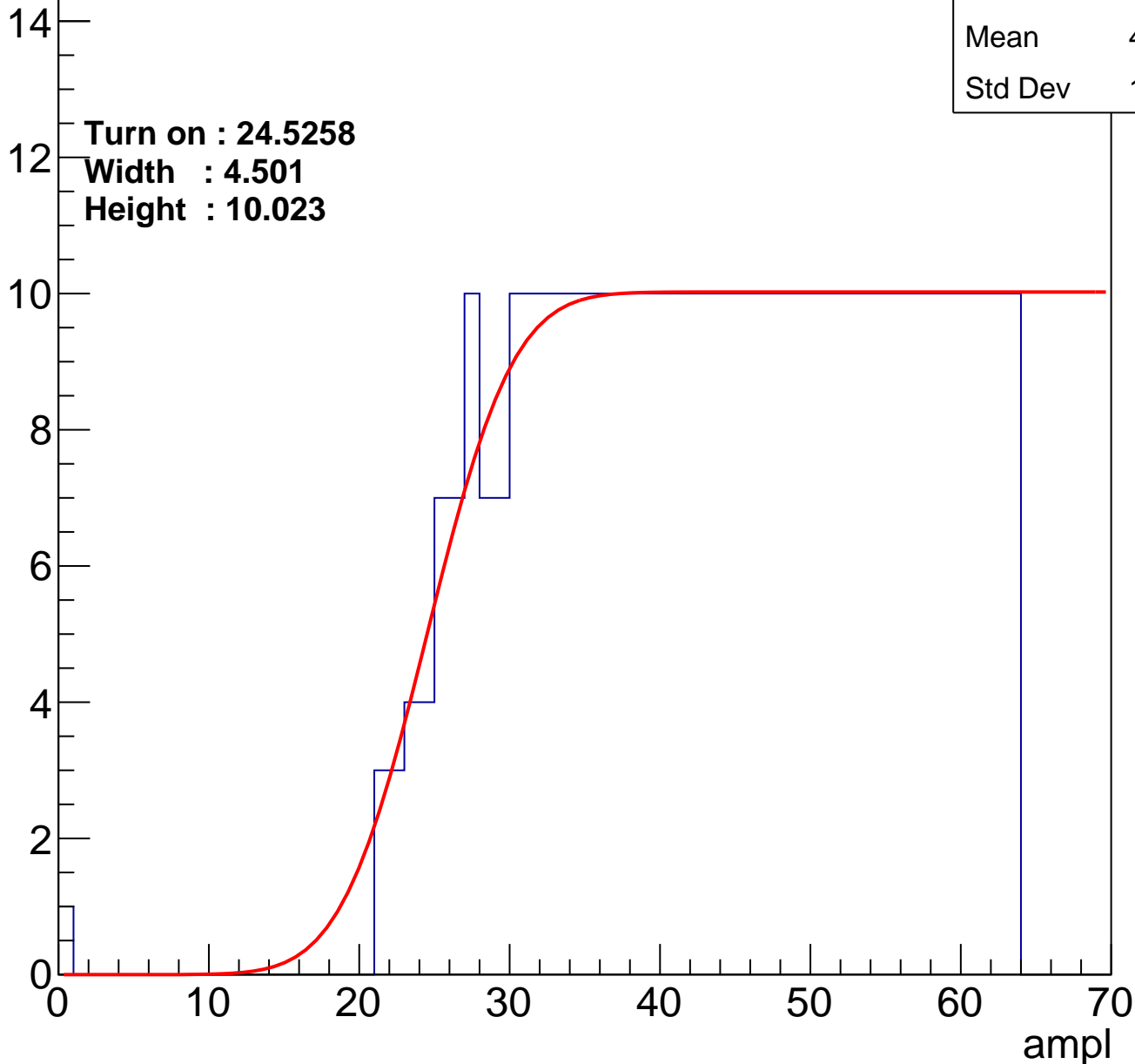
Entries	393
Mean	43.65
Std Dev	11.74

Turn on : 24.5258

Width : 4.501

Height : 10.023

Entry



B1L103S, U23-ch63

calib_packv5_042523_0143.root, FC#7, port C2

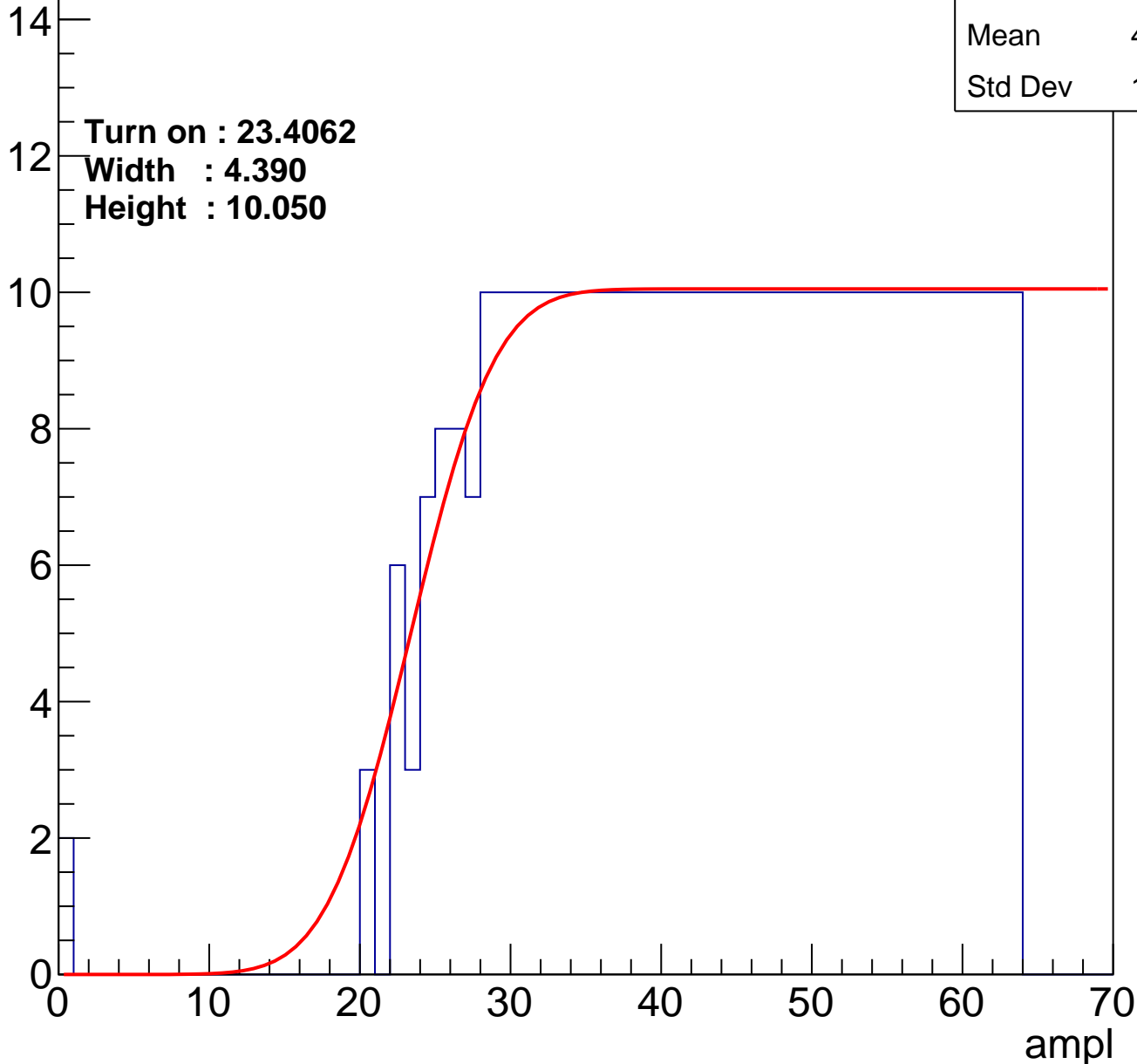
Entries	404
Mean	43.08
Std Dev	12.13

Turn on : 23.4062

Width : 4.390

Height : 10.050

Entry



B1L103S, U23-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.24
Std Dev	11.69

Turn on : 27.1671

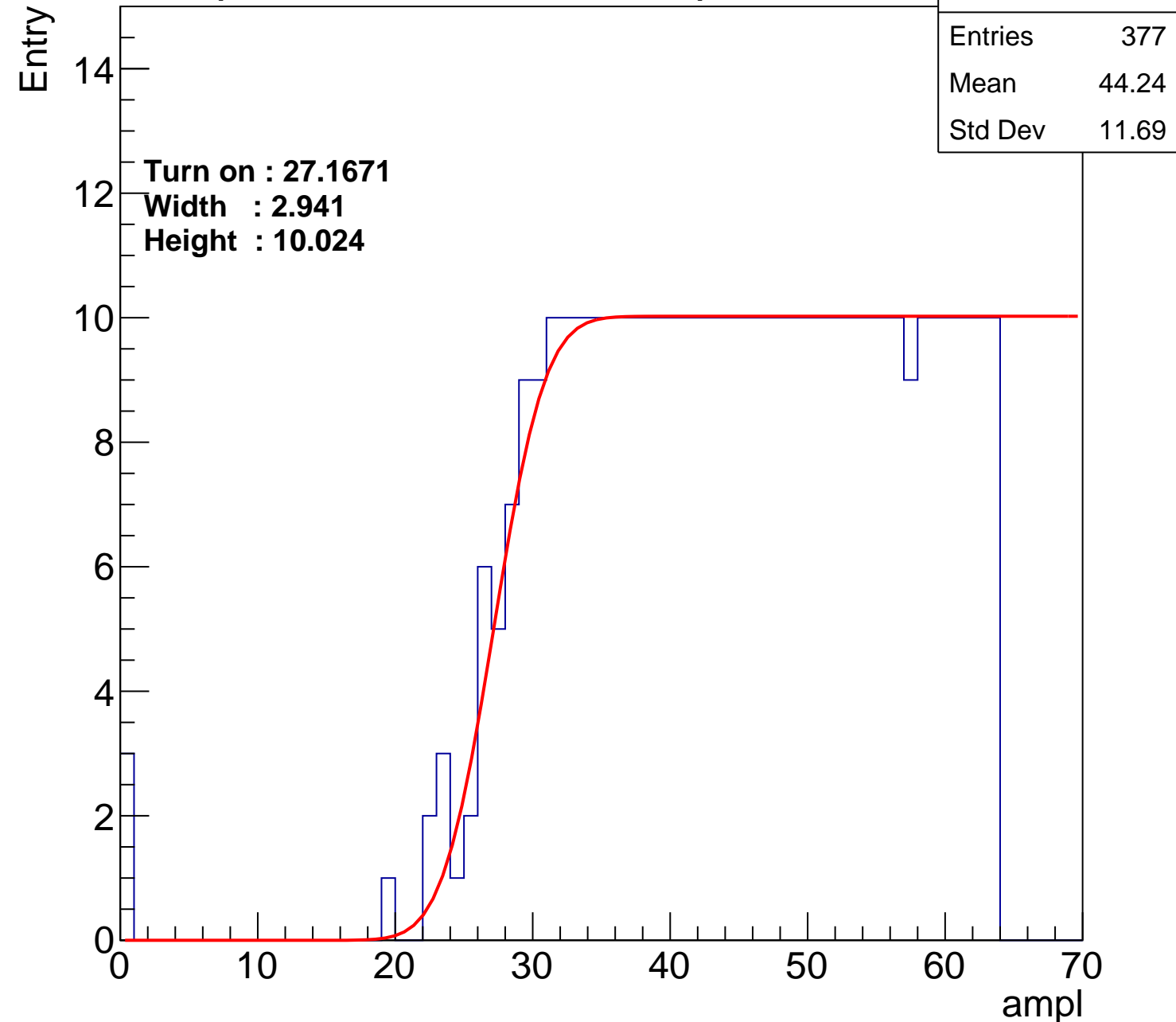
Width : 2.941

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.2
Std Dev	11.09

Turn on : 28.5500

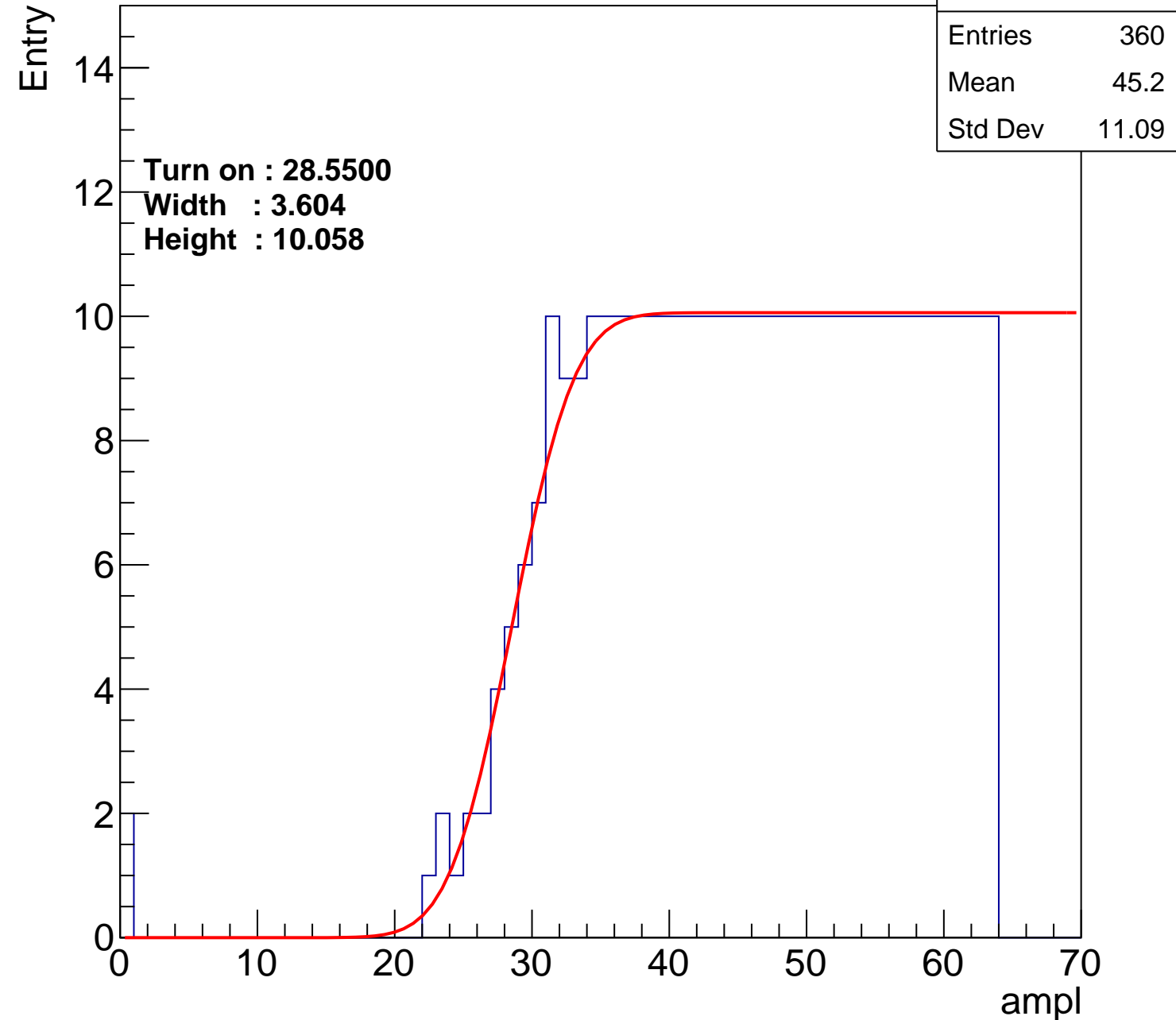
Width : 3.604

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch66

calib_packv5_042523_0143.root, FC#7, port C2

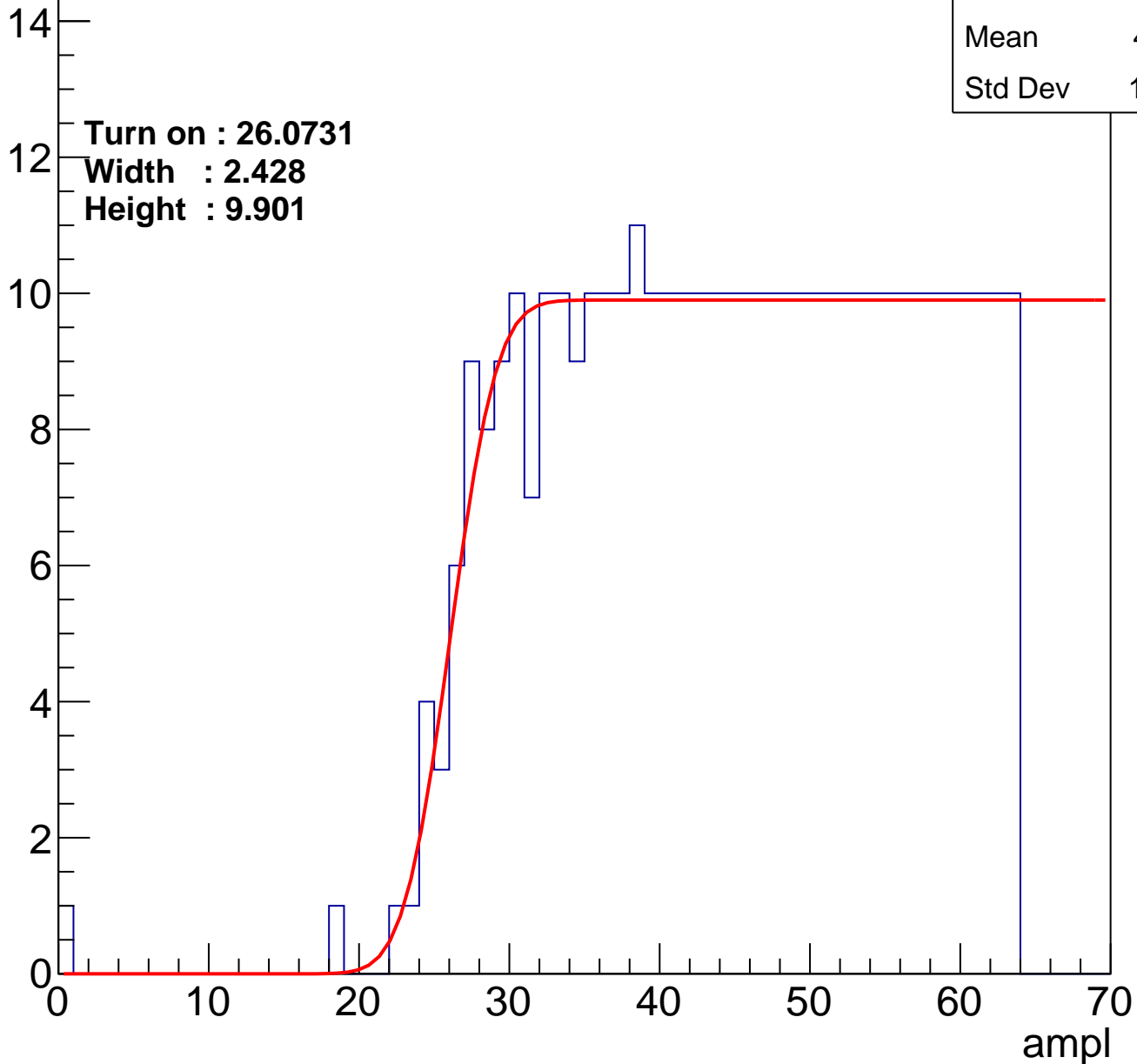
Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.0731

Width : 2.428

Height : 9.901

Entry



B1L103S, U23-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	45.01
Std Dev	11.3

Turn on : 28.1153

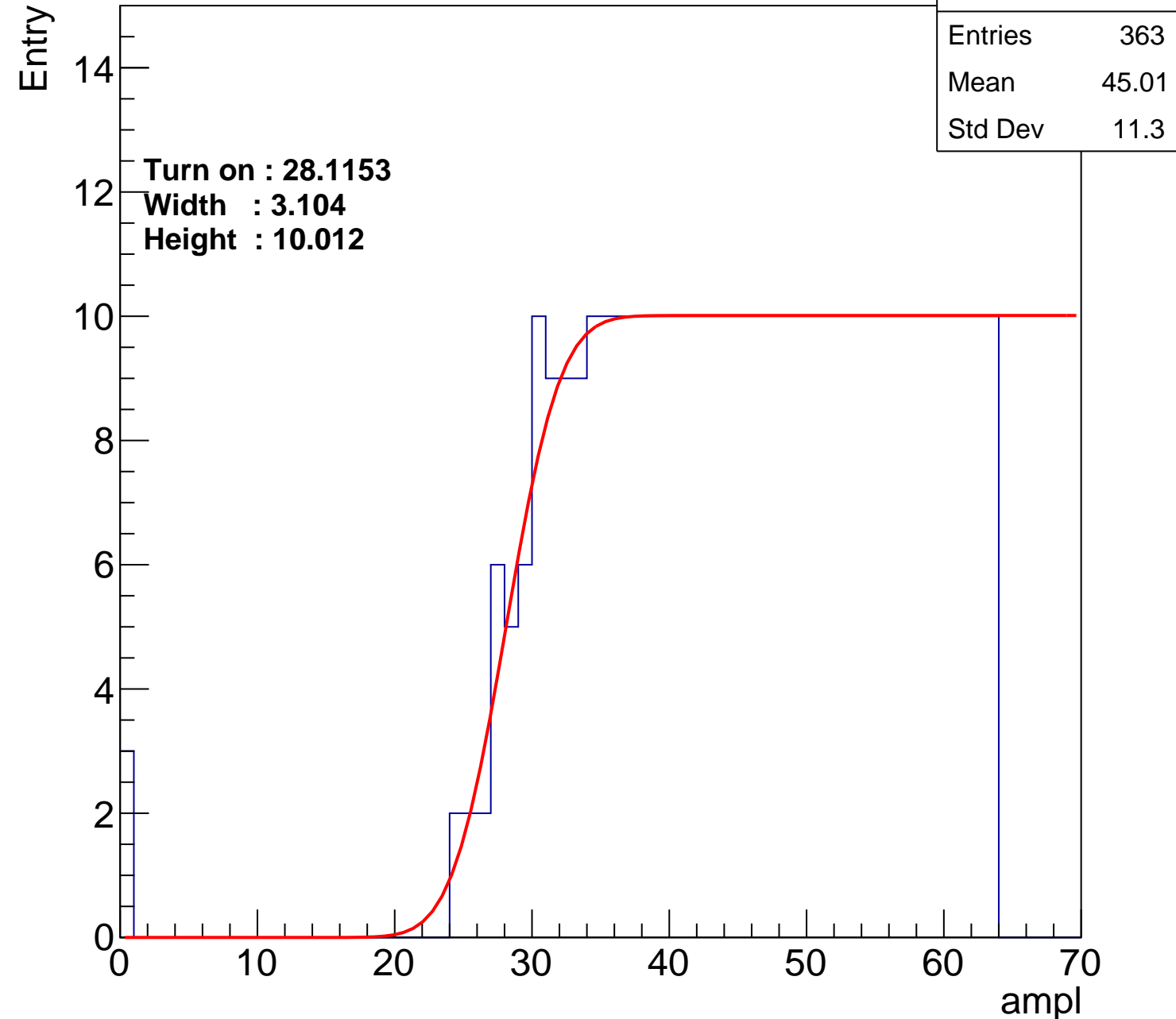
Width : 3.104

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.55
Std Dev	11.98

Turn on : 24.9328

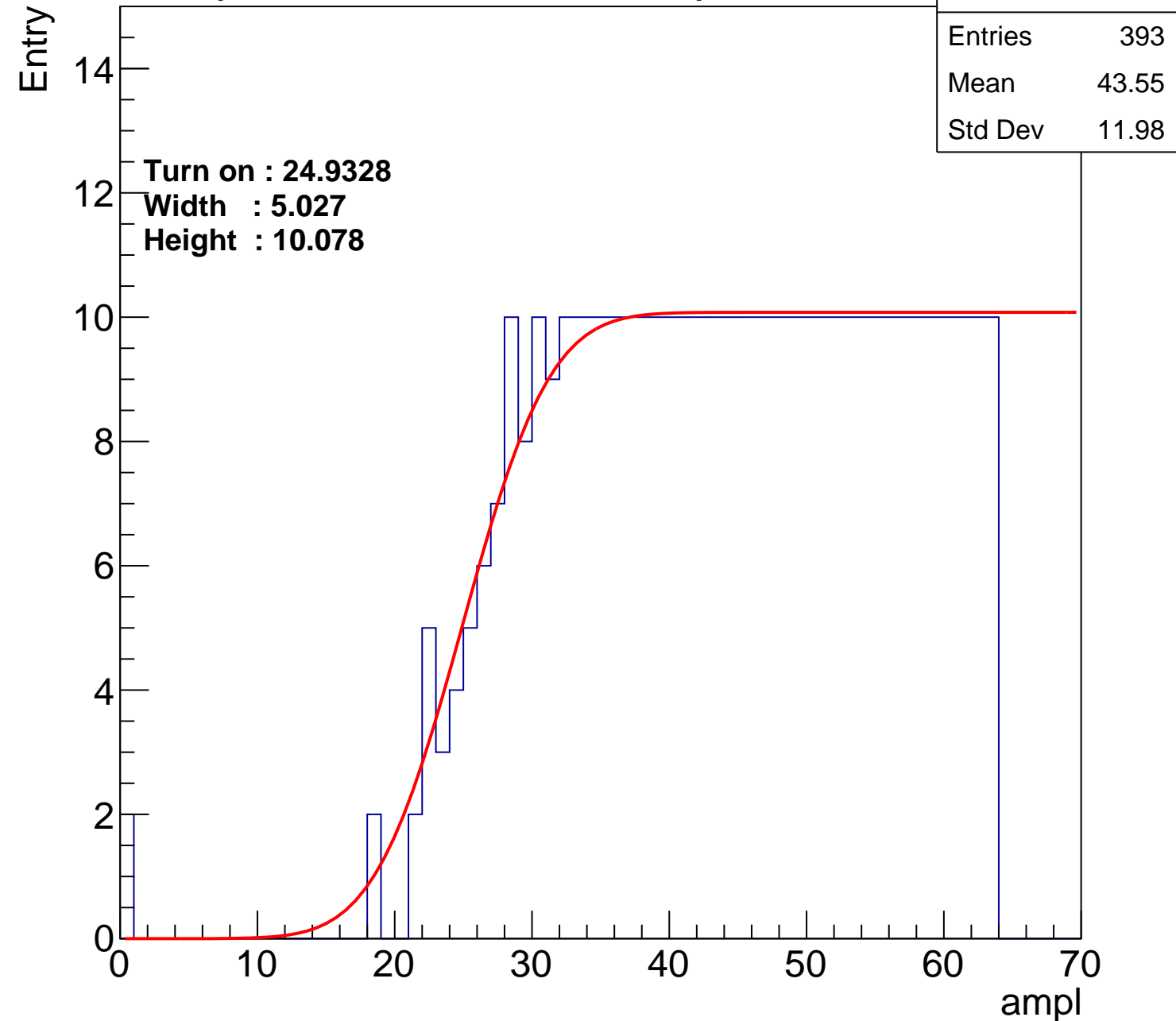
Width : 5.027

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.14
Std Dev	12.28

Turn on : 24.3565

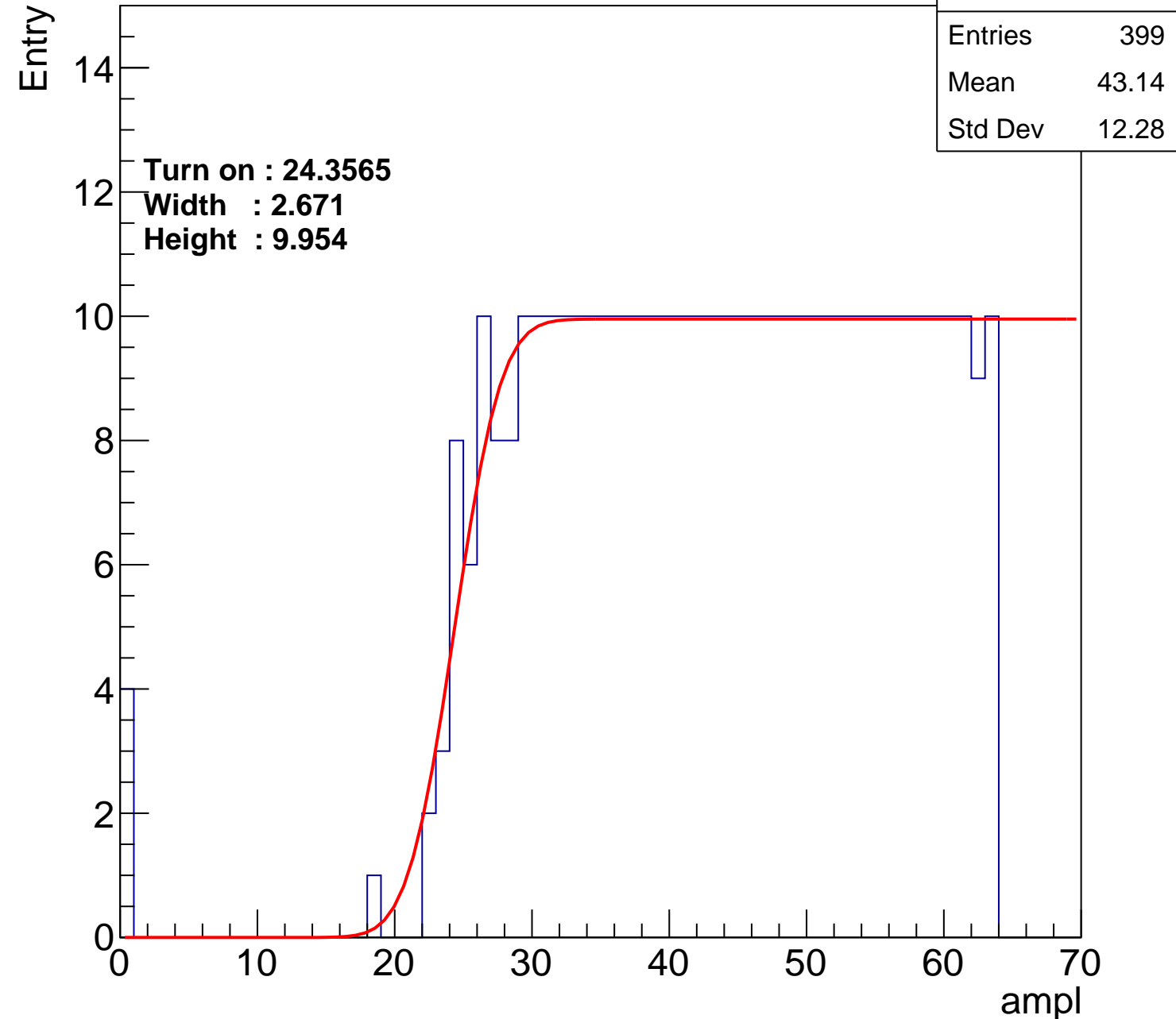
Width : 2.671

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch70

calib_packv5_042523_0143.root, FC#7, port C2

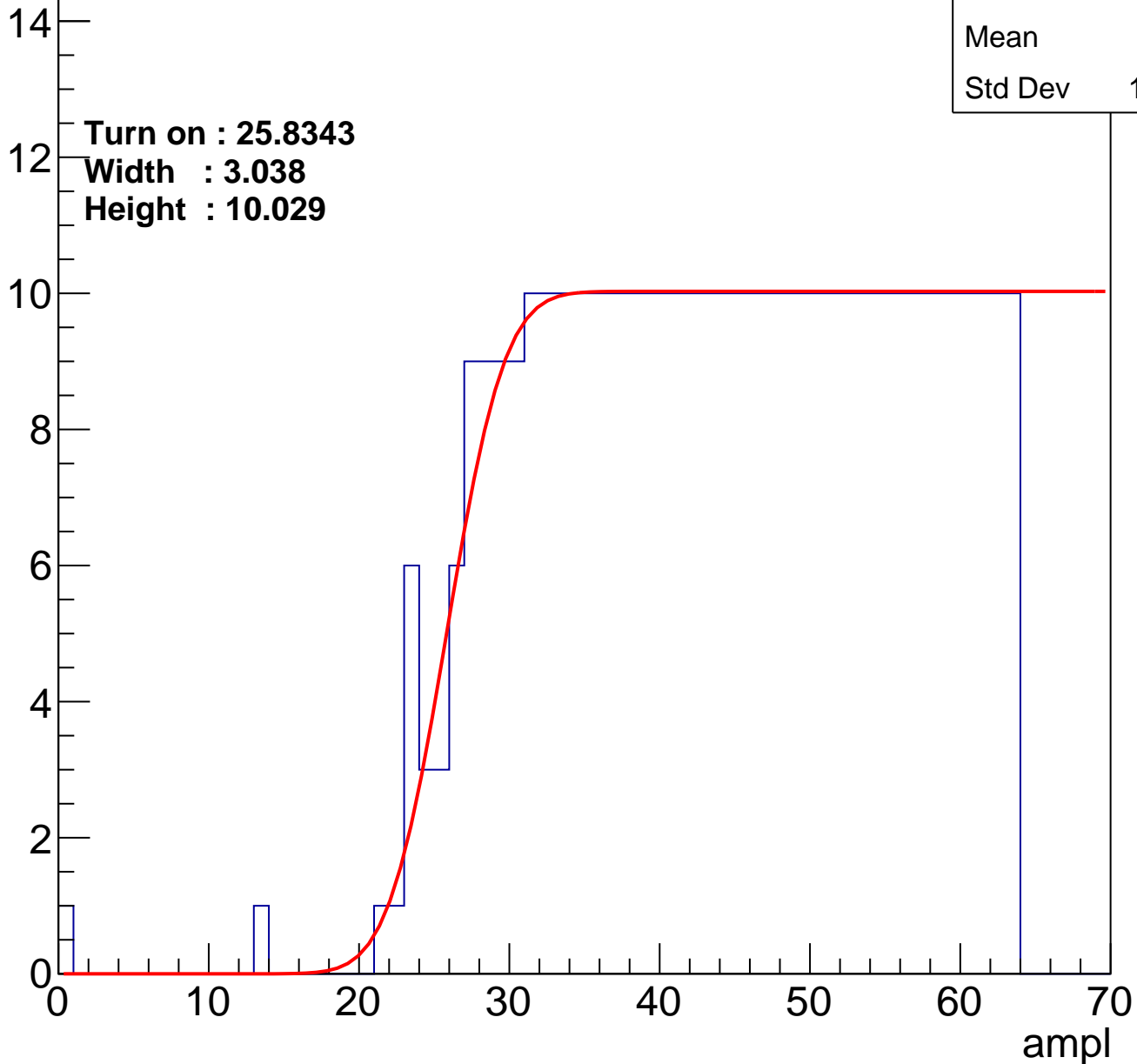
Entries	388
Mean	43.9
Std Dev	11.62

Turn on : 25.8343

Width : 3.038

Height : 10.029

Entry



B1L103S, U23-ch71

calib_packv5_042523_0143.root, FC#7, port C2

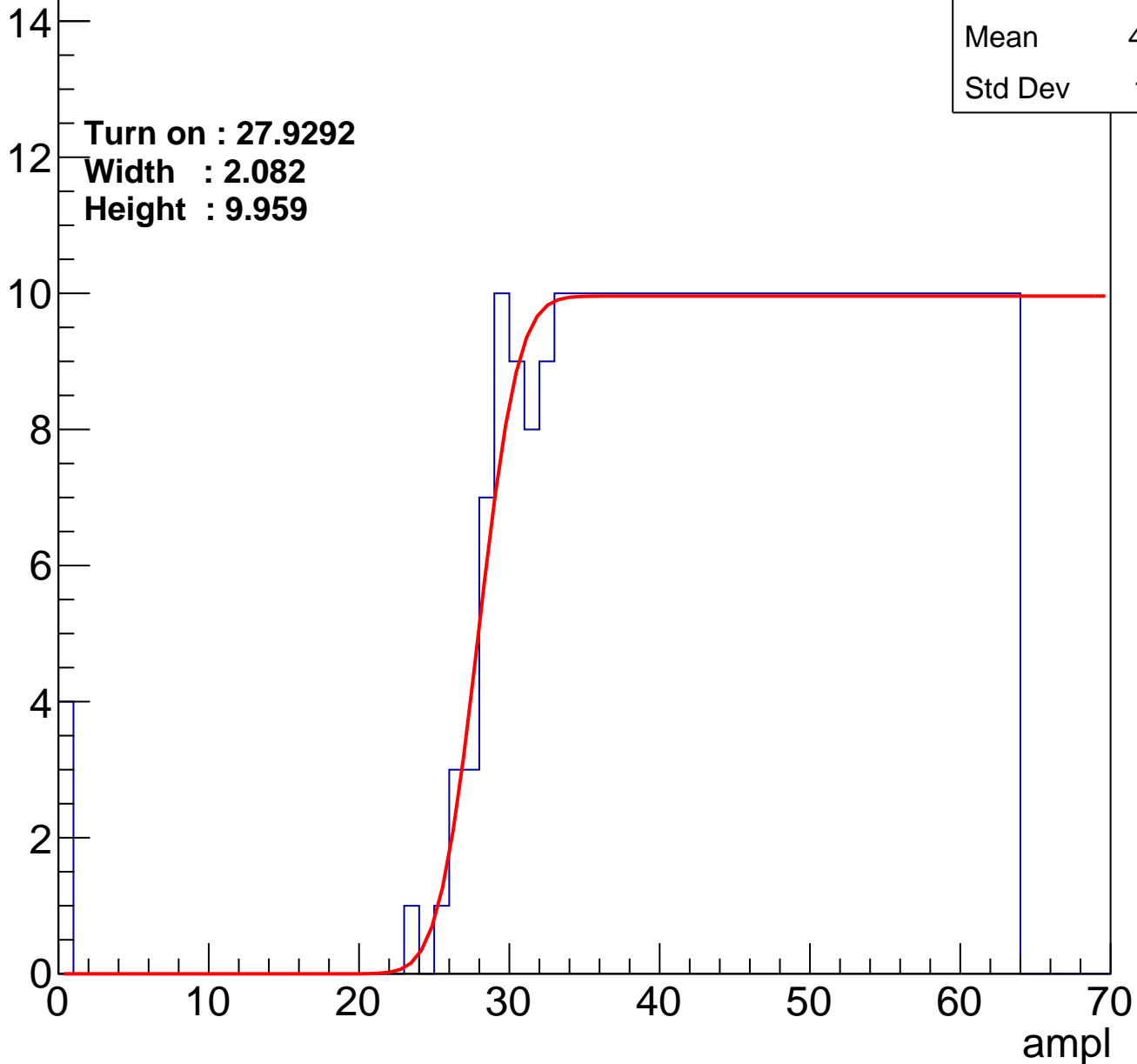
Entry

Entries	365
Mean	44.87
Std Dev	11.51

Turn on : 27.9292

Width : 2.082

Height : 9.959



B1L103S, U23-ch72

calib_packv5_042523_0143.root, FC#7, port C2

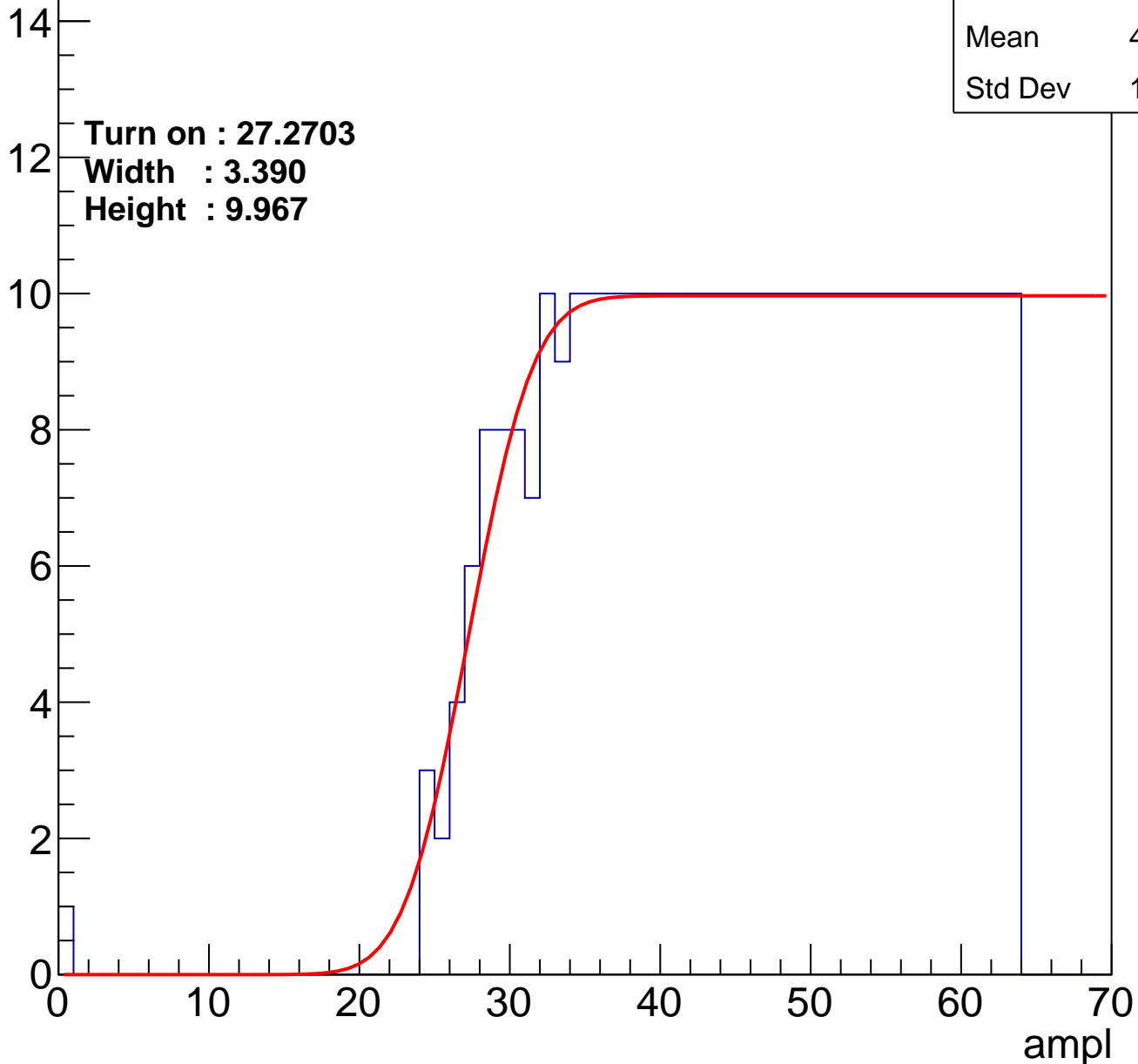
Entries	366
Mean	44.99
Std Dev	10.99

Turn on : 27.2703

Width : 3.390

Height : 9.967

Entry



B1L103S, U23-ch73

calib_packv5_042523_0143.root, FC#7, port C2

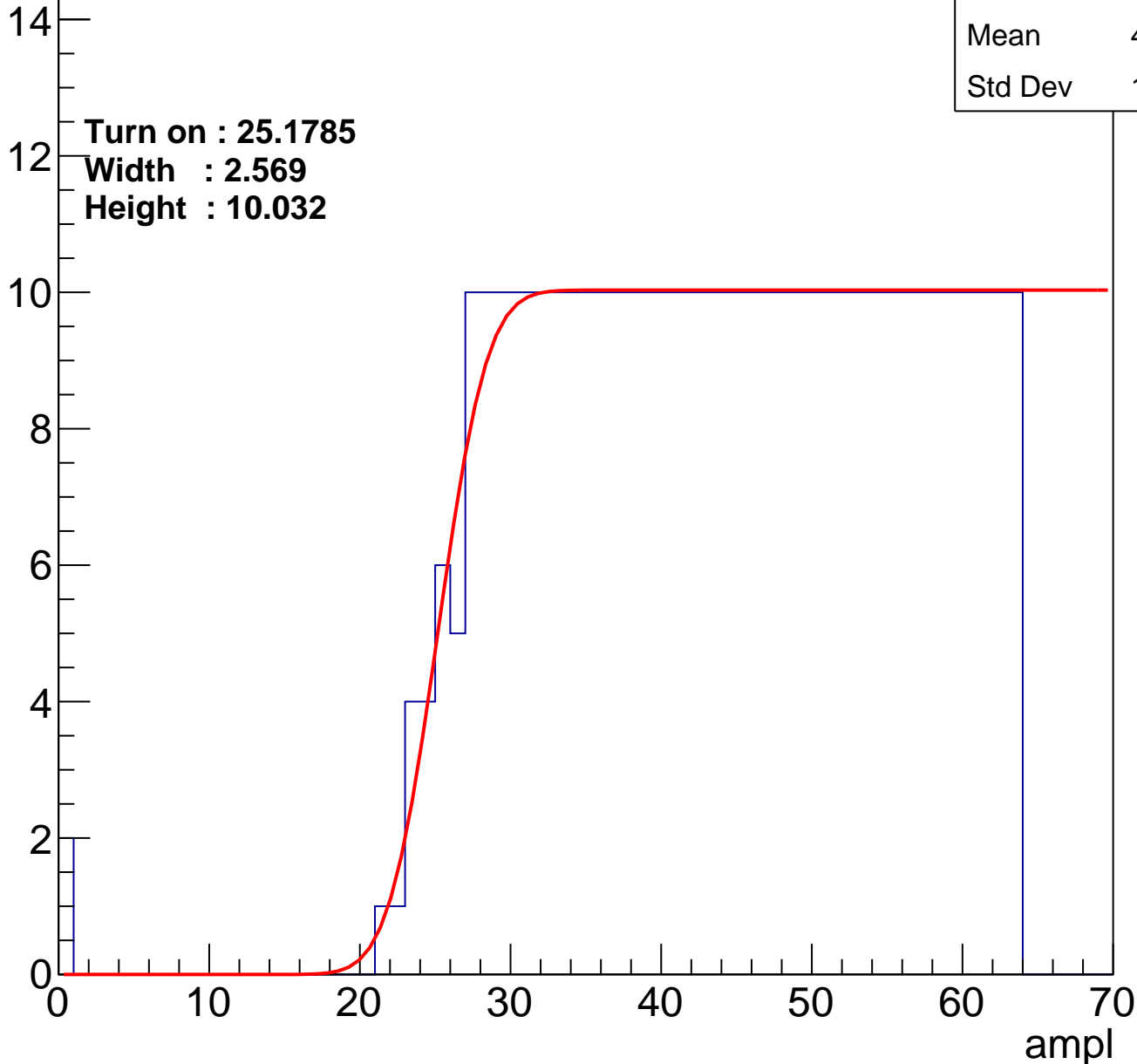
Entries	393
Mean	43.67
Std Dev	11.78

Turn on : 25.1785

Width : 2.569

Height : 10.032

Entry



B1L103S, U23-ch74

calib_packv5_042523_0143.root, FC#7, port C2

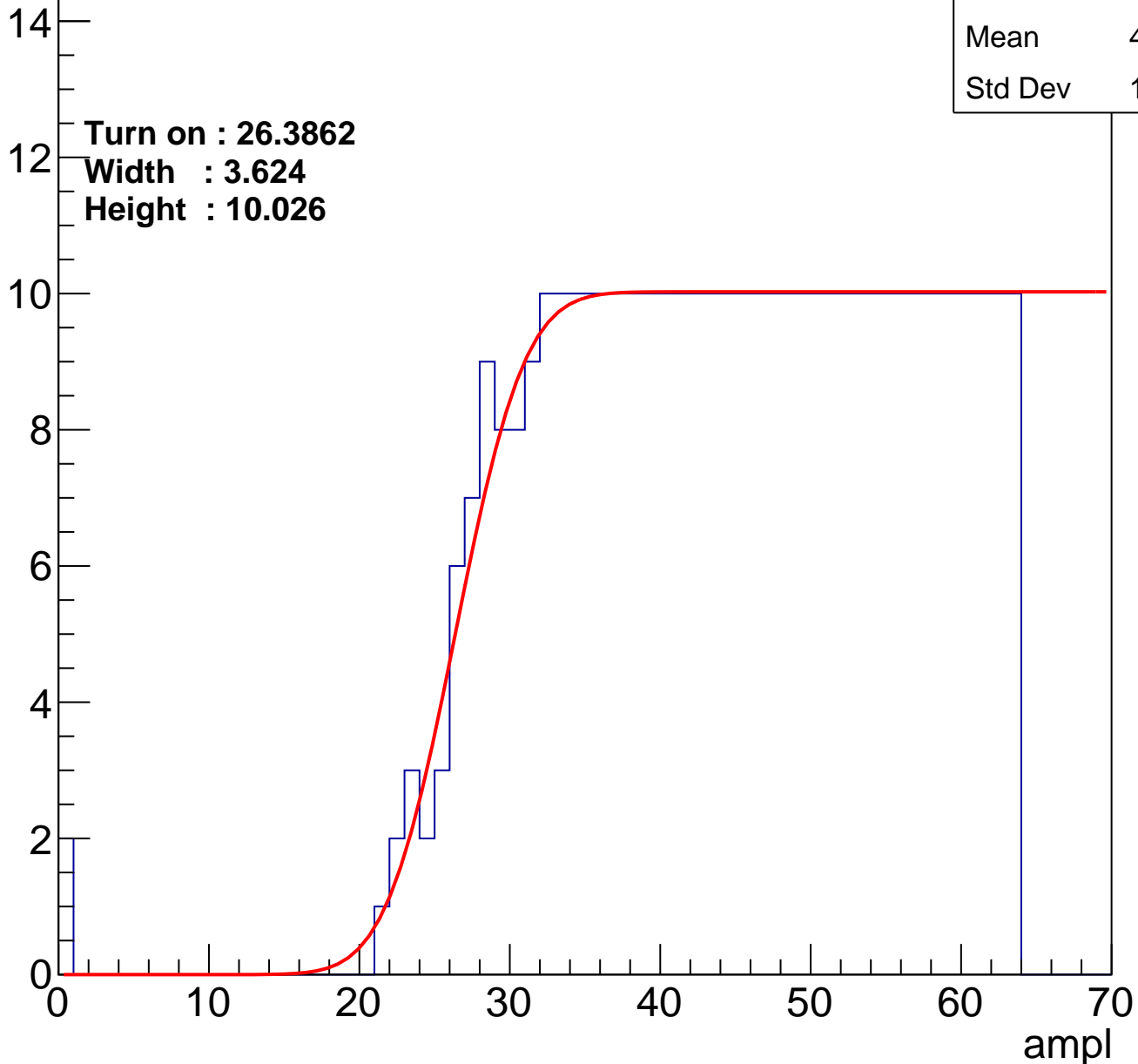
Entries	380
Mean	44.22
Std Dev	11.58

Turn on : 26.3862

Width : 3.624

Height : 10.026

Entry



B1L103S, U23-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	43.91
Std Dev	12.17

Turn on : 26.4919

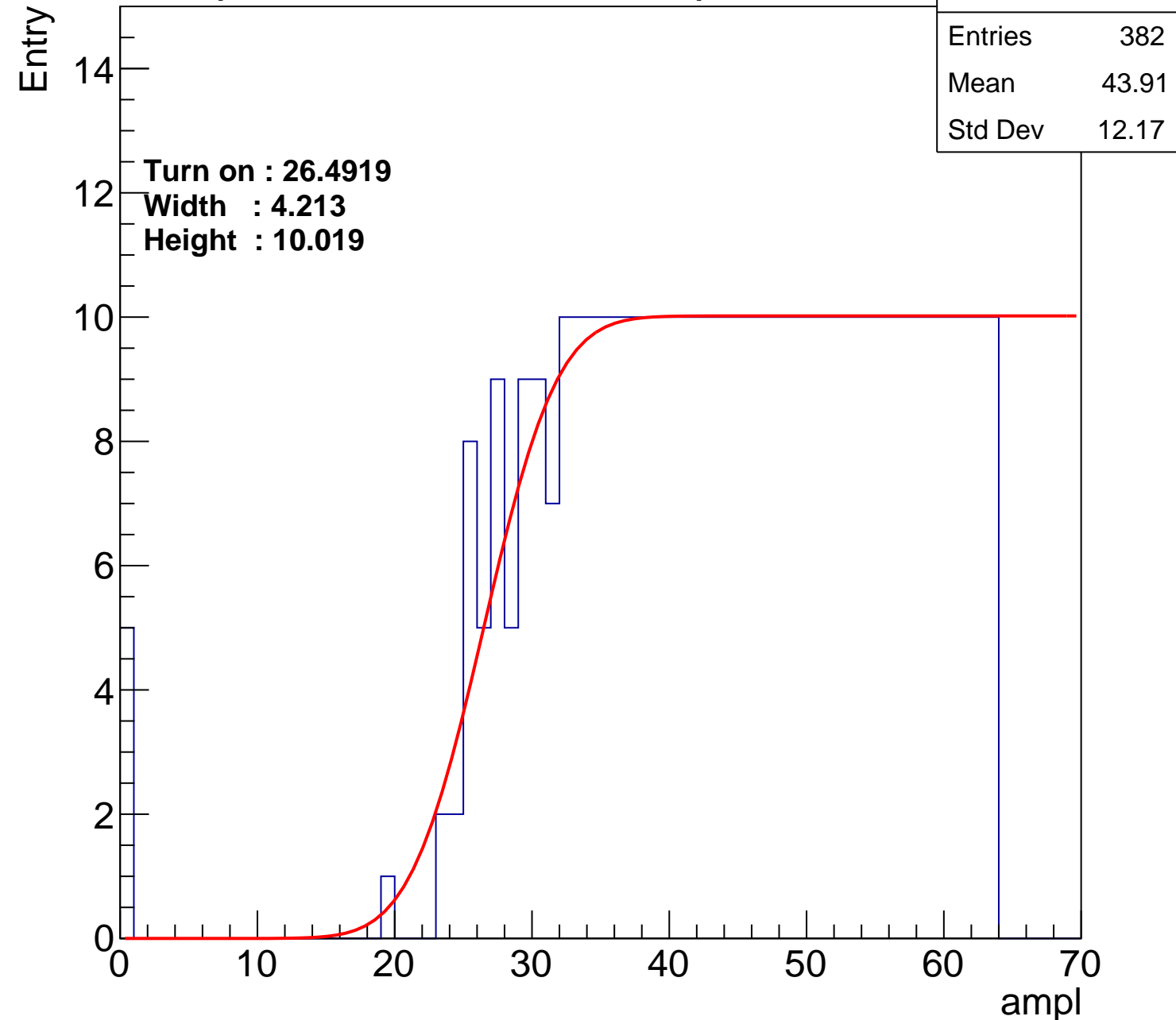
Width : 4.213

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch76

calib_packv5_042523_0143.root, FC#7, port C2

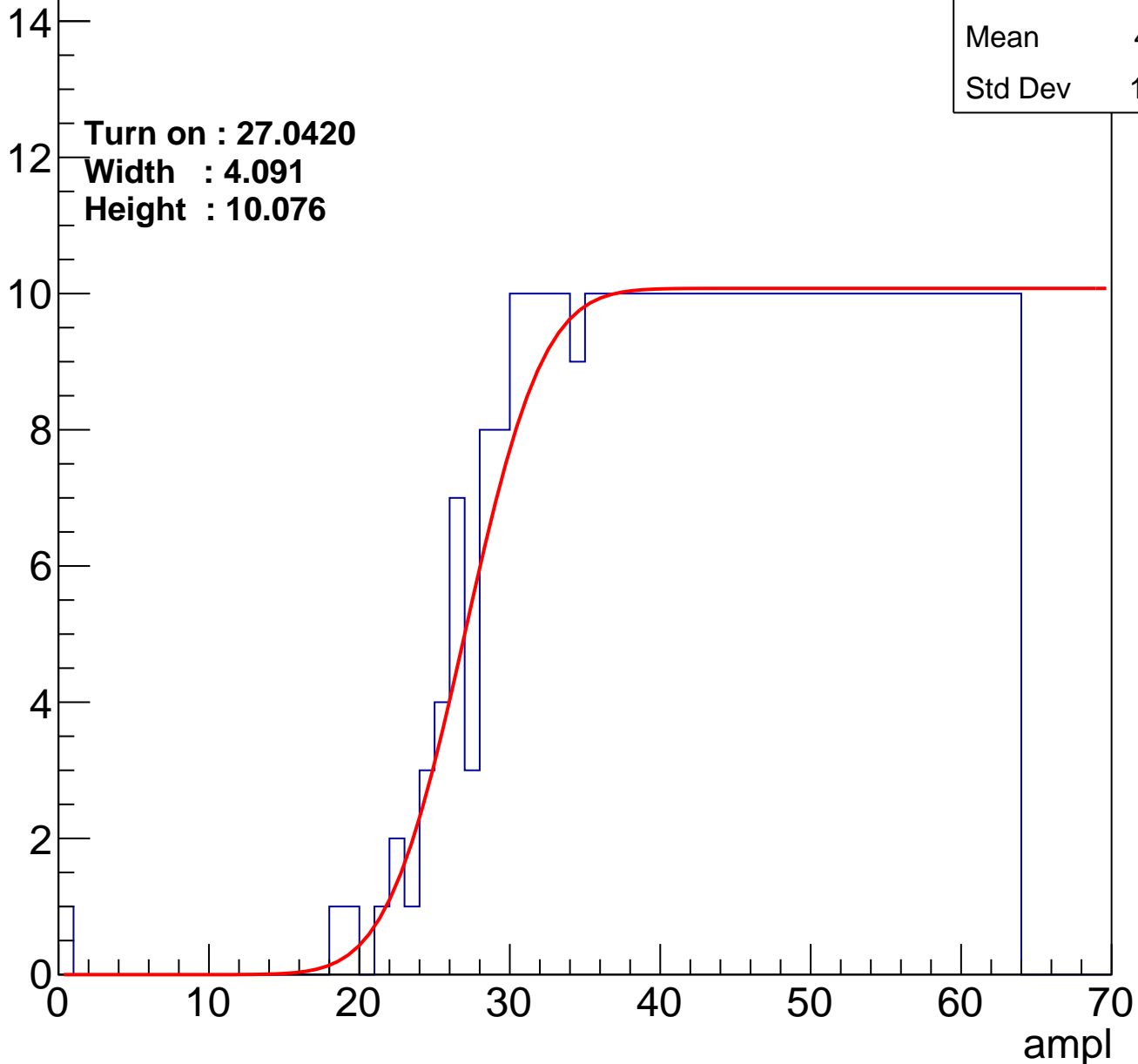
Entries	379
Mean	44.31
Std Dev	11.43

Turn on : 27.0420

Width : 4.091

Height : 10.076

Entry



B1L103S, U23-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.68
Std Dev	11.31

Turn on : 27.0597

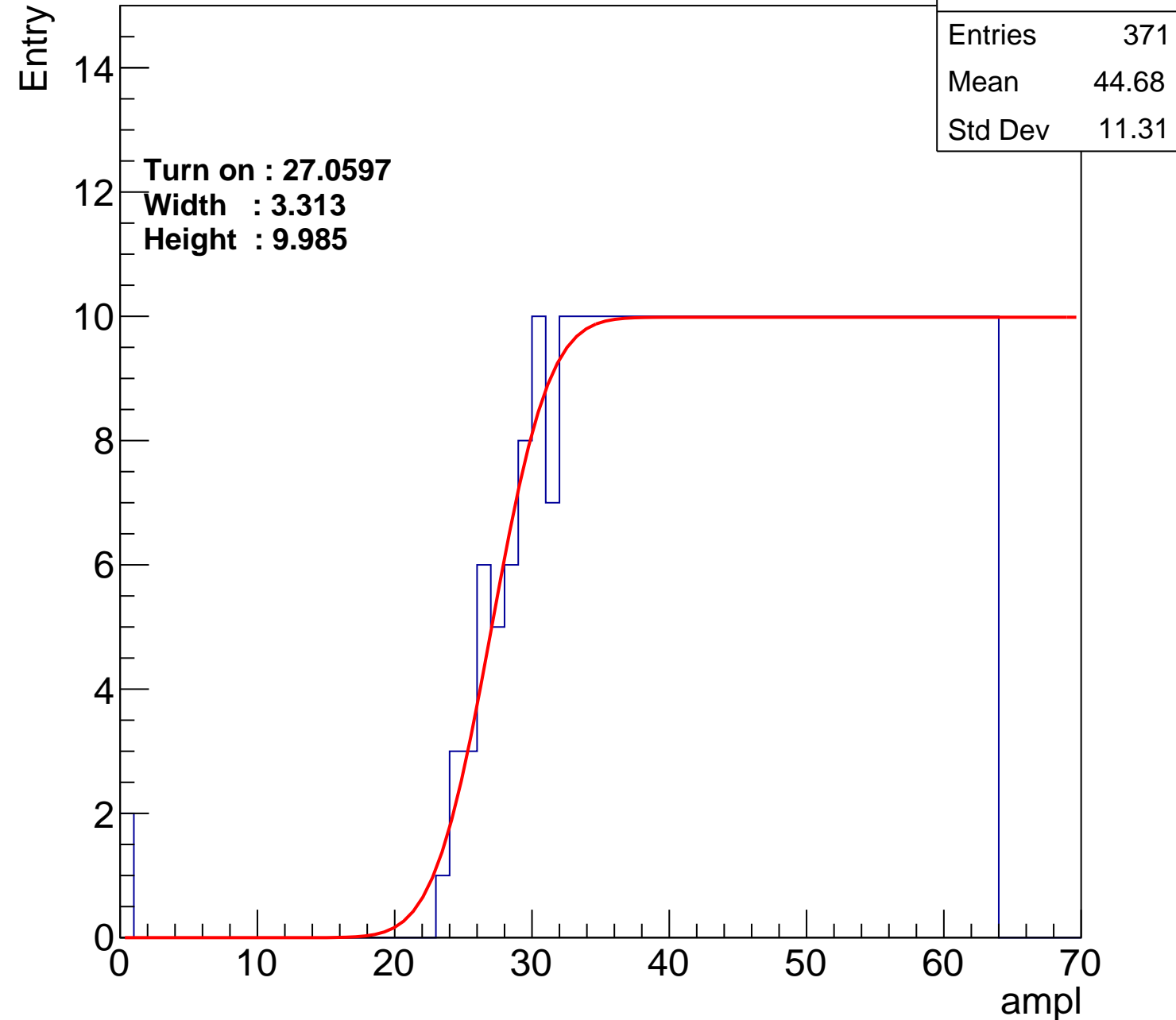
Width : 3.313

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.54
Std Dev	12.22

Turn on : 25.3340

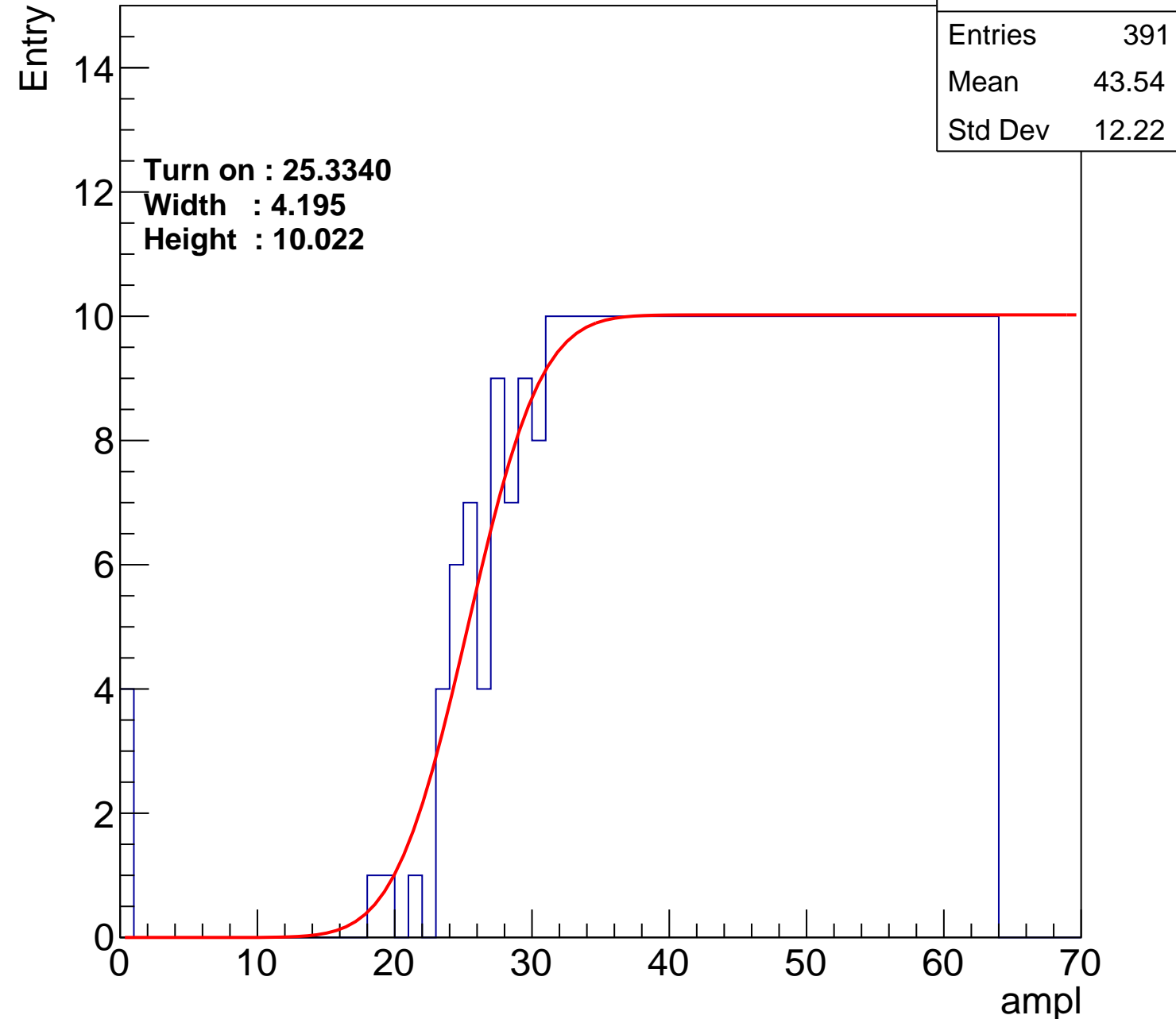
Width : 4.195

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.77
Std Dev	11.56

Turn on : 28.5511

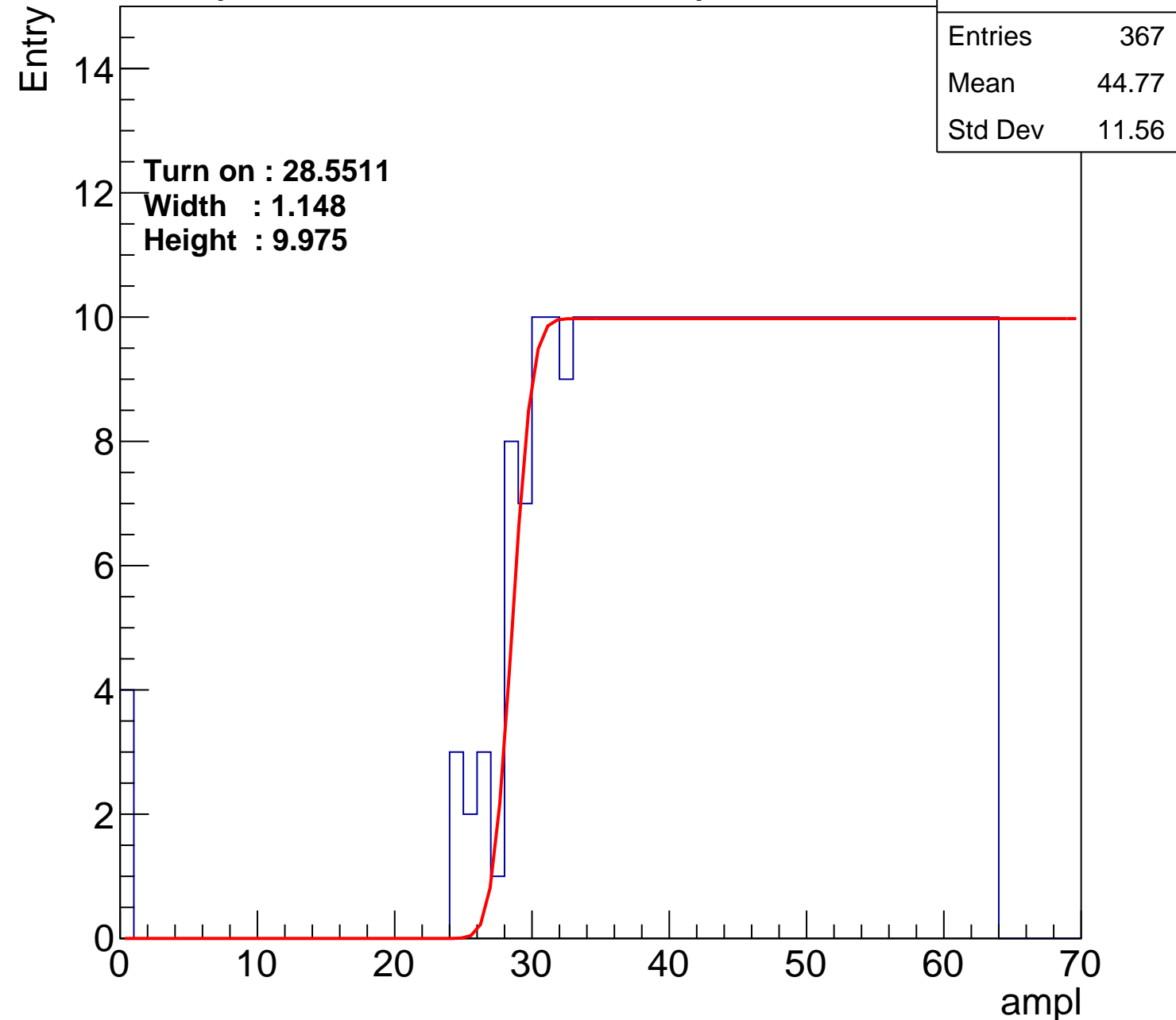
Width : 1.148

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.33
Std Dev	12.17

Turn on : 24.7838

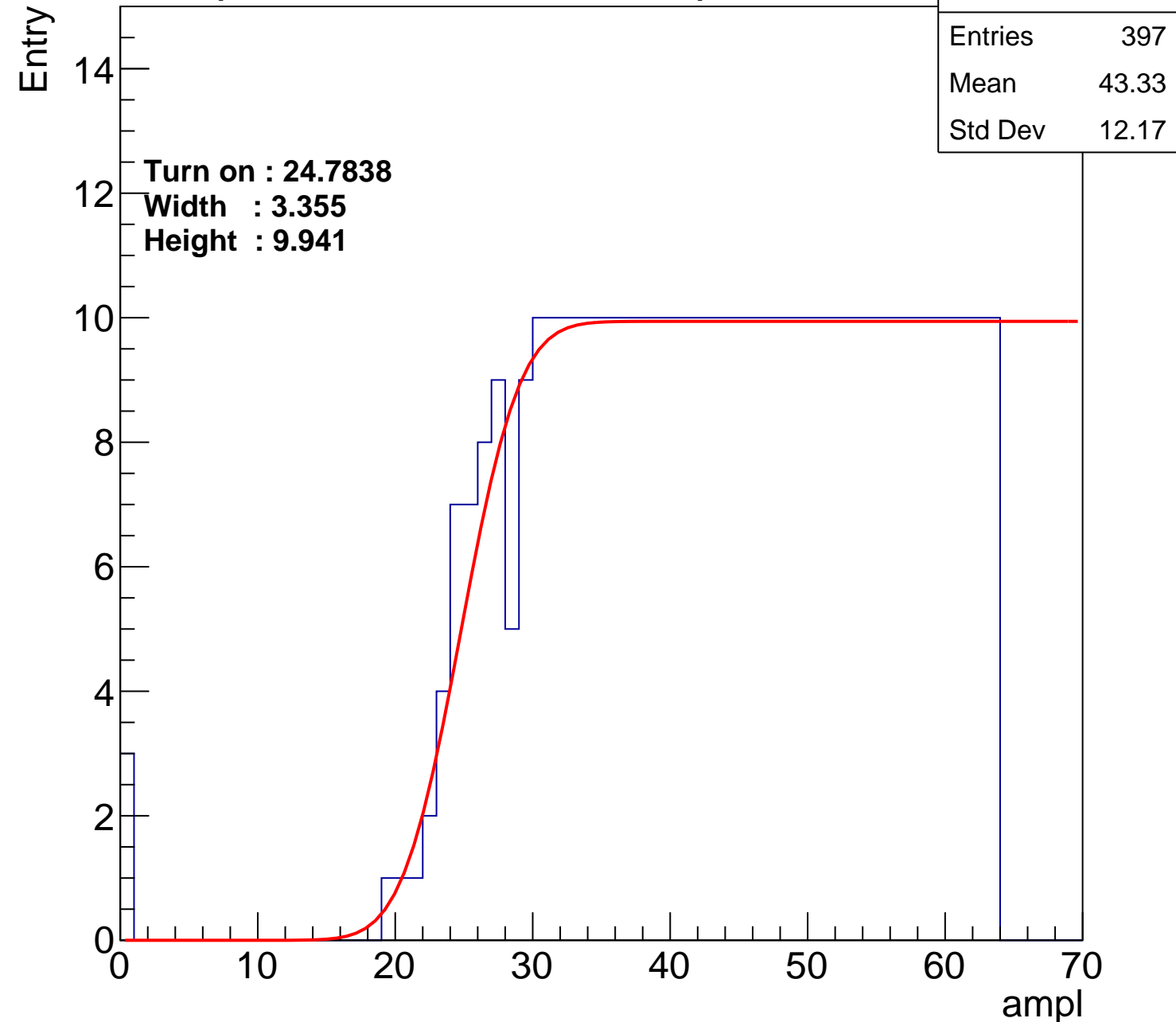
Width : 3.355

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 27.6964

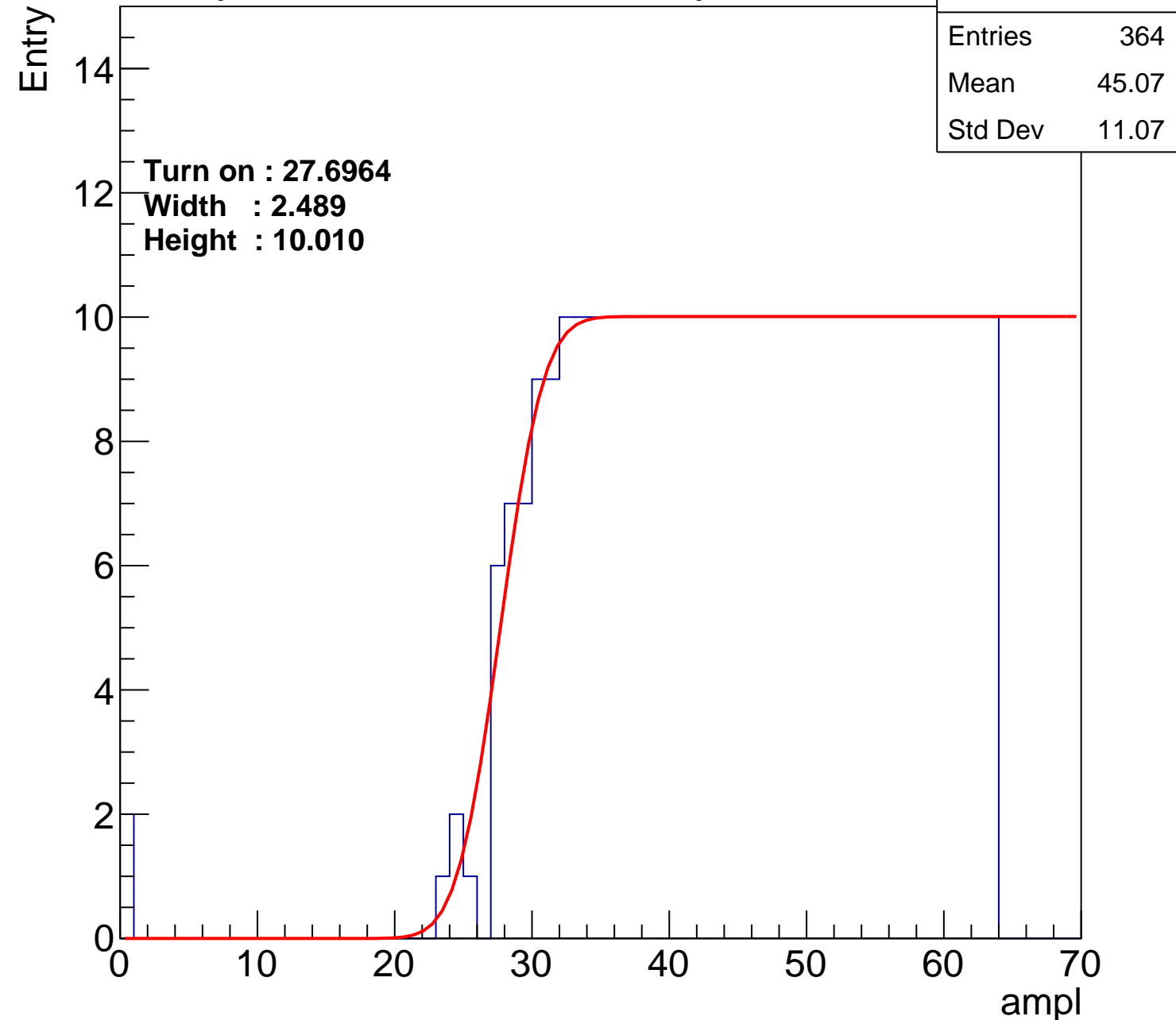
Width : 2.489

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	423
Mean	42.12
Std Dev	12.52

Turn on : 21.3766

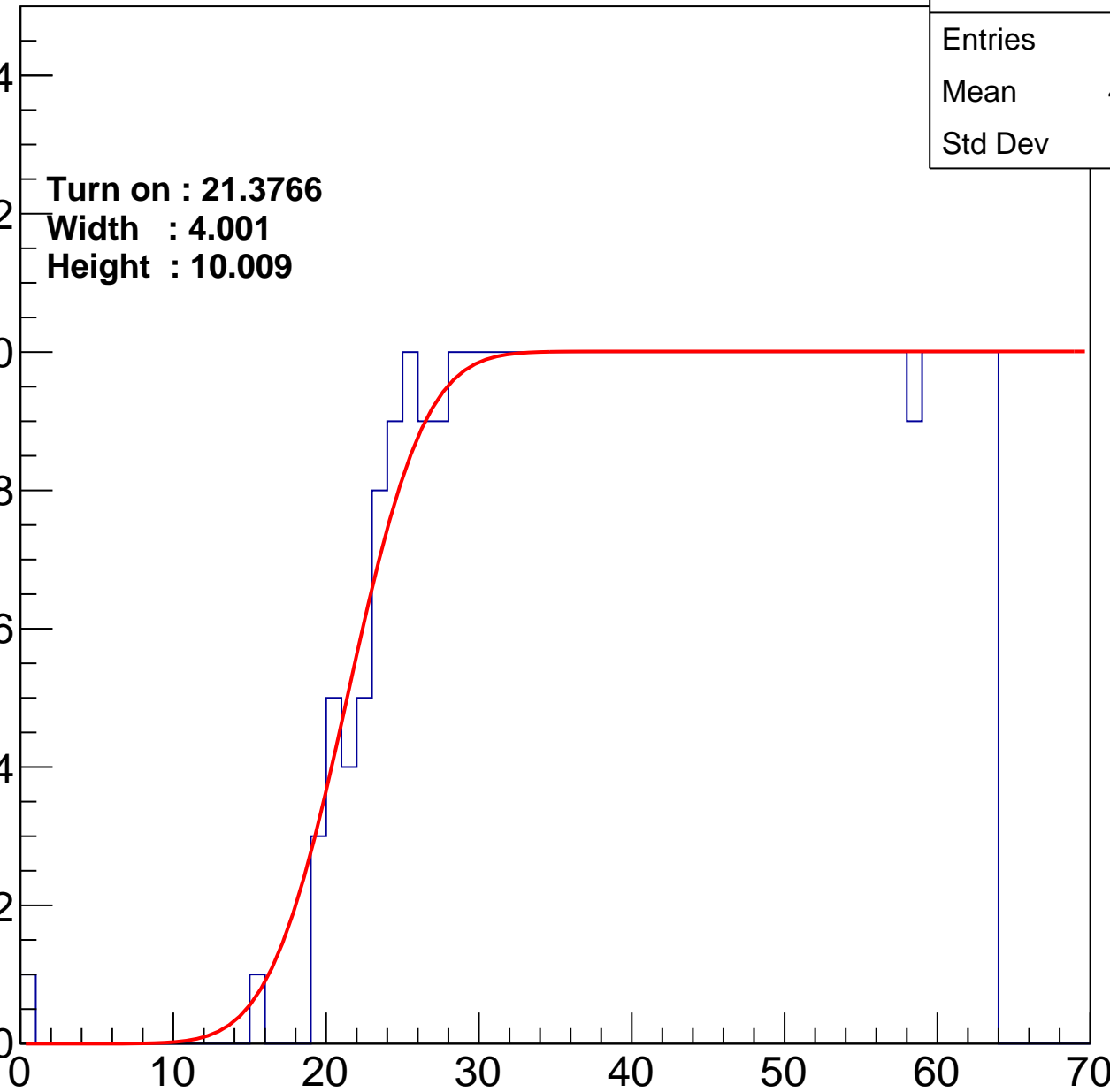
Width : 4.001

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.17
Std Dev	11.87

Turn on : 26.5638

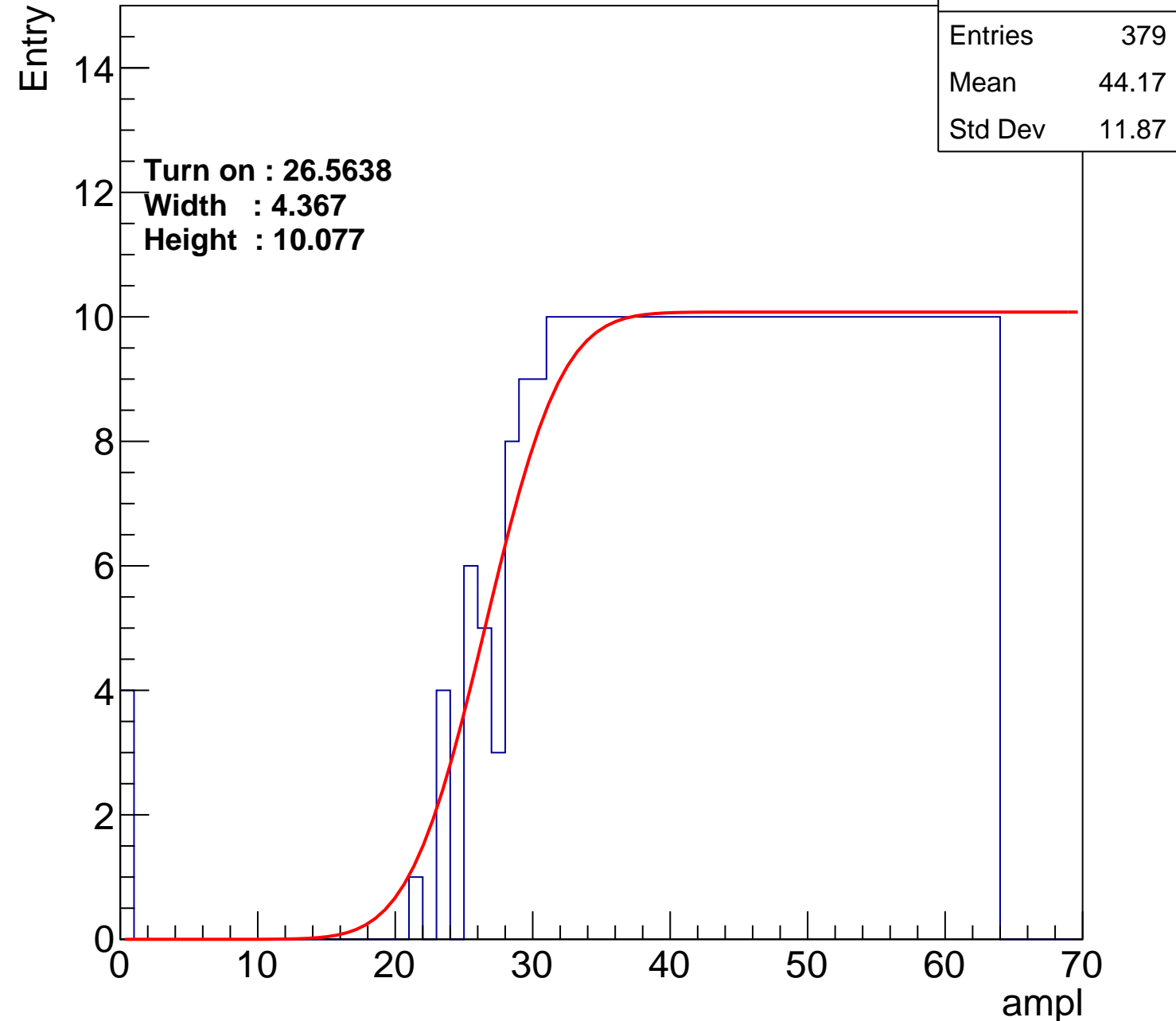
Width : 4.367

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	405
Mean	43
Std Dev	12.27

Turn on : 22.9078

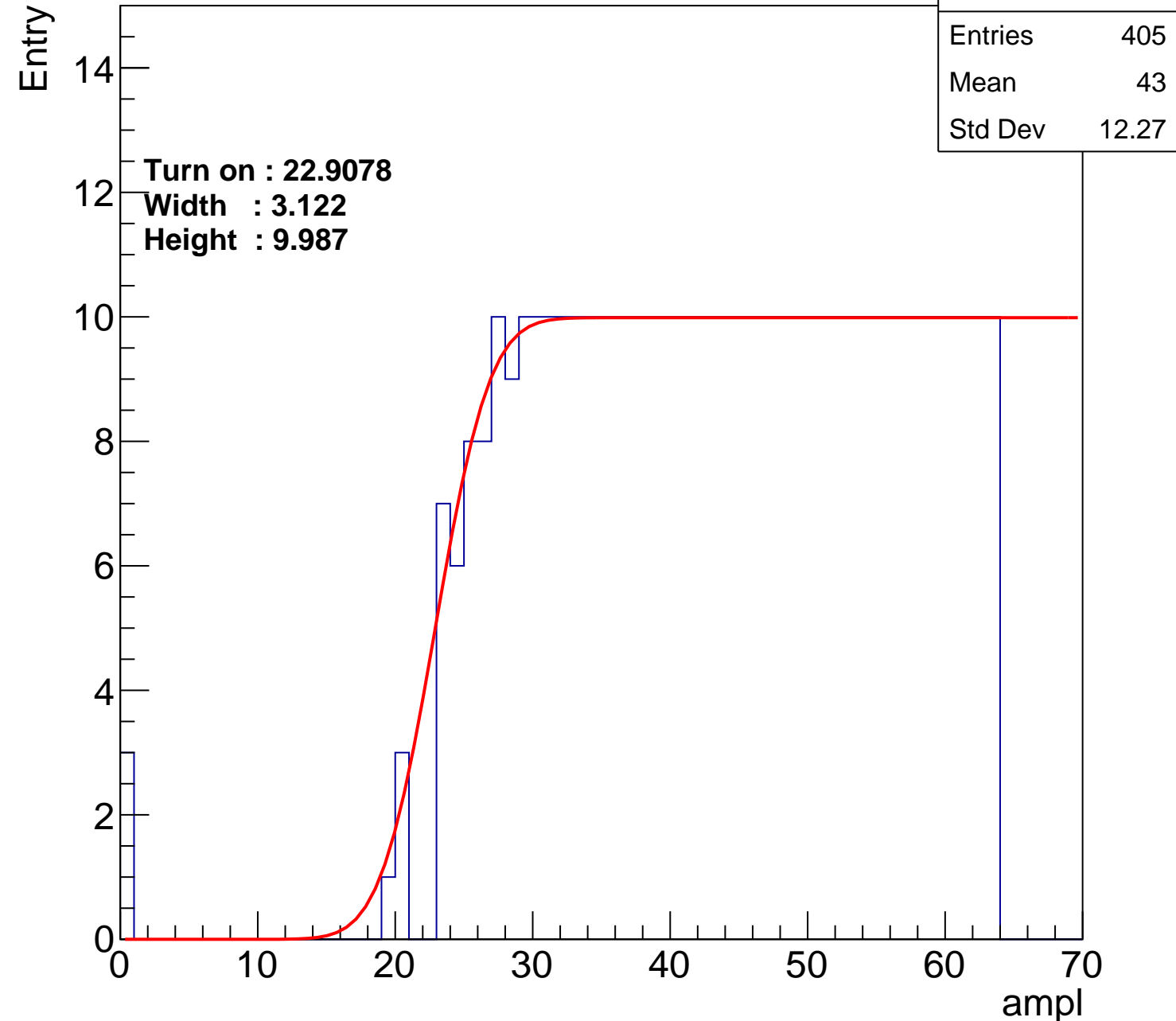
Width : 3.122

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch85

calib_packv5_042523_0143.root, FC#7, port C2

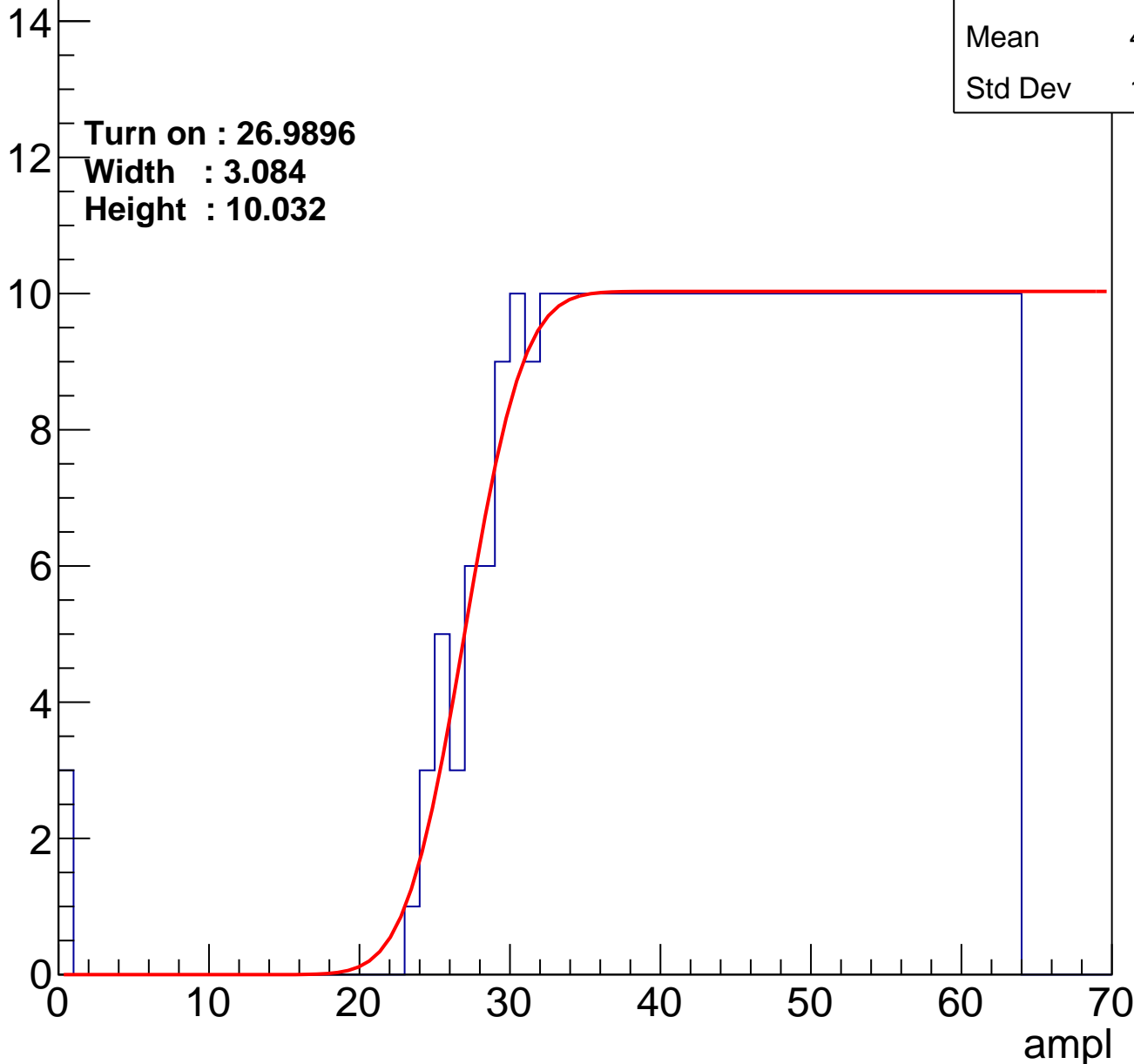
Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 26.9896

Width : 3.084

Height : 10.032

Entry



B1L103S, U23-ch86

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.69
Std Dev	11.74

Turn on : 24.7603

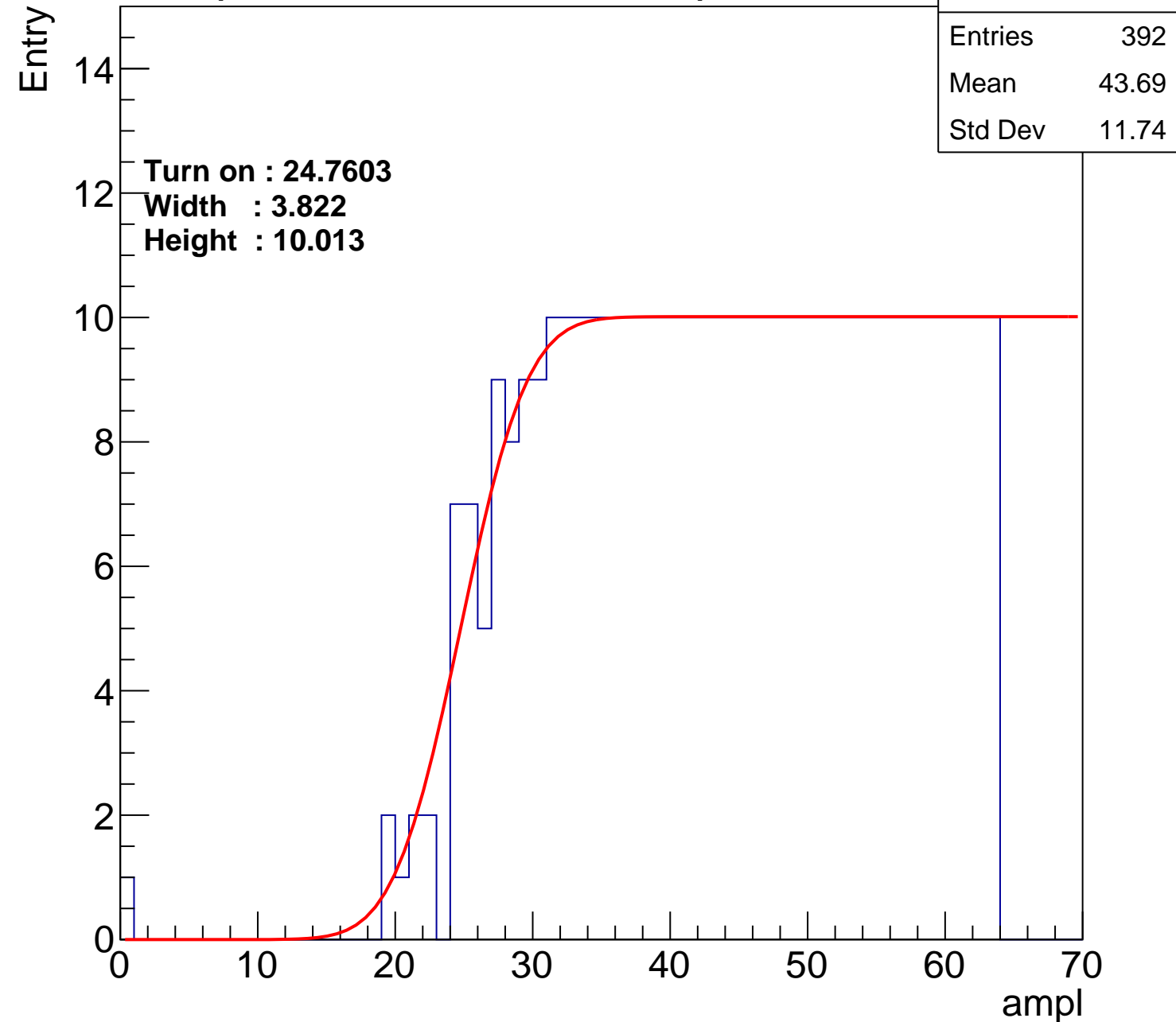
Width : 3.822

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.49
Std Dev	12.34

Turn on : 25.4834

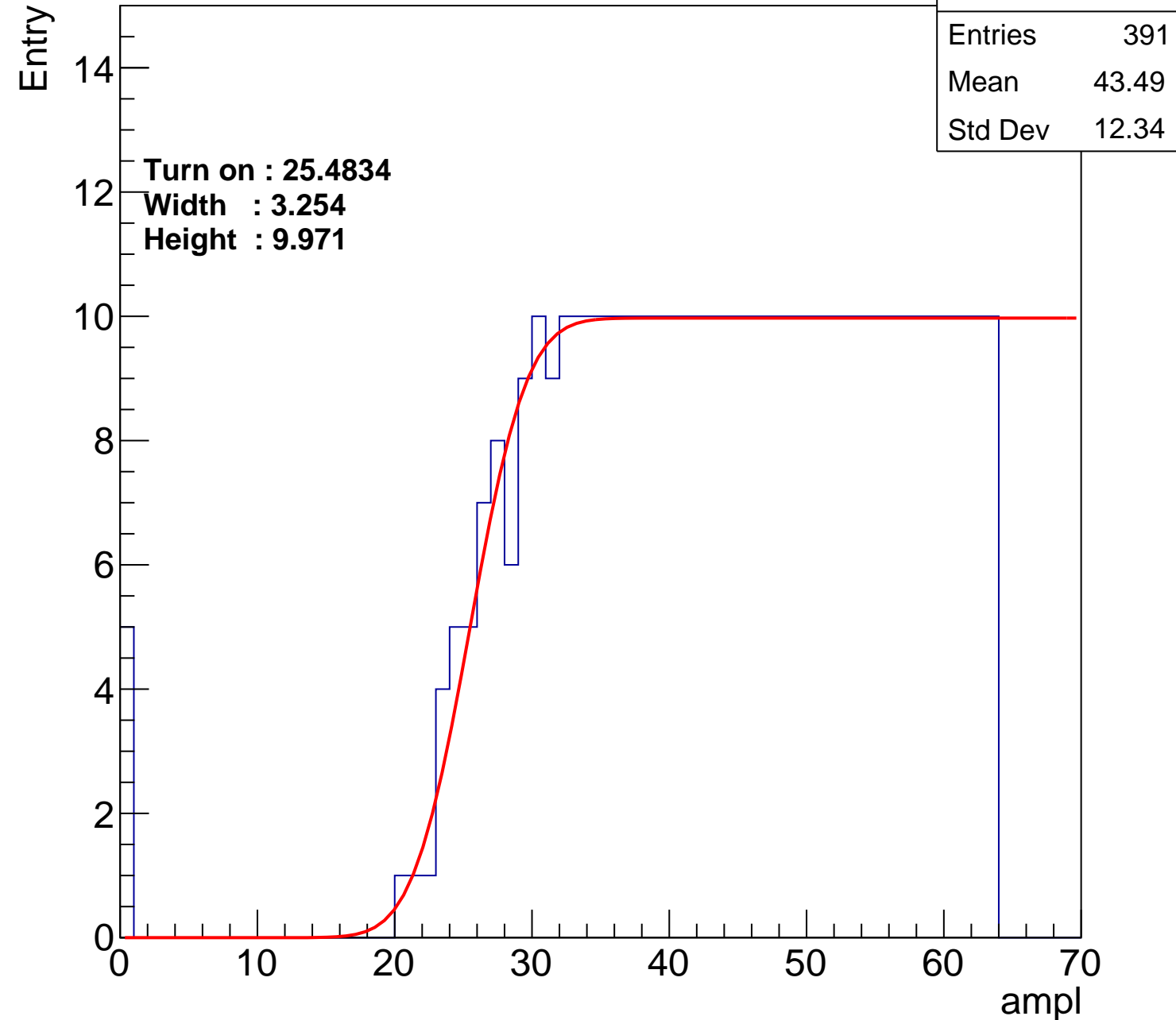
Width : 3.254

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch88

calib_packv5_042523_0143.root, FC#7, port C2

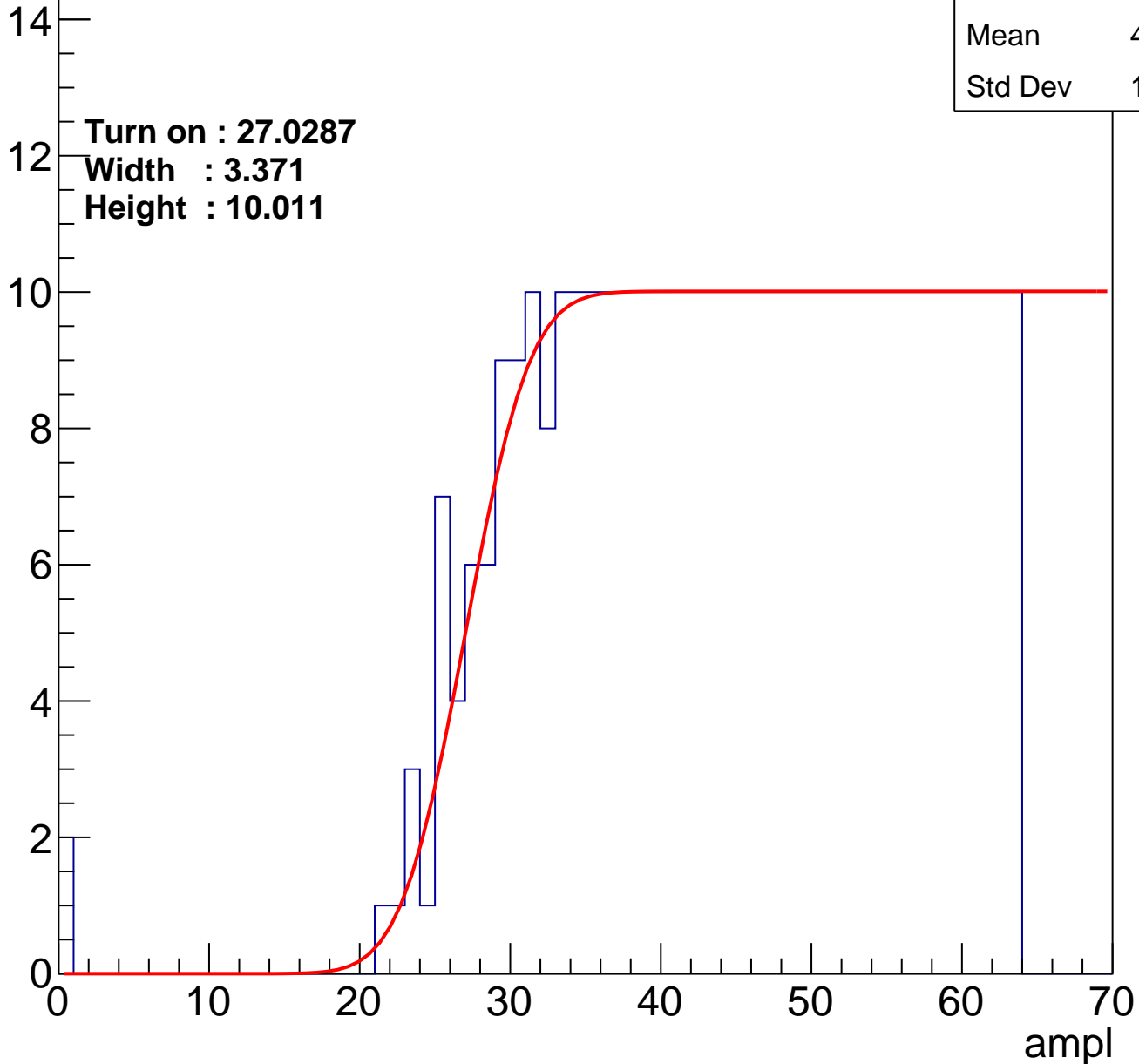
Entries	377
Mean	44.36
Std Dev	11.52

Turn on : 27.0287

Width : 3.371

Height : 10.011

Entry



B1L103S, U23-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.06
Std Dev	11.92

Turn on : 26.5729

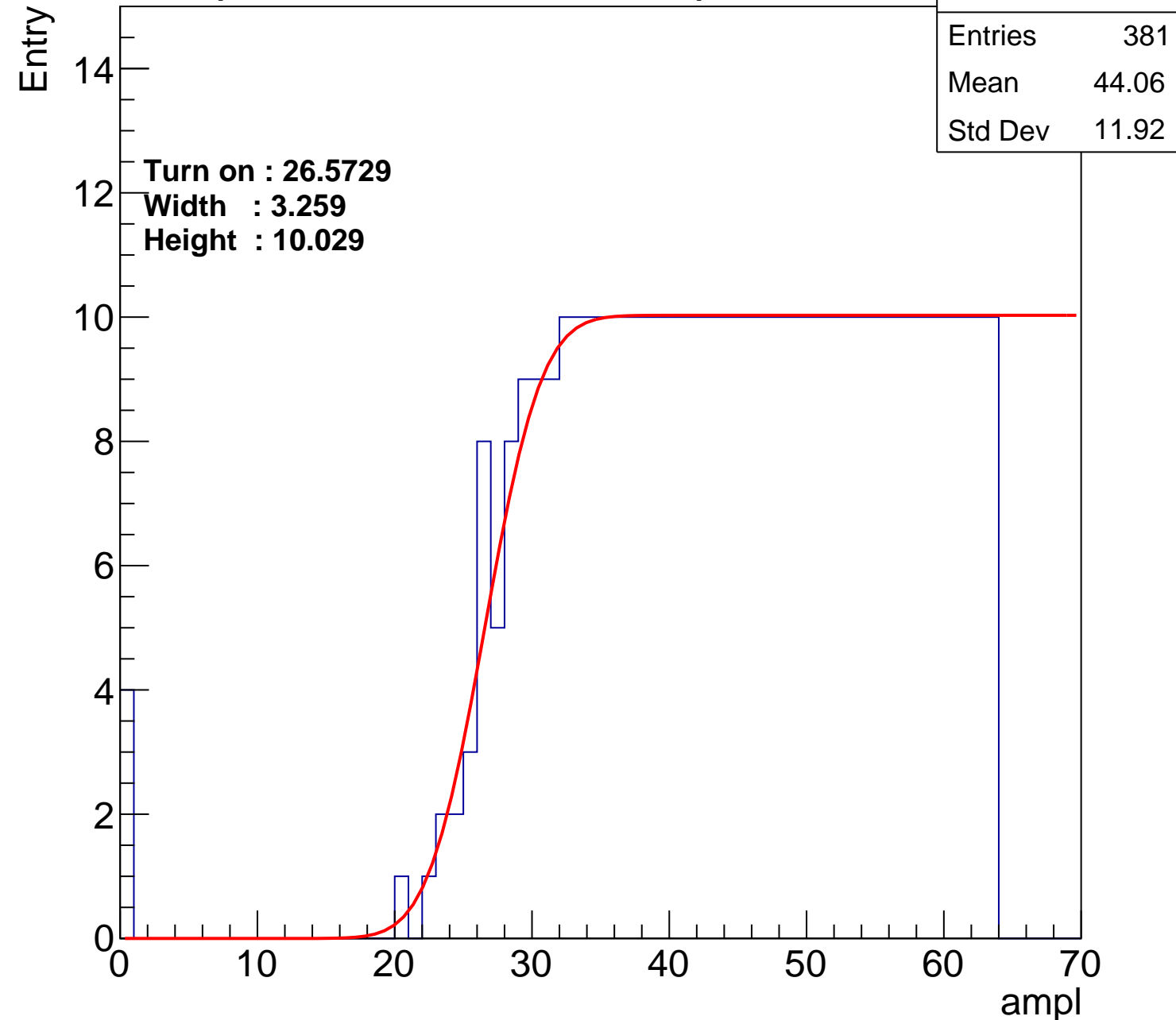
Width : 3.259

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch90

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.65
Std Dev	12.15

Turn on : 26.1720

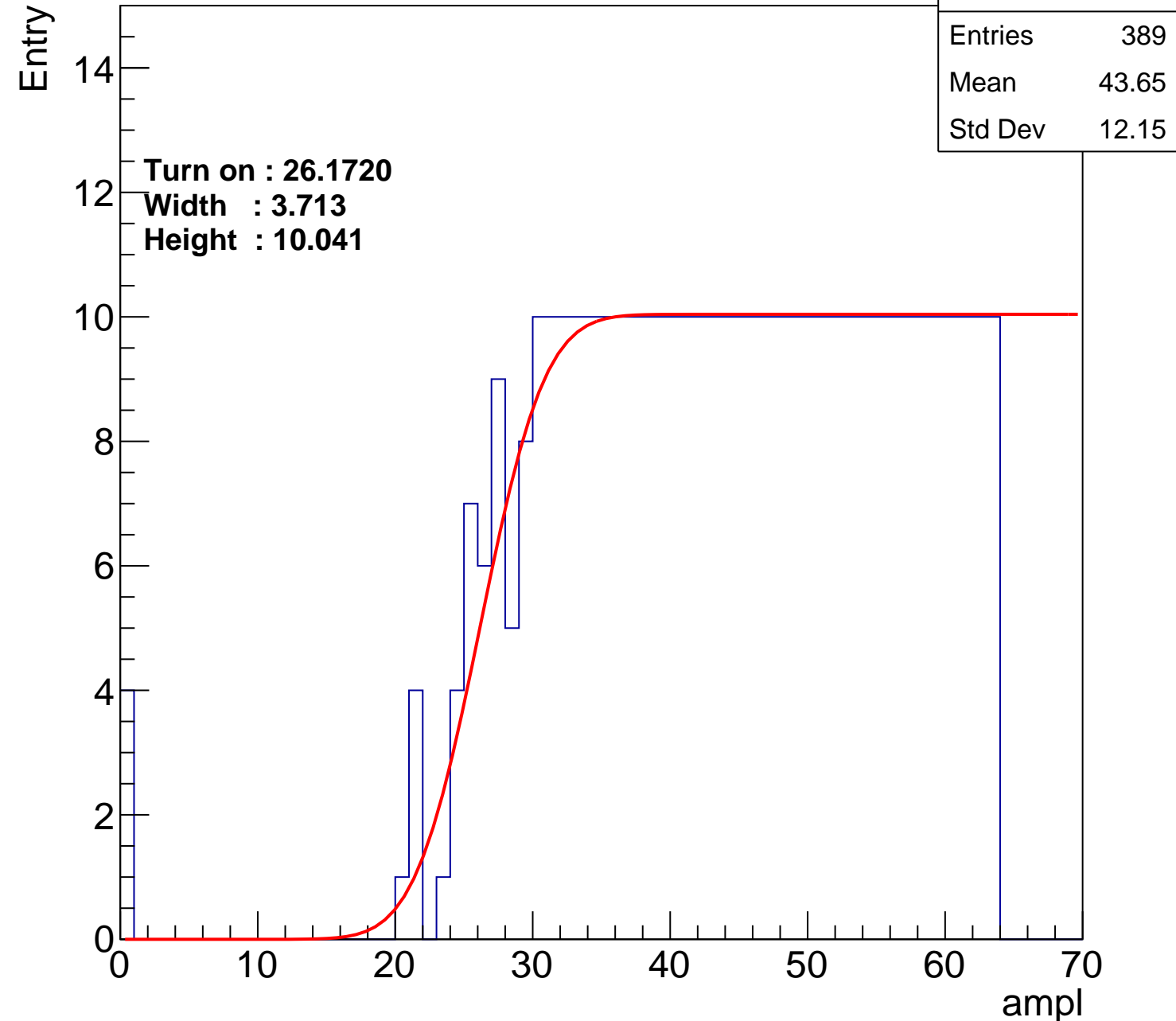
Width : 3.713

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.55
Std Dev	12.26

Turn on : 25.1195

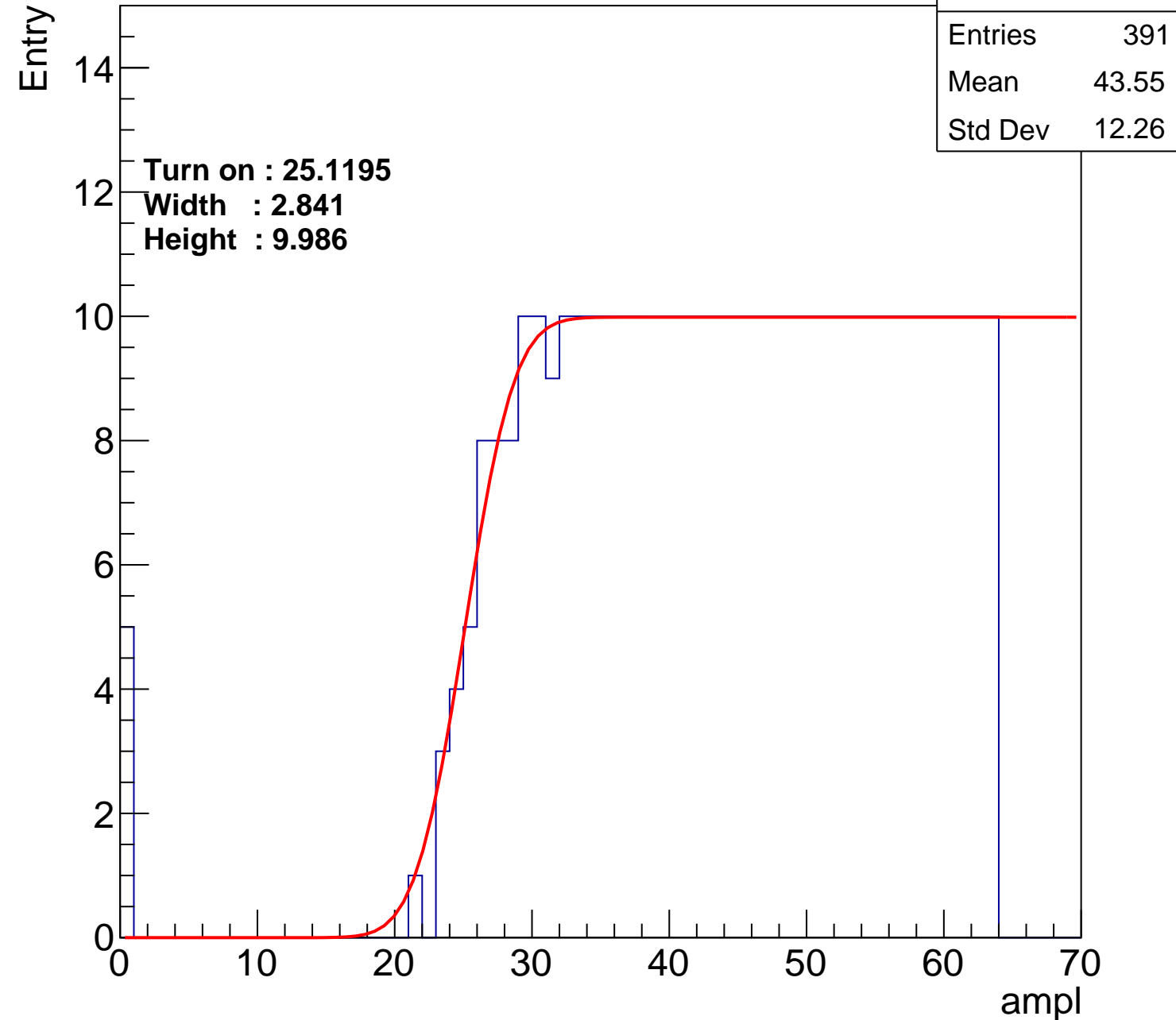
Width : 2.841

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch92

calib_packv5_042523_0143.root, FC#7, port C2

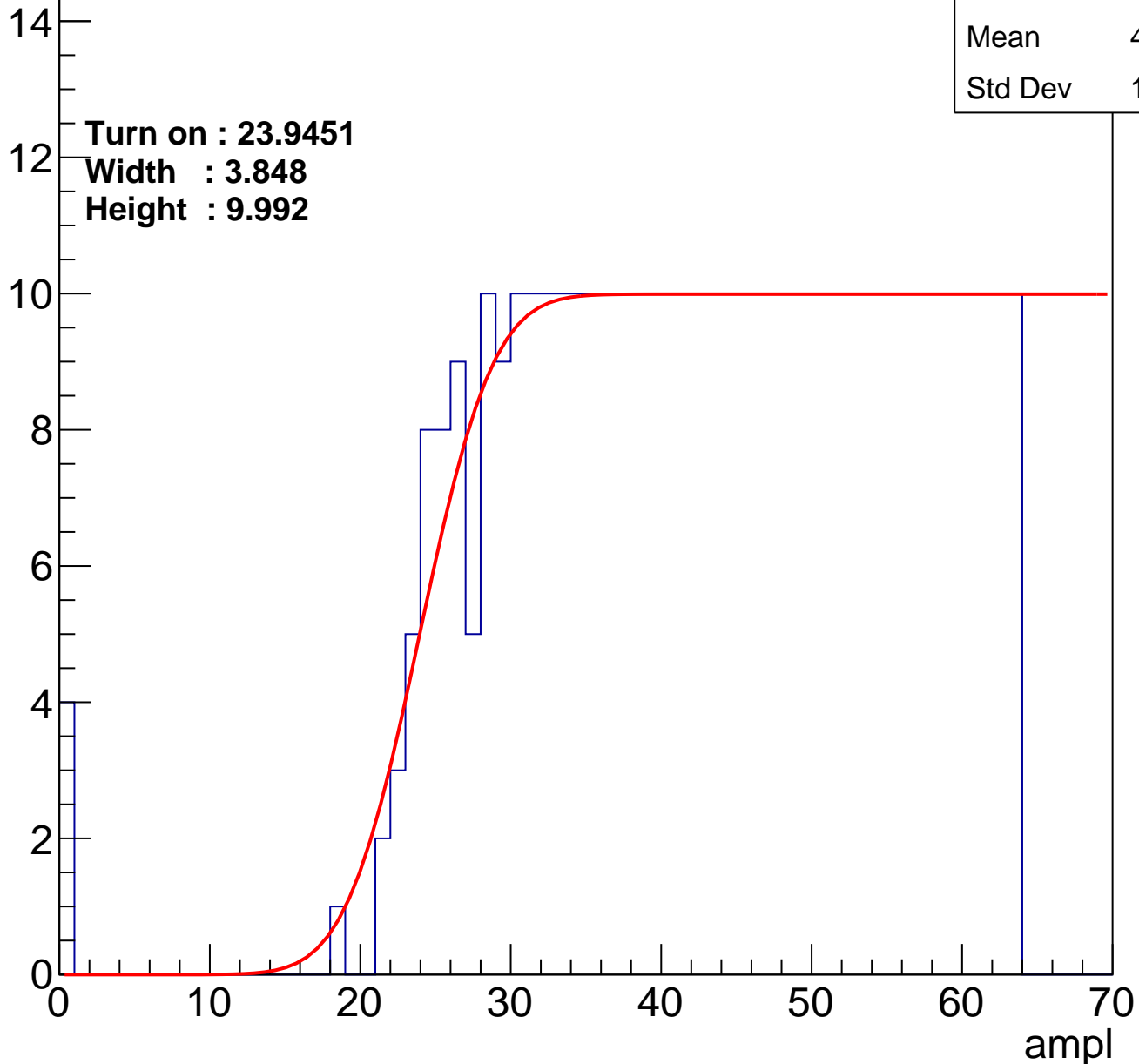
Entries	404
Mean	42.95
Std Dev	12.45

Turn on : 23.9451

Width : 3.848

Height : 9.992

Entry



B1L103S, U23-ch93

calib_packv5_042523_0143.root, FC#7, port C2

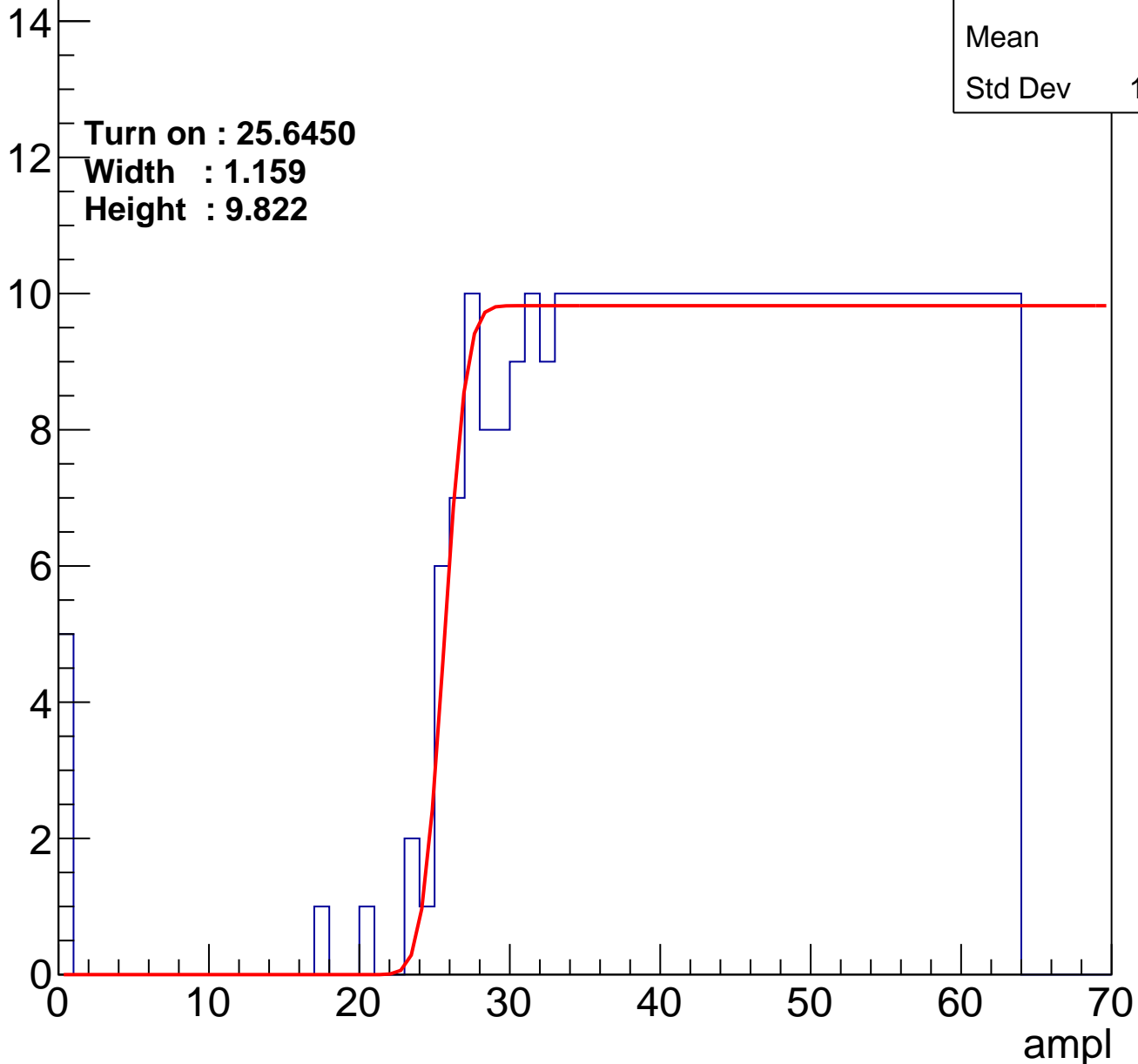
Entries	387
Mean	43.7
Std Dev	12.23

Turn on : 25.6450

Width : 1.159

Height : 9.822

Entry



B1L103S, U23-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.5
Std Dev	12.18

Turn on : 25.3943

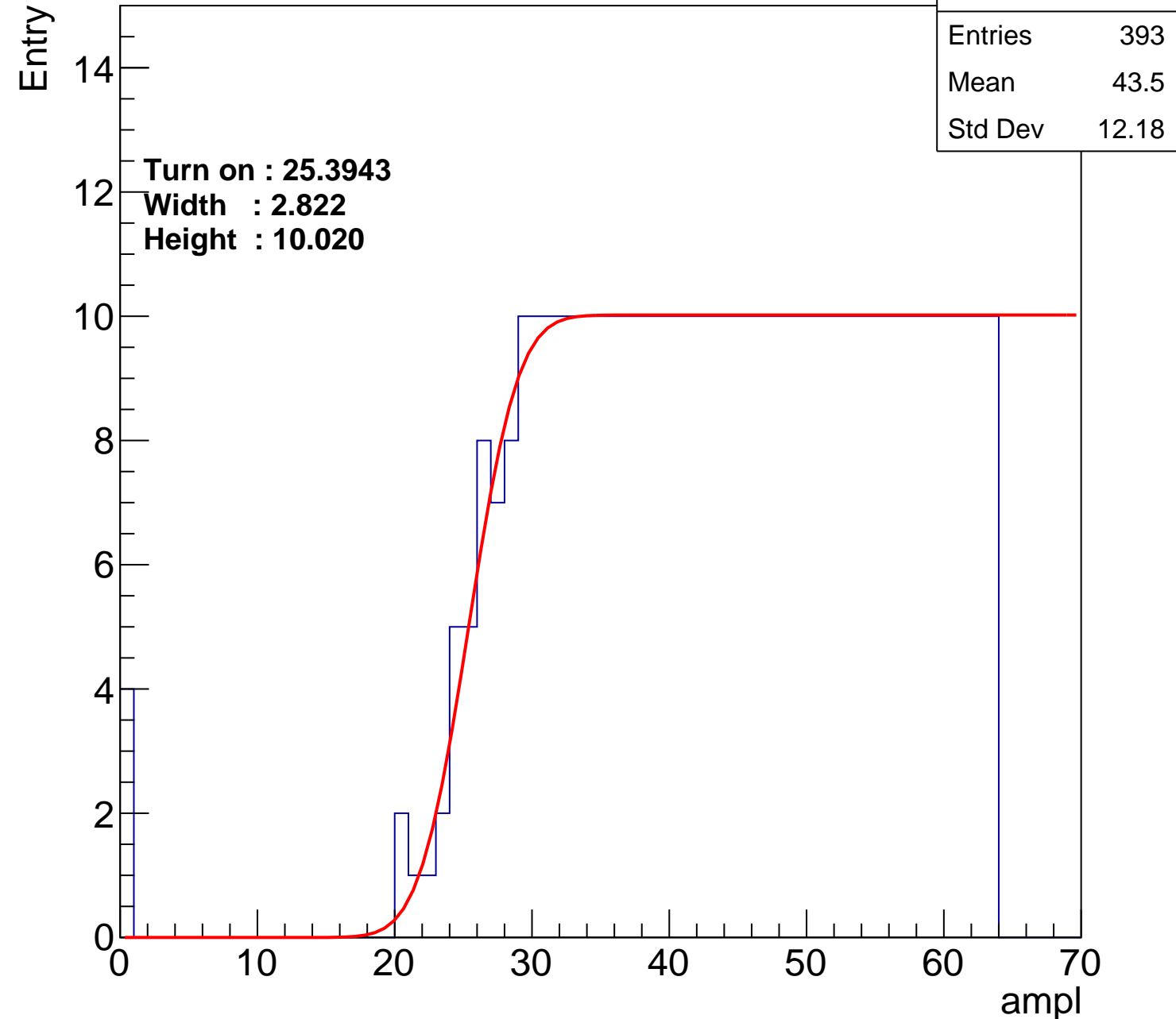
Width : 2.822

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.74
Std Dev	12.11

Turn on : 26.5989

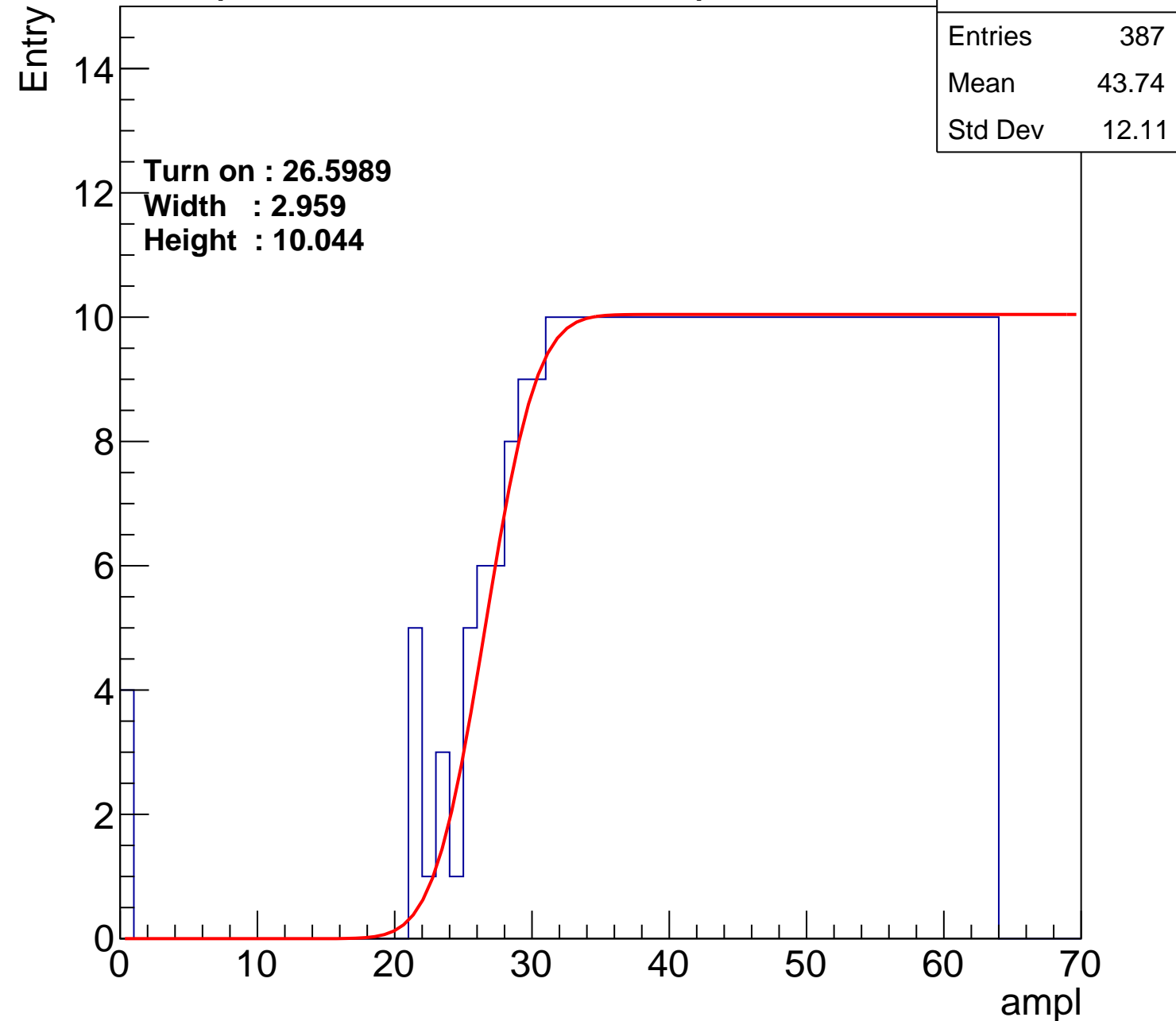
Width : 2.959

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch96

calib_packv5_042523_0143.root, FC#7, port C2

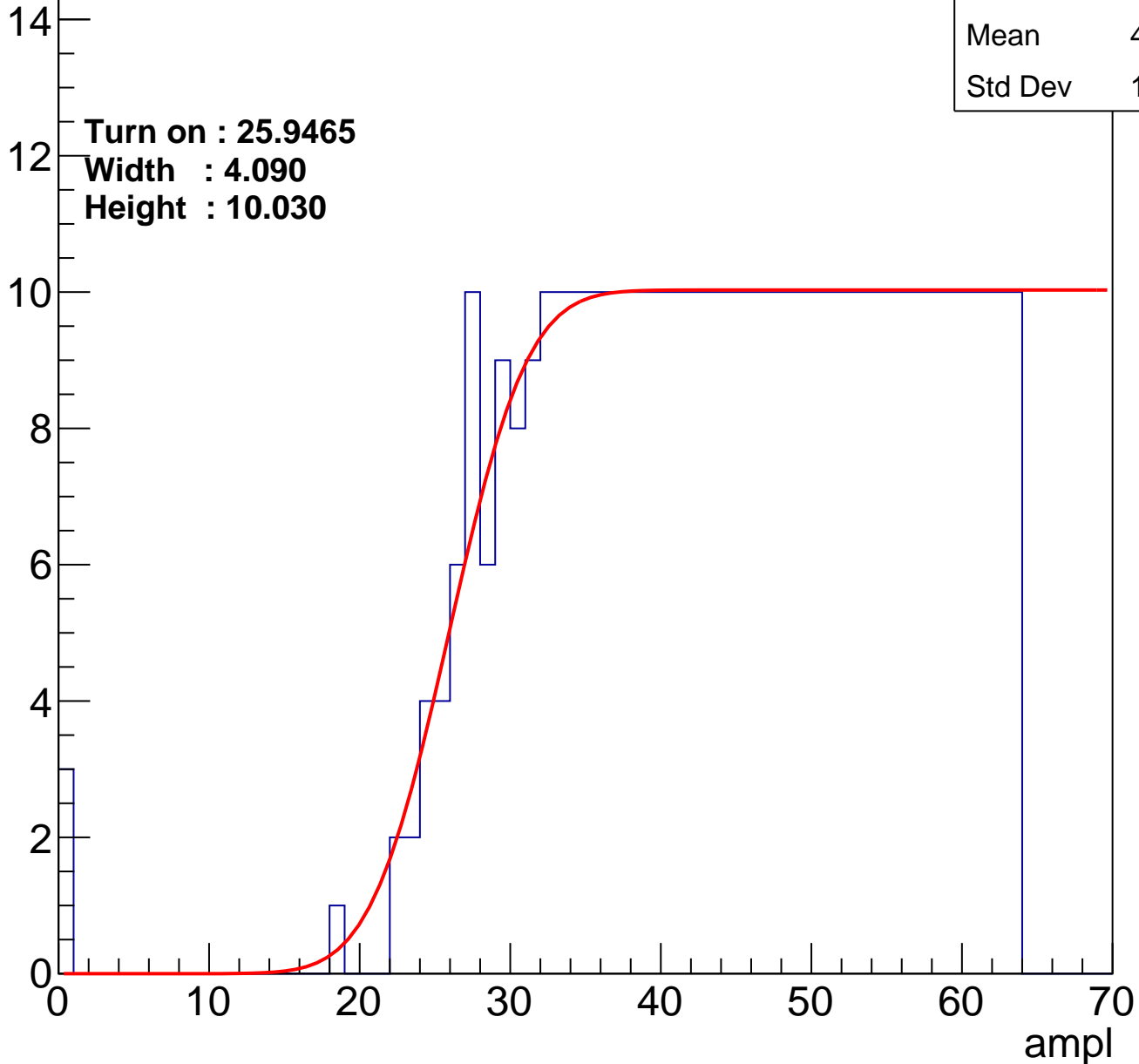
Entries	384
Mean	43.95
Std Dev	11.87

Turn on : 25.9465

Width : 4.090

Height : 10.030

Entry



B1L103S, U23-ch97

calib_packv5_042523_0143.root, FC#7, port C2

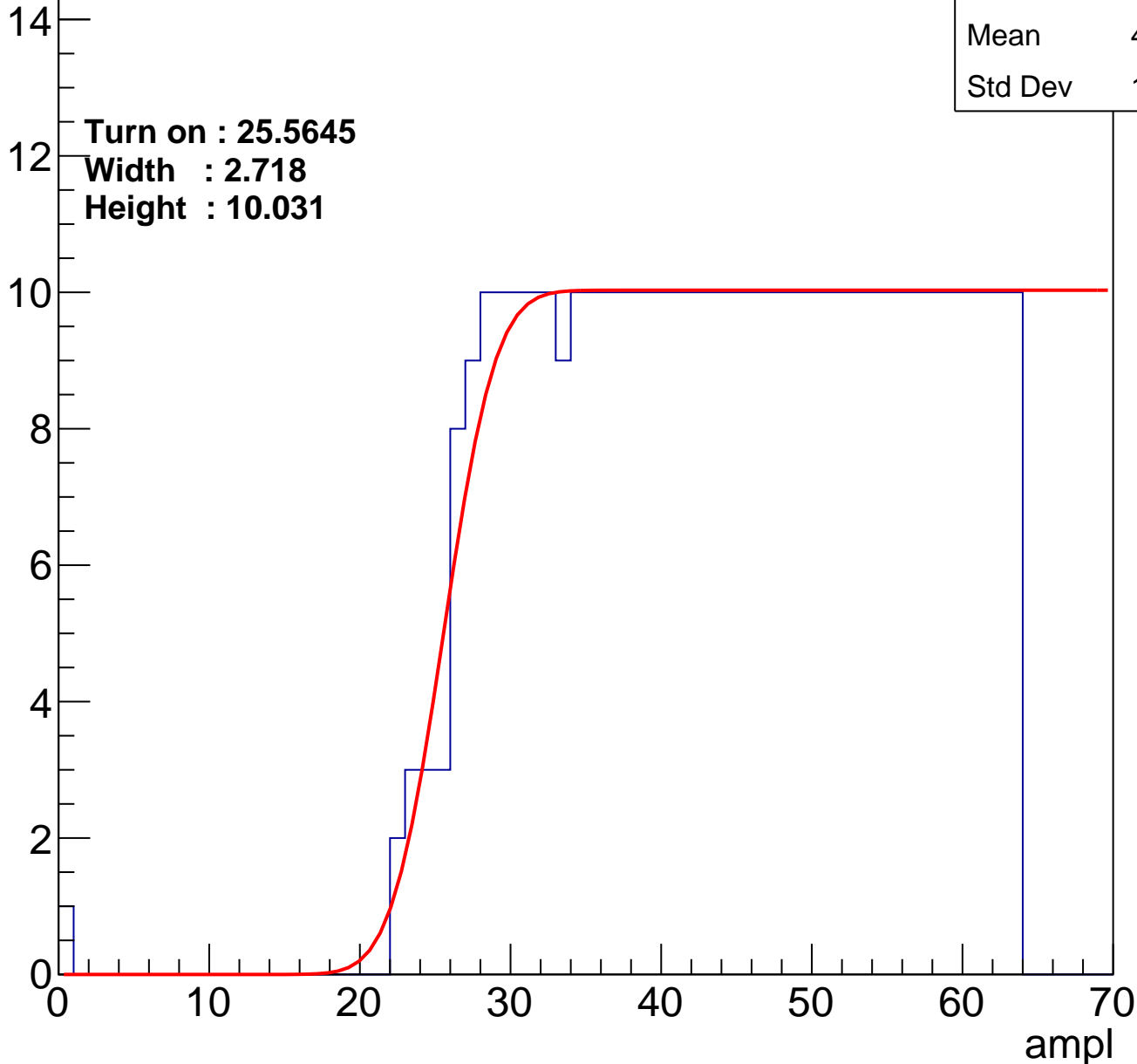
Entries	388
Mean	43.96
Std Dev	11.49

Turn on : 25.5645

Width : 2.718

Height : 10.031

Entry



B1L103S, U23-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.64
Std Dev	11.76

Turn on : 24.8669

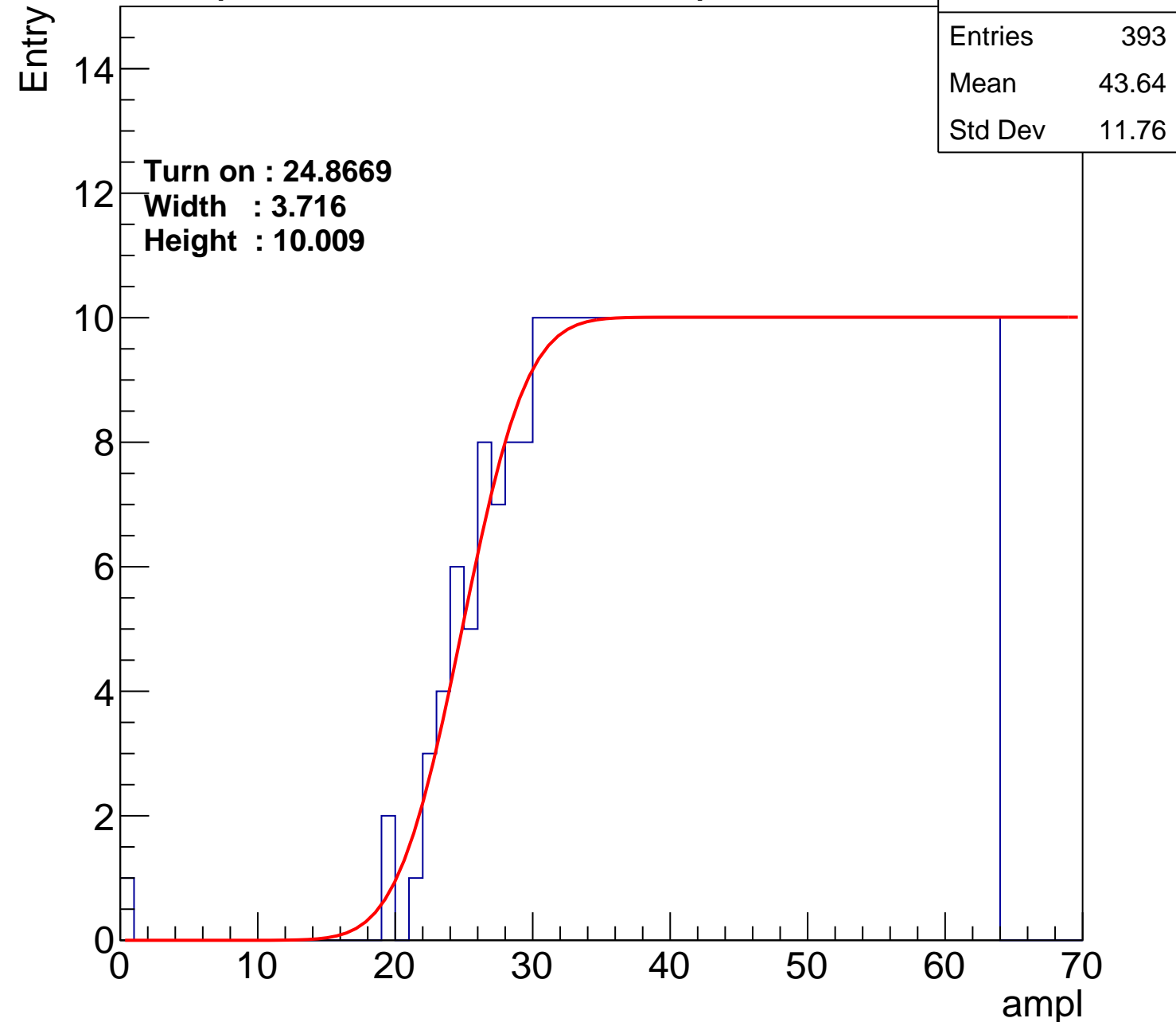
Width : 3.716

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.97
Std Dev	11.55

Turn on : 26.3581

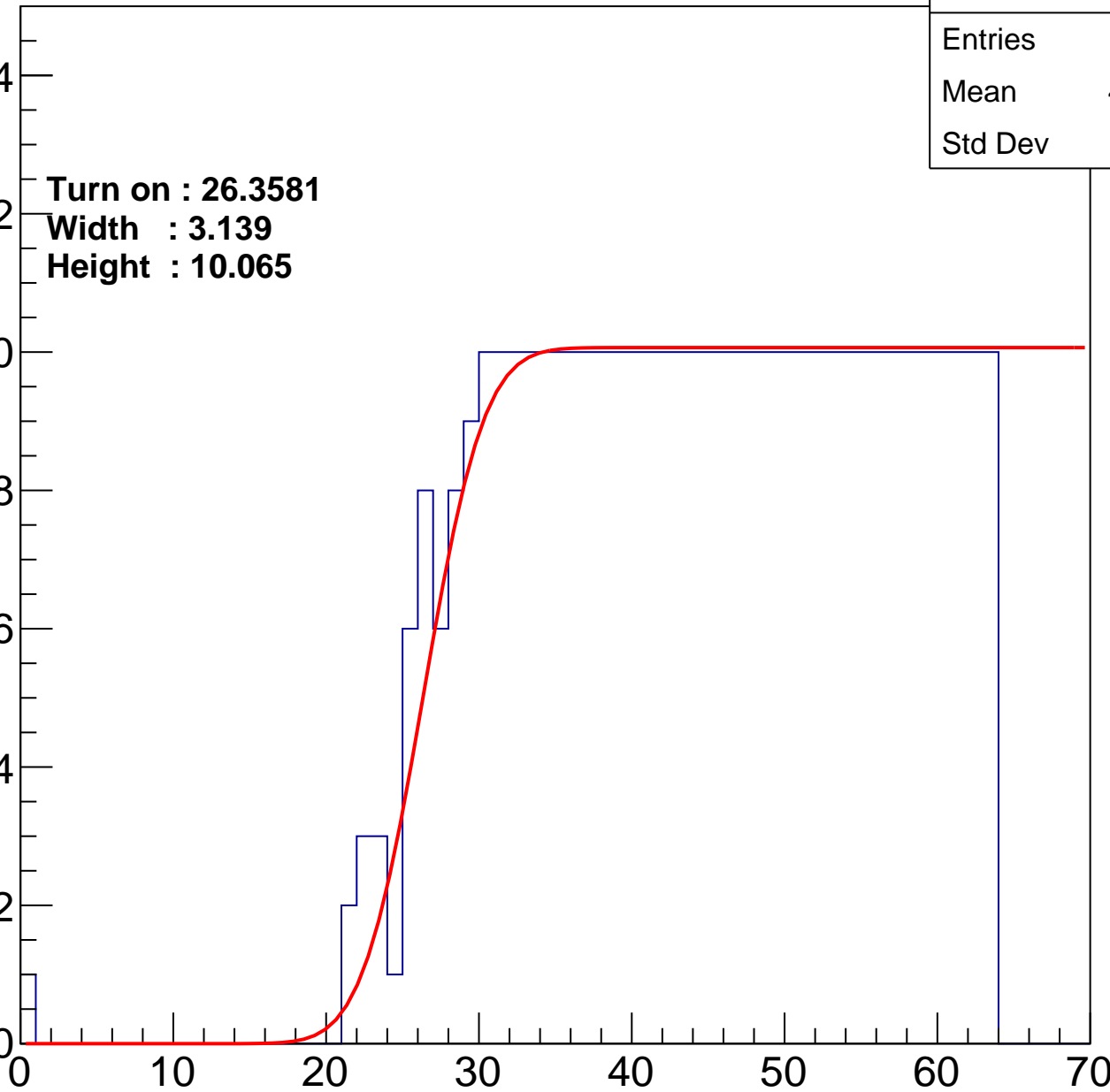
Width : 3.139

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch100

calib_packv5_042523_0143.root, FC#7, port C2

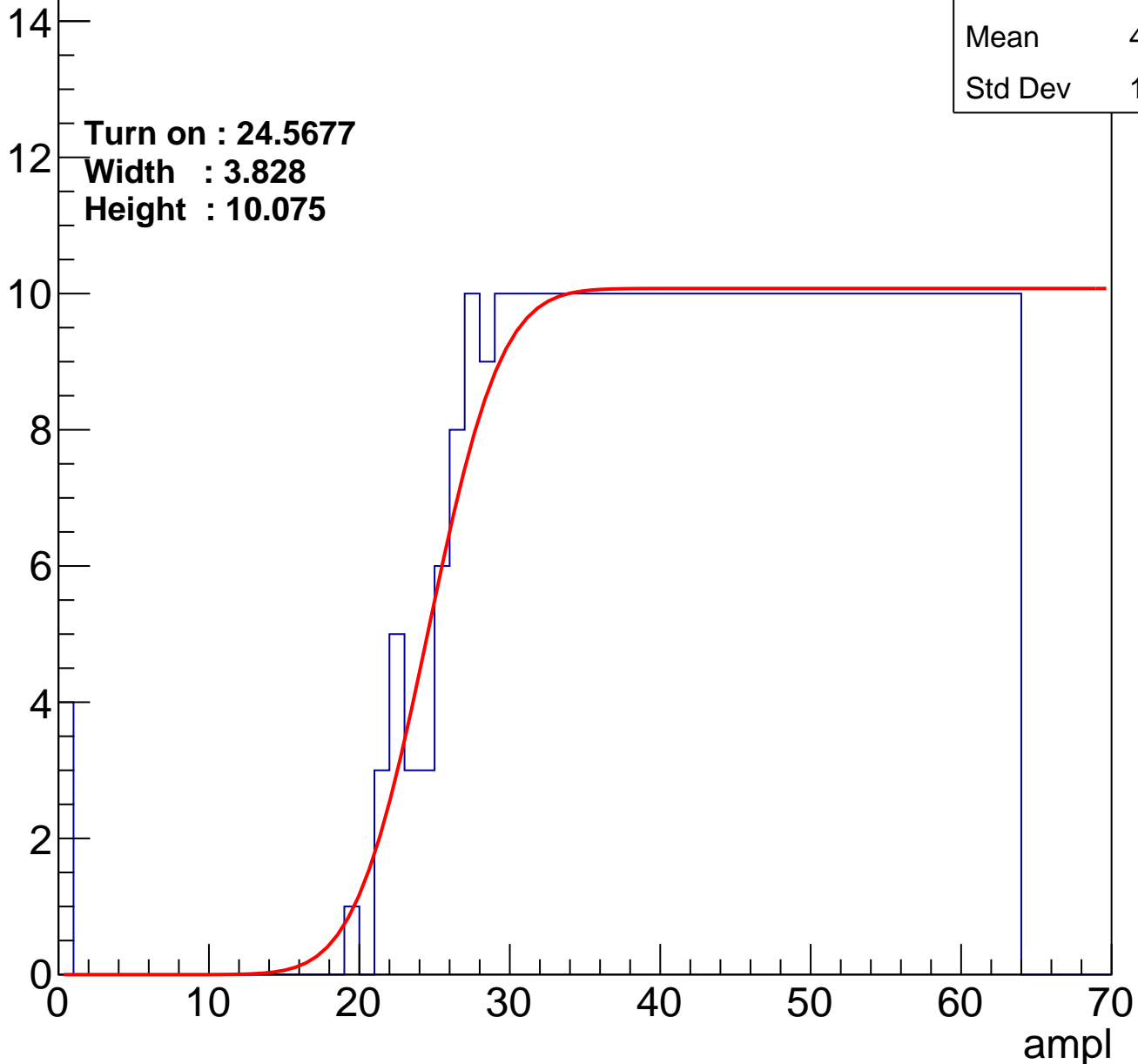
Entries	402
Mean	43.07
Std Dev	12.38

Turn on : 24.5677

Width : 3.828

Height : 10.075

Entry



B1L103S, U23-ch101

calib_packv5_042523_0143.root, FC#7, port C2

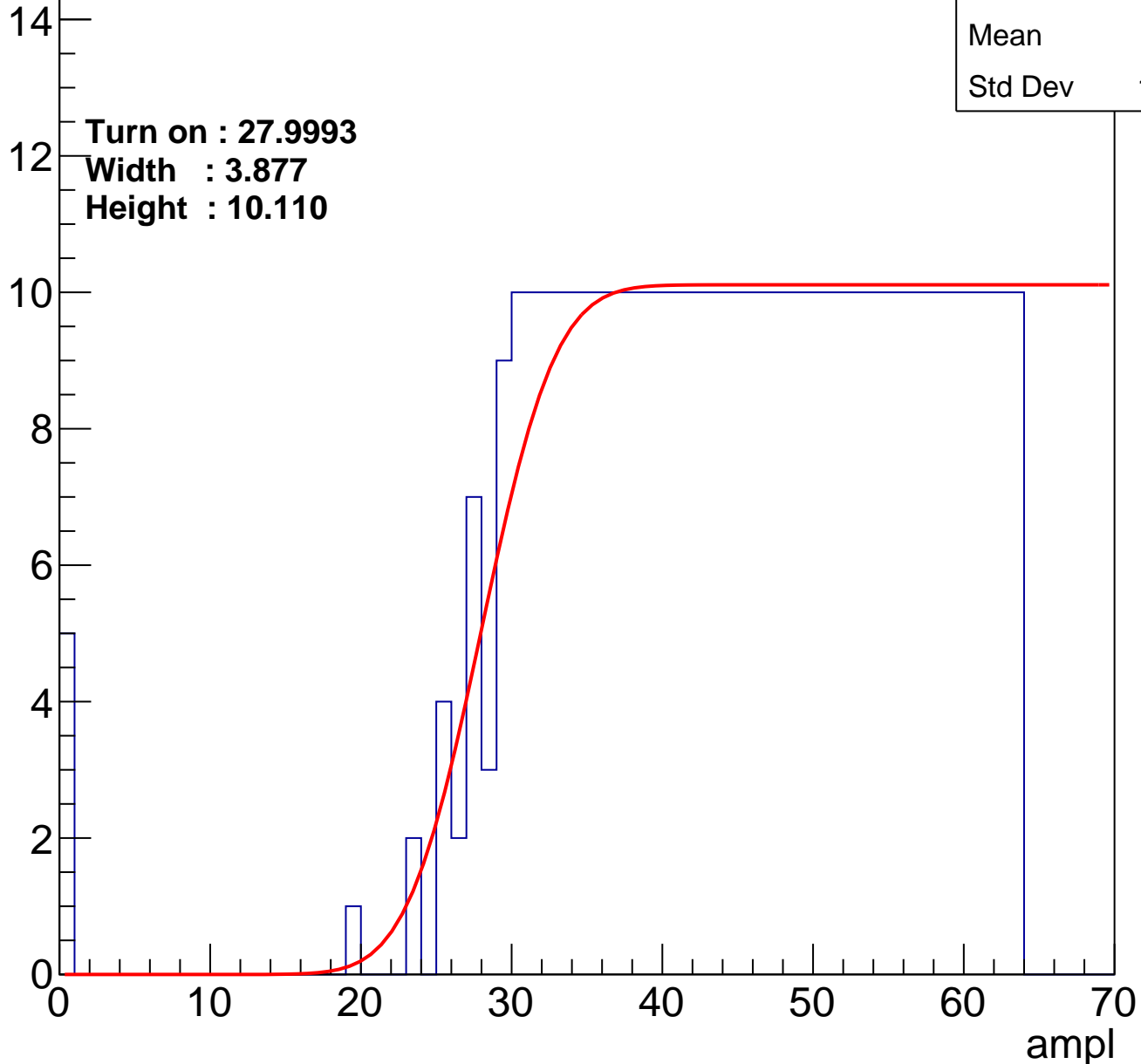
Entries	373
Mean	44.4
Std Dev	11.91

Turn on : 27.9993

Width : 3.877

Height : 10.110

Entry



B1L103S, U23-ch102

calib_packv5_042523_0143.root, FC#7, port C2

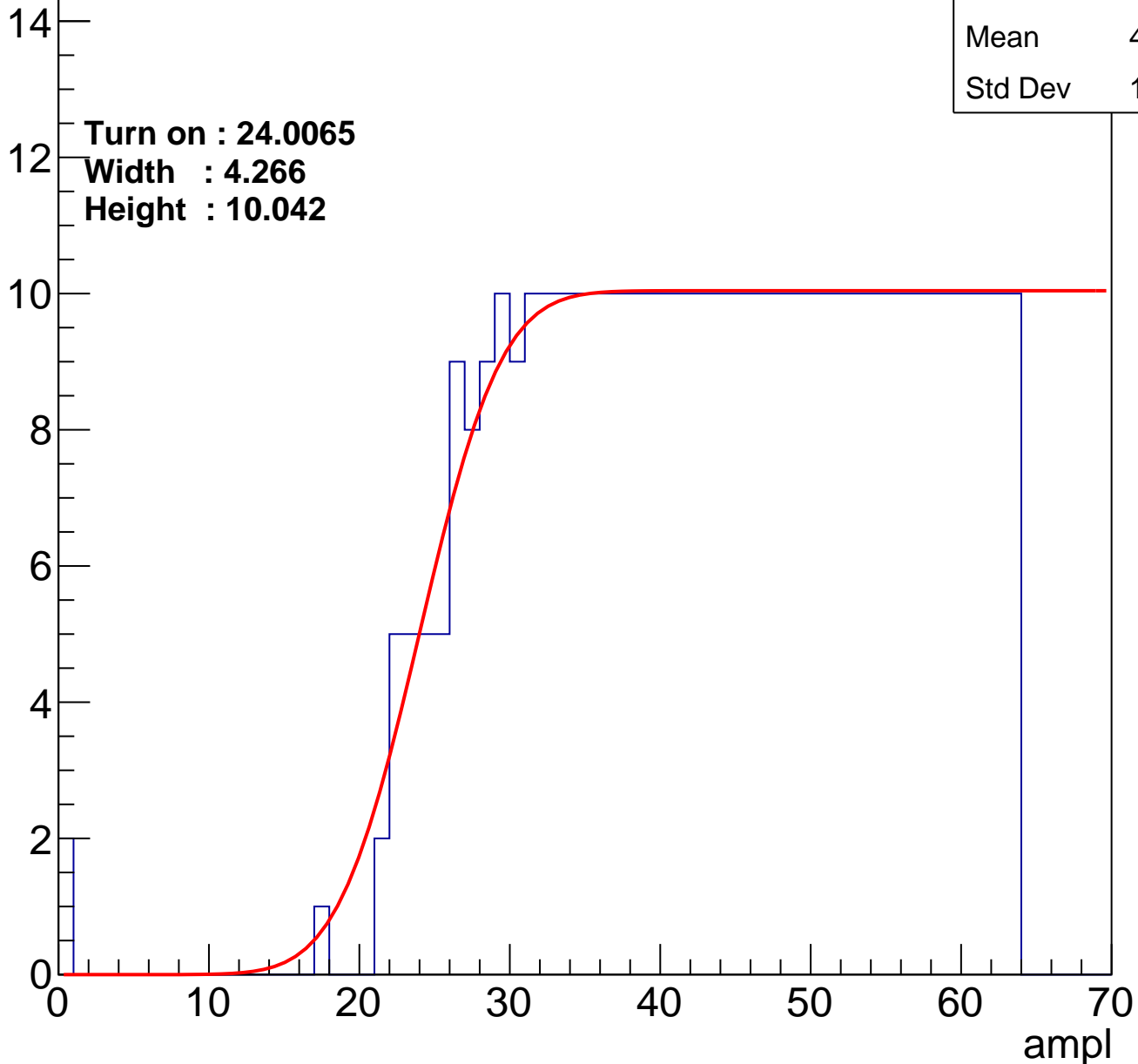
Entries	400
Mean	43.25
Std Dev	12.07

Turn on : 24.0065

Width : 4.266

Height : 10.042

Entry



B1L103S, U23-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 26.9622

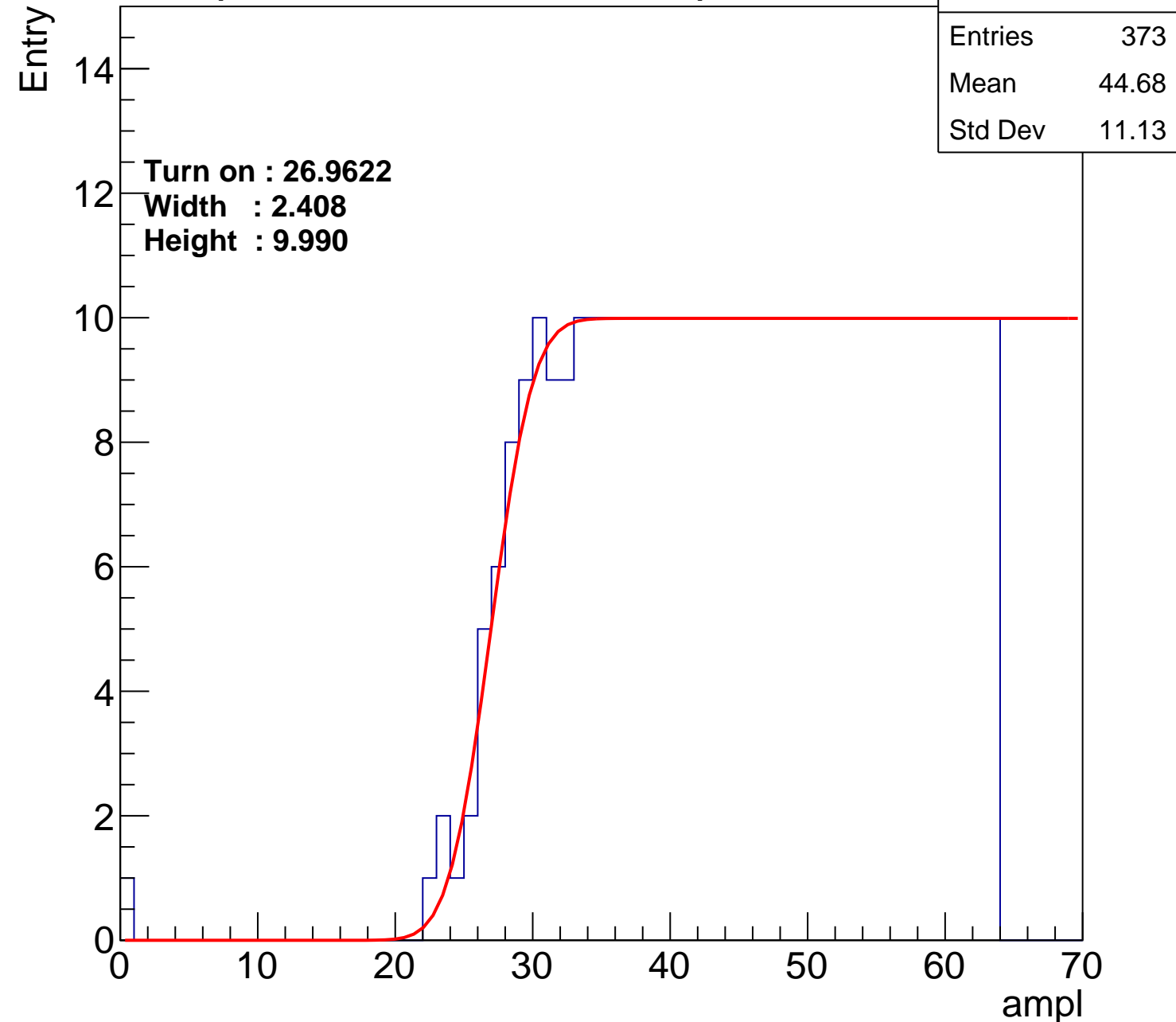
Width : 2.408

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.65
Std Dev	11.97

Turn on : 25.6825

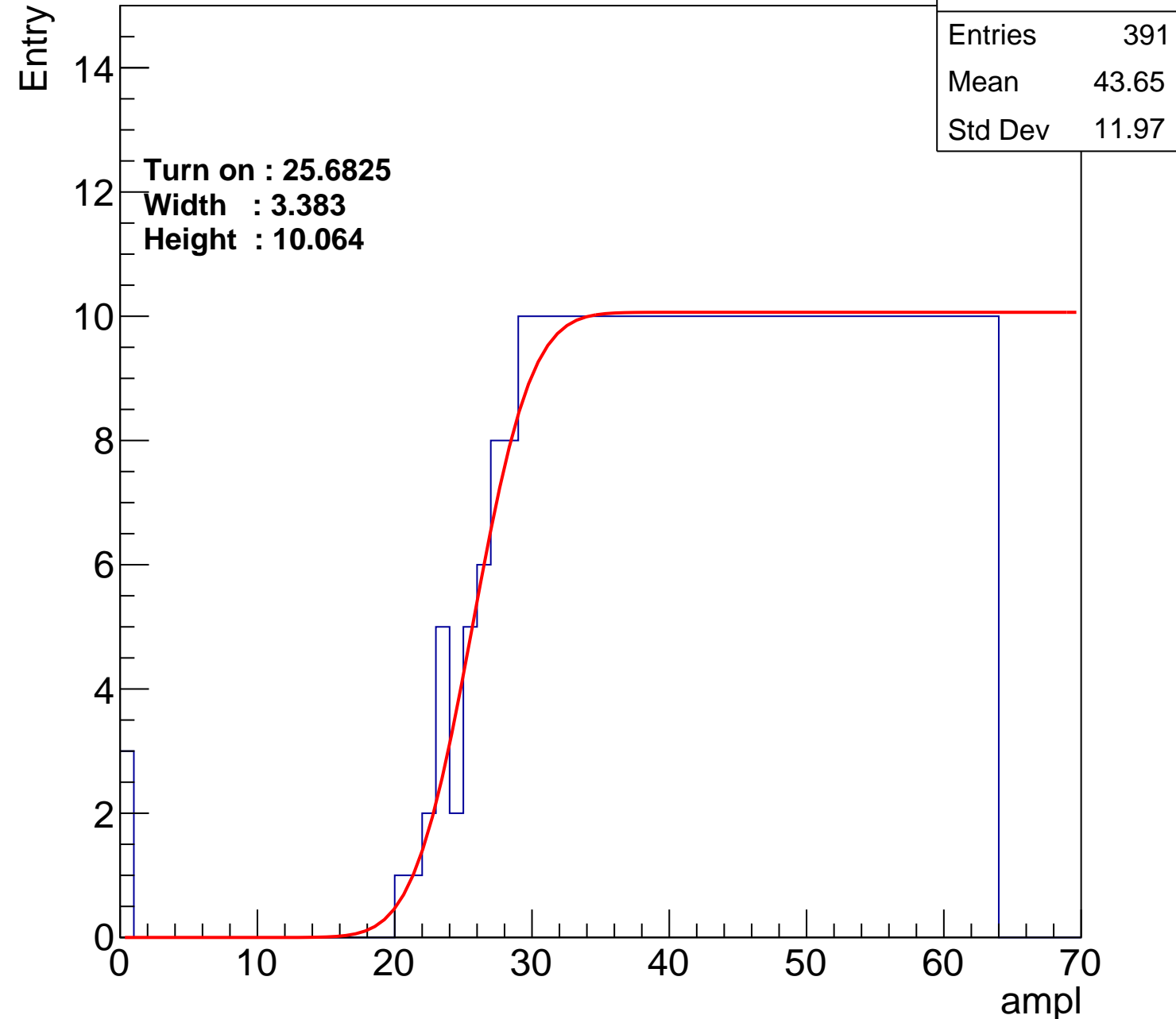
Width : 3.383

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.75
Std Dev	11.81

Turn on : 25.5992

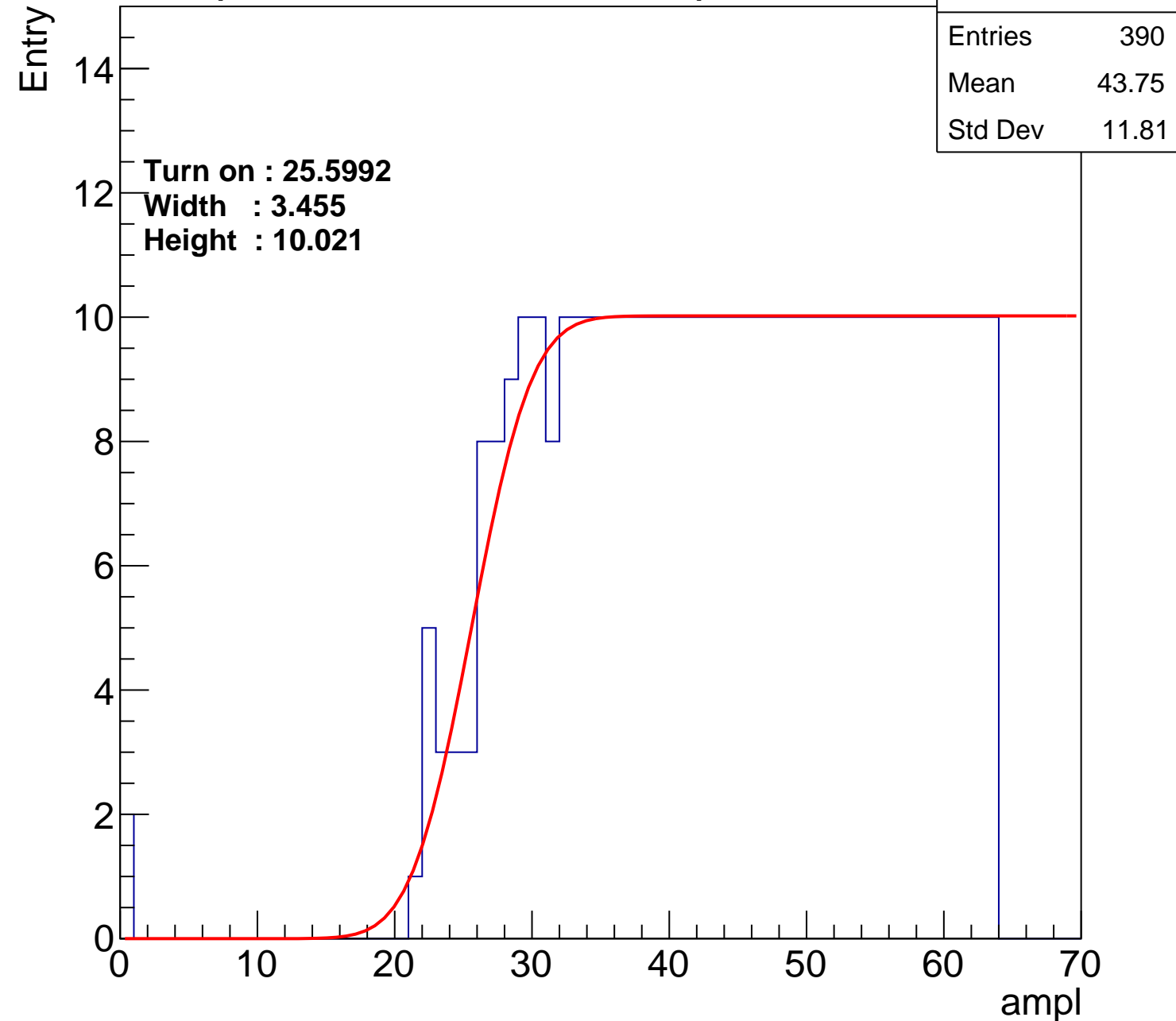
Width : 3.455

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch106

calib_packv5_042523_0143.root, FC#7, port C2

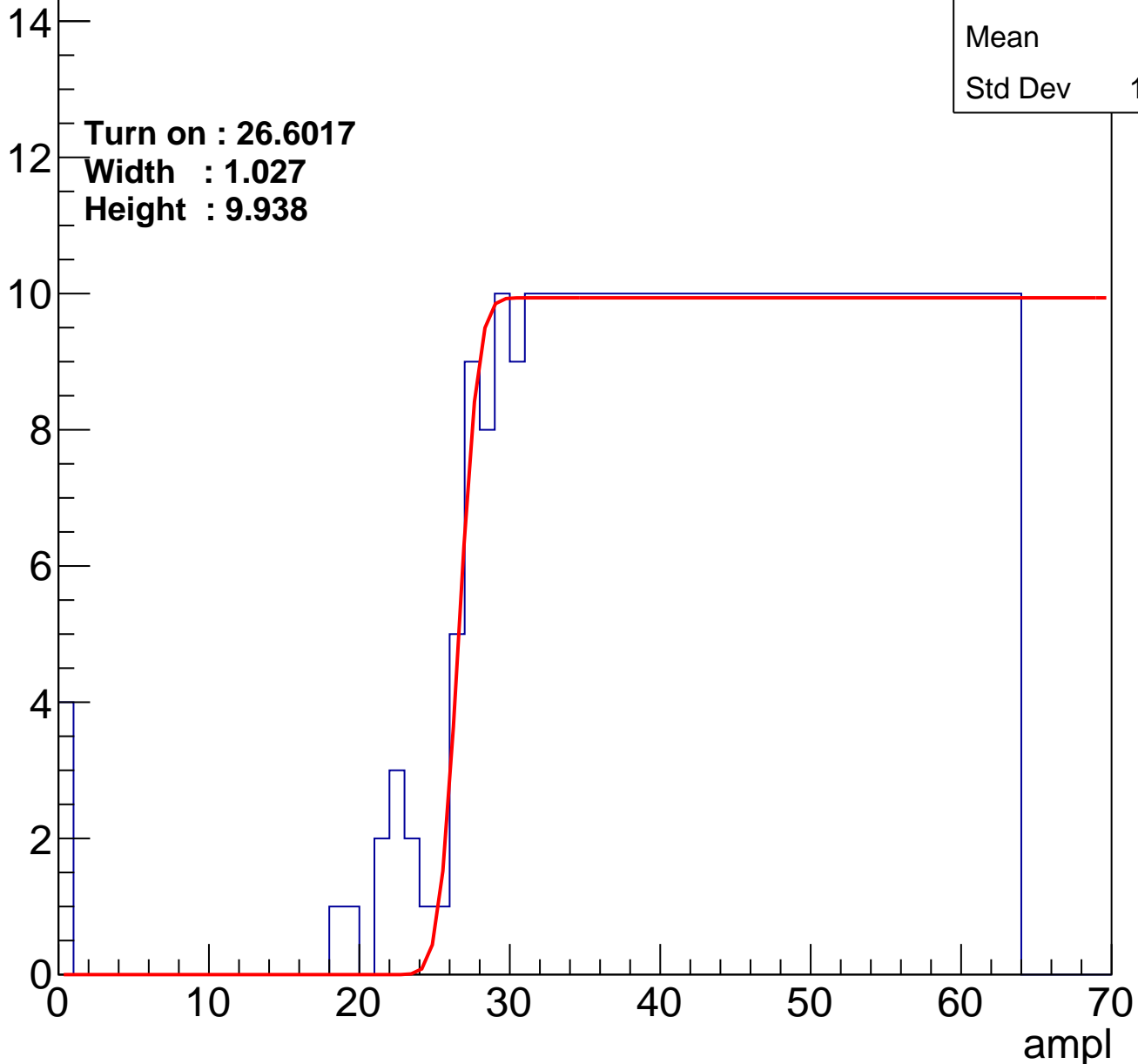
Entries	386
Mean	43.8
Std Dev	12.08

Turn on : 26.6017

Width : 1.027

Height : 9.938

Entry



B1L103S, U23-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.78
Std Dev	12.45

Turn on : 26.3732

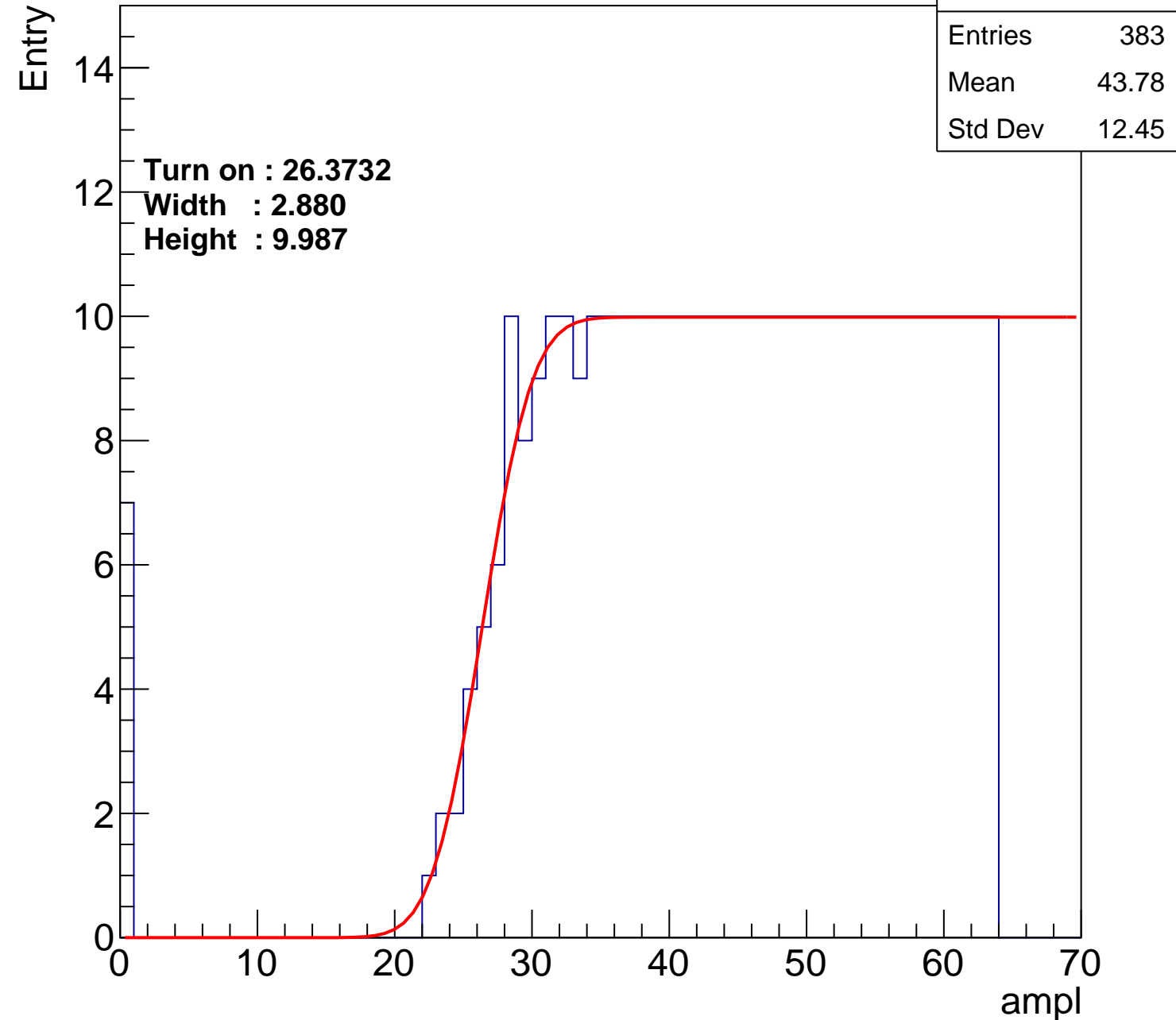
Width : 2.880

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch108

calib_packv5_042523_0143.root, FC#7, port C2

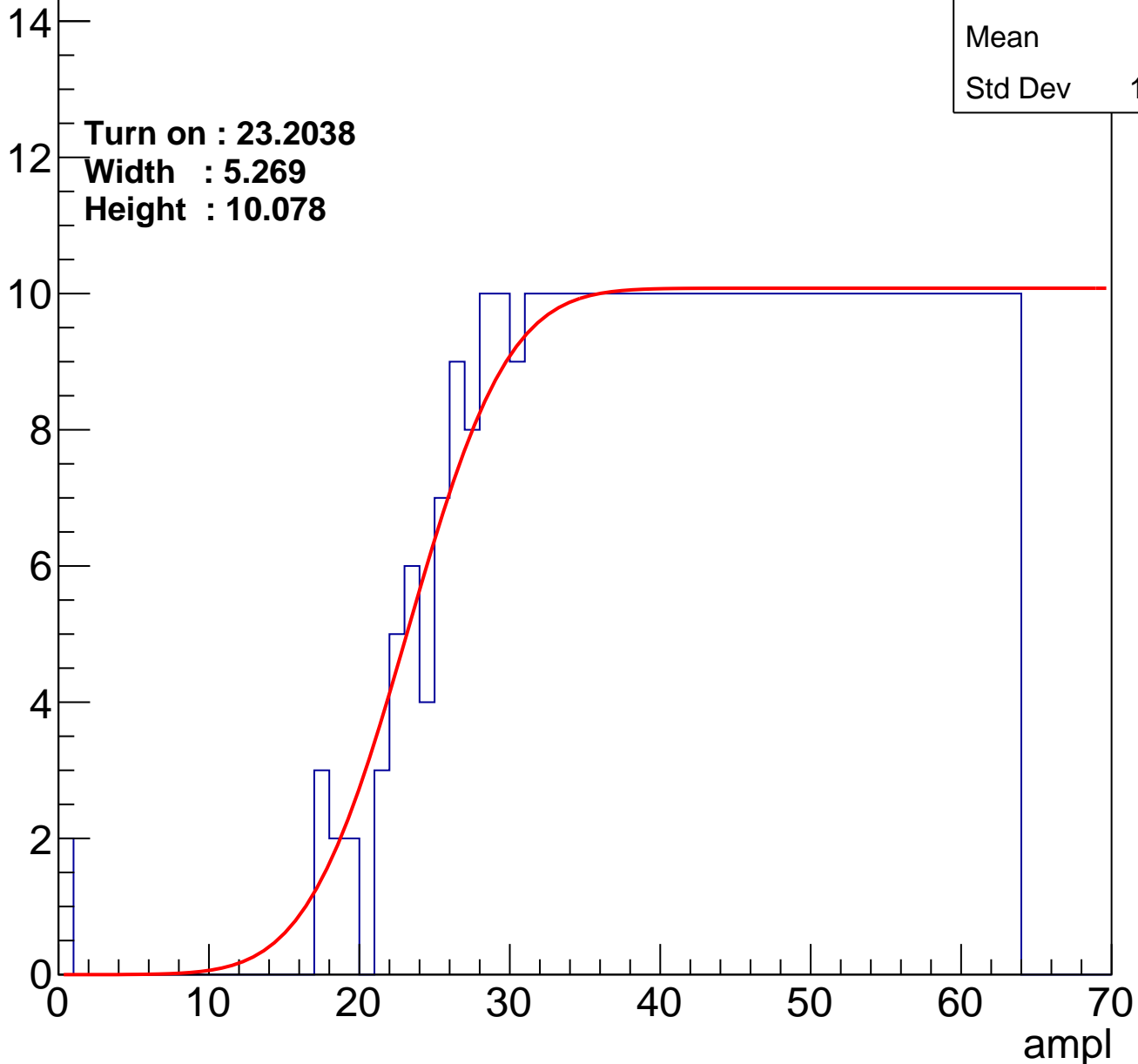
Entries	410
Mean	42.7
Std Dev	12.44

Turn on : 23.2038

Width : 5.269

Height : 10.078

Entry



B1L103S, U23-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 25.6634

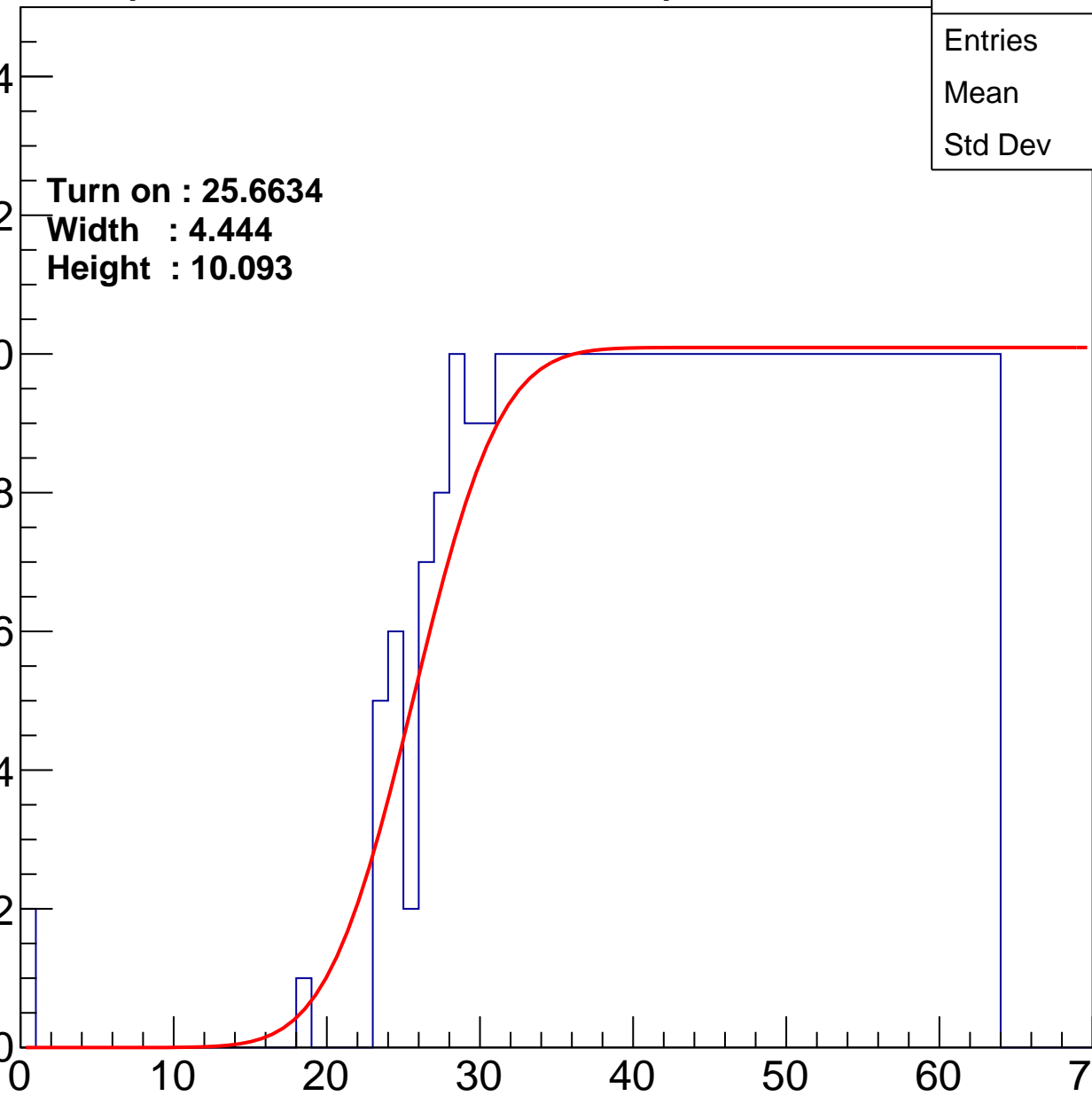
Width : 4.444

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch110

calib_packv5_042523_0143.root, FC#7, port C2

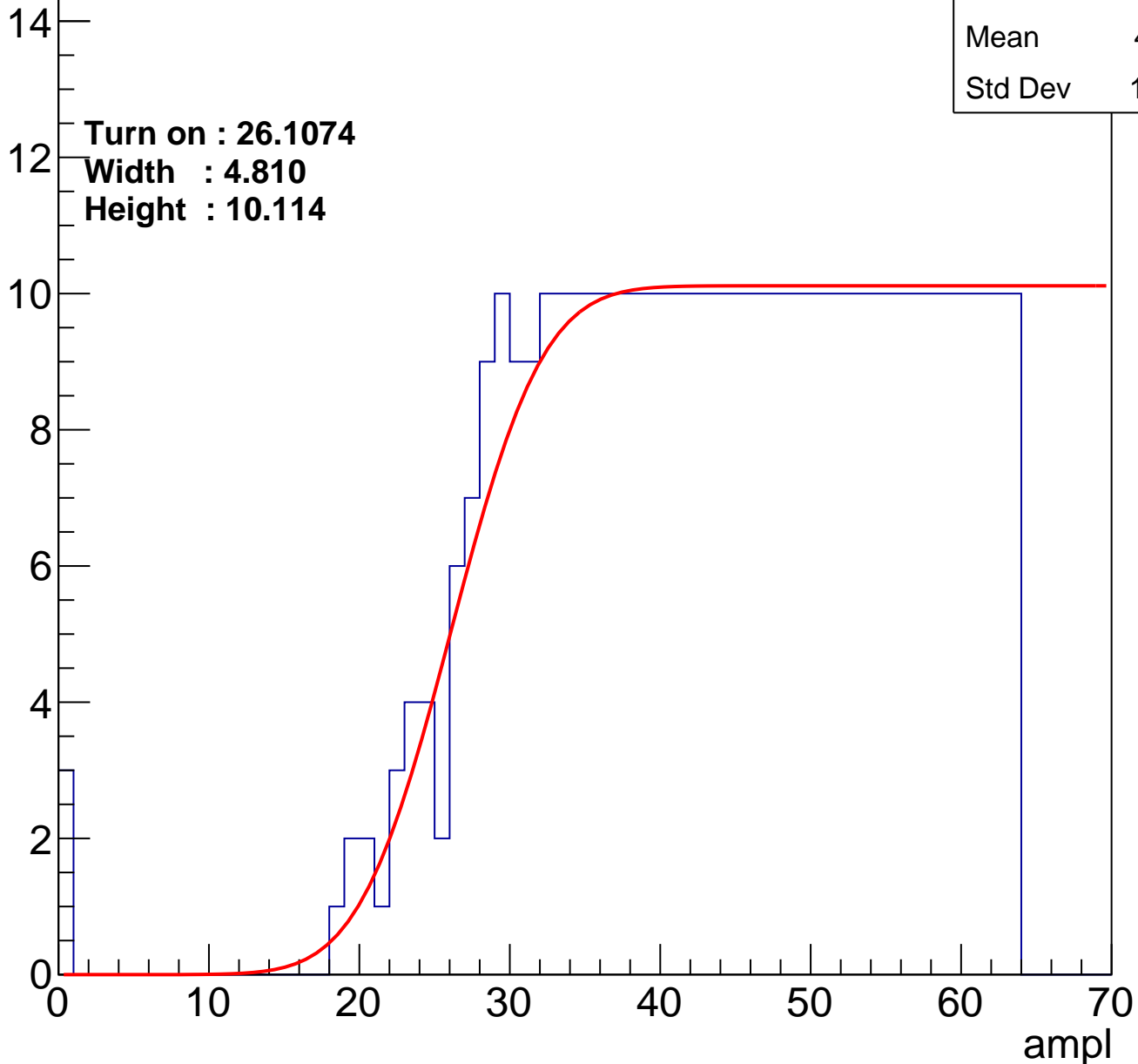
Entries	392
Mean	43.51
Std Dev	12.15

Turn on : 26.1074

Width : 4.810

Height : 10.114

Entry



B1L103S, U23-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.85
Std Dev	12.28

Turn on : 26.9080

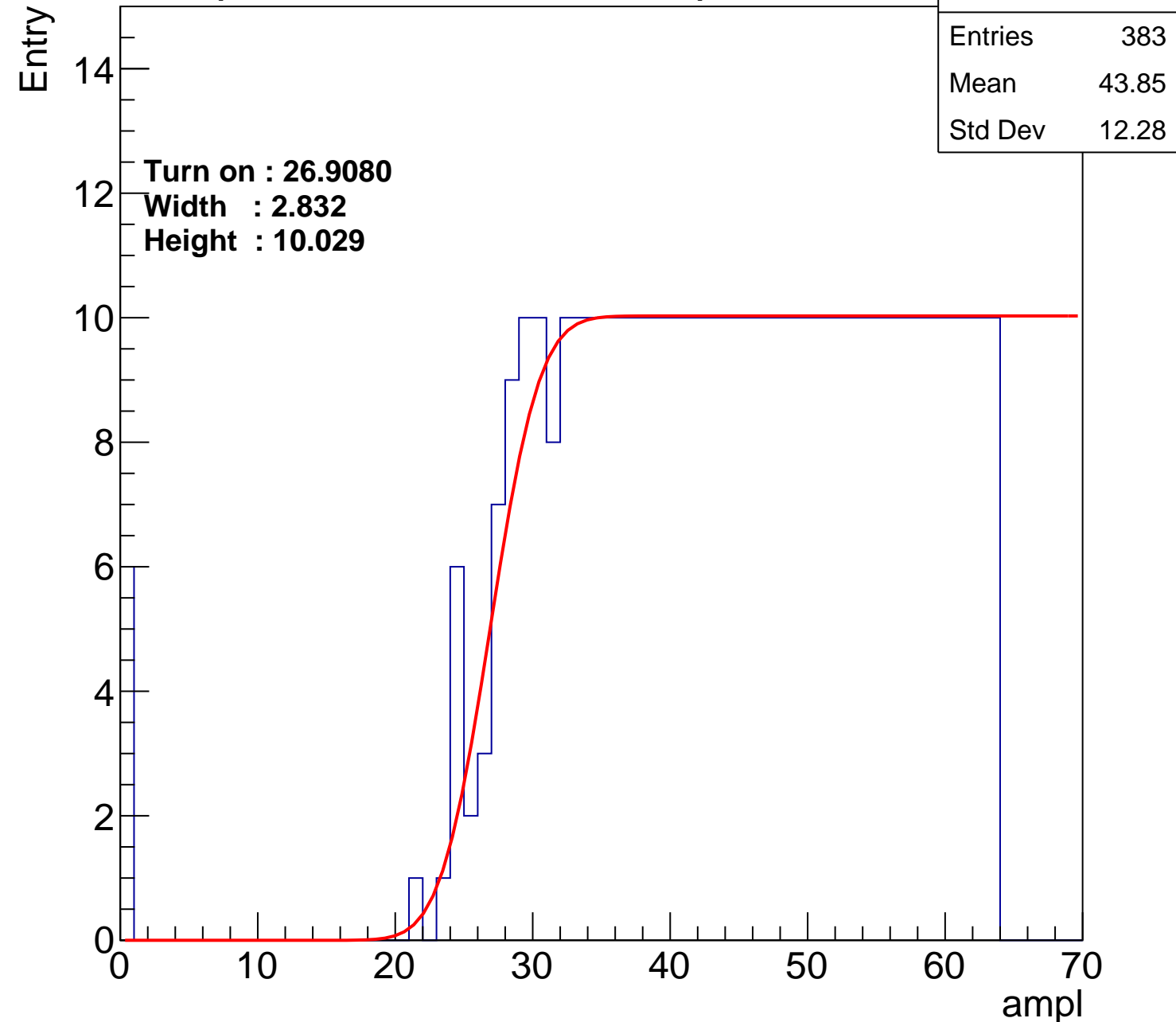
Width : 2.832

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	402
Mean	42.89
Std Dev	12.71

Turn on : 25.2473

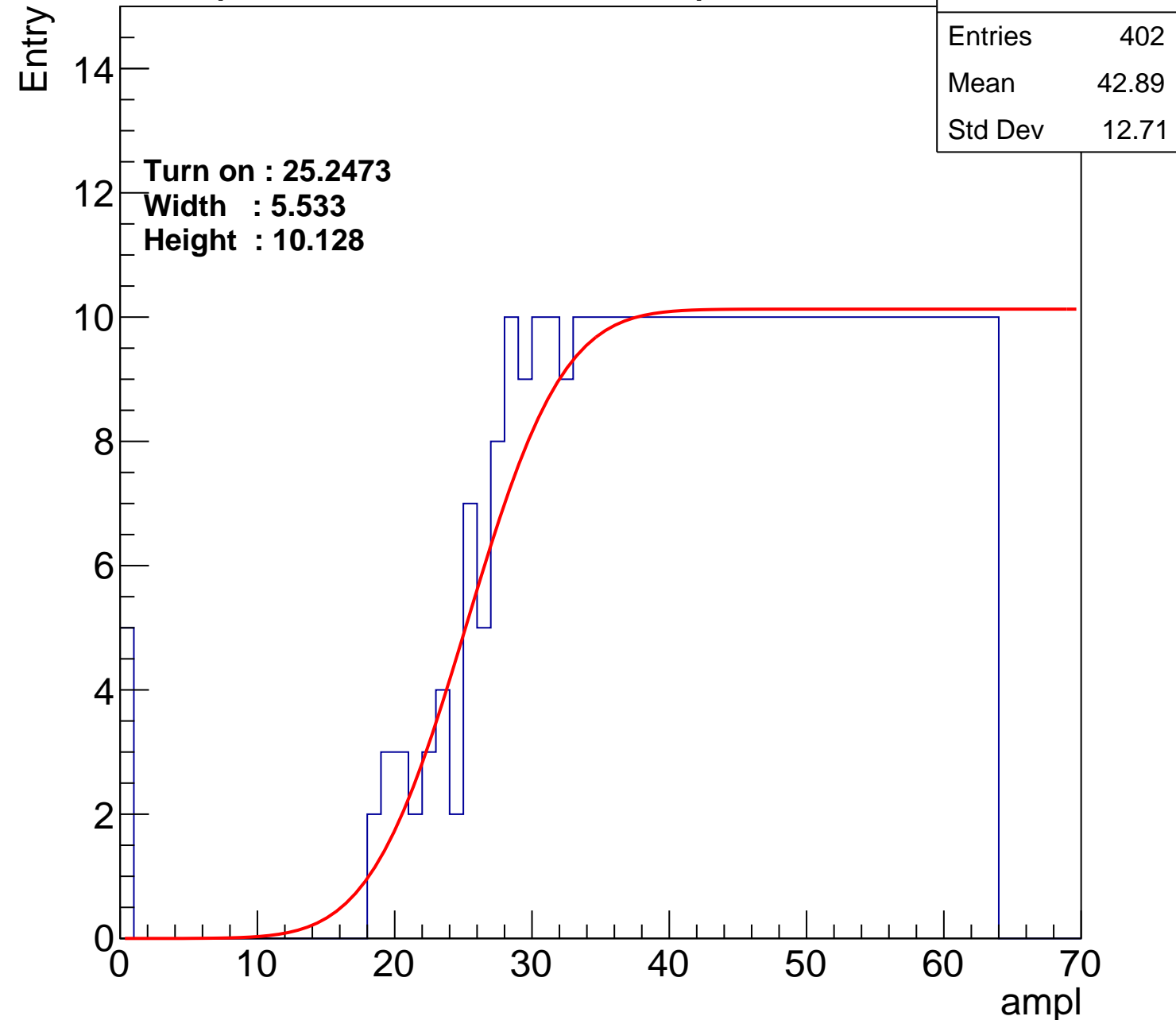
Width : 5.533

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.58
Std Dev	11.43

Turn on : 26.9696

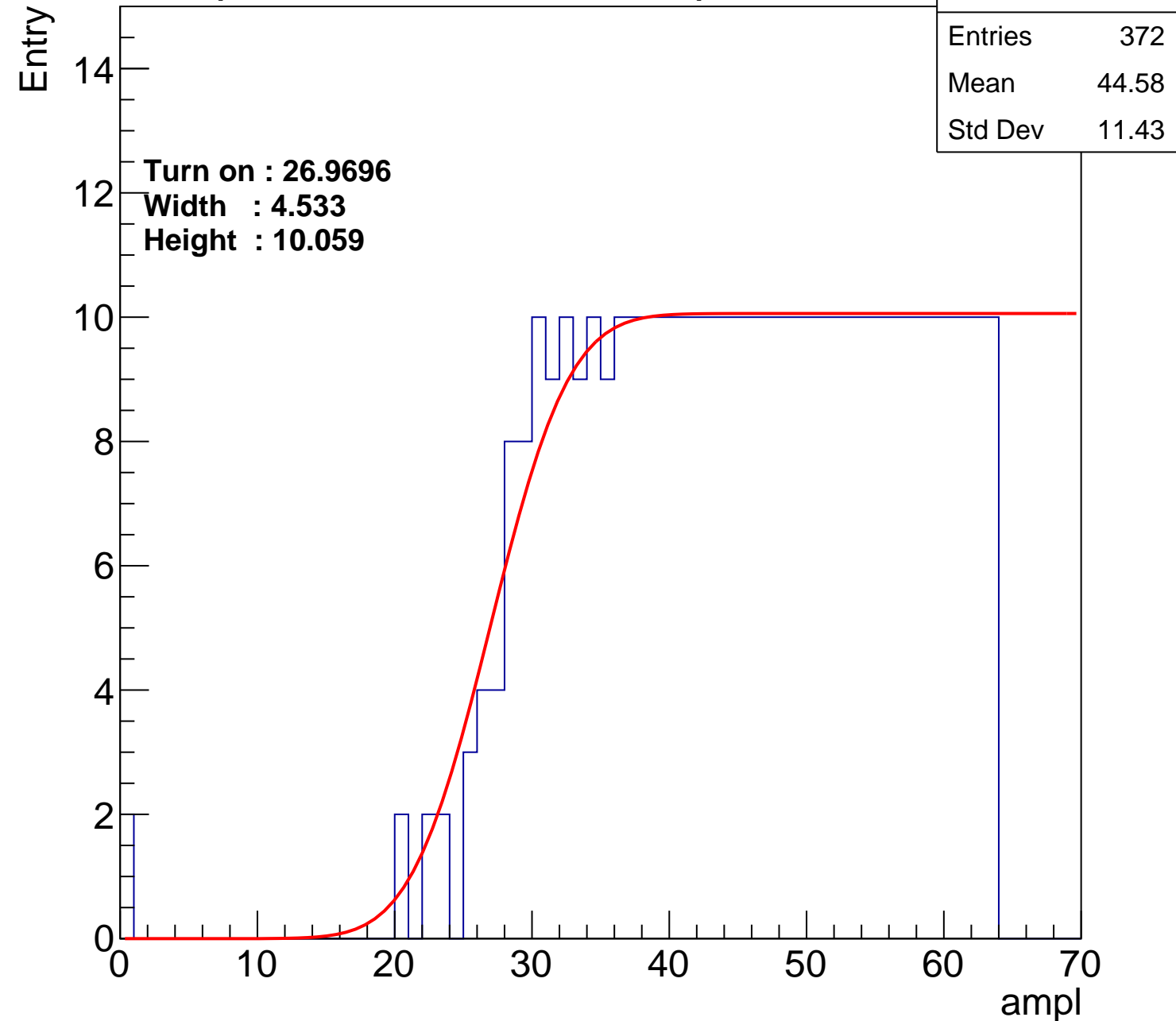
Width : 4.533

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	414
Mean	42.49
Std Dev	12.6

Turn on : 23.1323

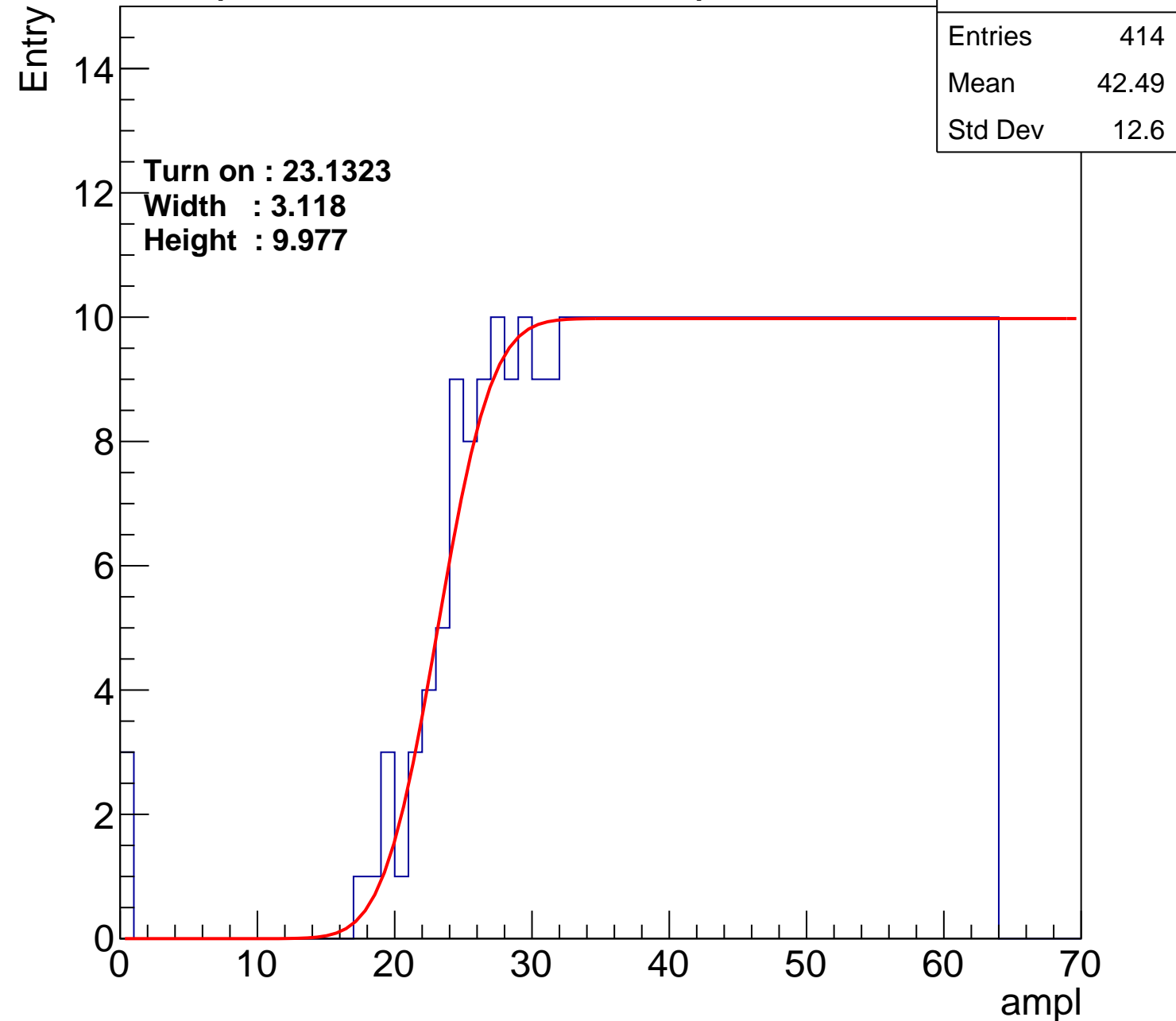
Width : 3.118

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.3
Std Dev	11.34

Turn on : 26.8951

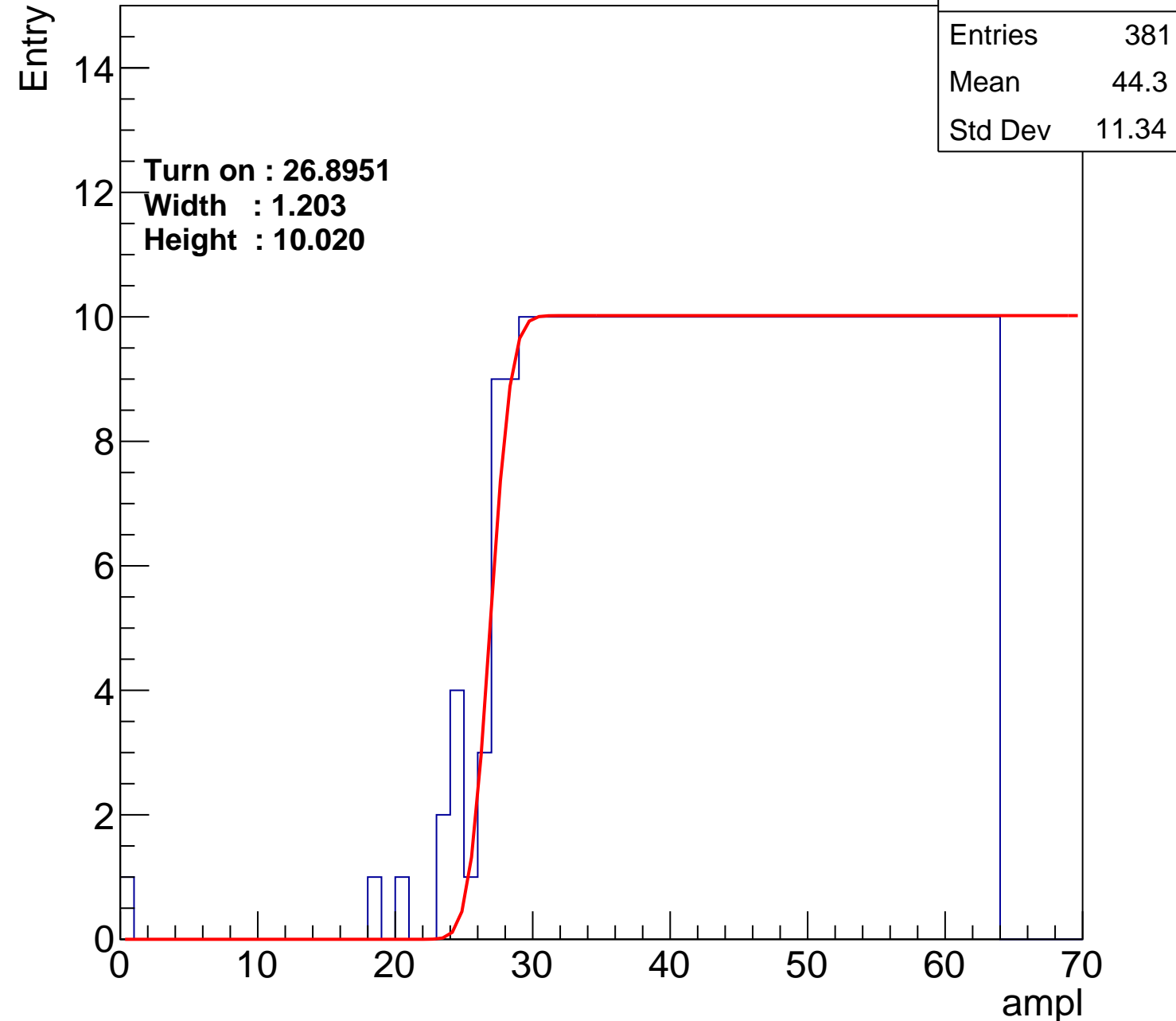
Width : 1.203

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.38
Std Dev	12.2

Turn on : 25.2220

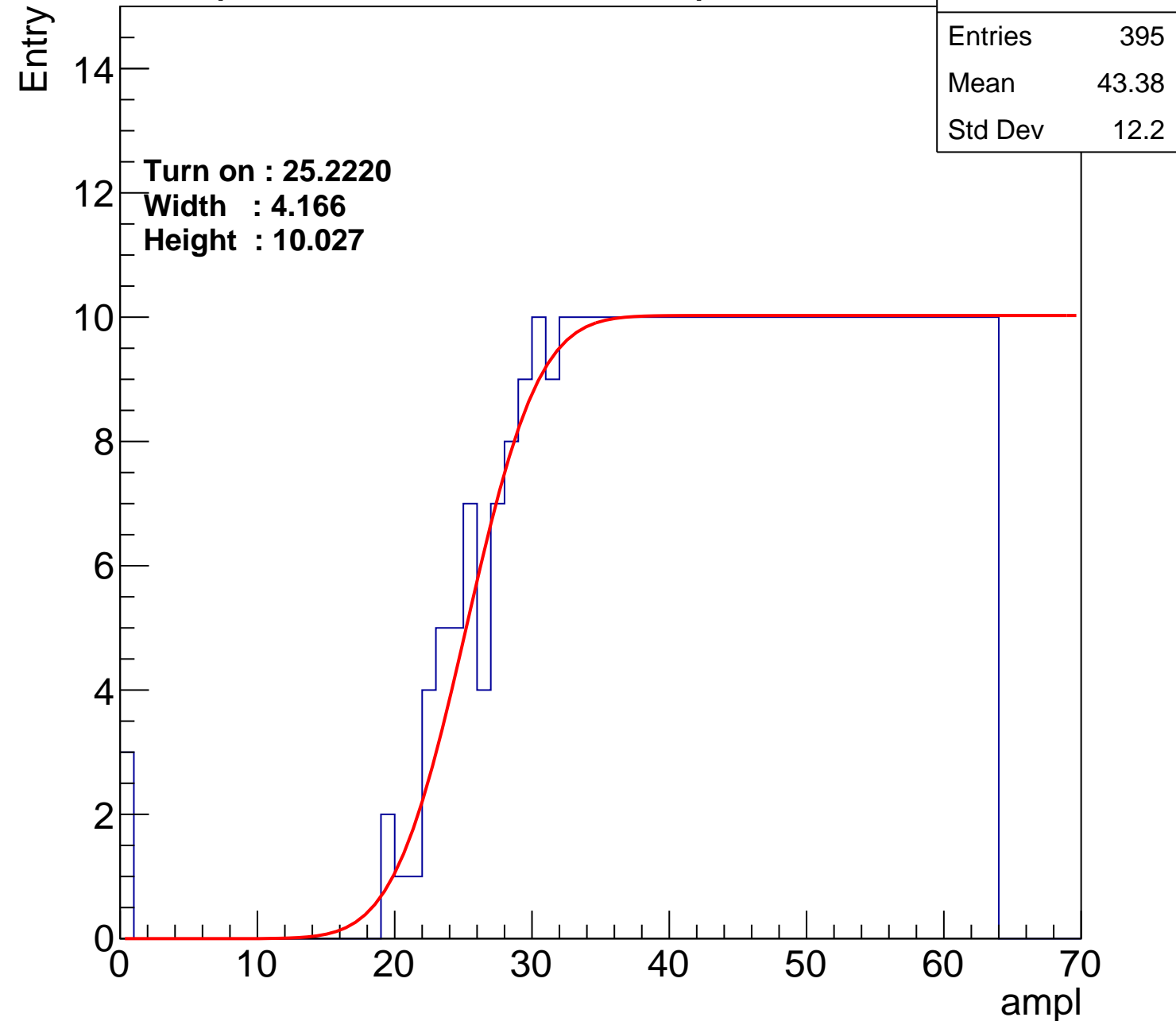
Width : 4.166

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.3
Std Dev	11.74

Turn on : 27.1037

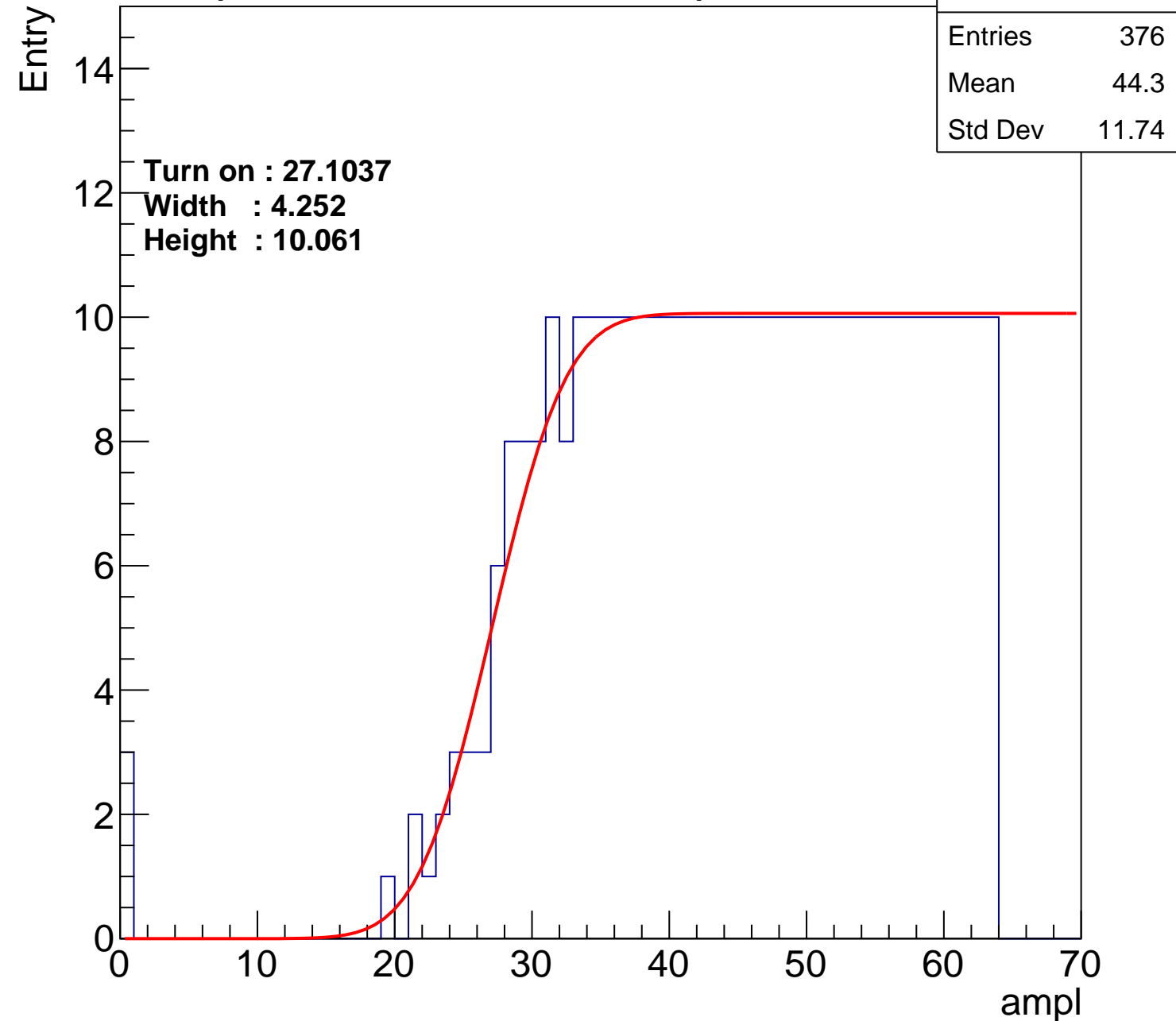
Width : 4.252

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.37
Std Dev	12.15

Turn on : 24.8286

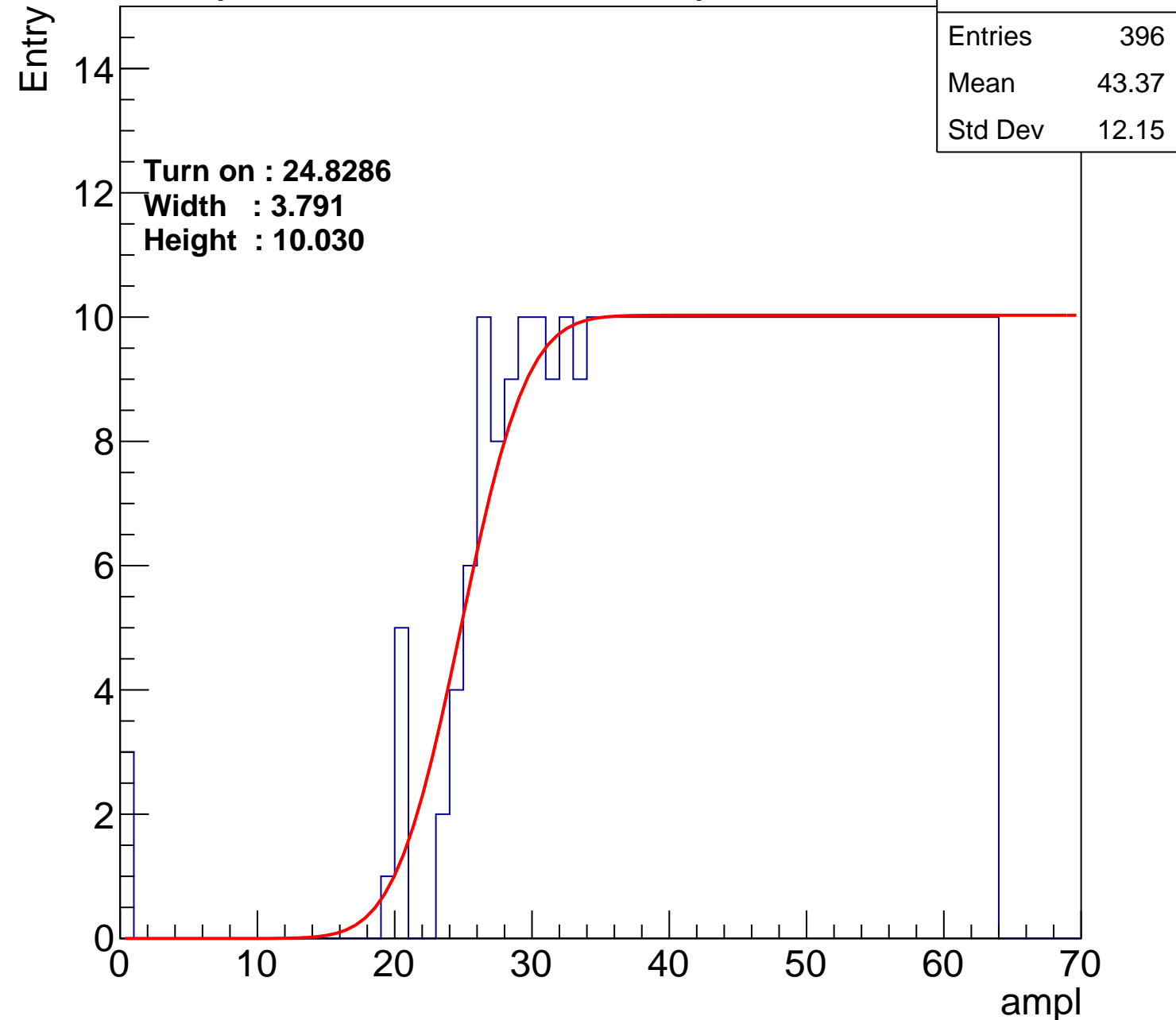
Width : 3.791

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.68
Std Dev	12.16

Turn on : 26.7285

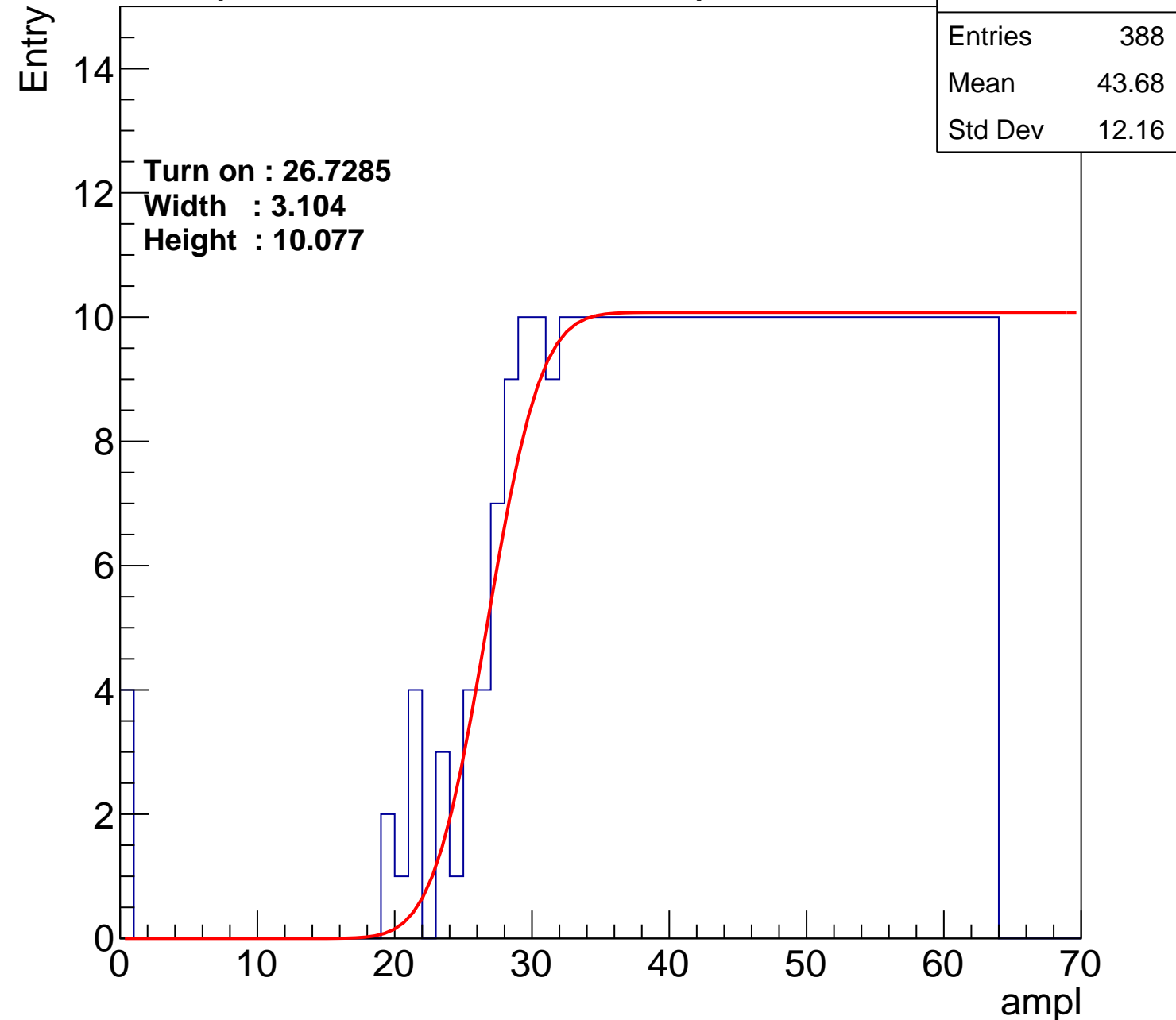
Width : 3.104

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch120

calib_packv5_042523_0143.root, FC#7, port C2

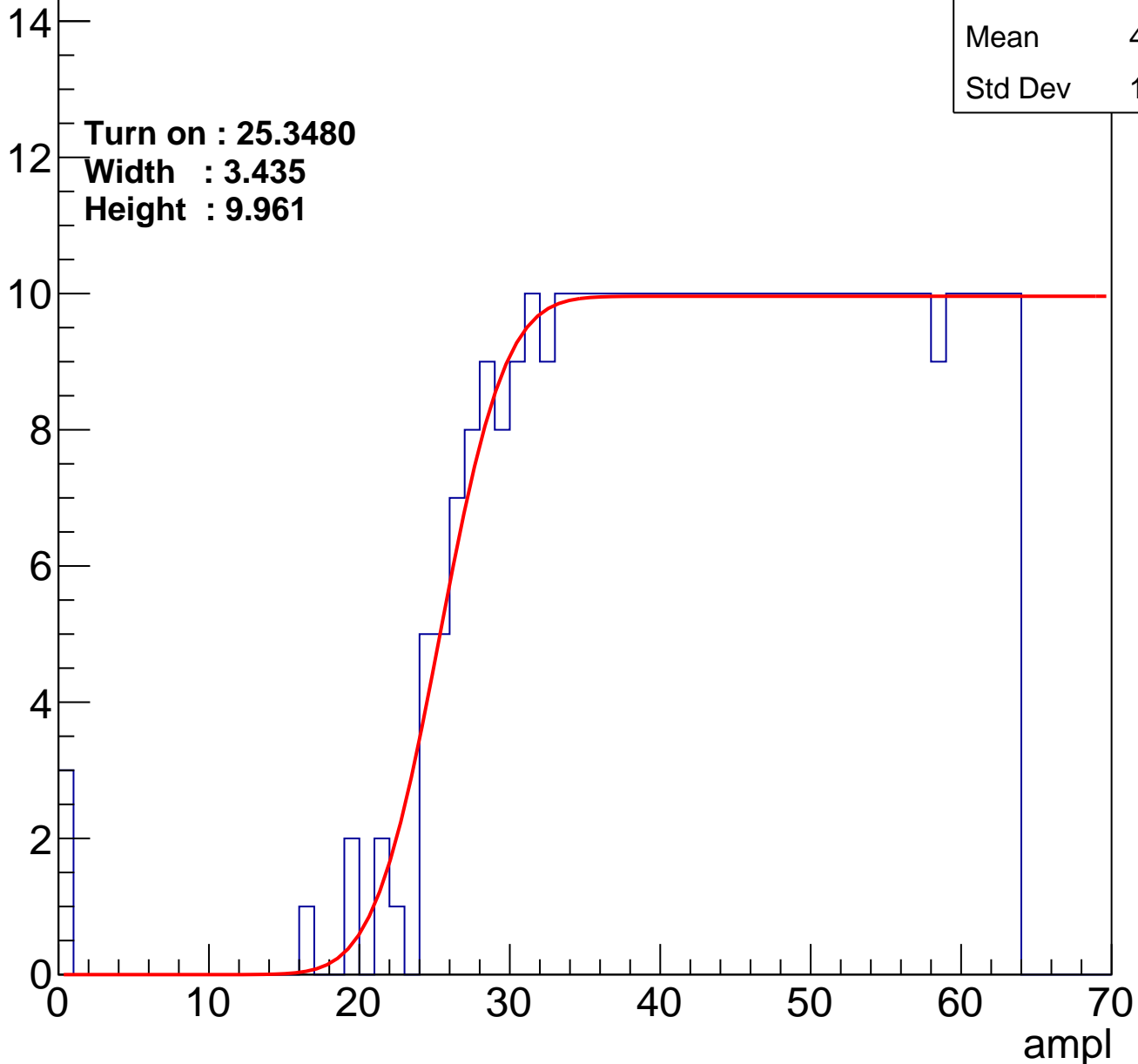
Entries	388
Mean	43.65
Std Dev	12.03

Turn on : 25.3480

Width : 3.435

Height : 9.961

Entry



B1L103S, U23-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	404
Mean	42.86
Std Dev	12.7

Turn on : 24.3150

Width : 2.341

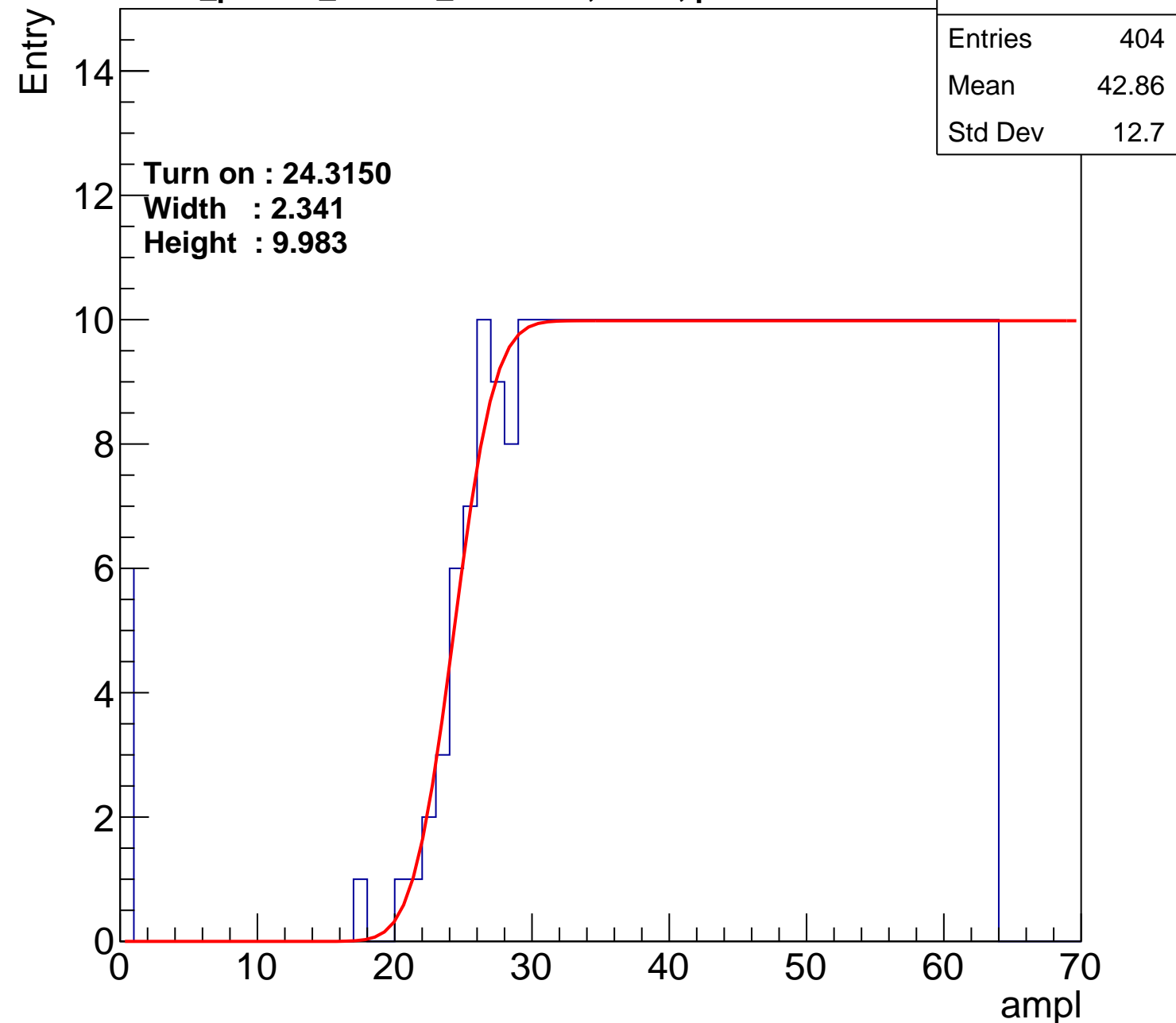
Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U23-ch122

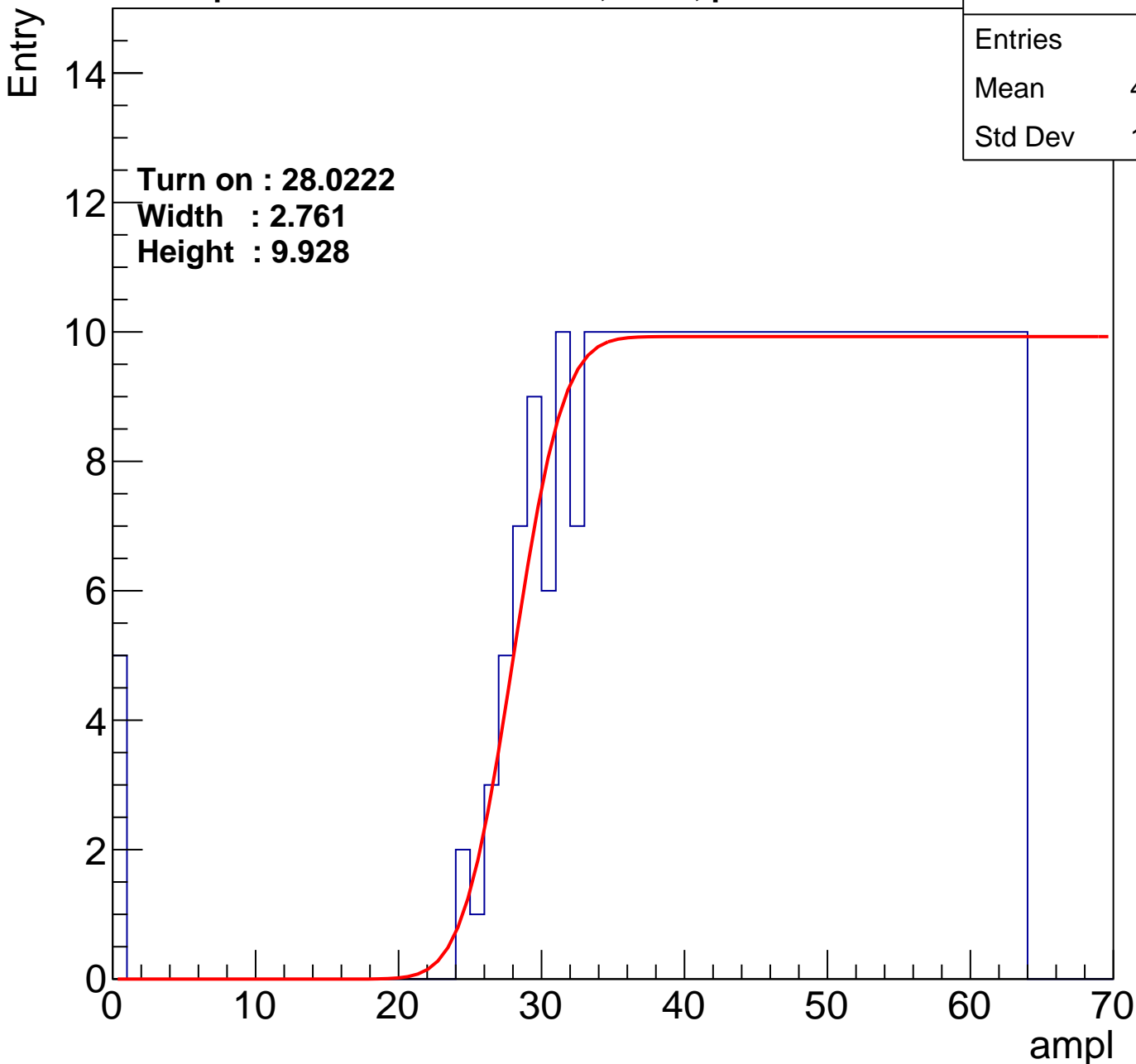
calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.76
Std Dev	11.76

Turn on : 28.0222

Width : 2.761

Height : 9.928



B1L103S, U23-ch123

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.31
Std Dev	11.37

Turn on : 29.5188

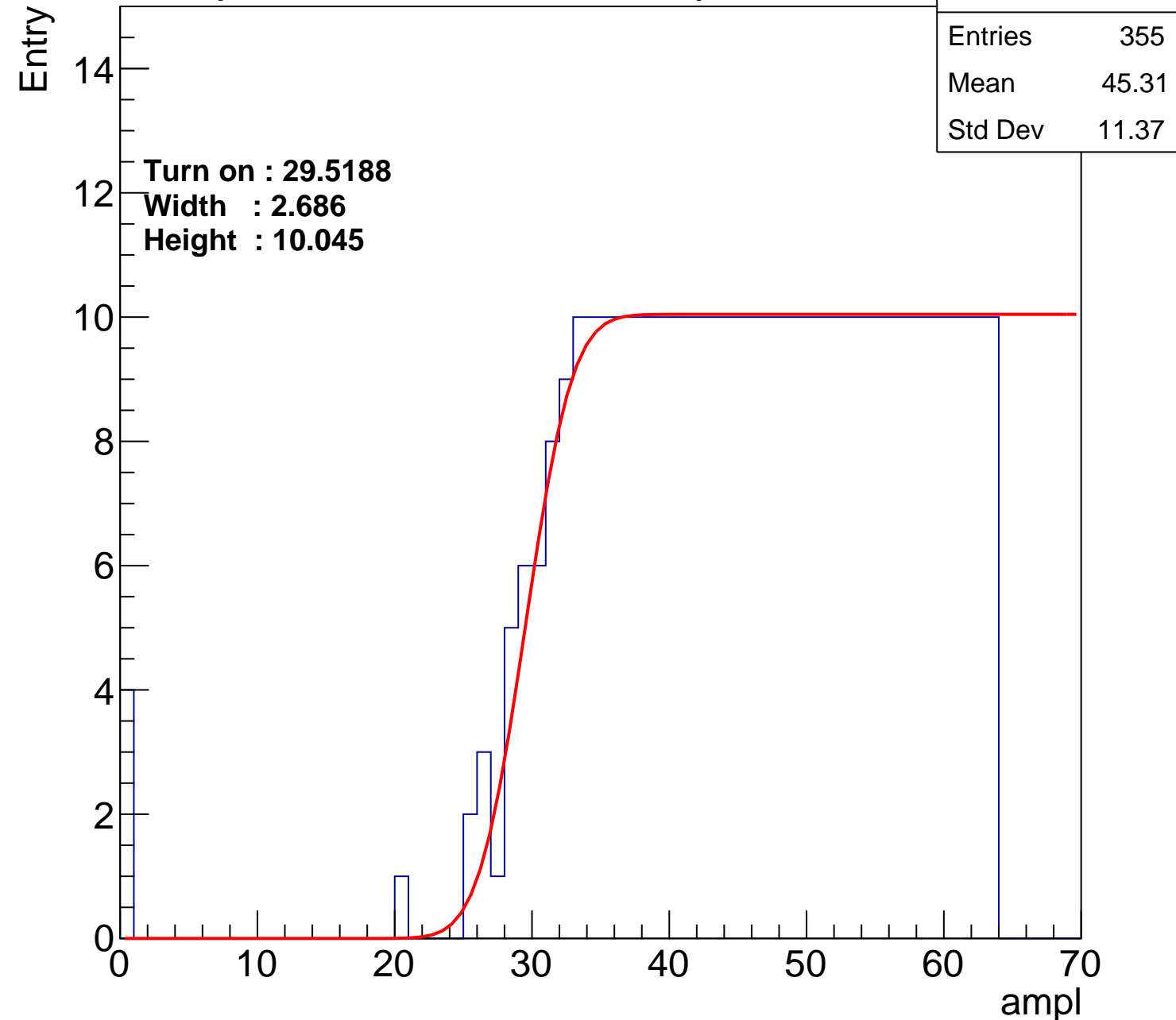
Width : 2.686

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U23-ch124

calib_packv5_042523_0143.root, FC#7, port C2

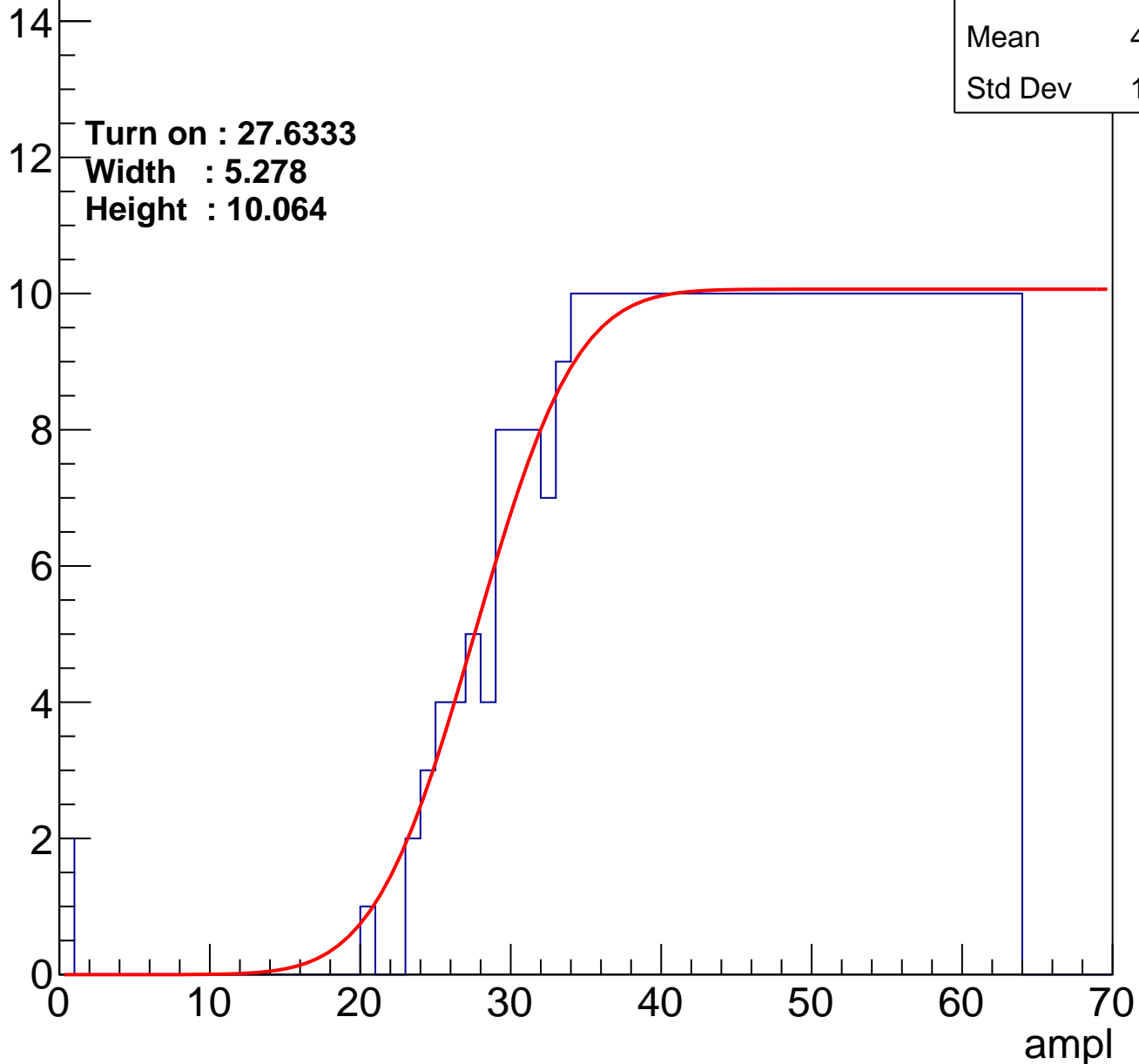
Entries	365
Mean	44.88
Std Dev	11.33

Turn on : 27.6333

Width : 5.278

Height : 10.064

Entry



B1L103S, U23-ch125

calib_packv5_042523_0143.root, FC#7, port C2

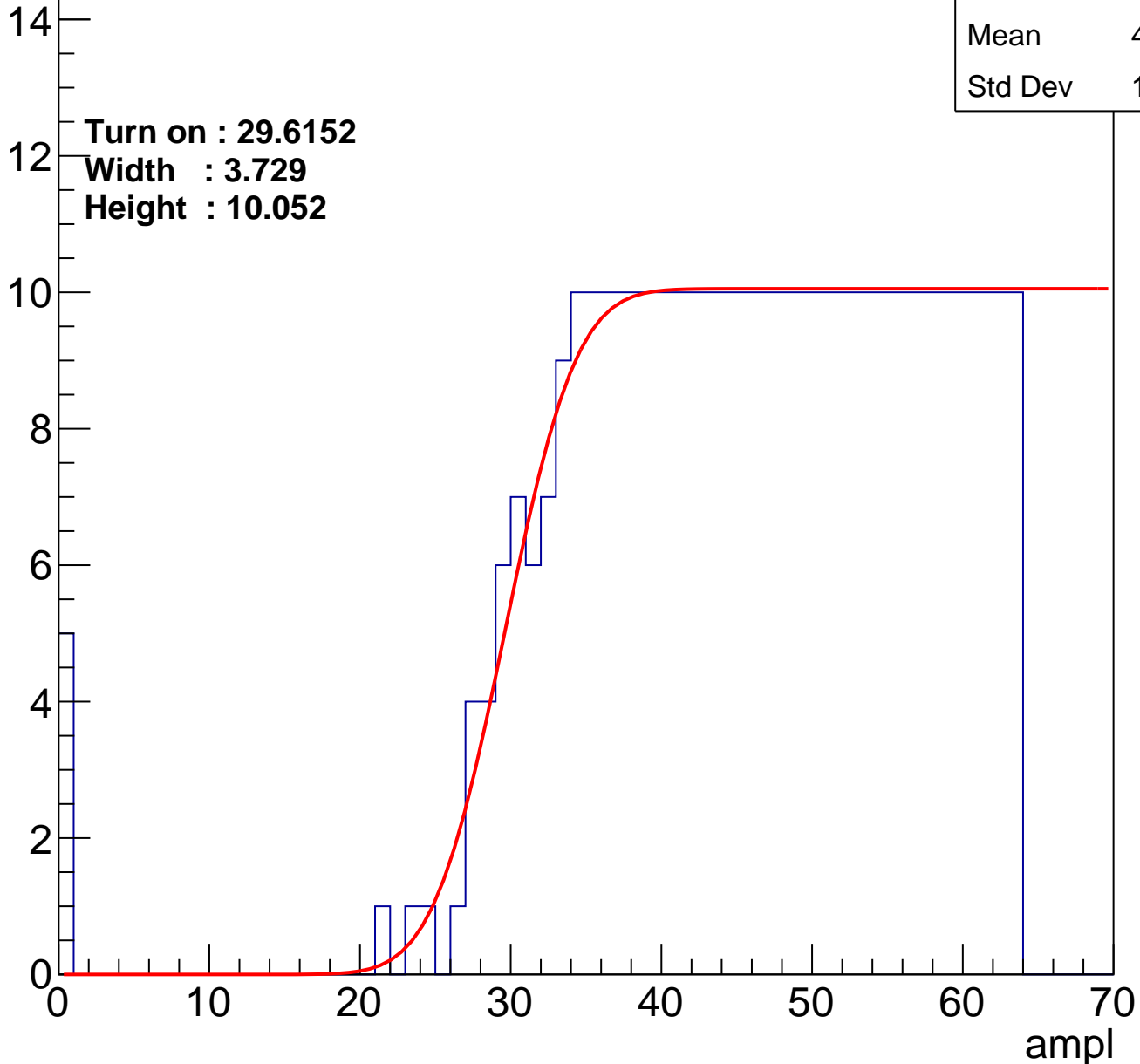
Entries	352
Mean	45.33
Std Dev	11.59

Turn on : 29.6152

Width : 3.729

Height : 10.052

Entry



B1L103S, U23-ch126

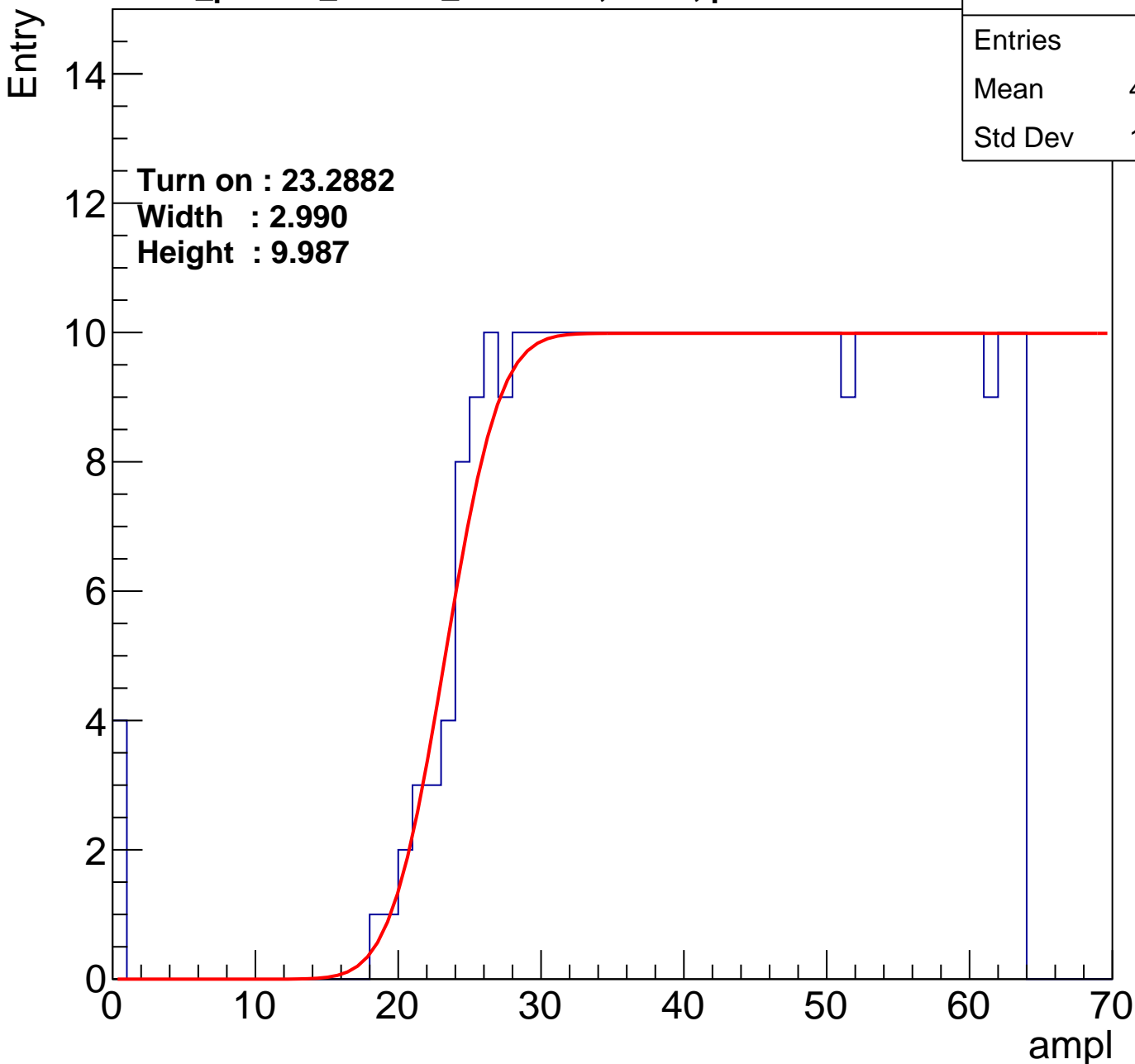
calib_packv5_042523_0143.root, FC#7, port C2

Entries	412
Mean	42.44
Std Dev	12.62

Turn on : 23.2882

Width : 2.990

Height : 9.987



B1L103S, U23-ch127

calib_packv5_042523_0143.root, FC#7, port C2

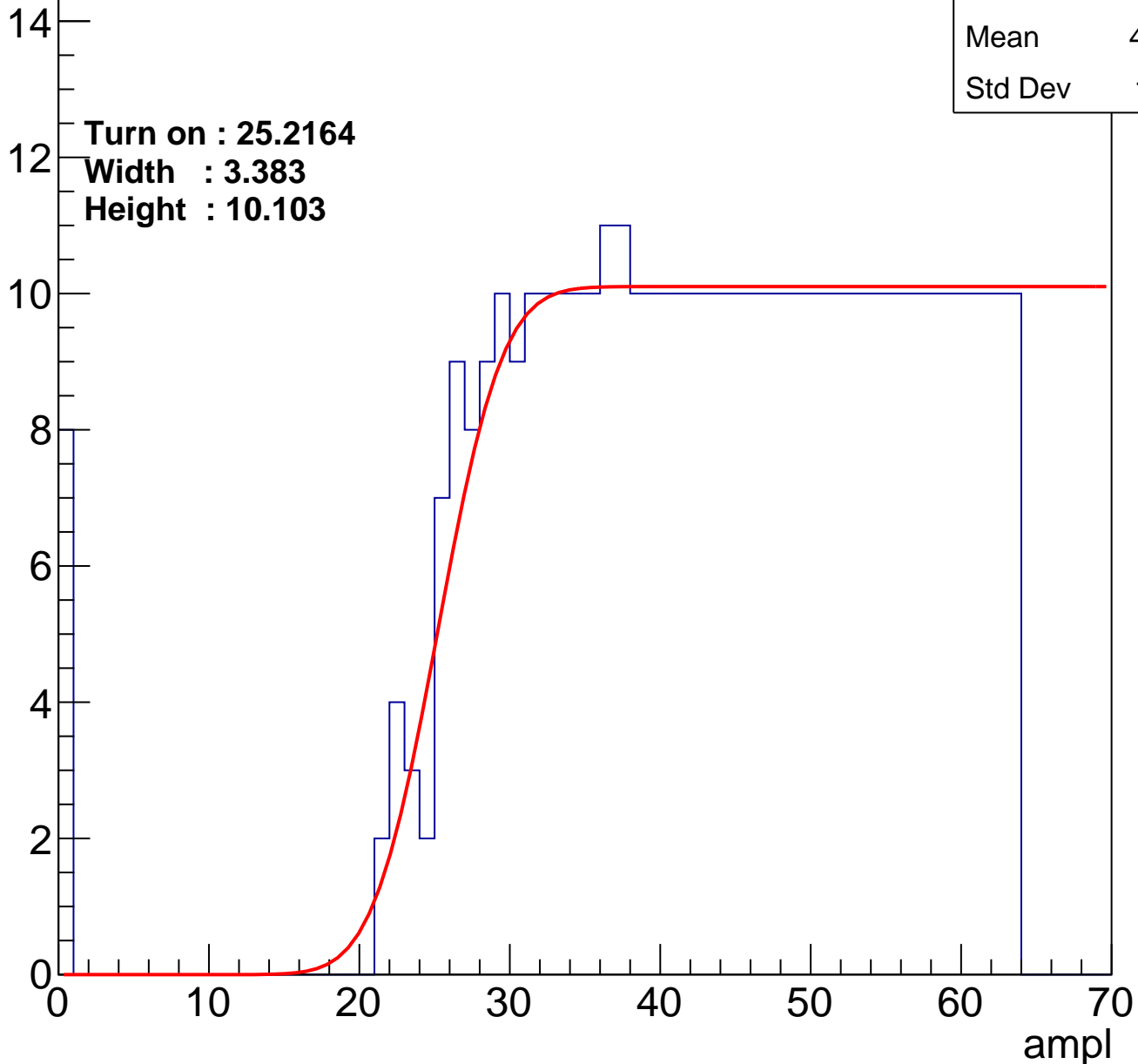
Entries	403
Mean	42.85
Std Dev	12.91

Turn on : 25.2164

Width : 3.383

Height : 10.103

Entry



B1L103S, U23-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	42.85
Std Dev	12.91

Turn on : 25.2164

Width : 3.383

Height : 10.103

Entry

