



# B1L002S, U18-ch0, adc0

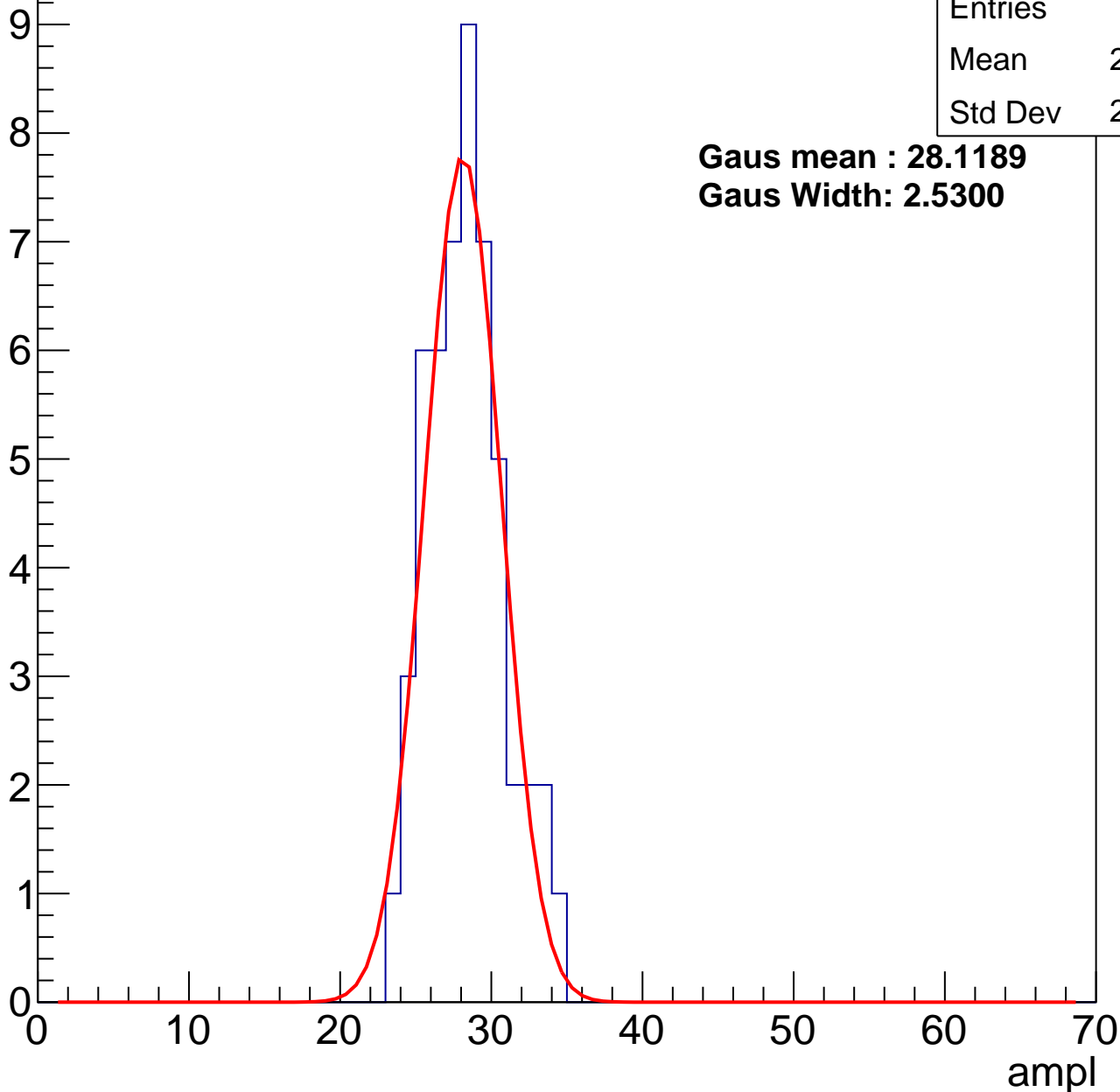
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	27.86
Std Dev	2.505

**Gaus mean : 28.1189**

**Gaus Width: 2.5300**



# B1L002S, U18-ch0, adc1

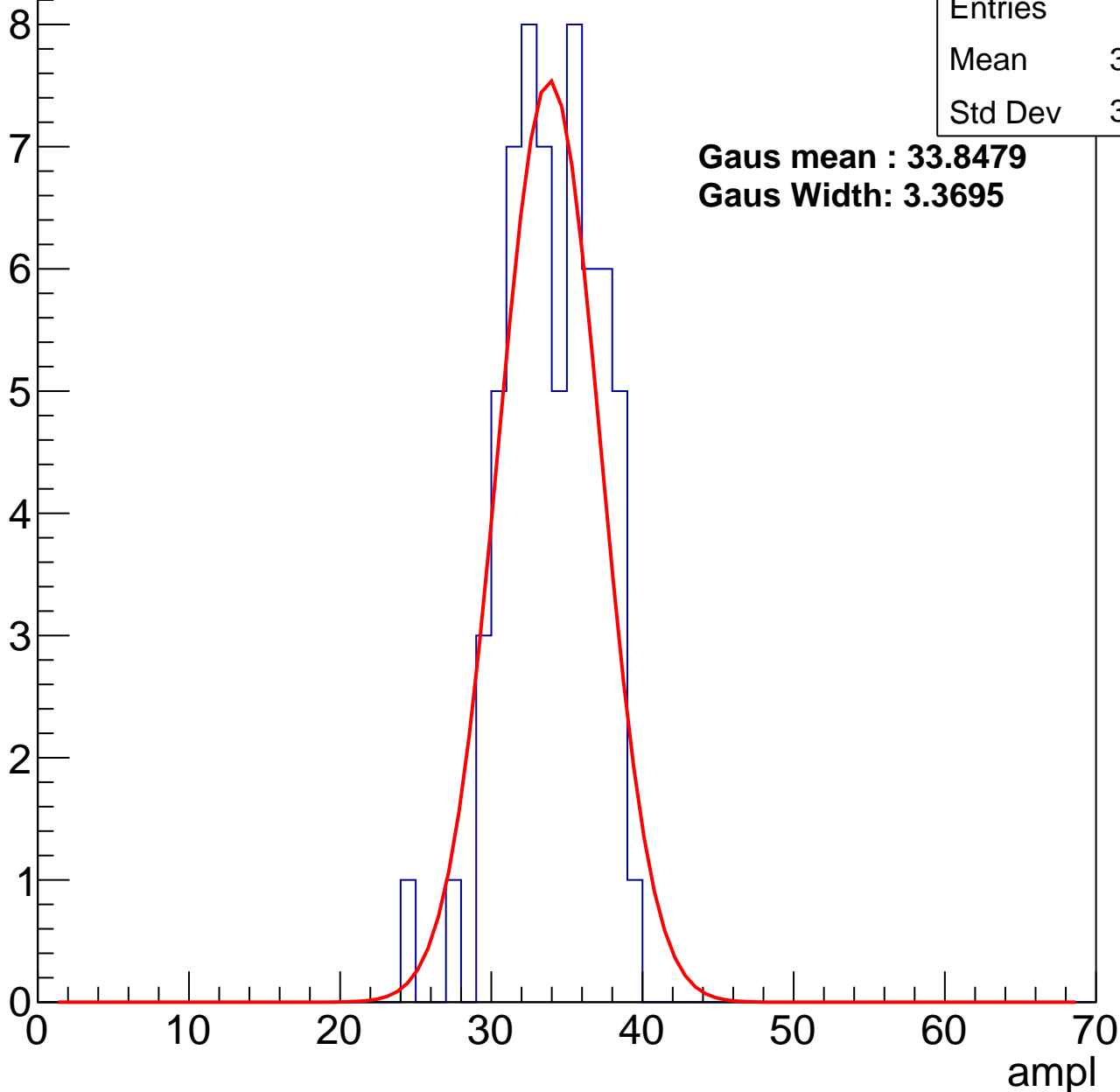
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.48
Std Dev	3.033

**Gaus mean : 33.8479**

**Gaus Width: 3.3695**



# B1L002S, U18-ch0, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.91
Std Dev	3.026

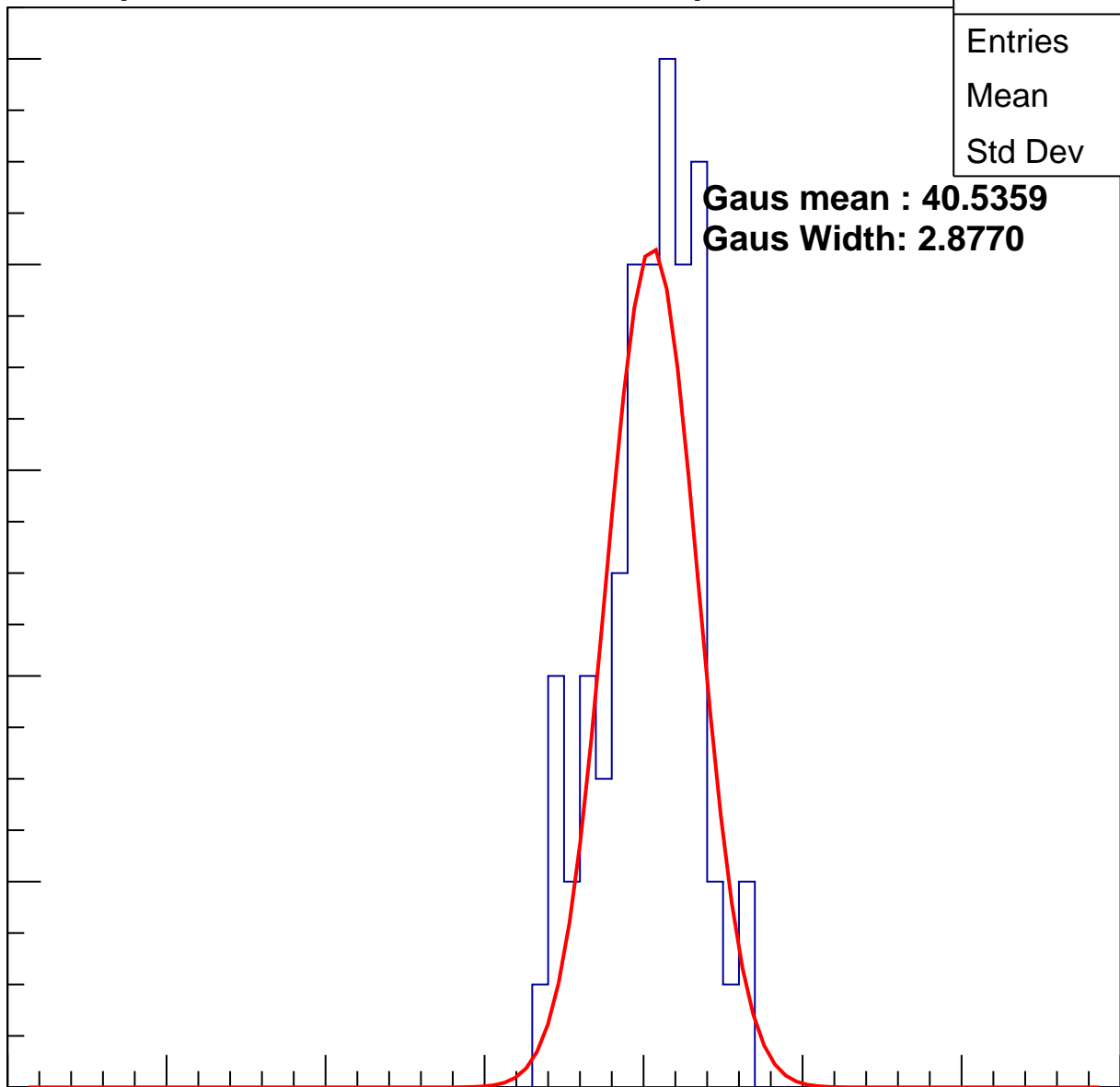
**Gaus mean : 40.5359**

**Gaus Width: 2.8770**

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

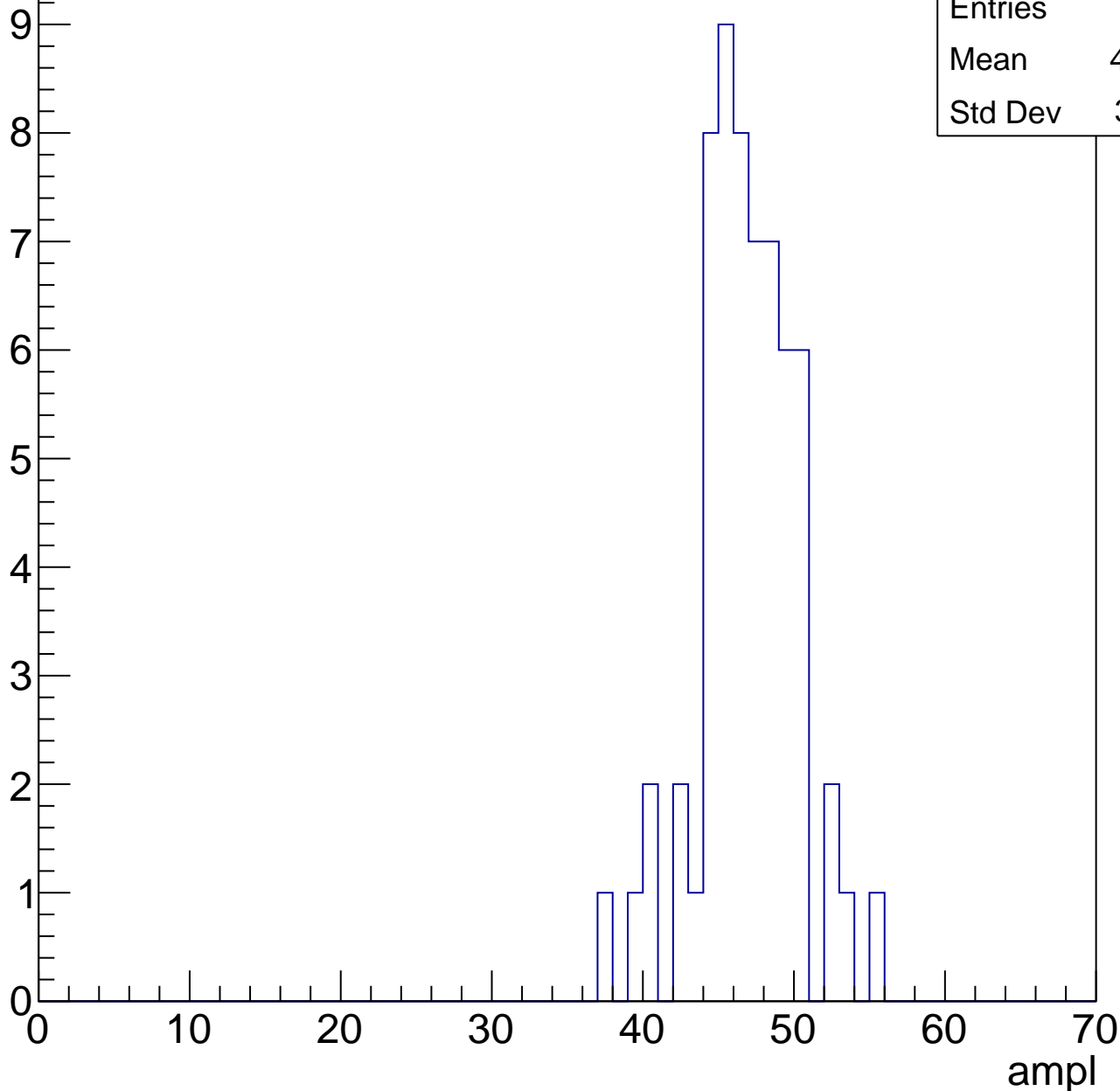
ampl



# B1L002S, U18-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



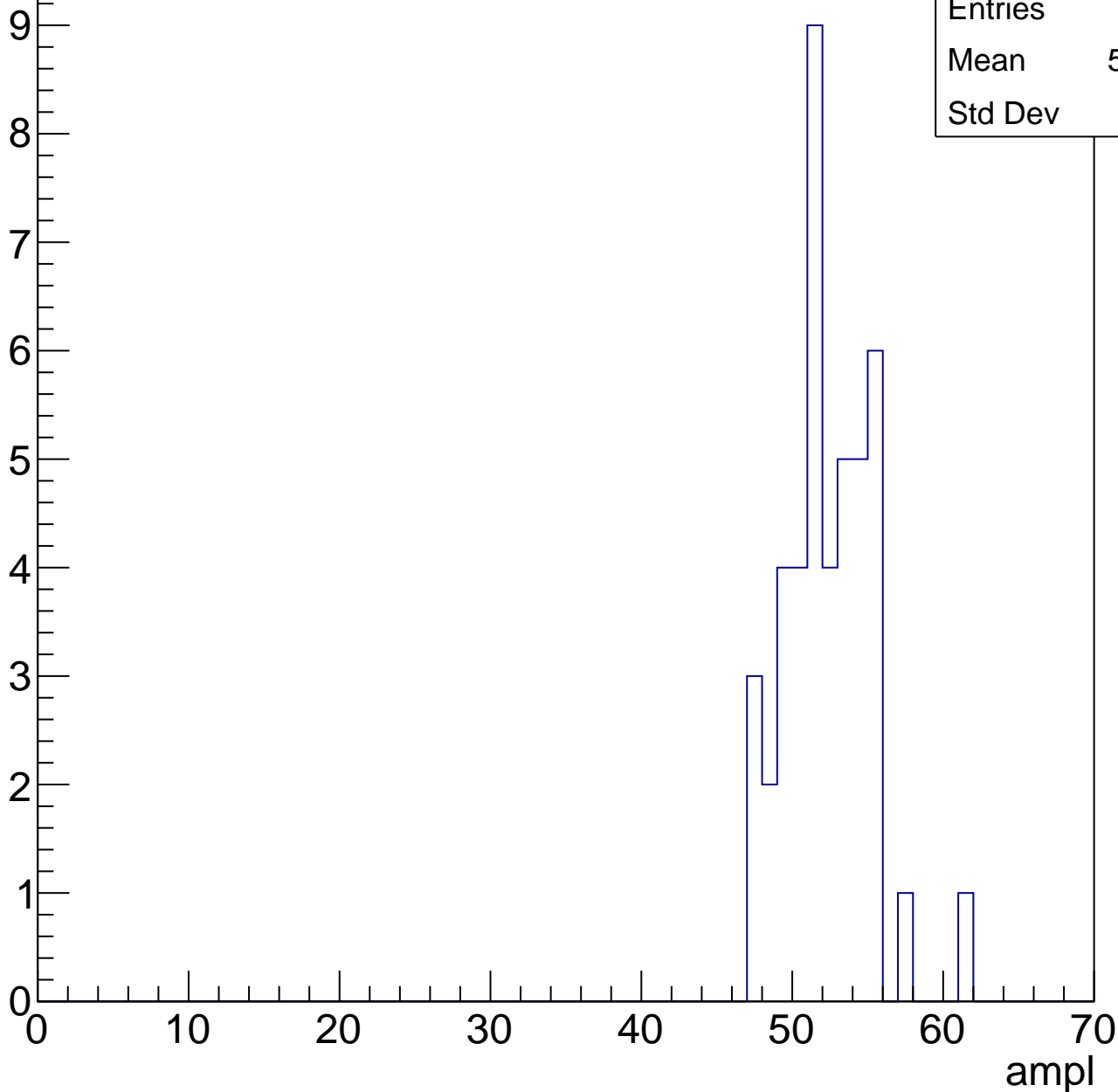
Entries	62
Mean	46.44
Std Dev	3.261

# B1L002S, U18-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.89
Std Dev	2.83



# B1L002S, U18-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	57.41
Std Dev	3.205

Entry

10

8

6

4

2

0

0

10

20

30

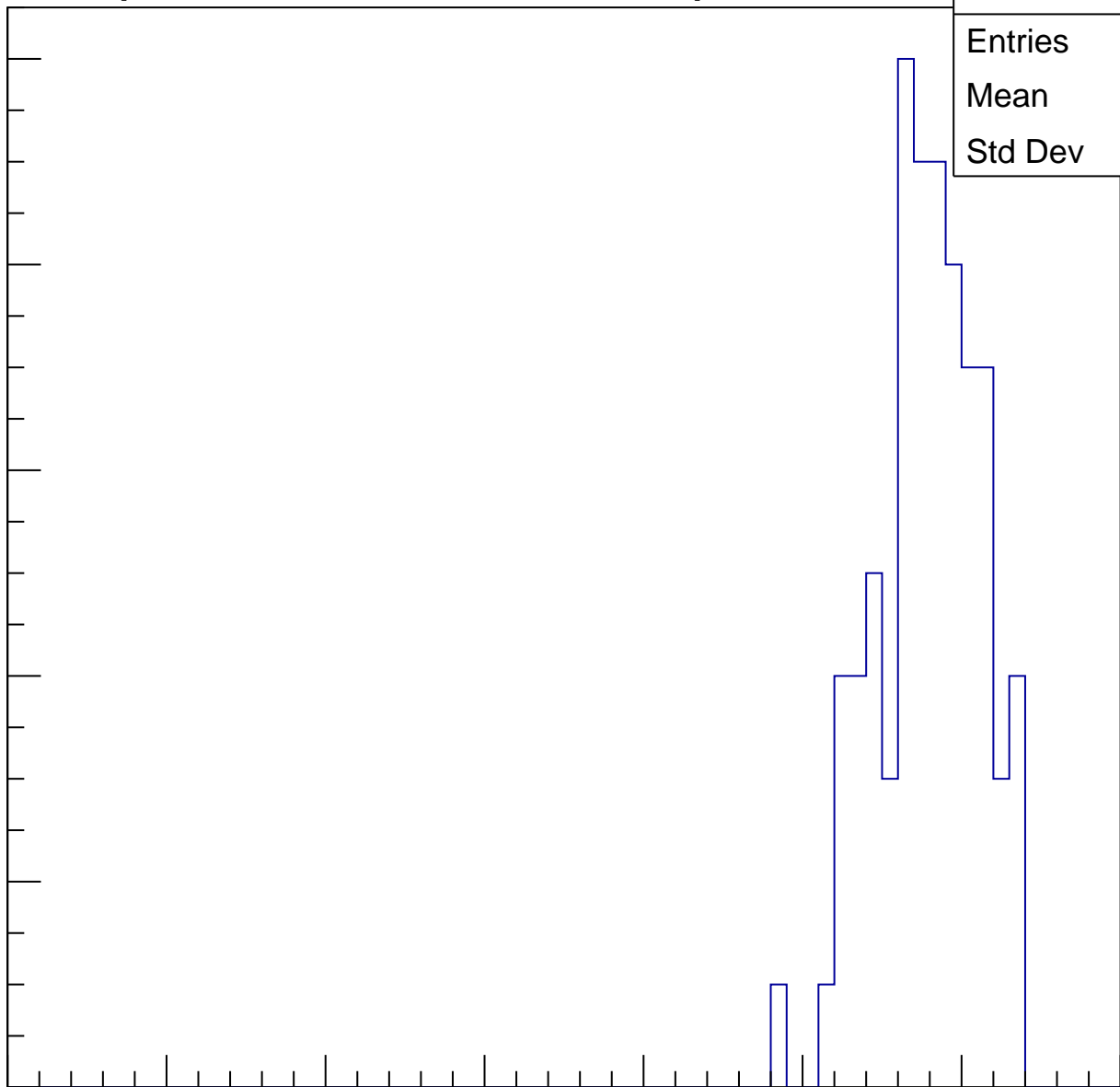
40

50

60

70

ampl

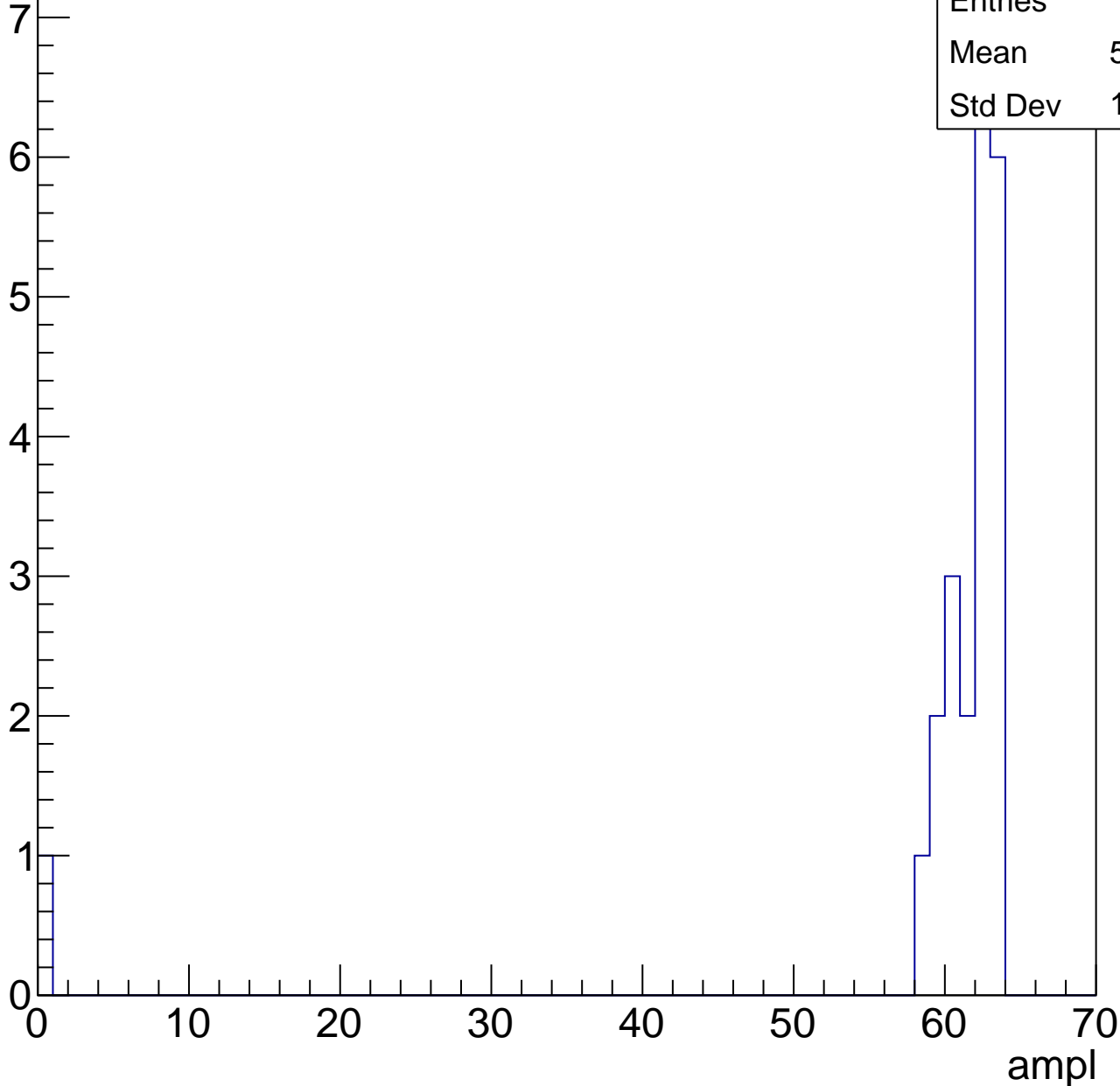


# B1L002S, U18-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.64
Std Dev	12.88

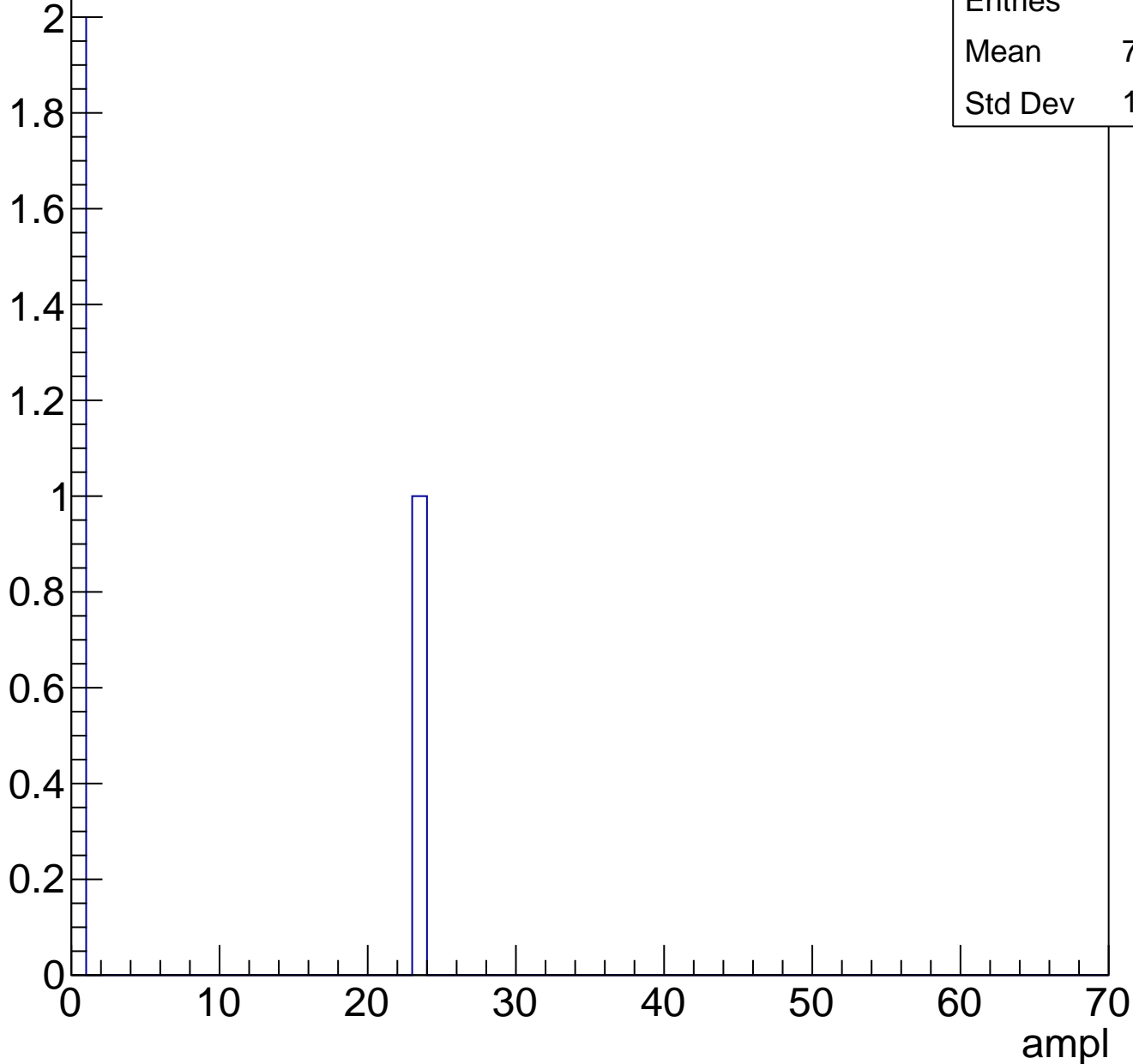




# B1L002S, U18-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch1, adc0

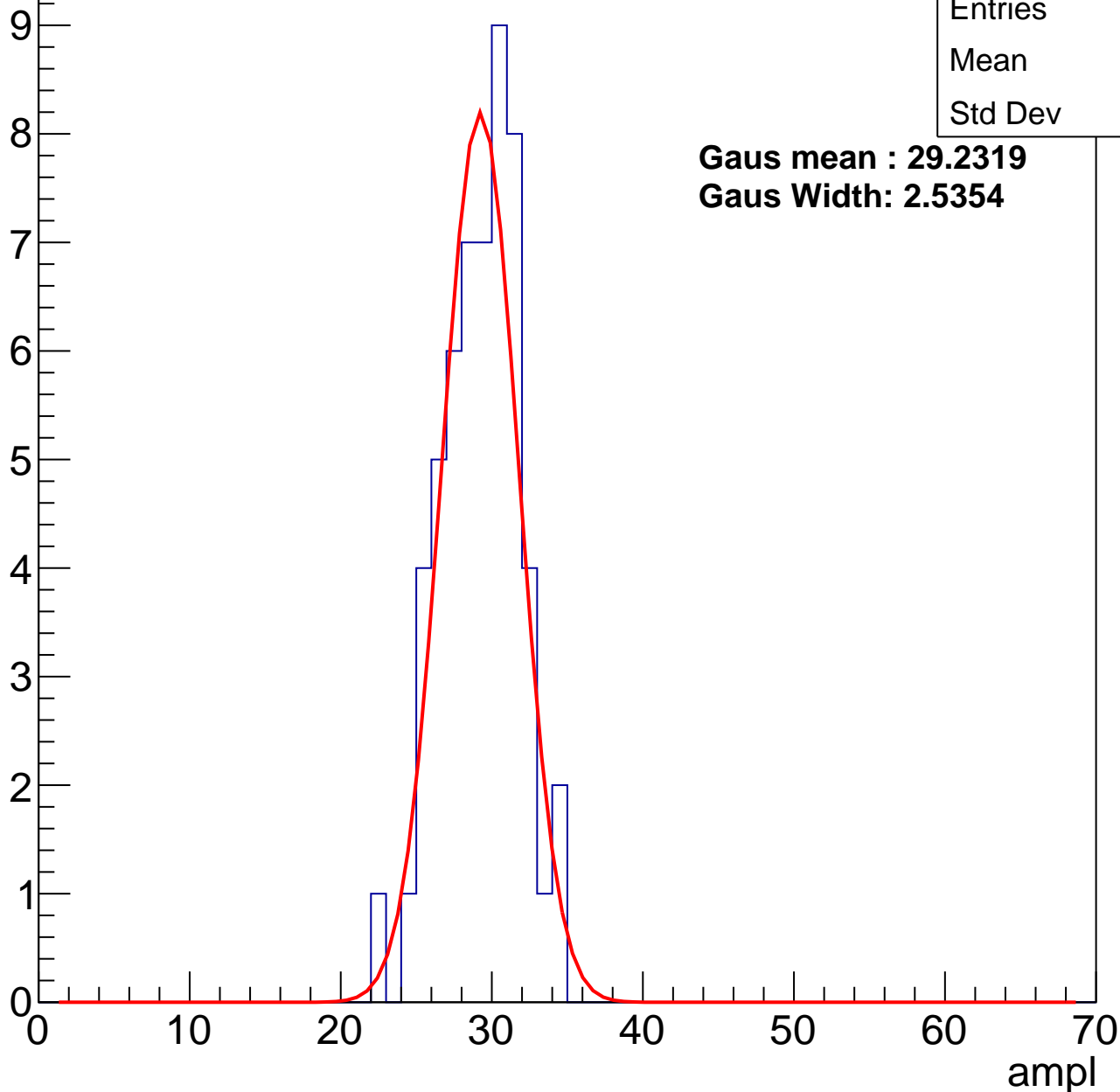
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	28.8
Std Dev	2.54

**Gaus mean : 29.2319**

**Gaus Width: 2.5354**



# B1L002S, U18-ch1, adc1

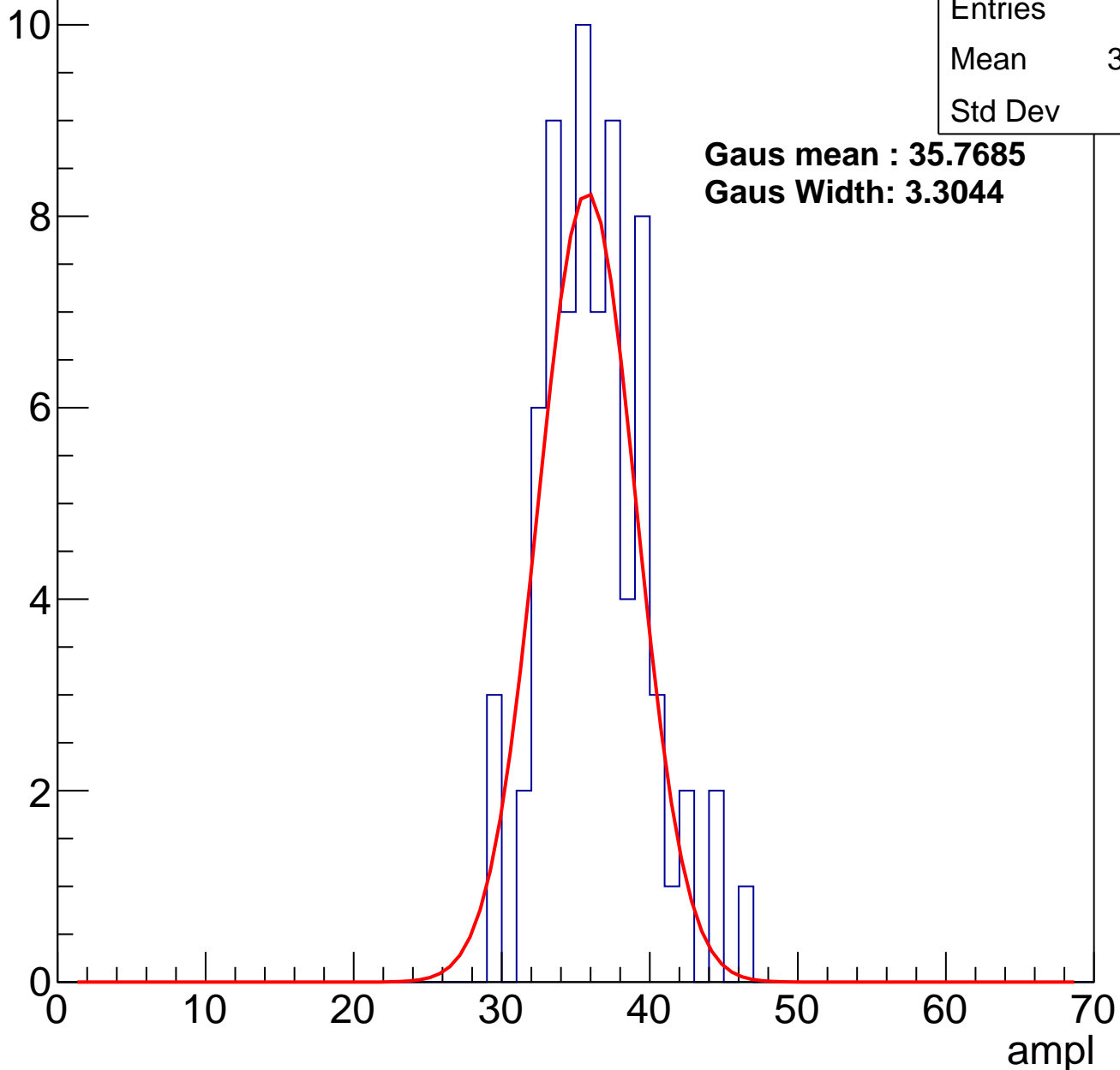
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	35.86
Std Dev	3.45

**Gaus mean : 35.7685**

**Gaus Width: 3.3044**

Entry



# B1L002S, U18-ch1, adc2

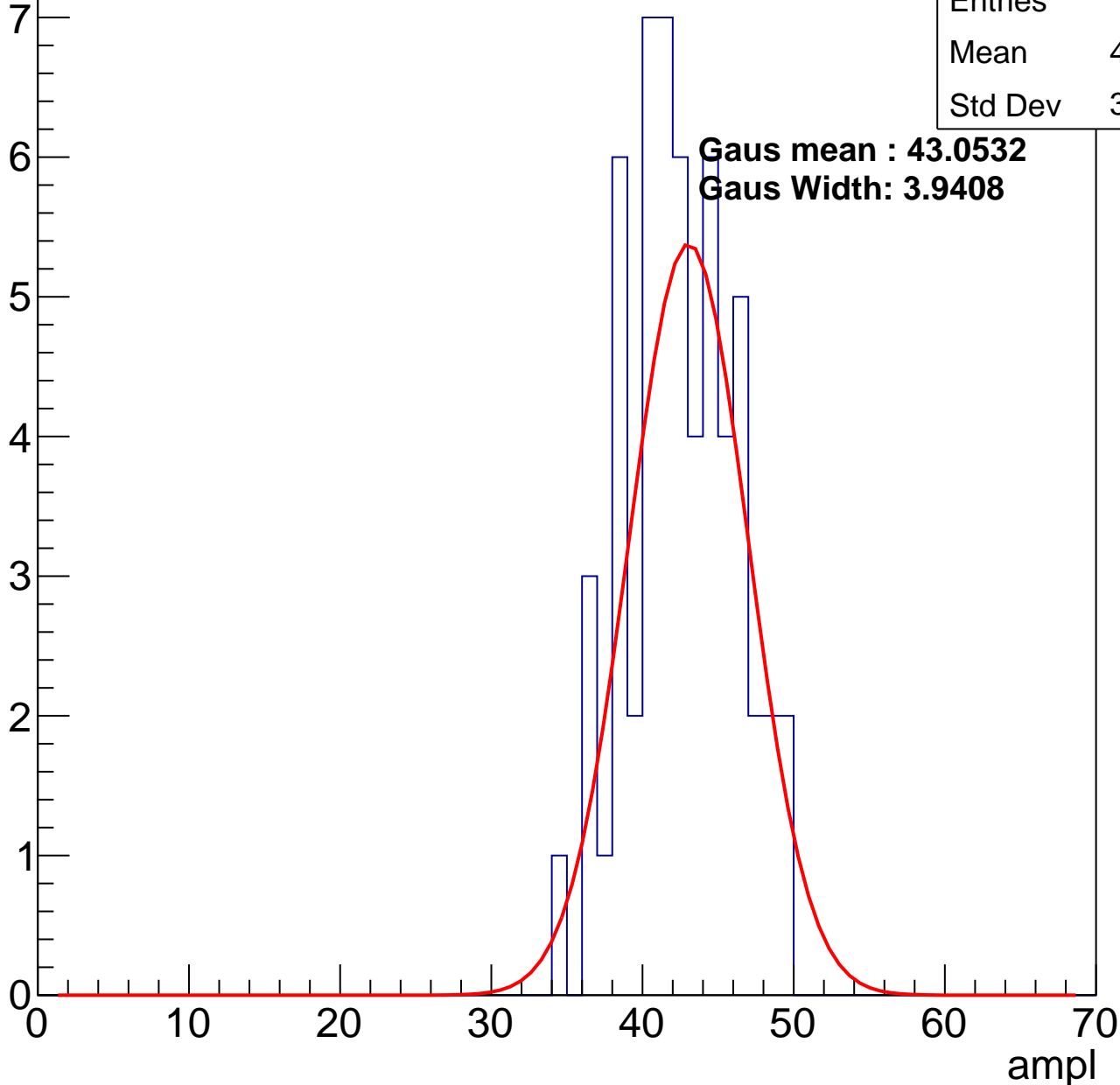
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	42.03
Std Dev	3.499

**Gaus mean : 43.0532**

**Gaus Width: 3.9408**

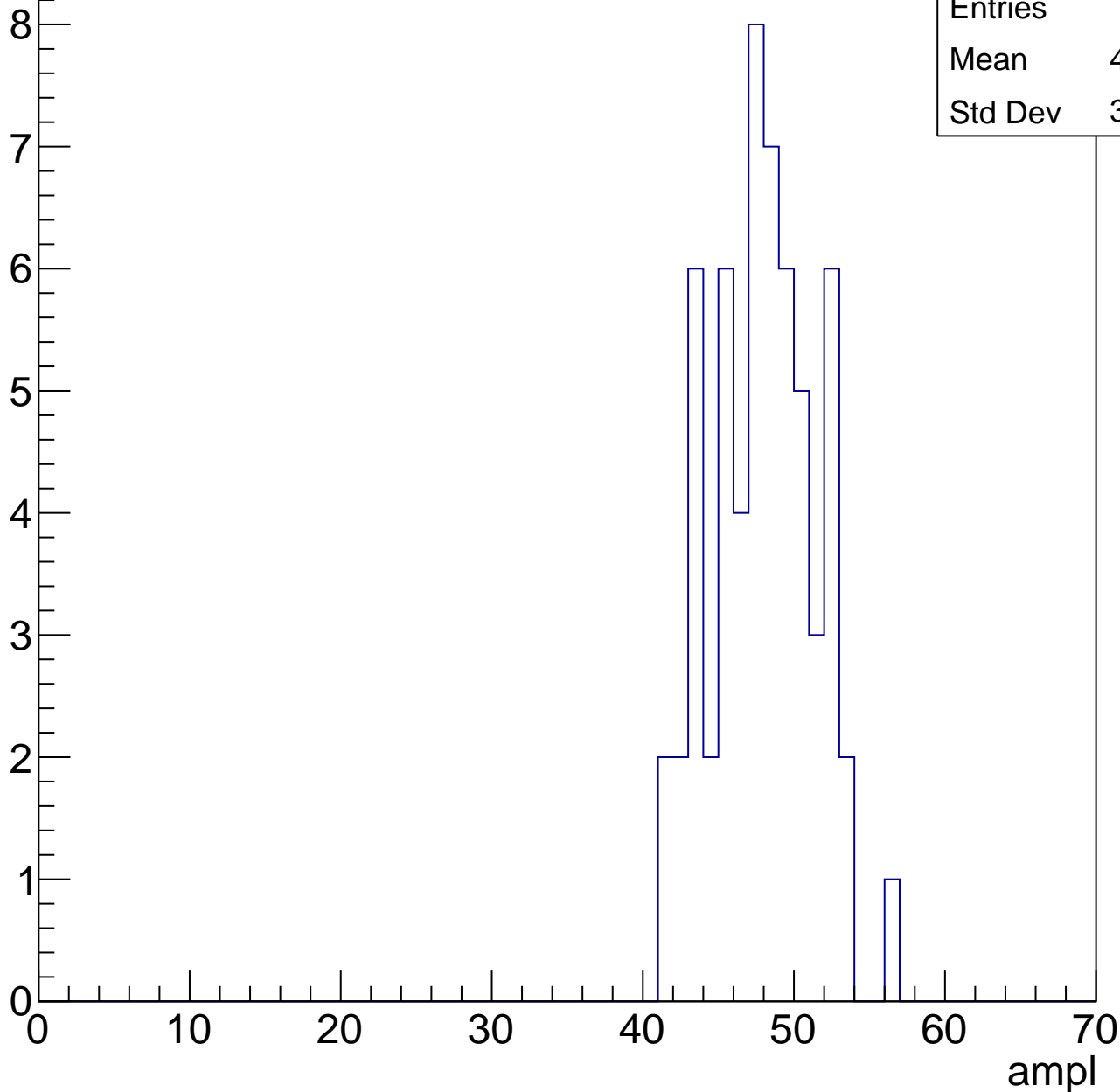


# B1L002S, U18-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	47.48
Std Dev	3.359



# B1L002S, U18-ch1, adc4

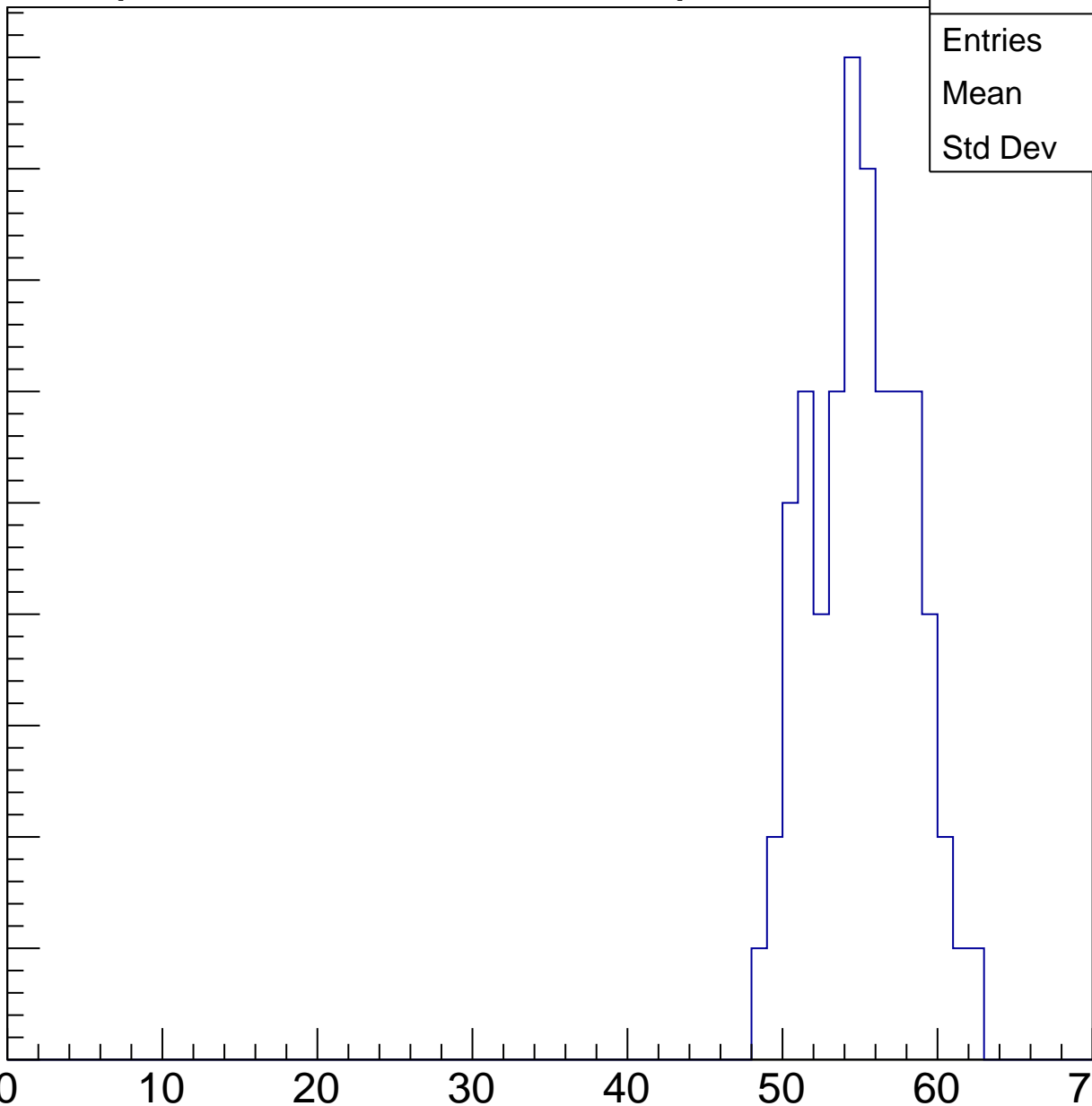
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	67
Mean	54.61
Std Dev	3.195

ampl

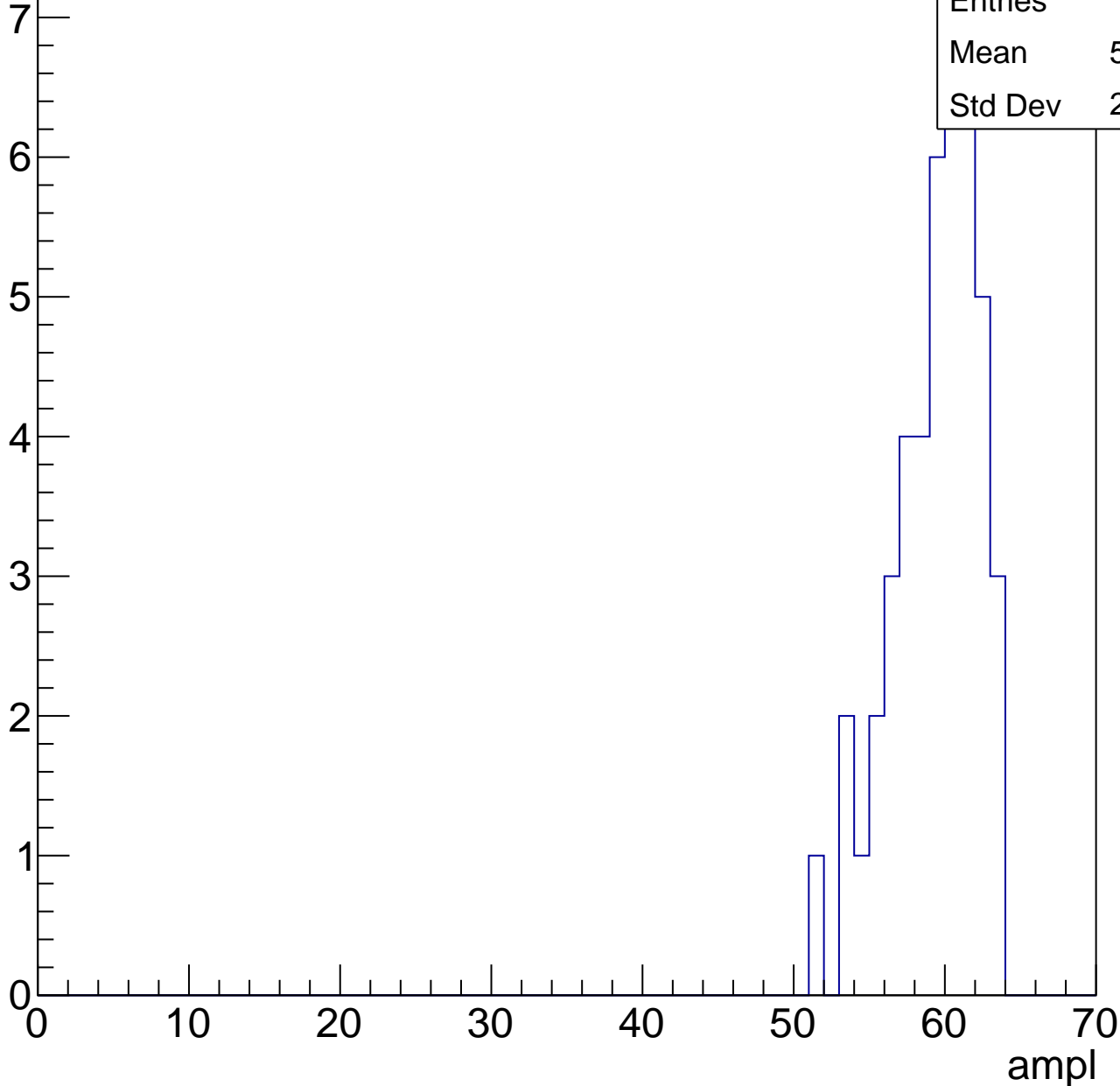


# B1L002S, U18-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	58.87
Std Dev	2.857

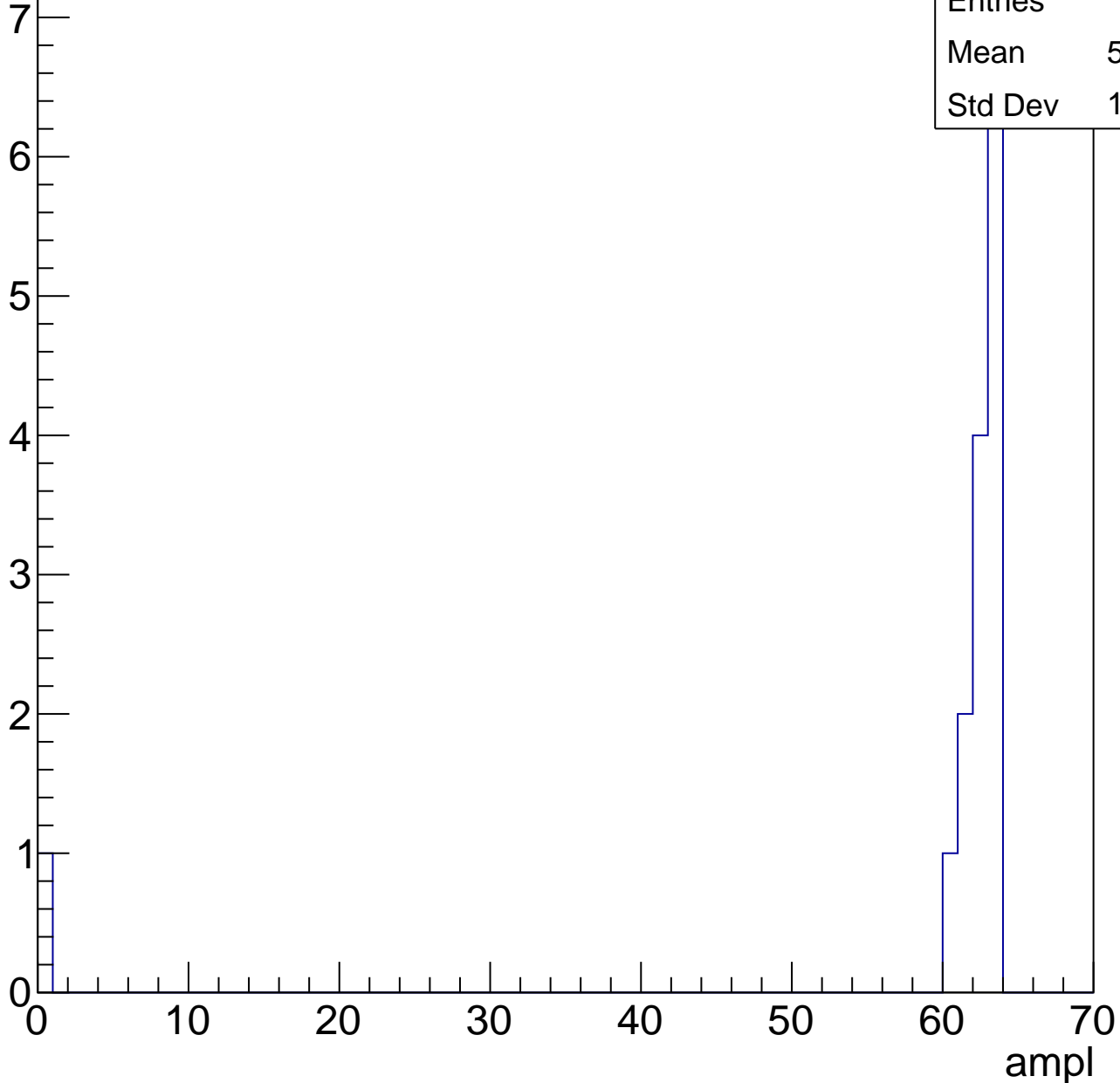


# B1L002S, U18-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	58.07
Std Dev	15.55





# B1L002S, U18-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U18-ch2, adc0

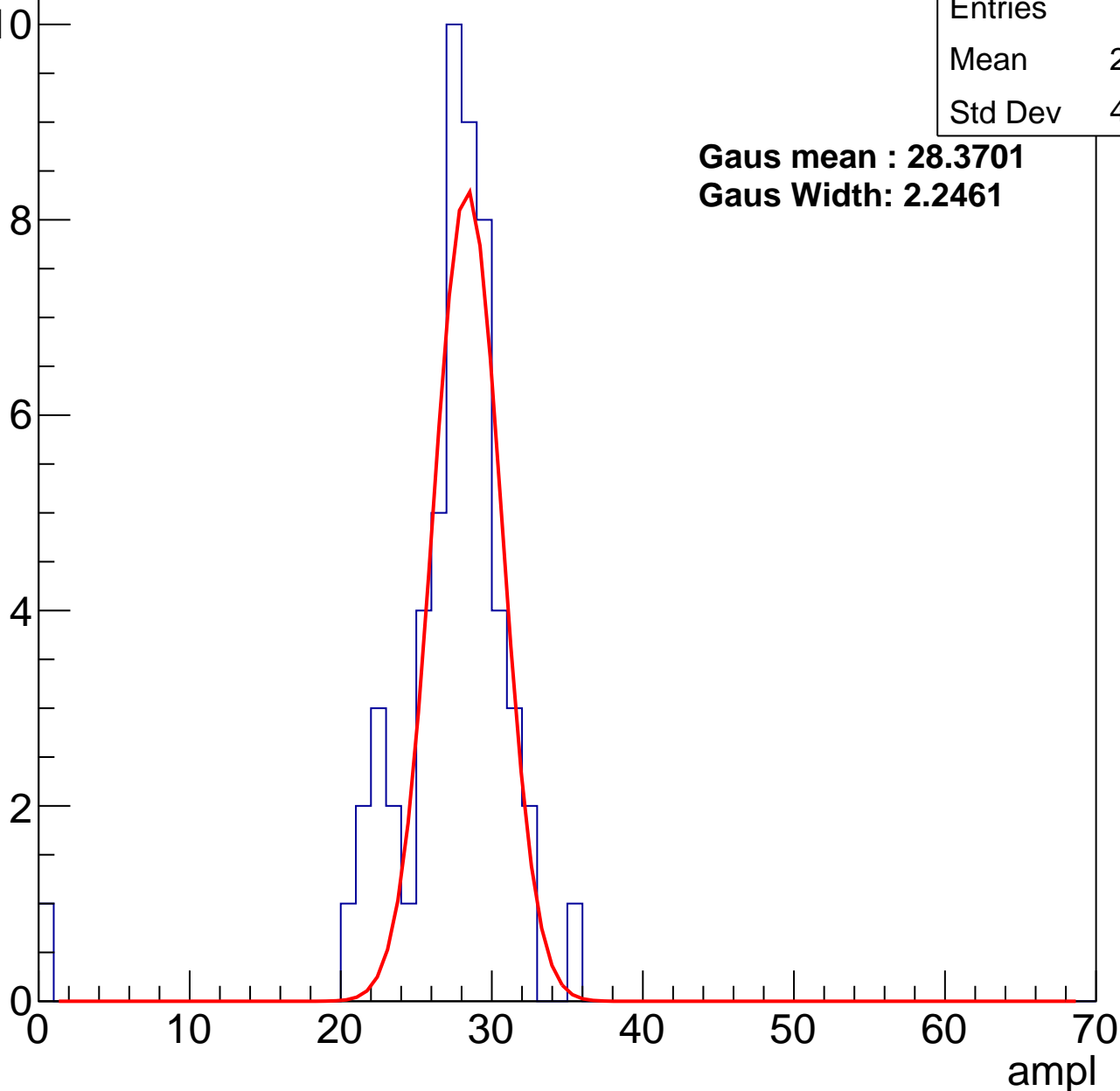
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	26.68
Std Dev	4.668

**Gaus mean : 28.3701**

**Gaus Width: 2.2461**



# B1L002S, U18-ch2, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	33.64
Std Dev	3.306

**Gaus mean : 34.1466**

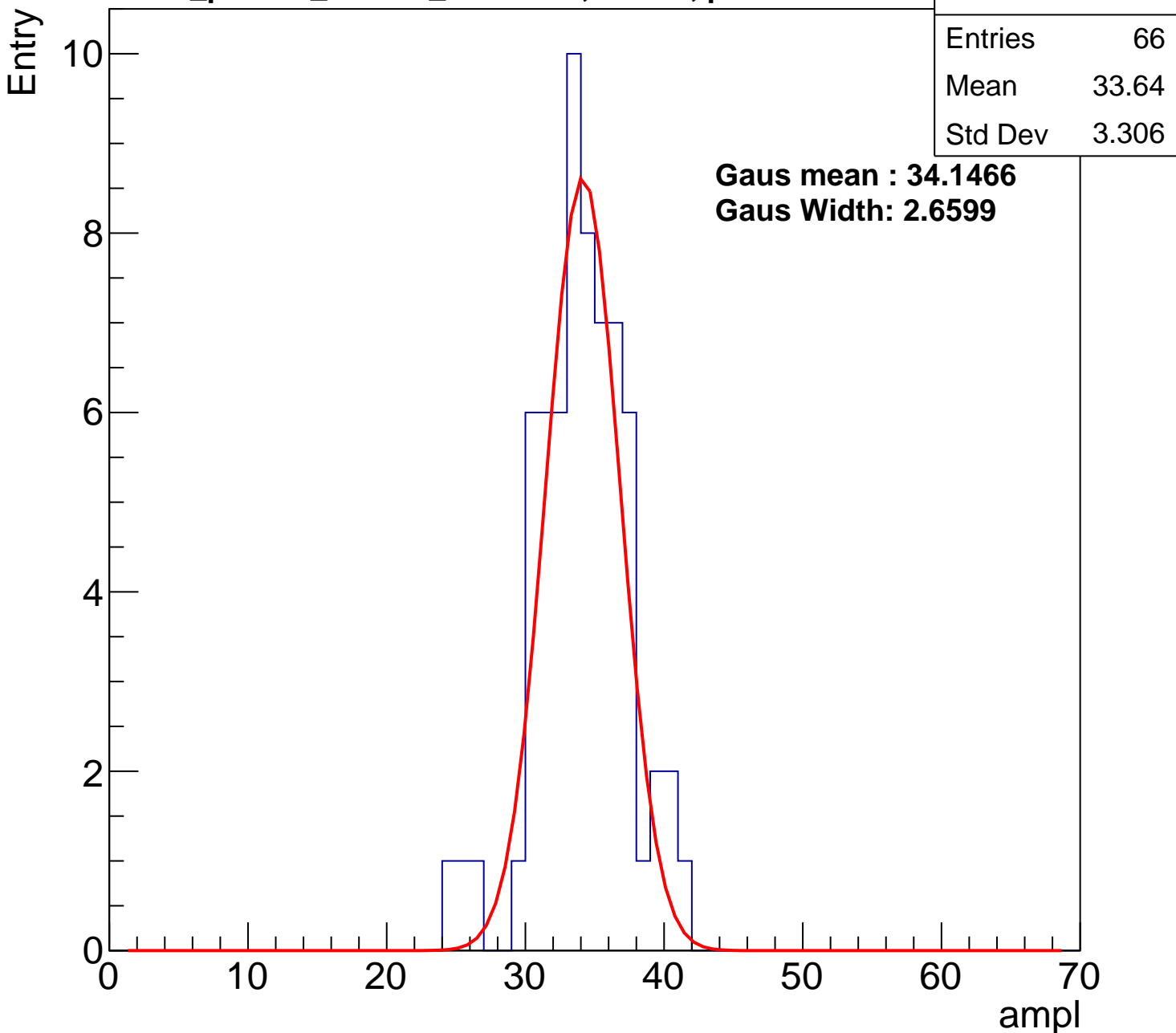
**Gaus Width: 2.6599**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch2, adc2

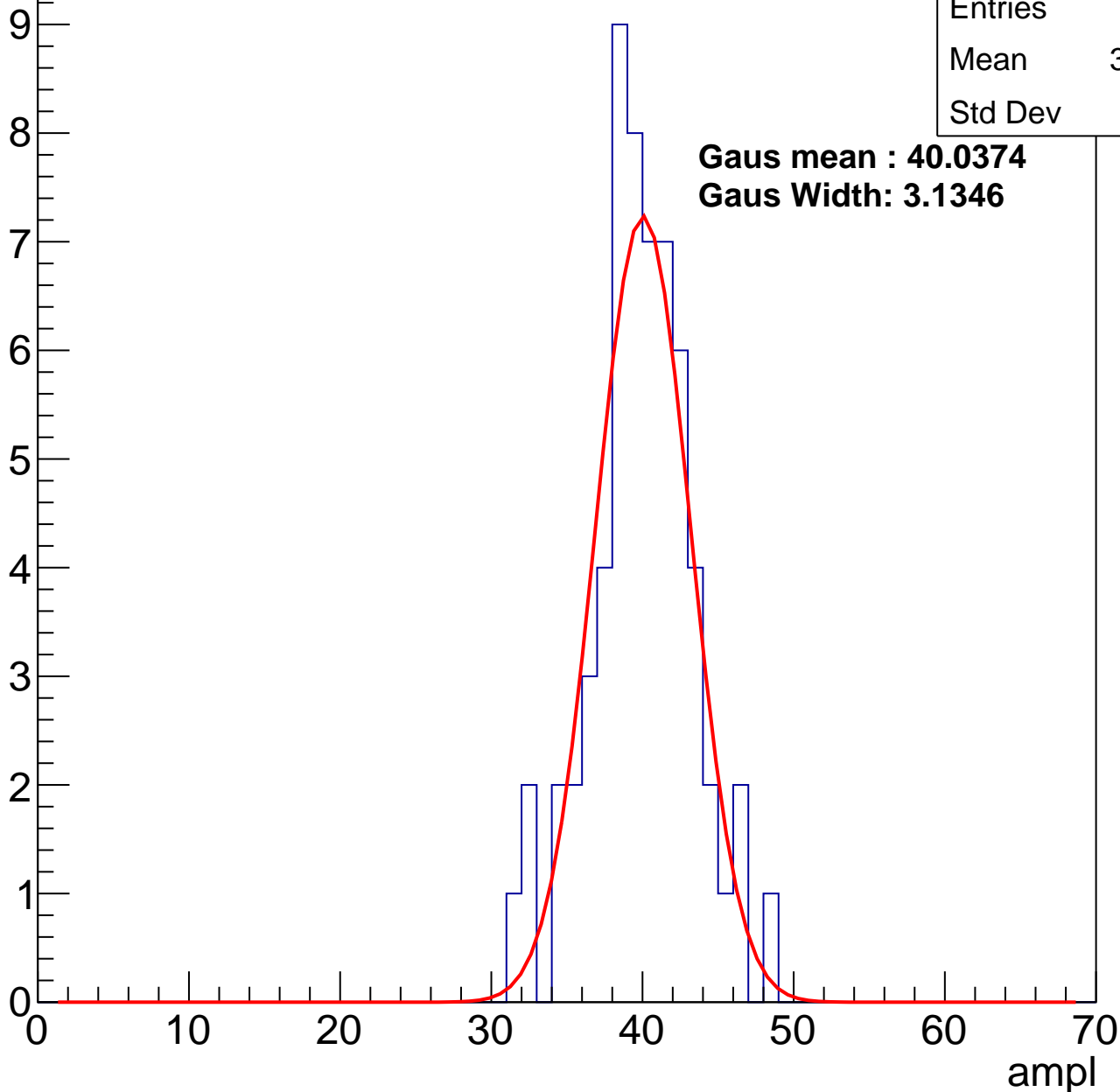
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.46
Std Dev	3.39

**Gaus mean : 40.0374**

**Gaus Width: 3.1346**

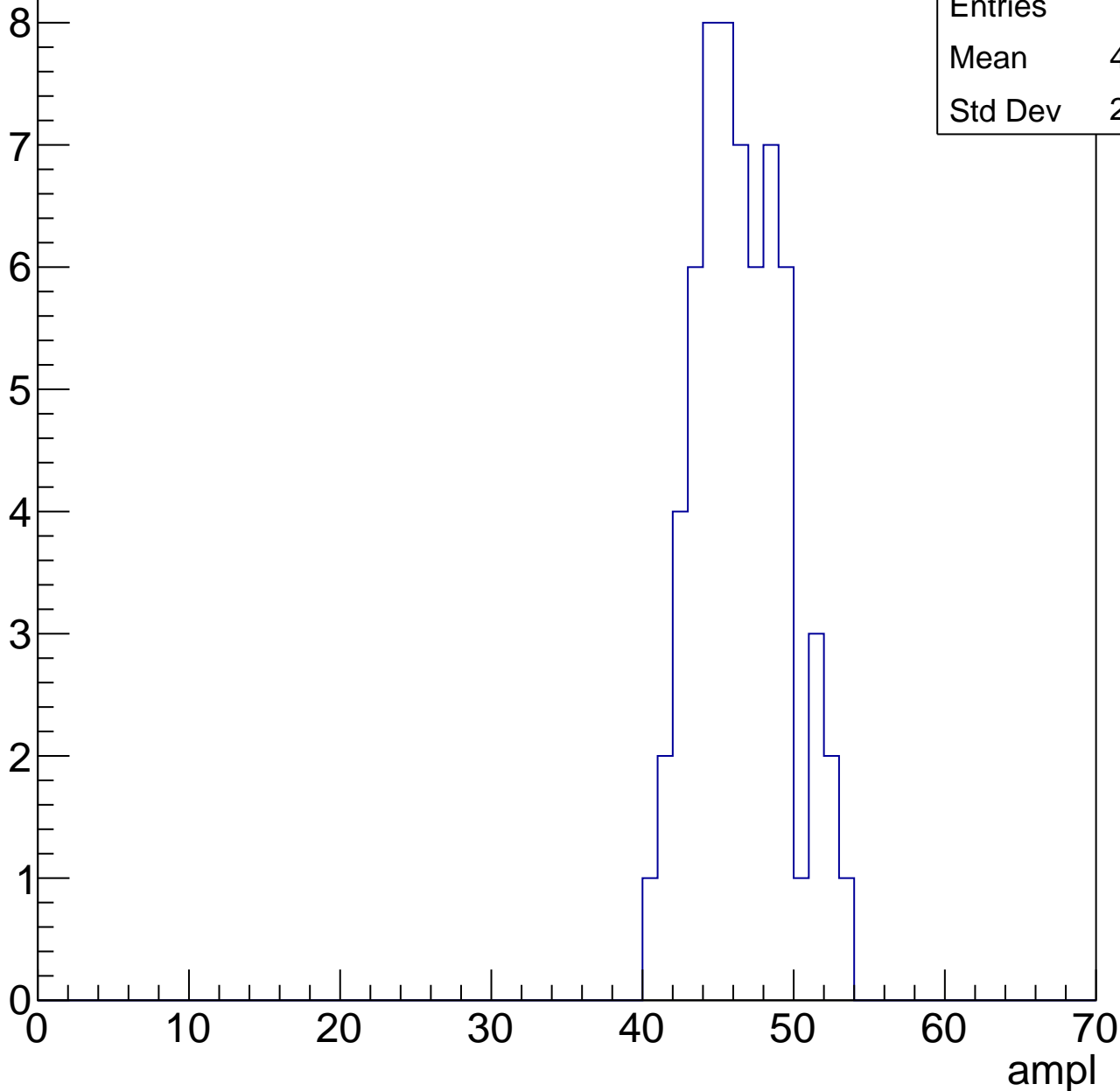


# B1L002S, U18-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

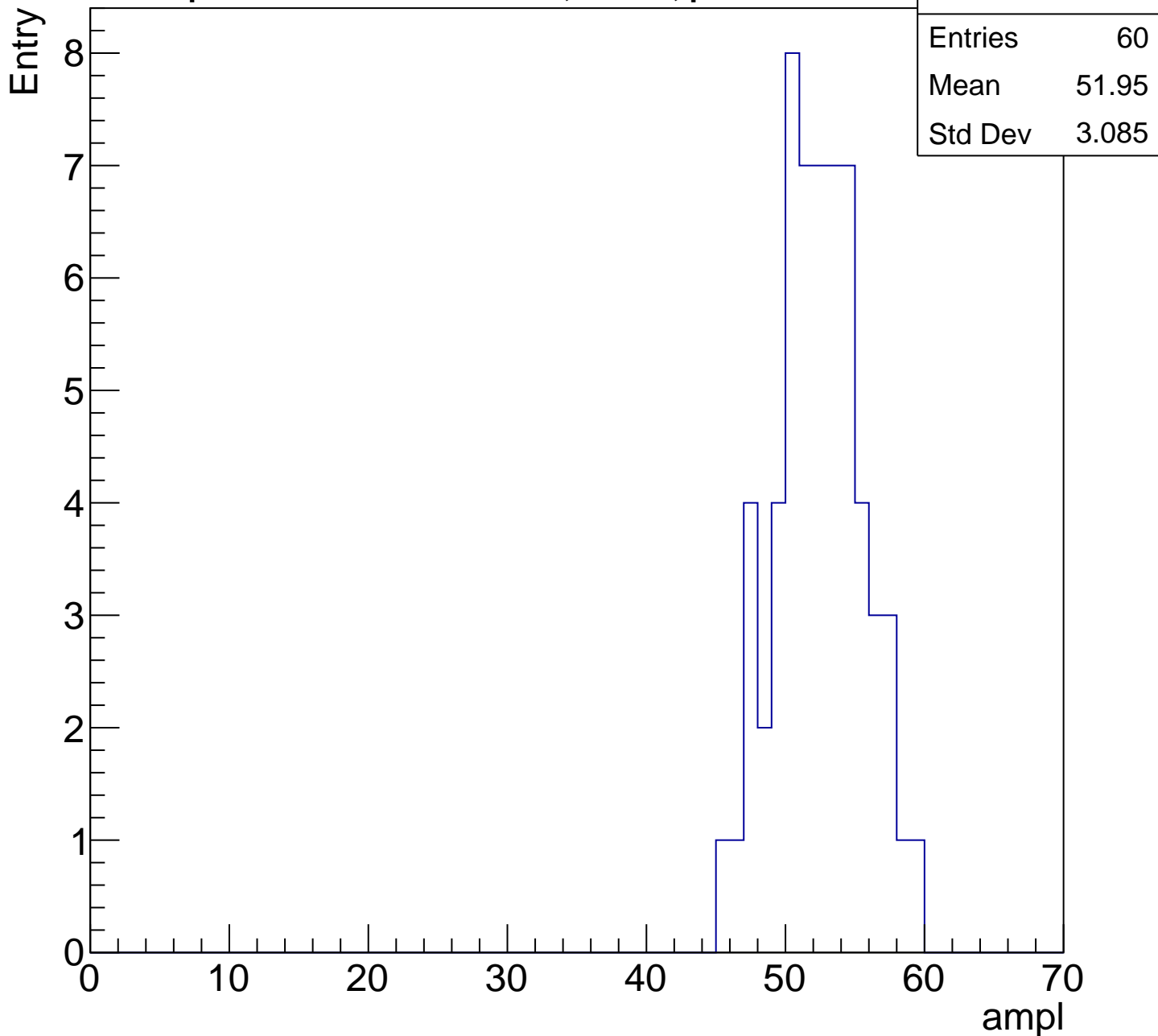
Entry

Entries	62
Mean	46.03
Std Dev	2.962



# B1L002S, U18-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

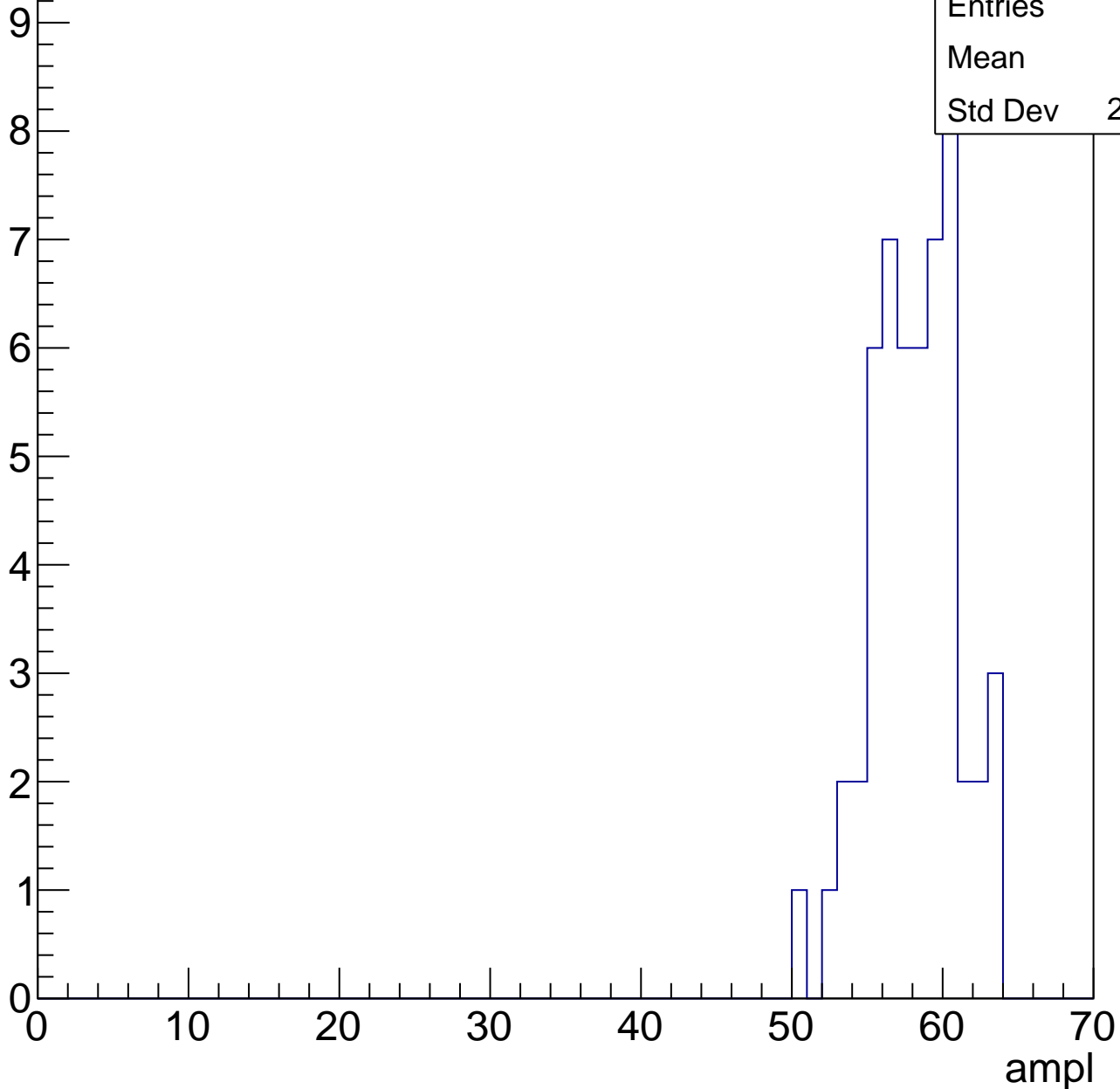


# B1L002S, U18-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.7
Std Dev	2.833

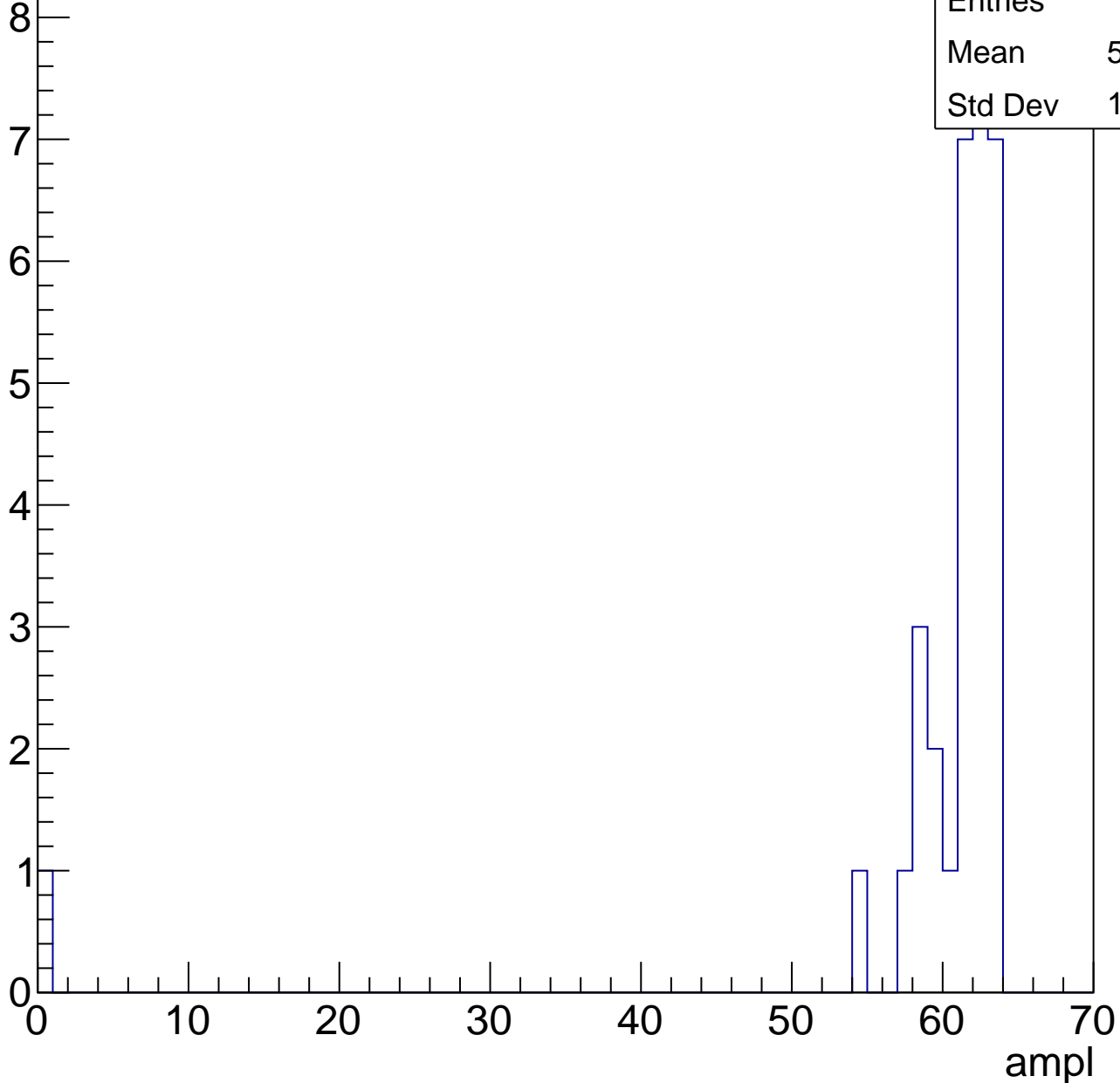


# B1L002S, U18-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.94
Std Dev	10.96





# B1L002S, U18-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch3, adc0

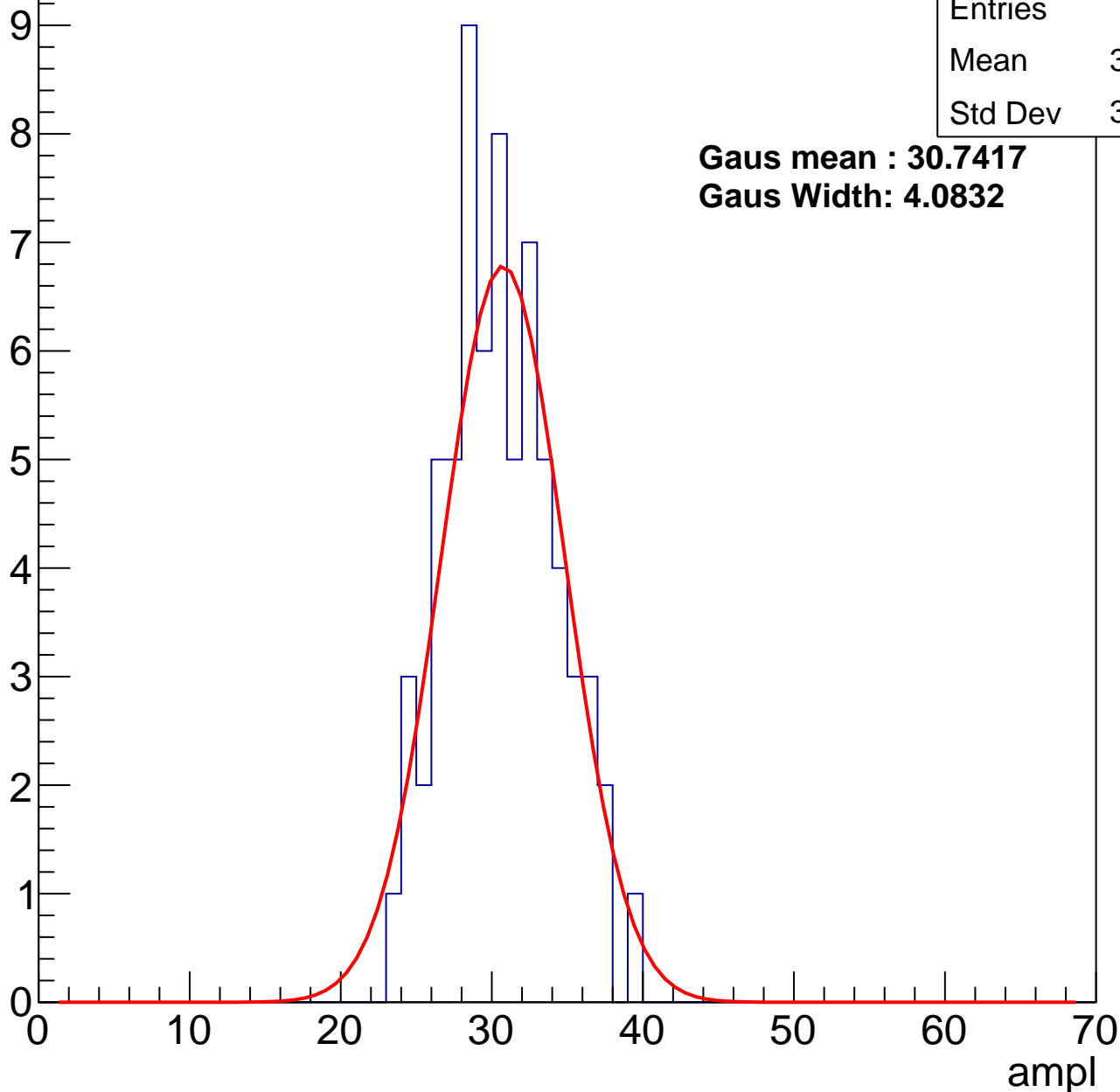
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	30.17
Std Dev	3.563

**Gaus mean : 30.7417**

**Gaus Width: 4.0832**



# B1L002S, U18-ch3, adc1

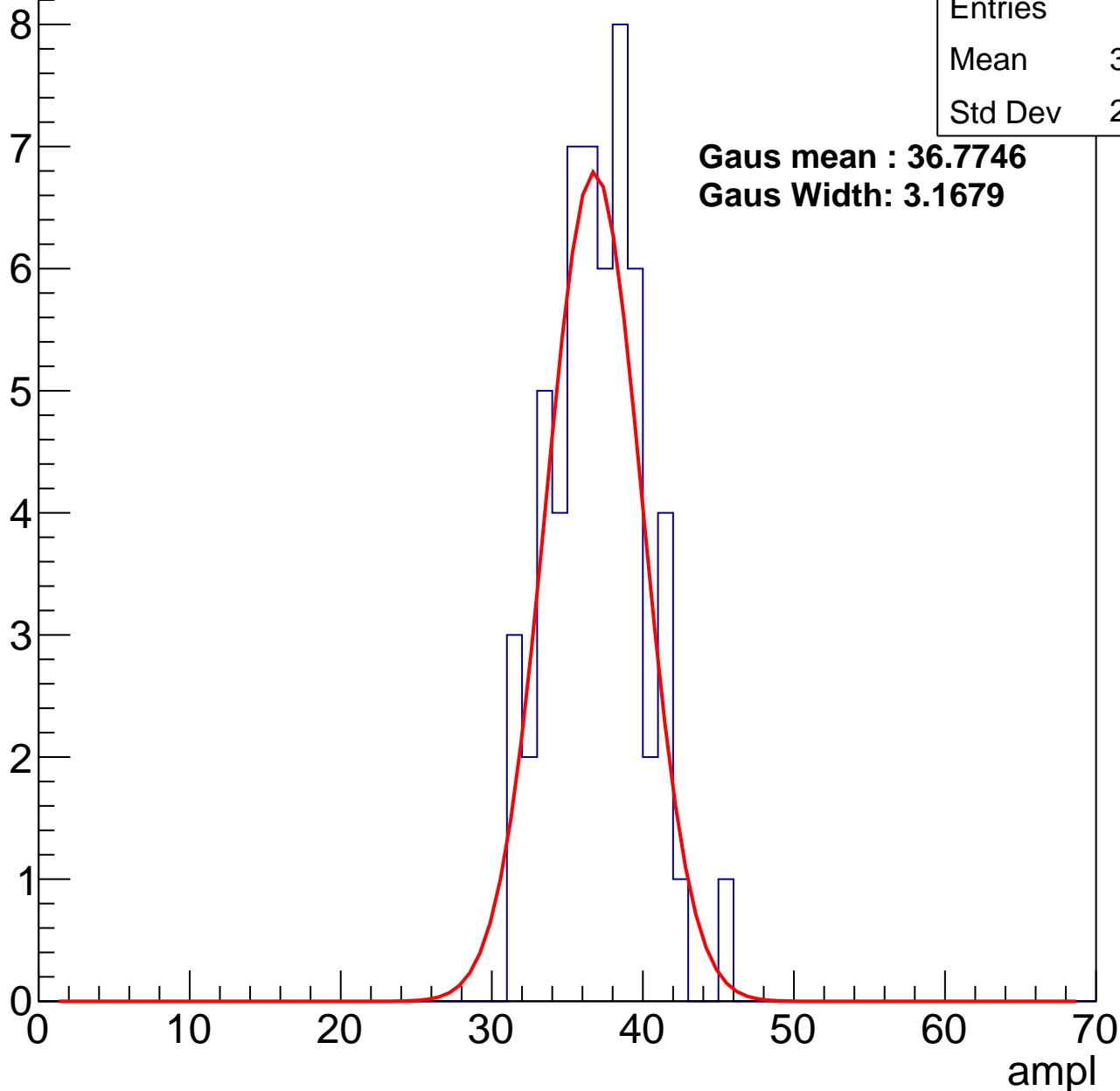
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	36.54
Std Dev	2.988

**Gaus mean : 36.7746**

**Gaus Width: 3.1679**



# B1L002S, U18-ch3, adc2

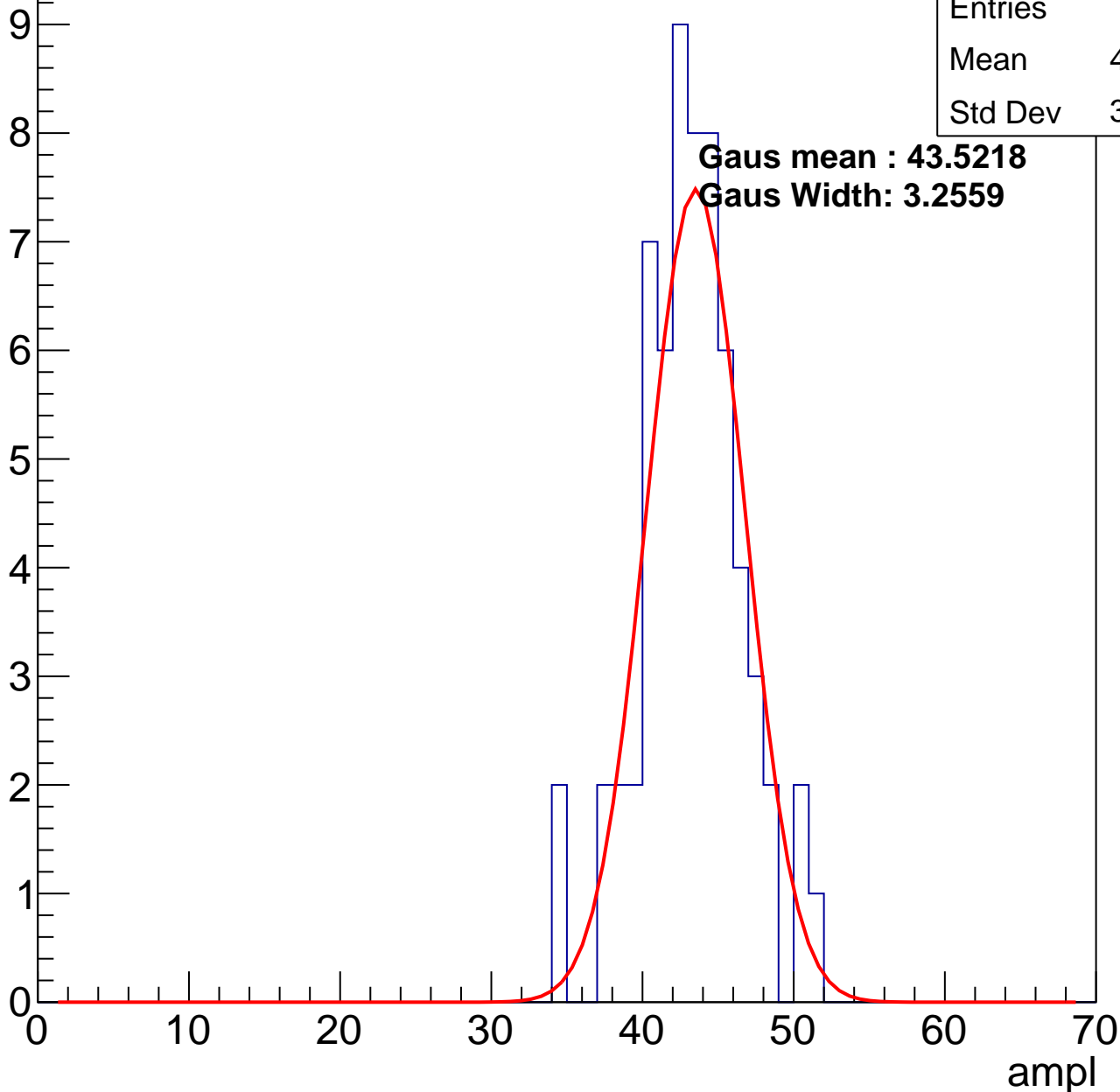
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	42.78
Std Dev	3.393

**Gaus mean : 43.5218**

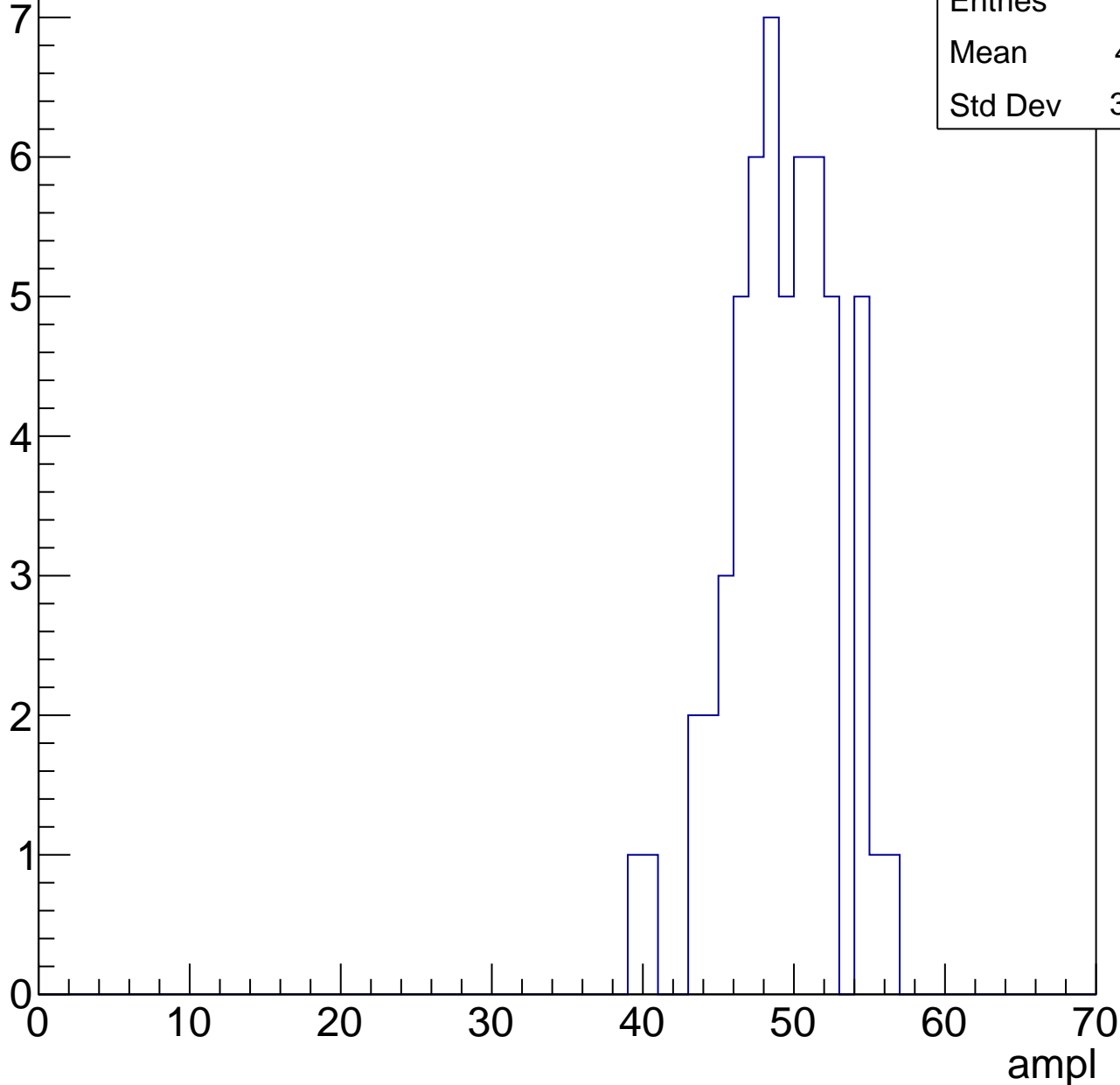
**Gaus Width: 3.2559**



# B1L002S, U18-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



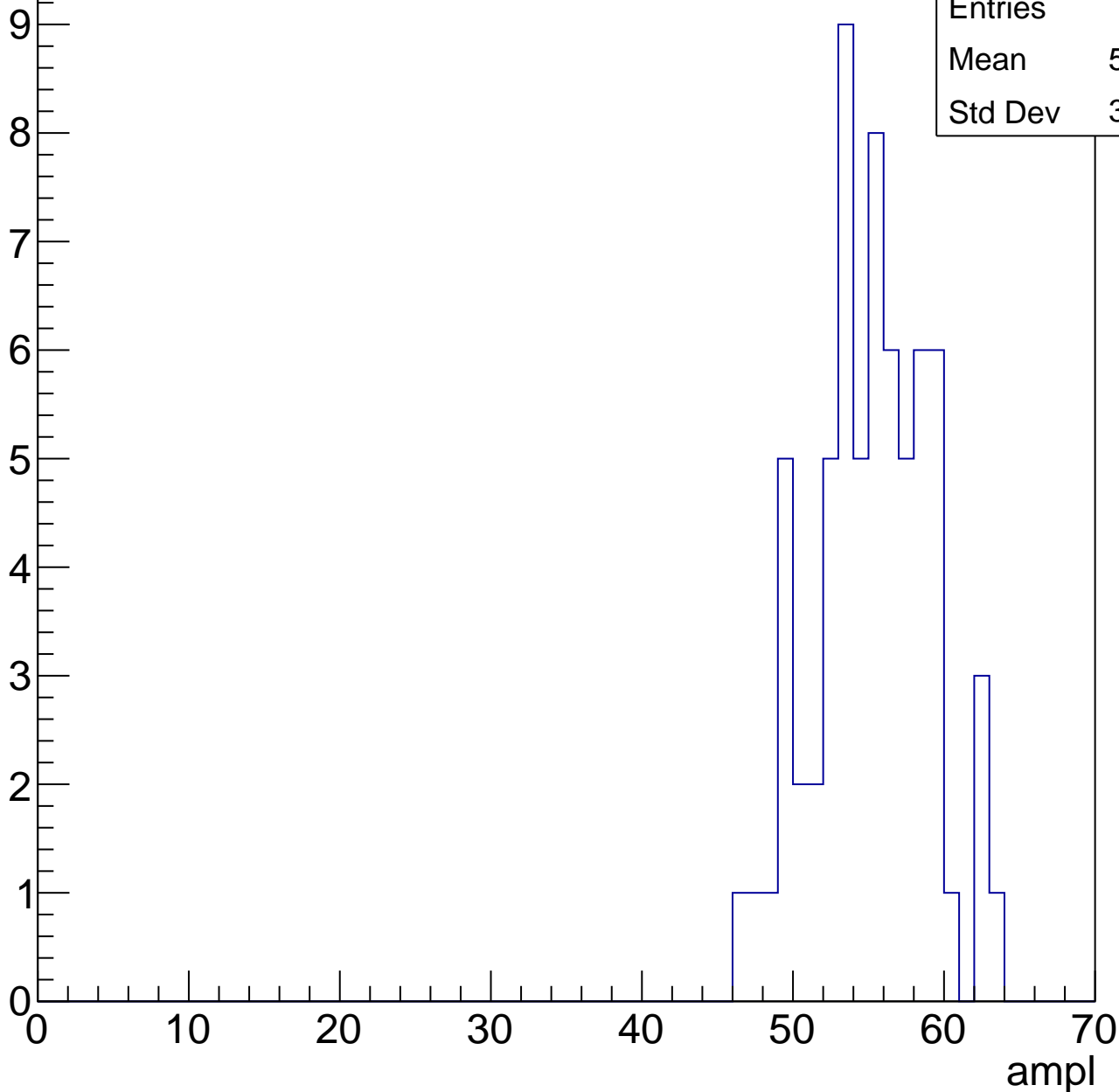
Entries	56
Mean	48.71
Std Dev	3.554

# B1L002S, U18-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	54.73
Std Dev	3.764

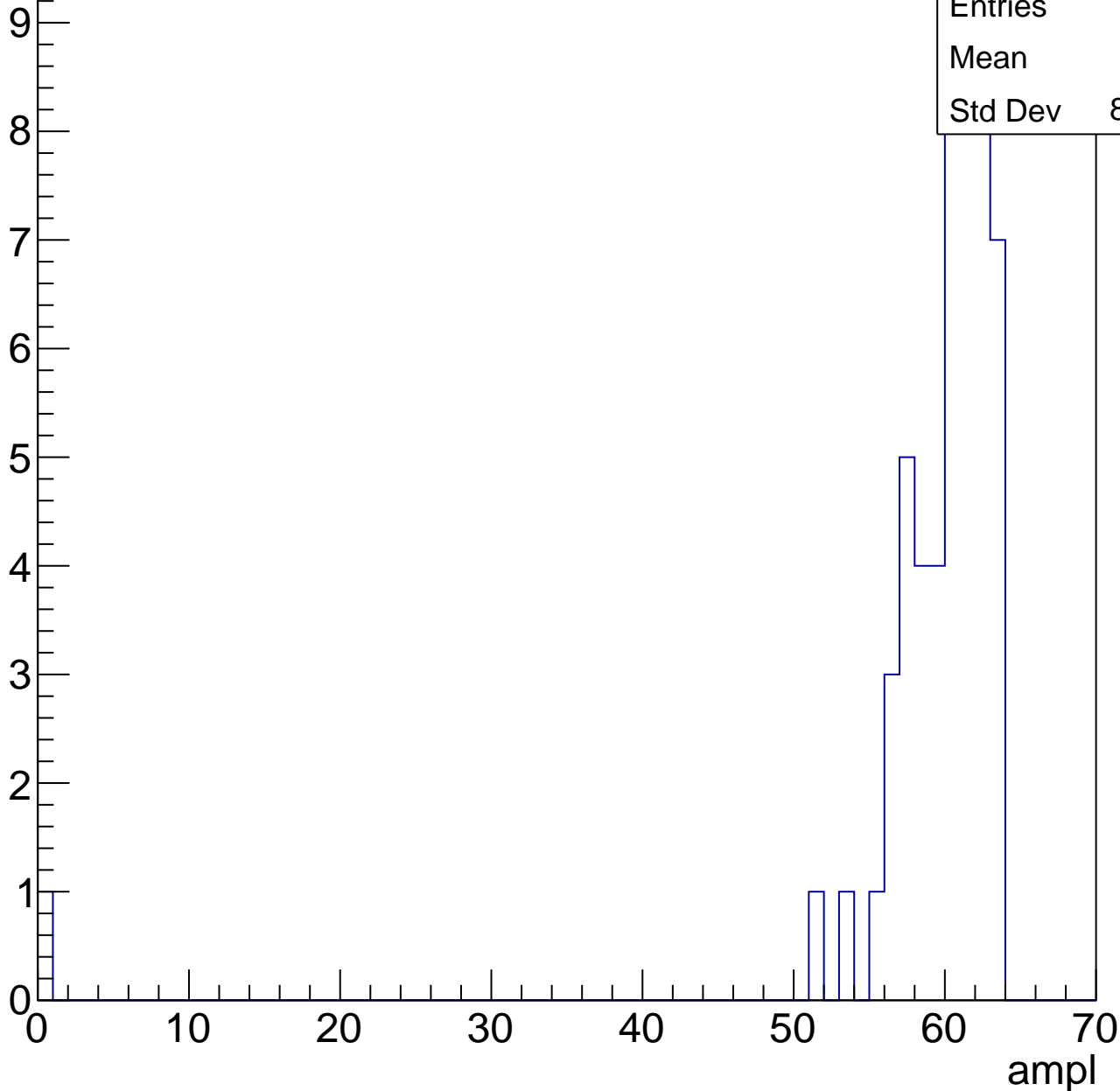


# B1L002S, U18-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	58.6
Std Dev	8.544



# B1L002S, U18-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl

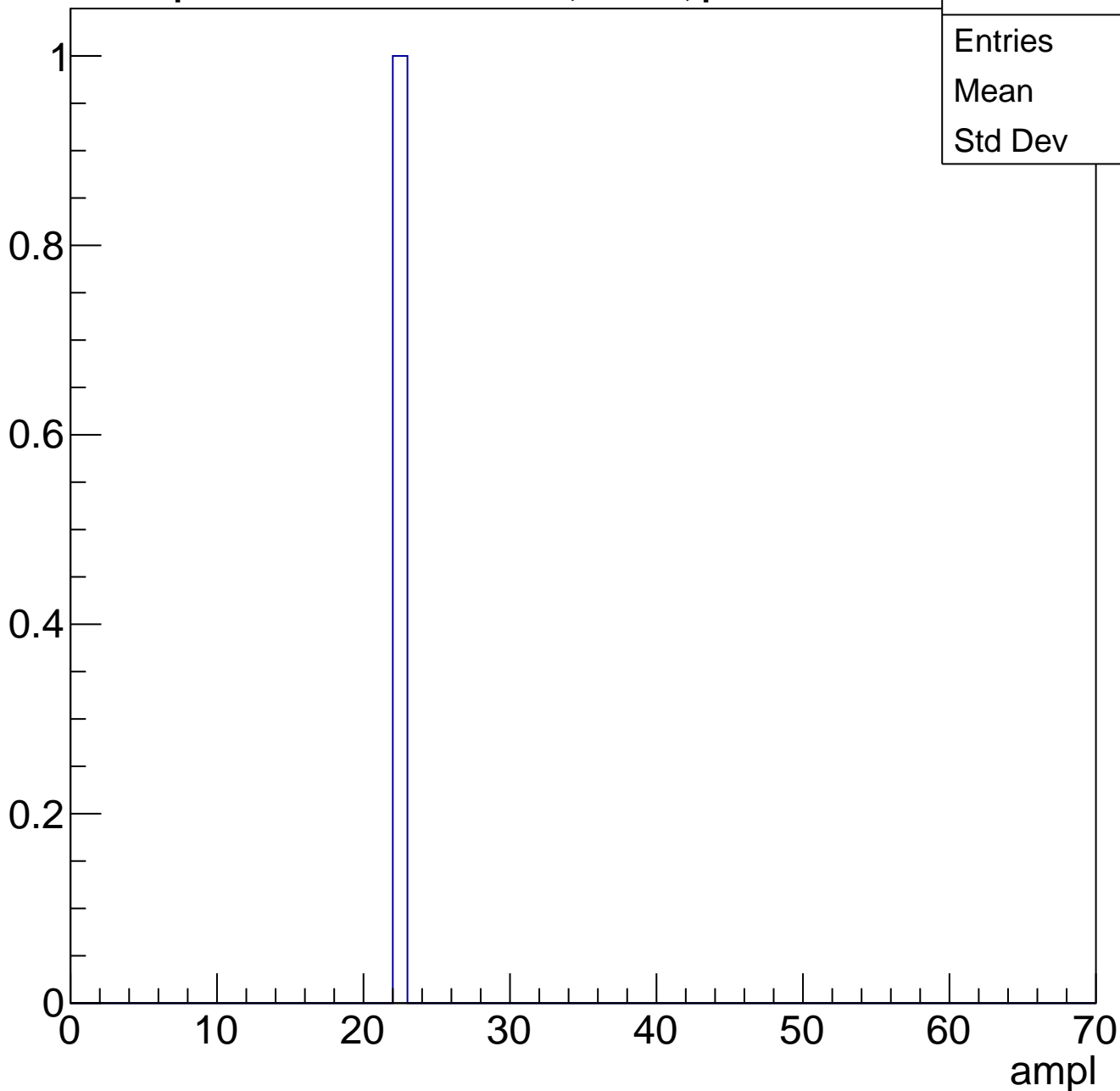




# B1L002S, U18-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch4, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	28.76
Std Dev	4.594

**Gaus mean : 29.0690**

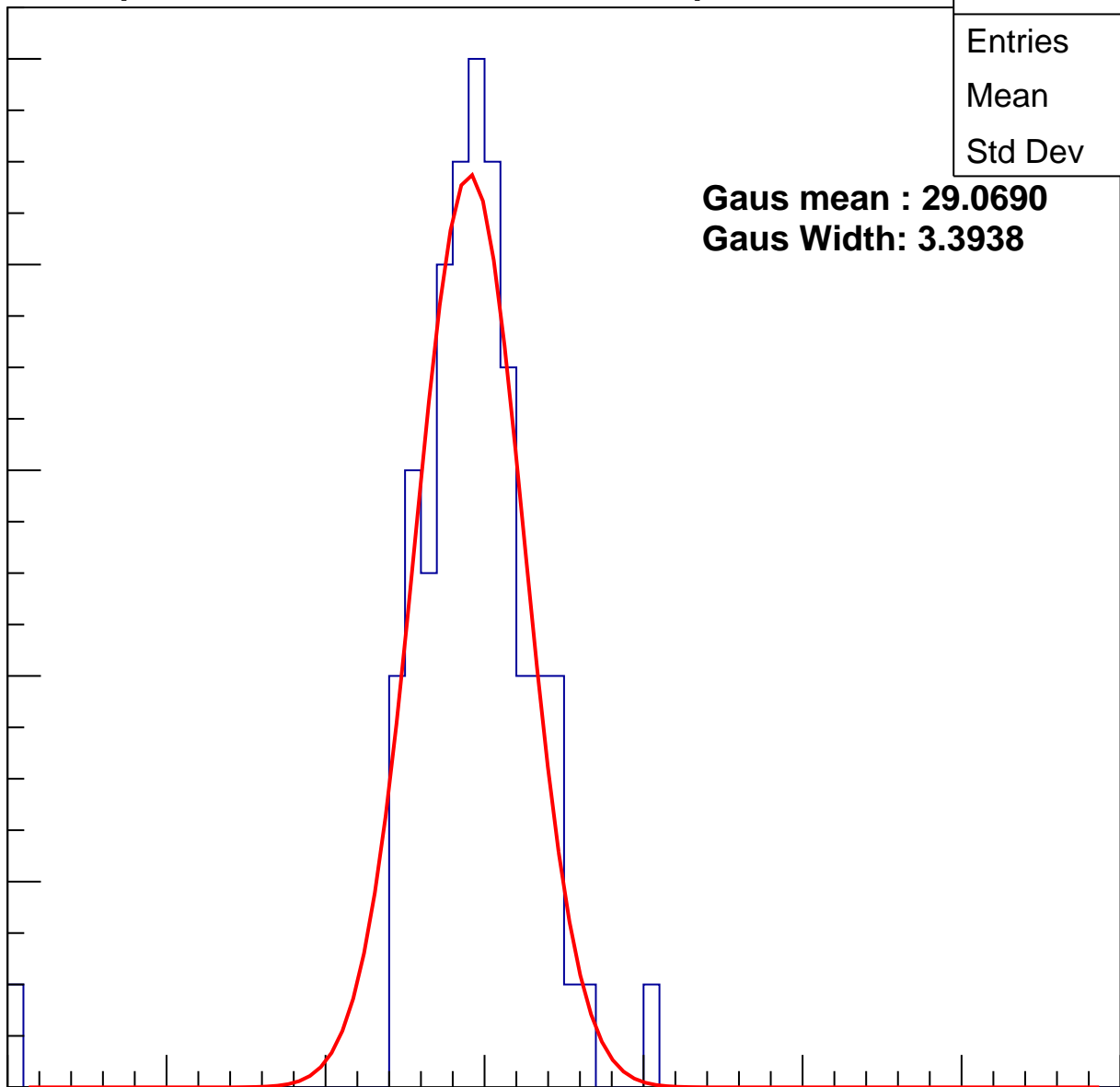
**Gaus Width: 3.3938**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch4, adc1

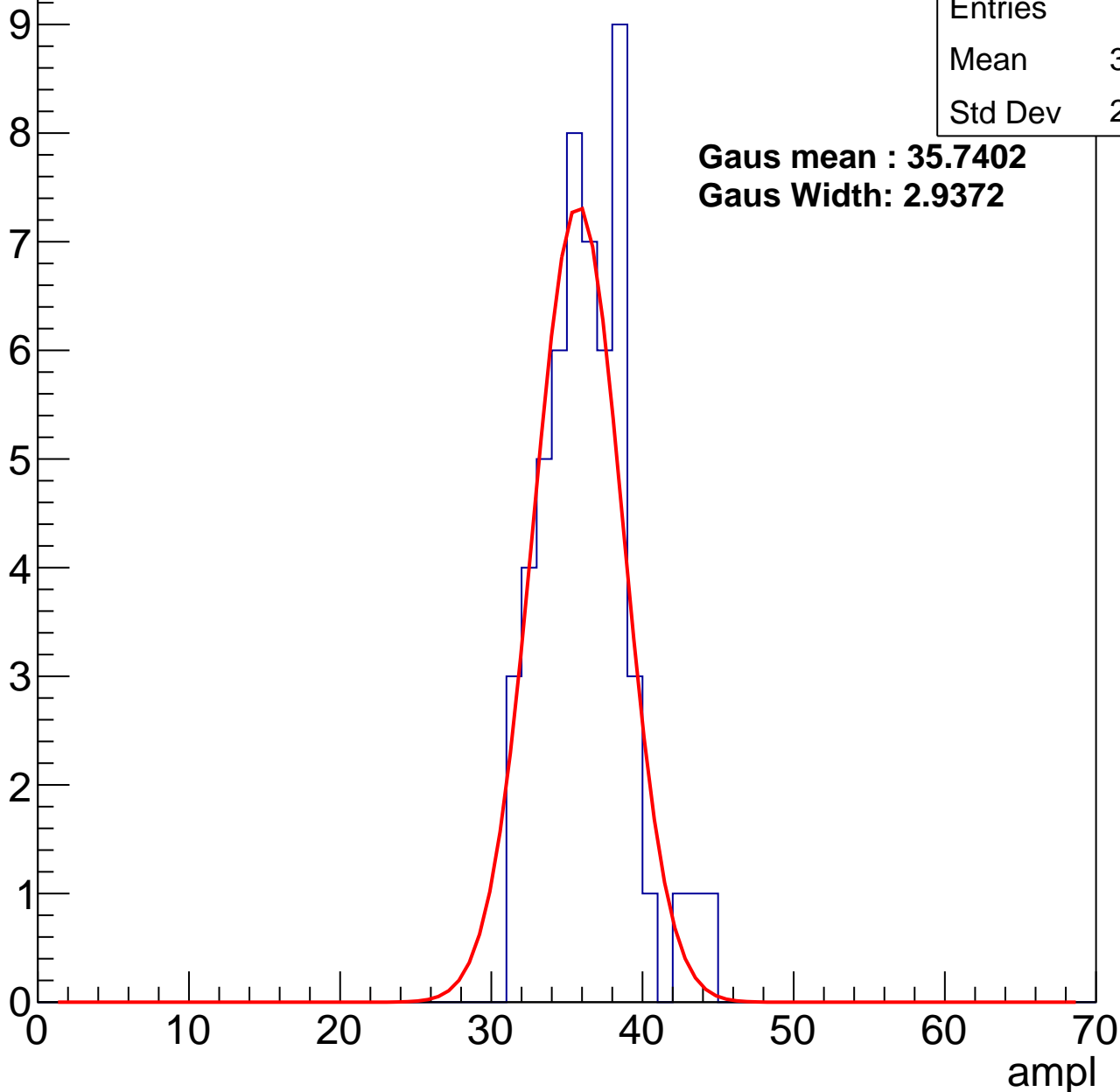
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	35.85
Std Dev	2.857

**Gaus mean : 35.7402**

**Gaus Width: 2.9372**



# B1L002S, U18-ch4, adc2

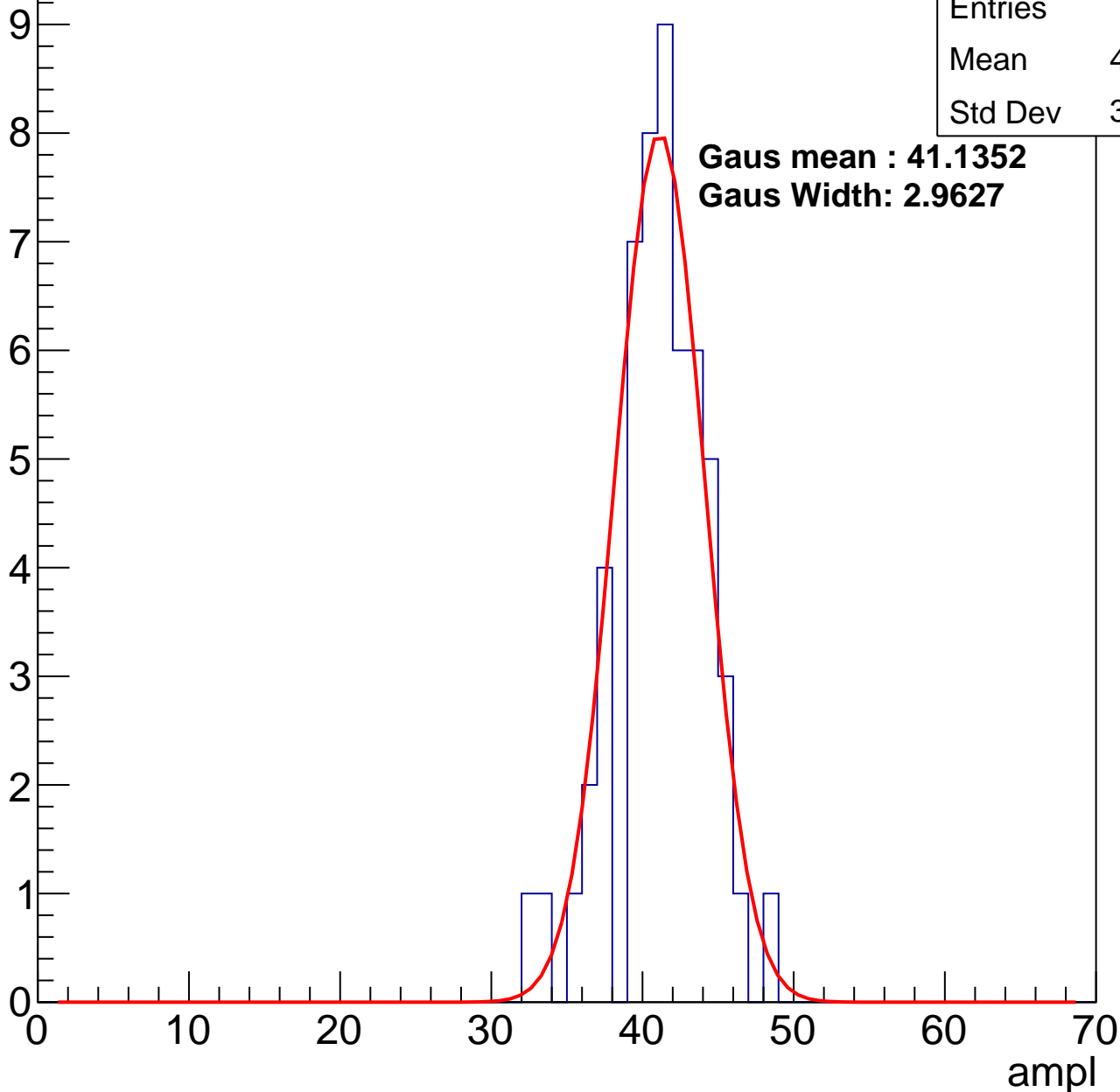
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.75
Std Dev	3.088

**Gaus mean : 41.1352**

**Gaus Width: 2.9627**

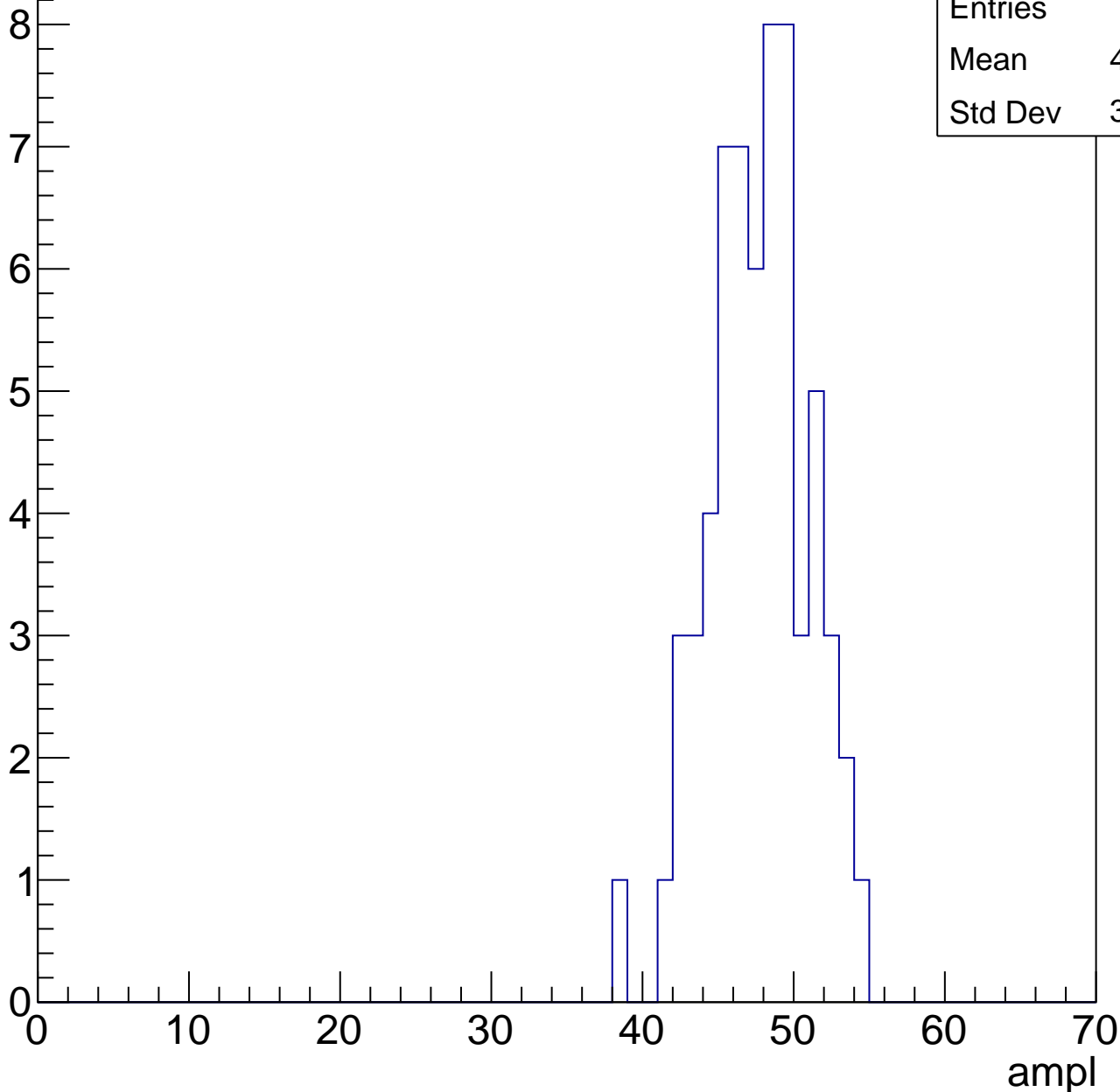


# B1L002S, U18-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	47.19
Std Dev	3.242

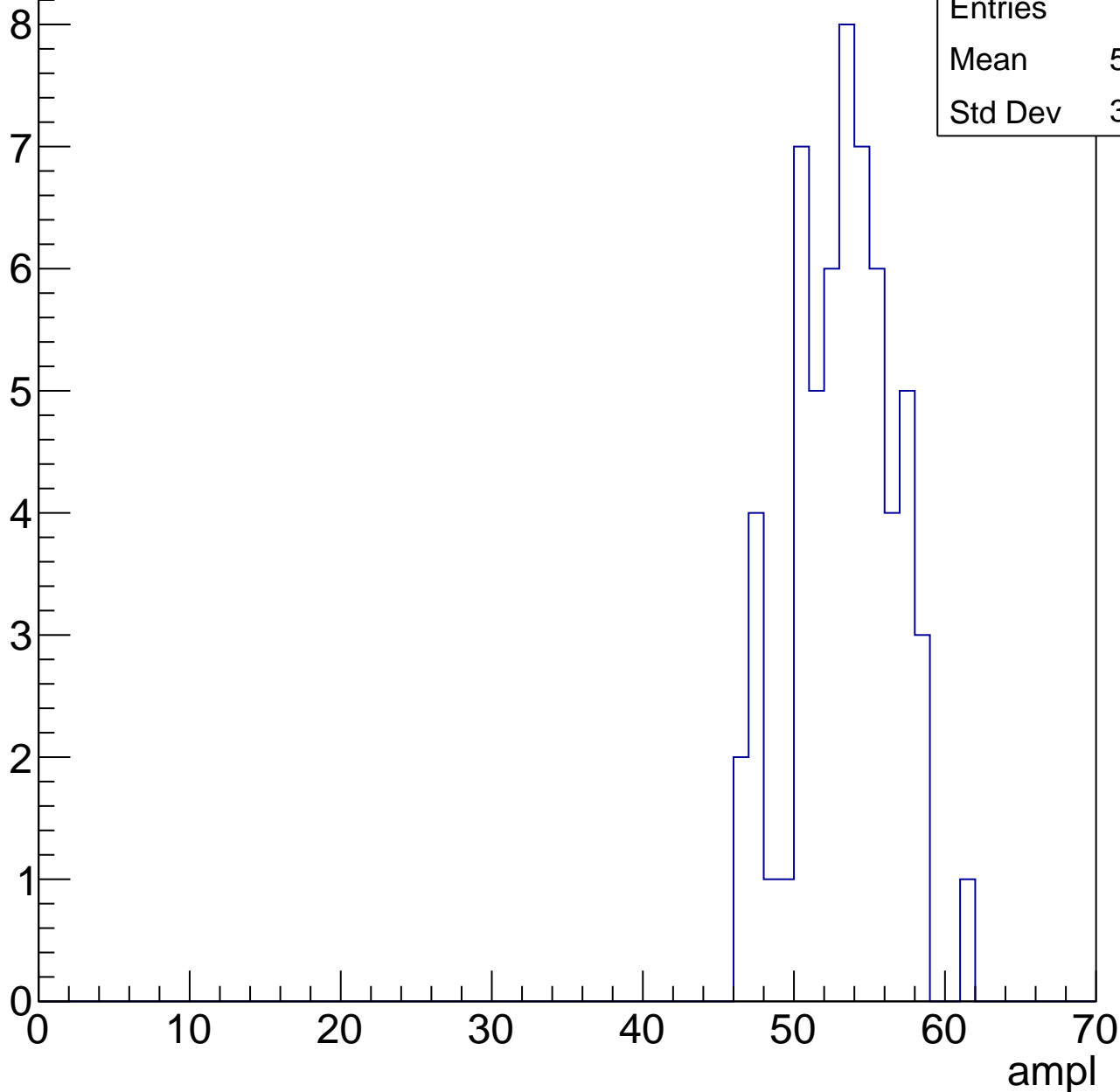


# B1L002S, U18-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.83
Std Dev	3.307



# B1L002S, U18-ch4, adc5

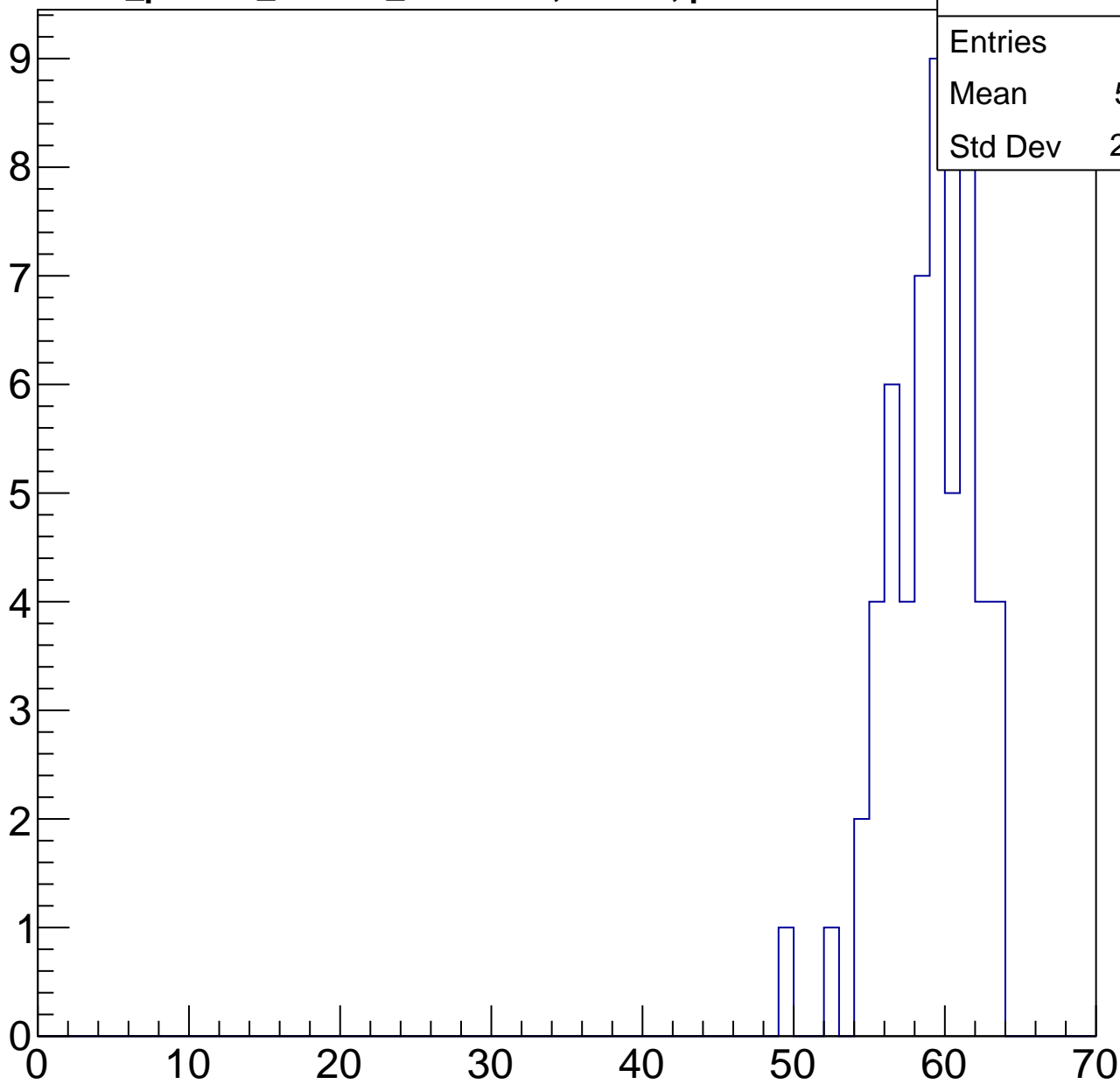
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	58.51
Std Dev	2.904

ampl

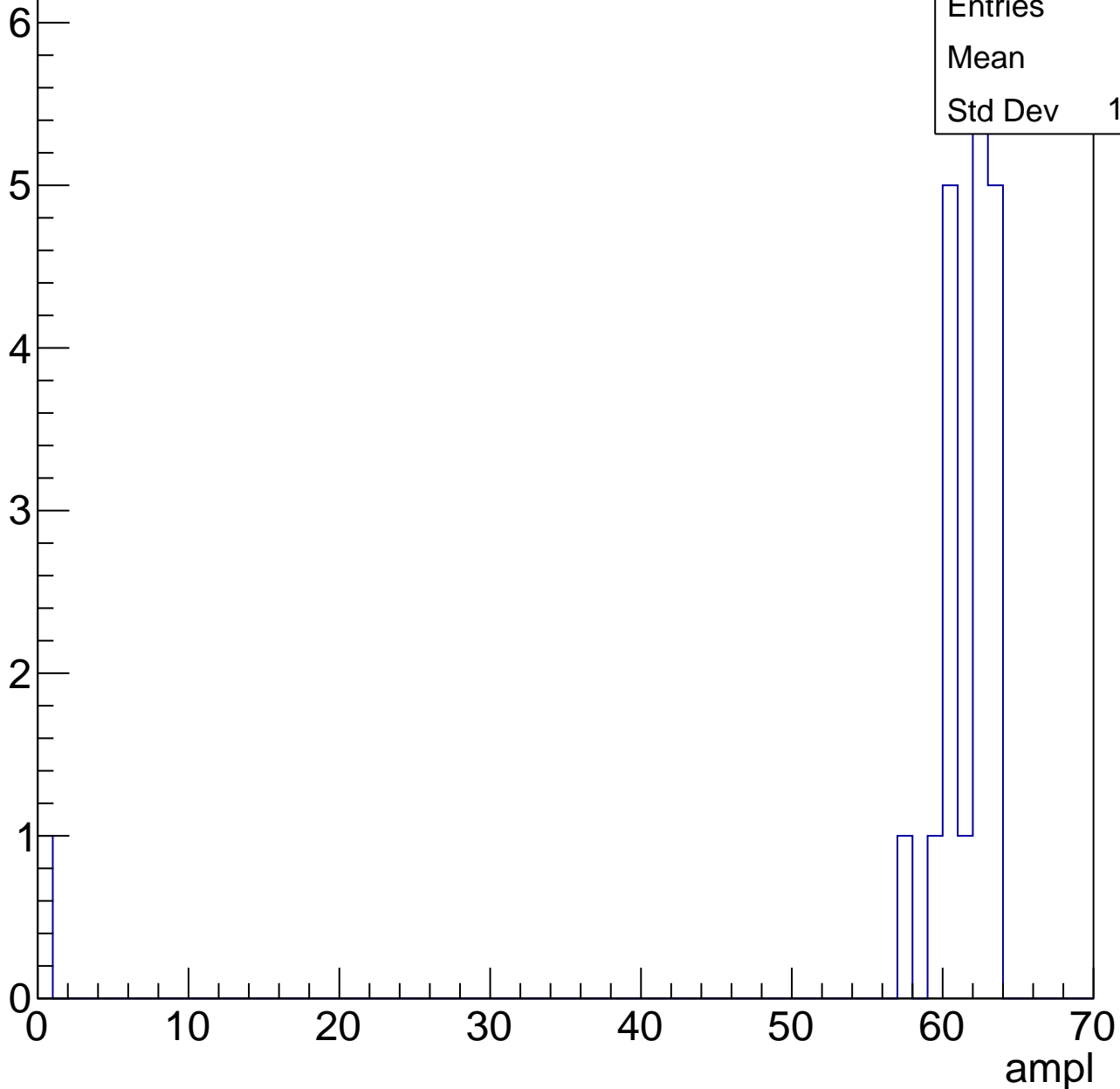


# B1L002S, U18-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.2
Std Dev	13.44

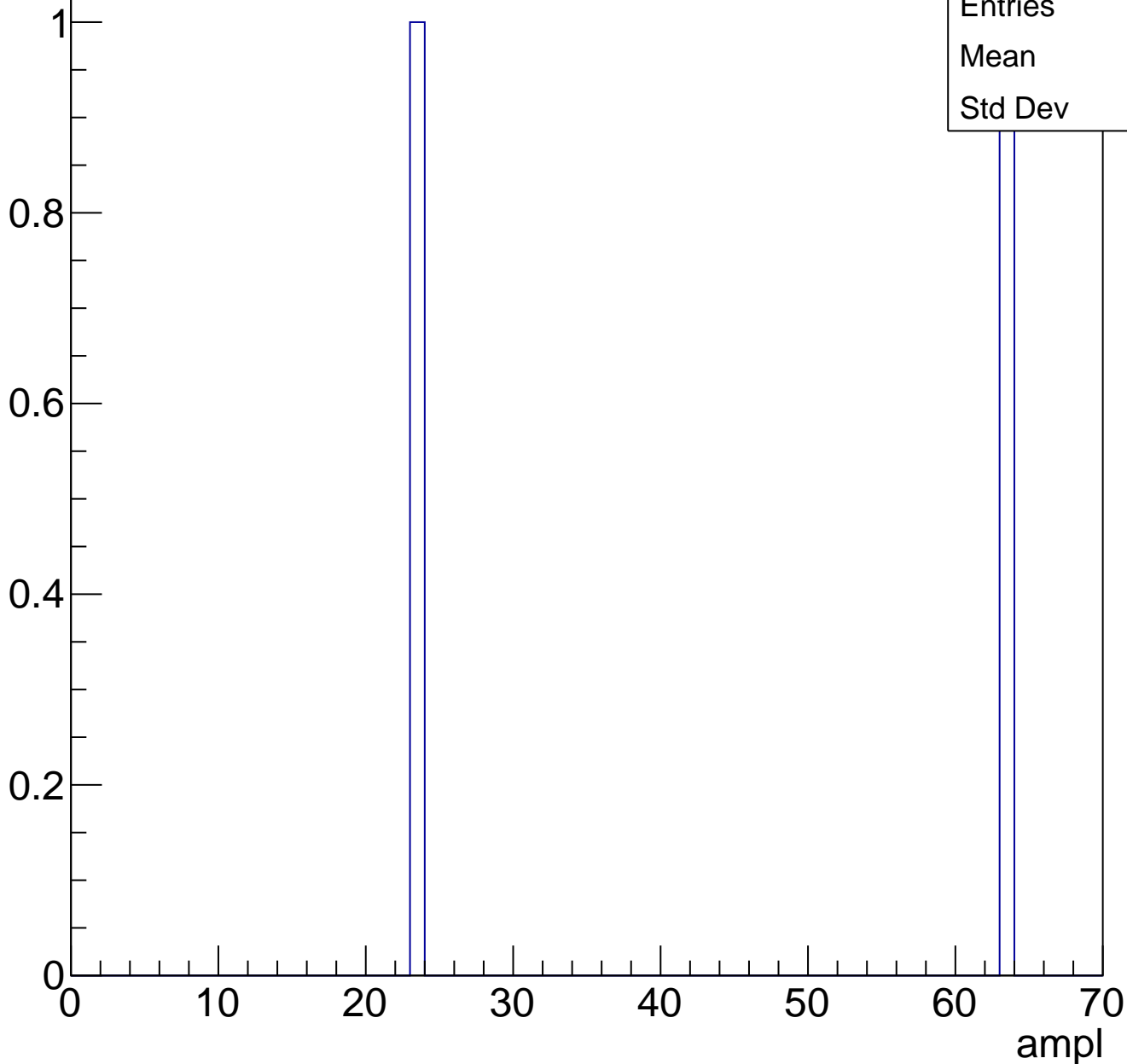




# B1L002S, U18-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch5, adc0

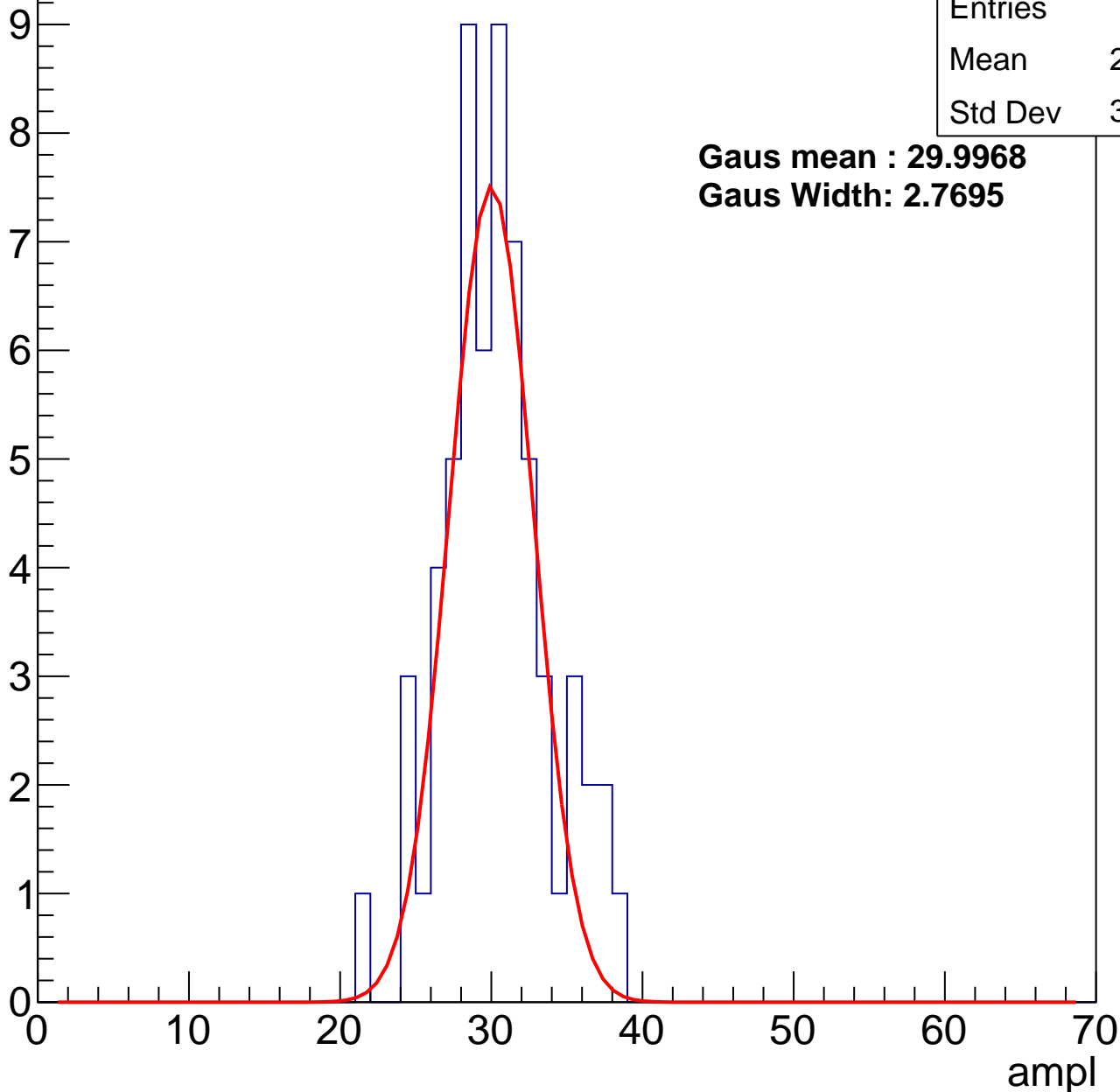
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.87
Std Dev	3.462

**Gaus mean : 29.9968**

**Gaus Width: 2.7695**



# B1L002S, U18-ch5, adc1

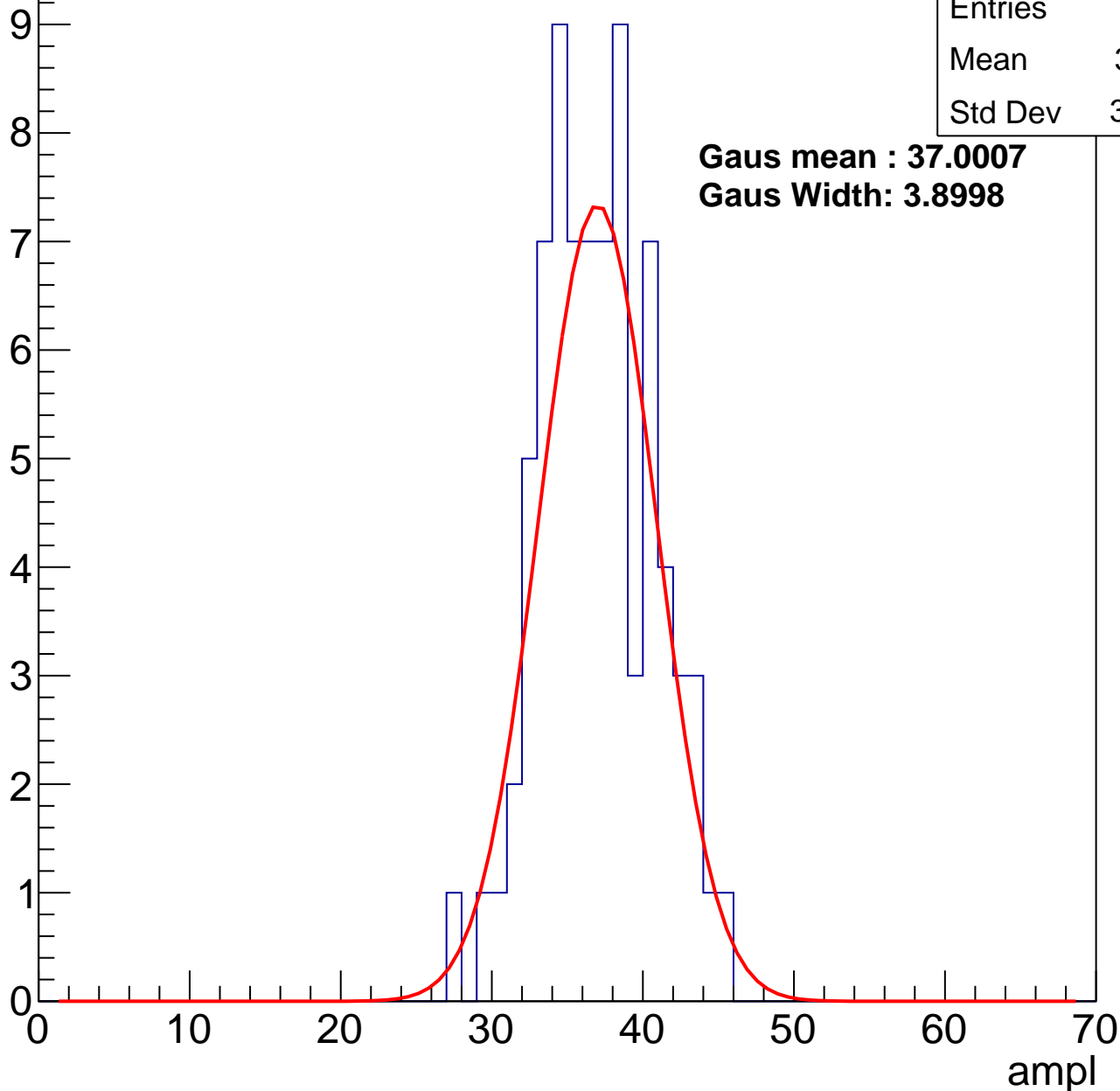
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	36.51
Std Dev	3.692

**Gaus mean : 37.0007**

**Gaus Width: 3.8998**



# B1L002S, U18-ch5, adc2

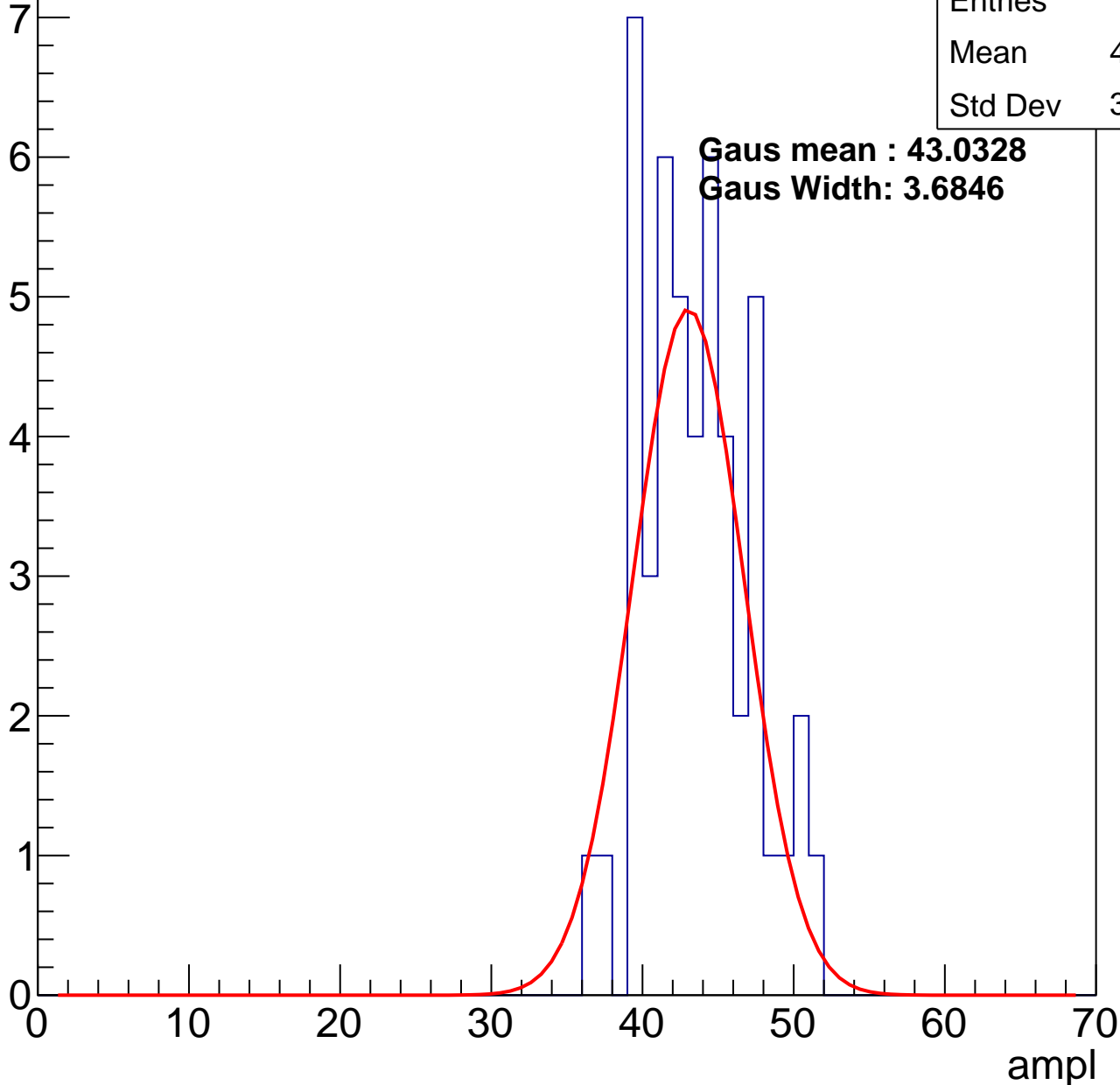
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	43.12
Std Dev	3.485

**Gaus mean : 43.0328**

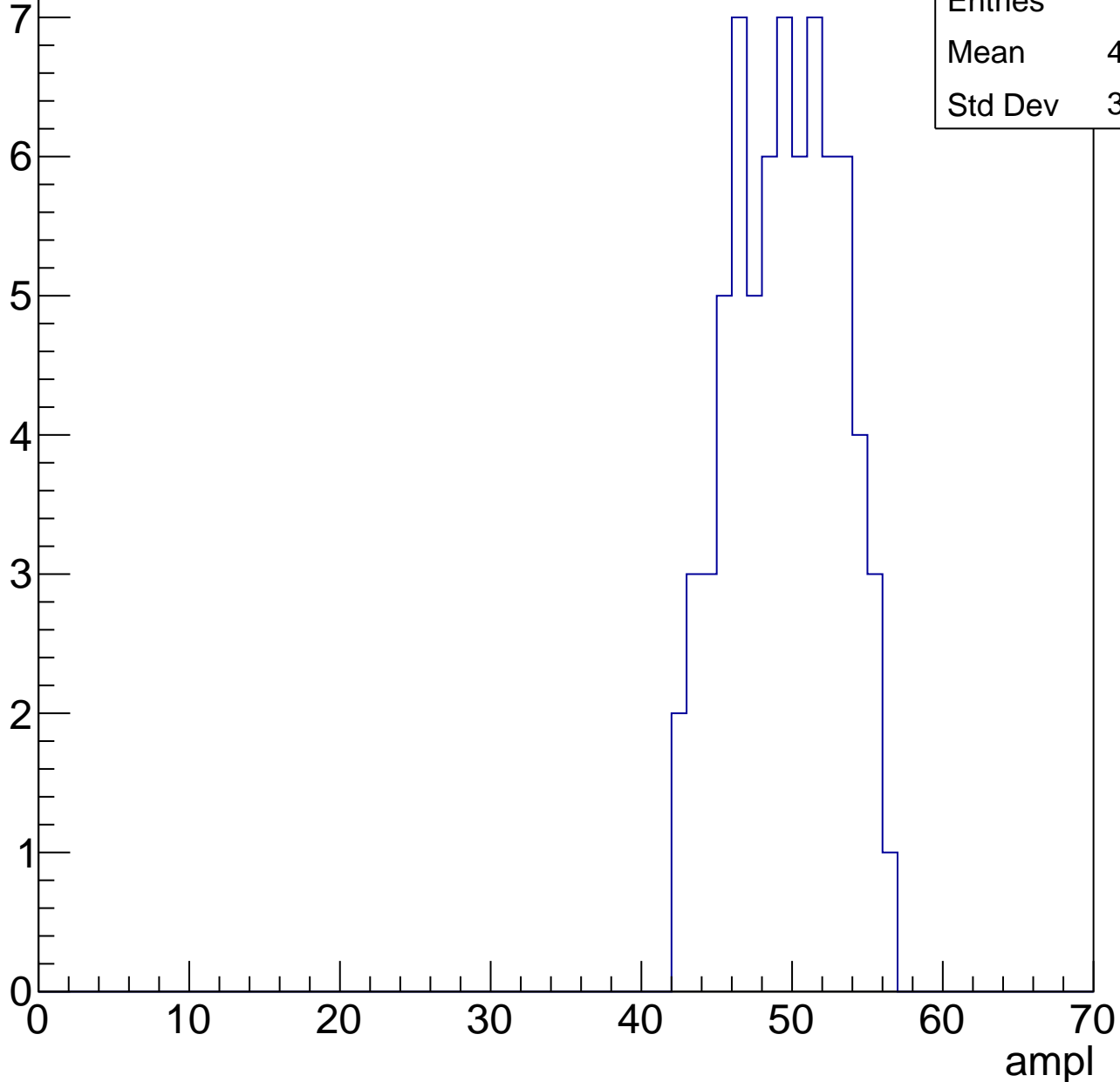
**Gaus Width: 3.6846**



# B1L002S, U18-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

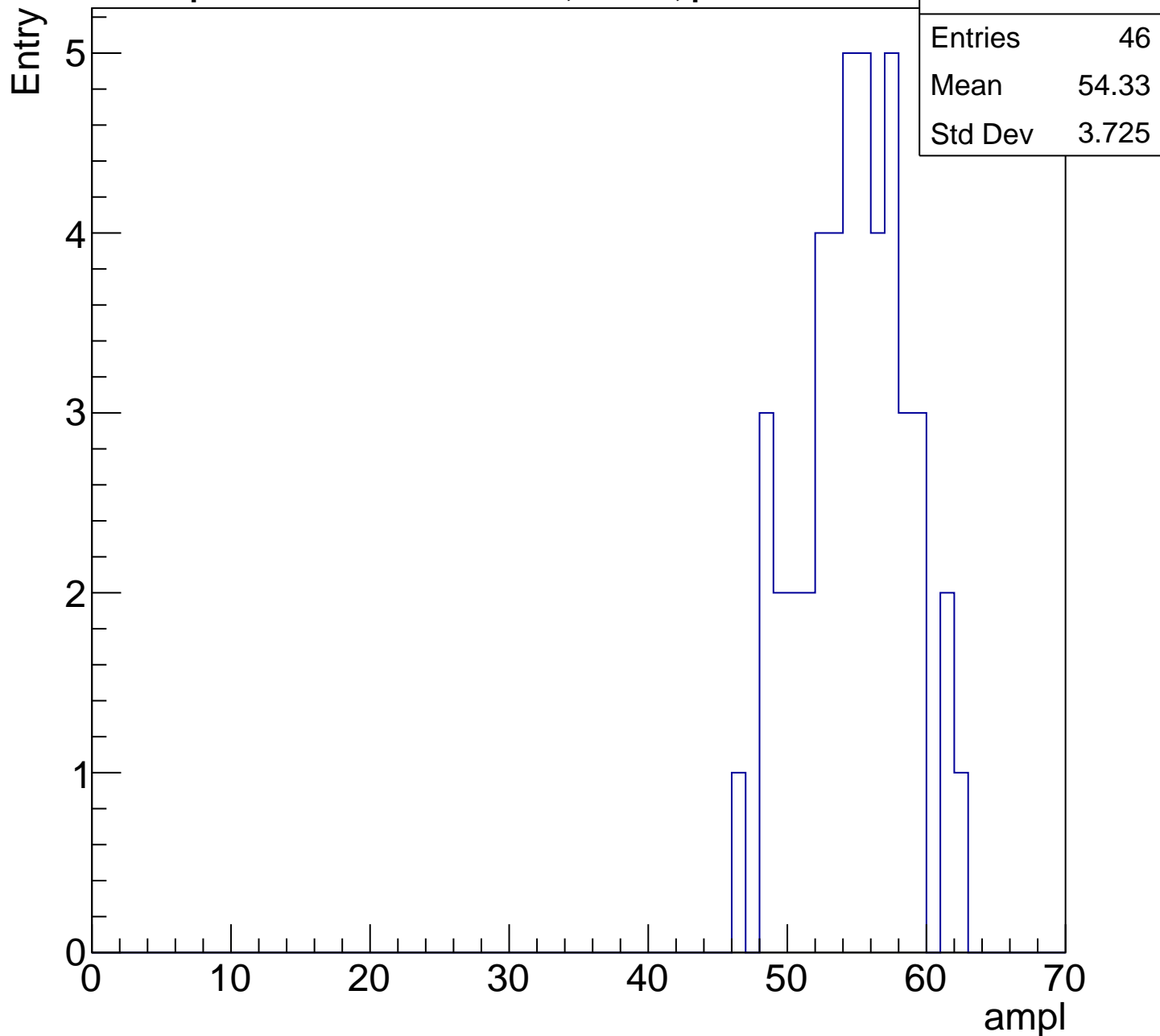
Entry



Entries	71
Mean	49.04
Std Dev	3.542

# B1L002S, U18-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

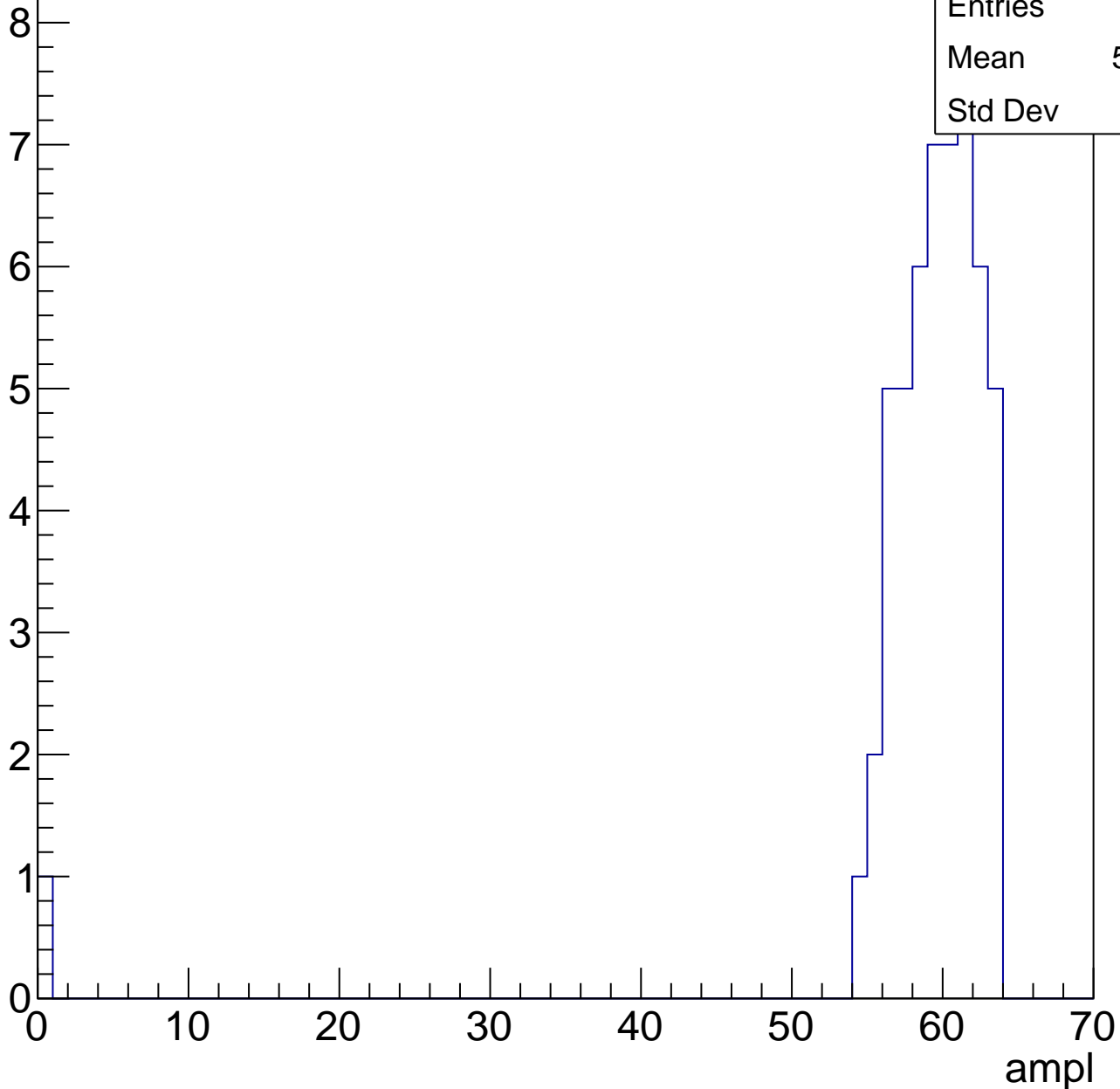


# B1L002S, U18-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	58.21
Std Dev	8.41

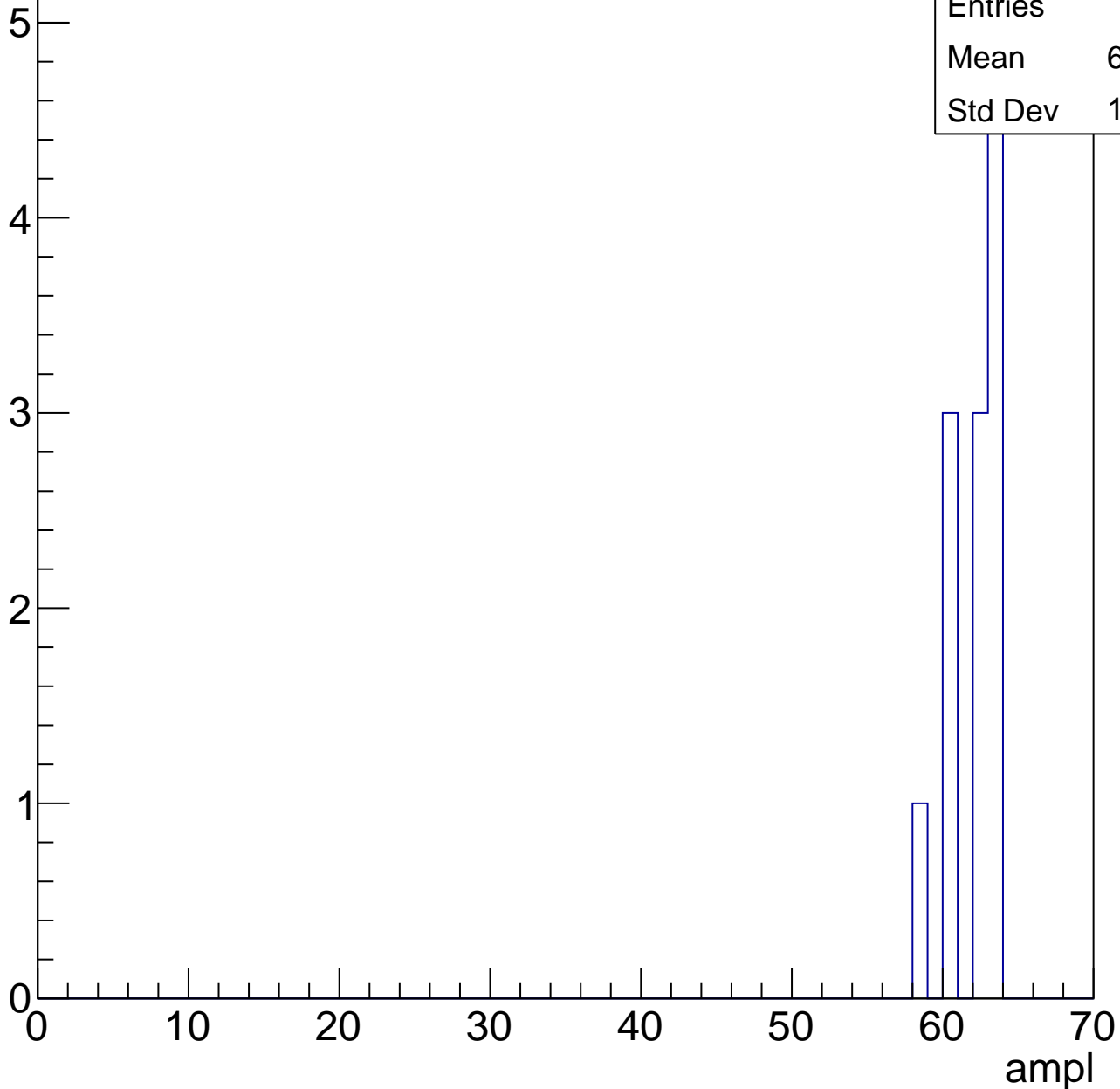


# B1L002S, U18-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	12
Mean	61.58
Std Dev	1.605





# B1L002S, U18-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch6, adc0

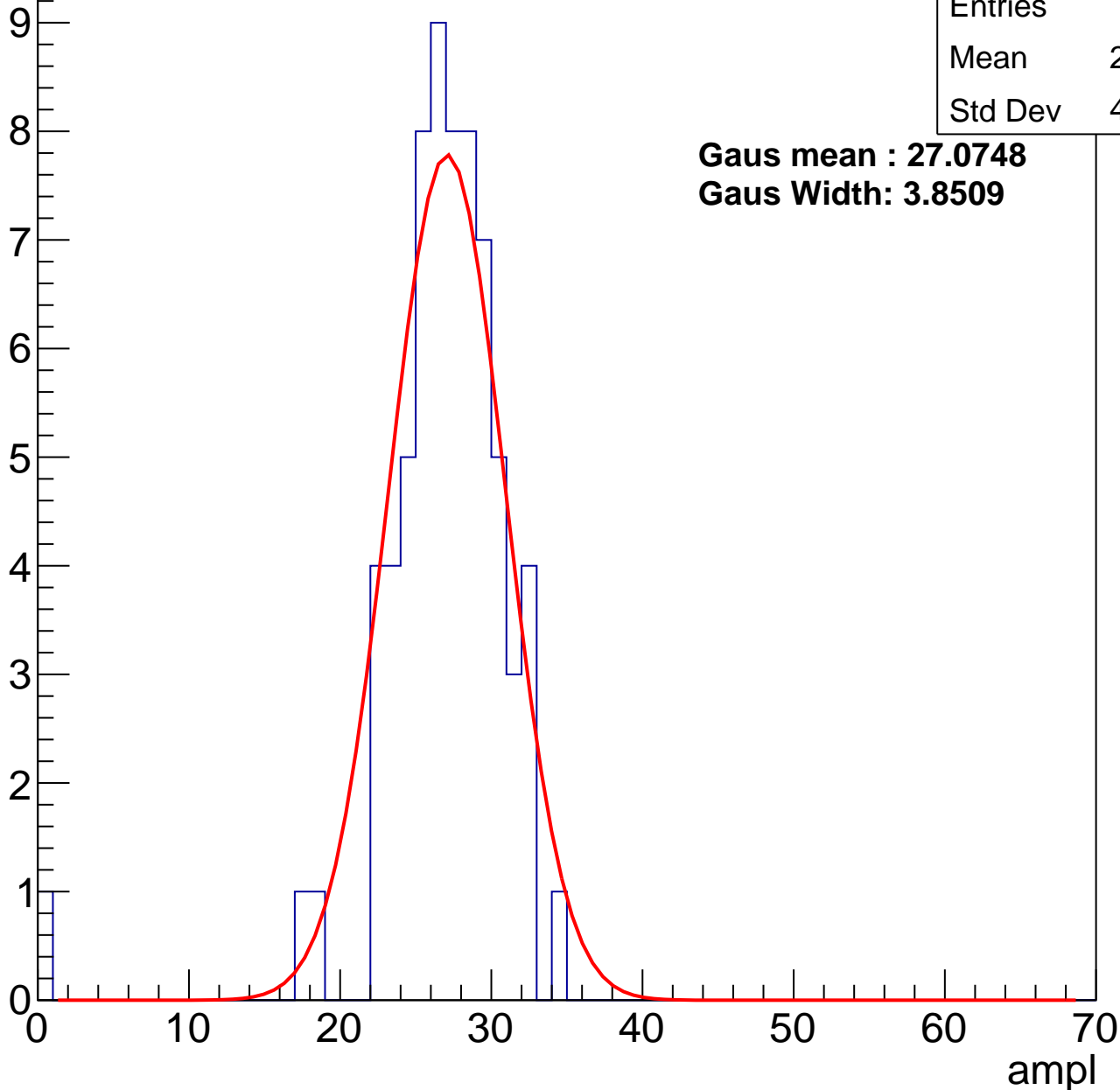
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	26.33
Std Dev	4.516

**Gaus mean : 27.0748**

**Gaus Width: 3.8509**



# B1L002S, U18-ch6, adc1

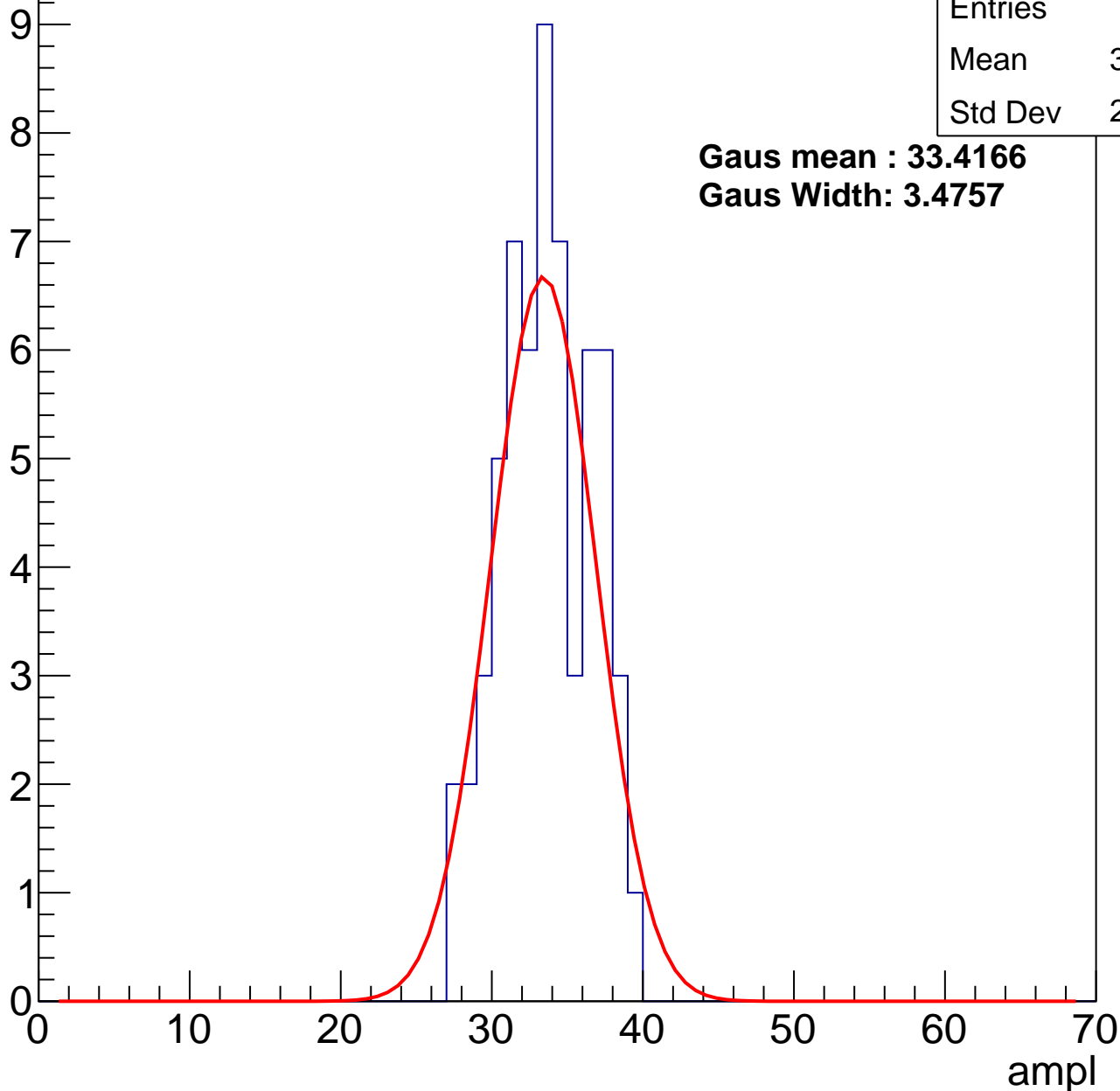
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.12
Std Dev	2.967

**Gaus mean : 33.4166**

**Gaus Width: 3.4757**



# B1L002S, U18-ch6, adc2

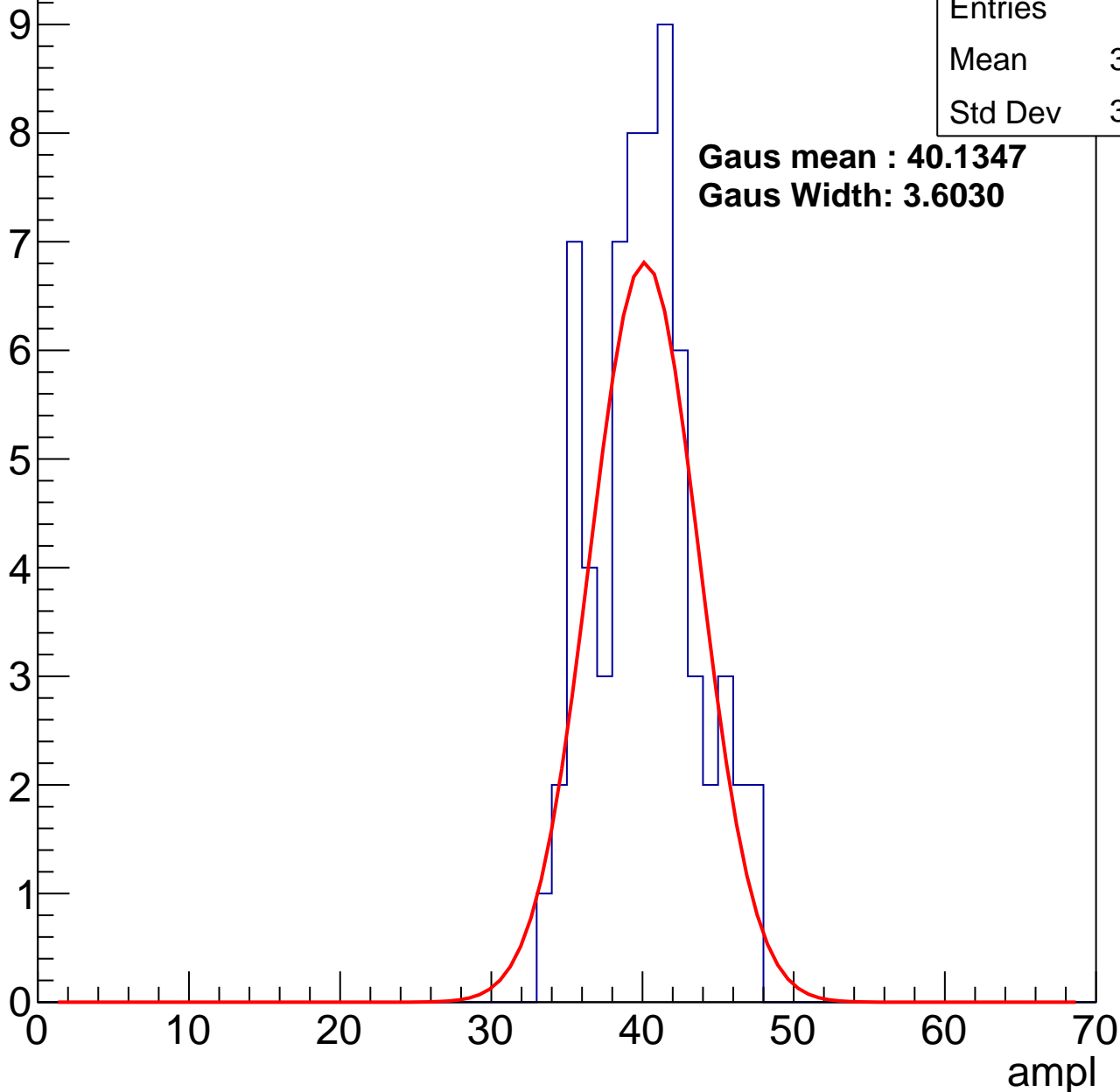
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.67
Std Dev	3.352

**Gaus mean : 40.1347**

**Gaus Width: 3.6030**

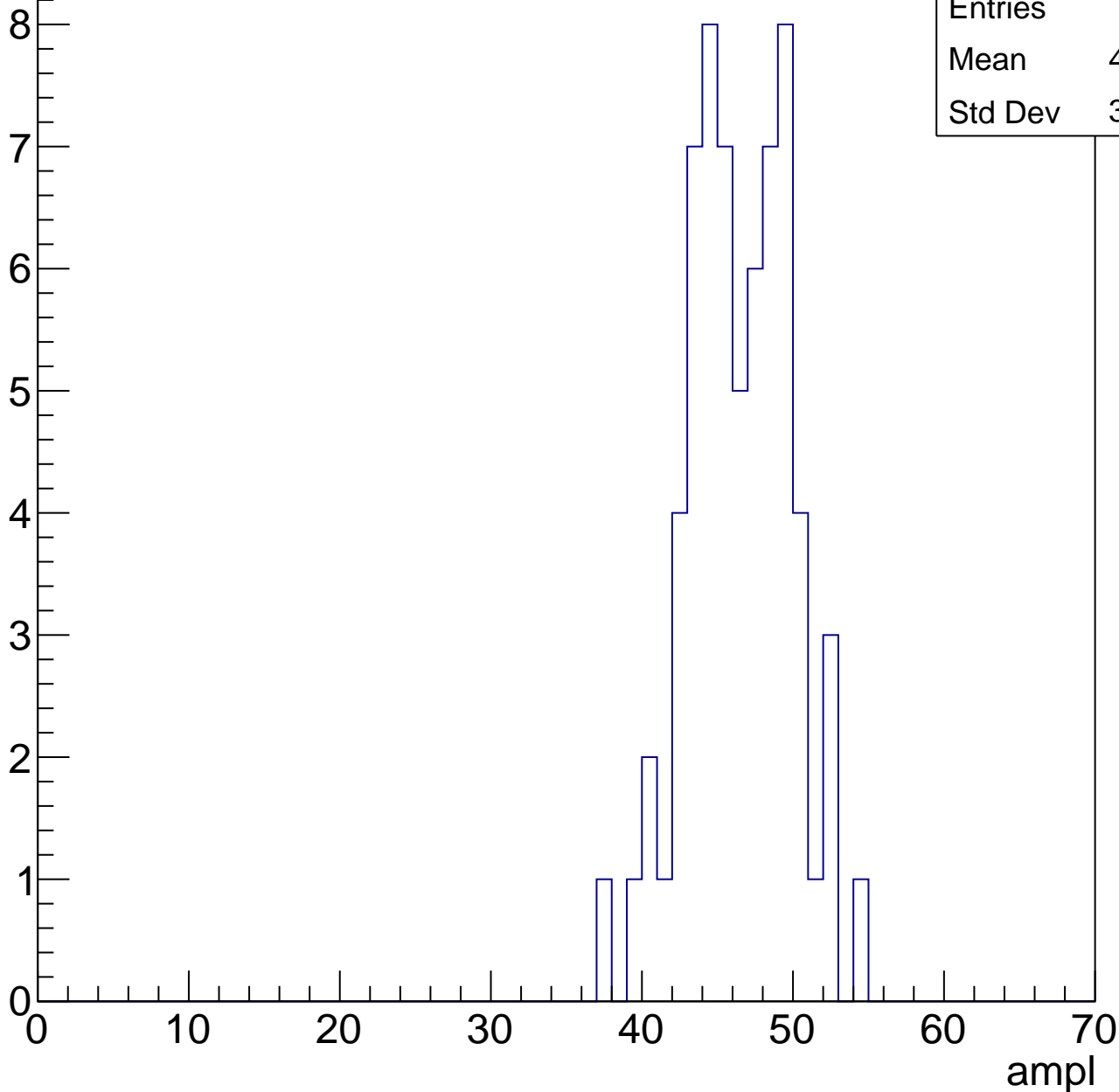


# B1L002S, U18-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	45.97
Std Dev	3.393

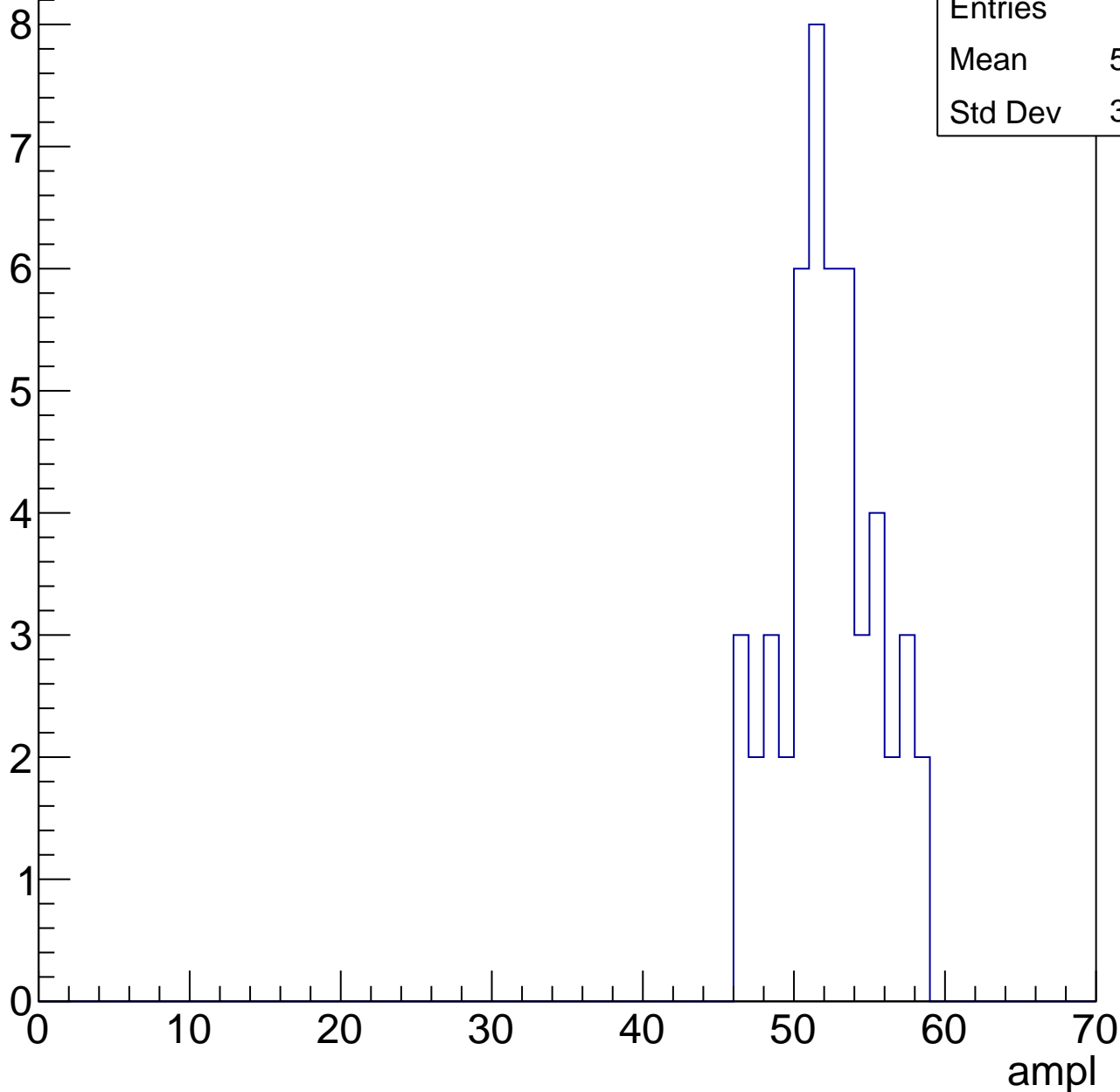


# B1L002S, U18-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

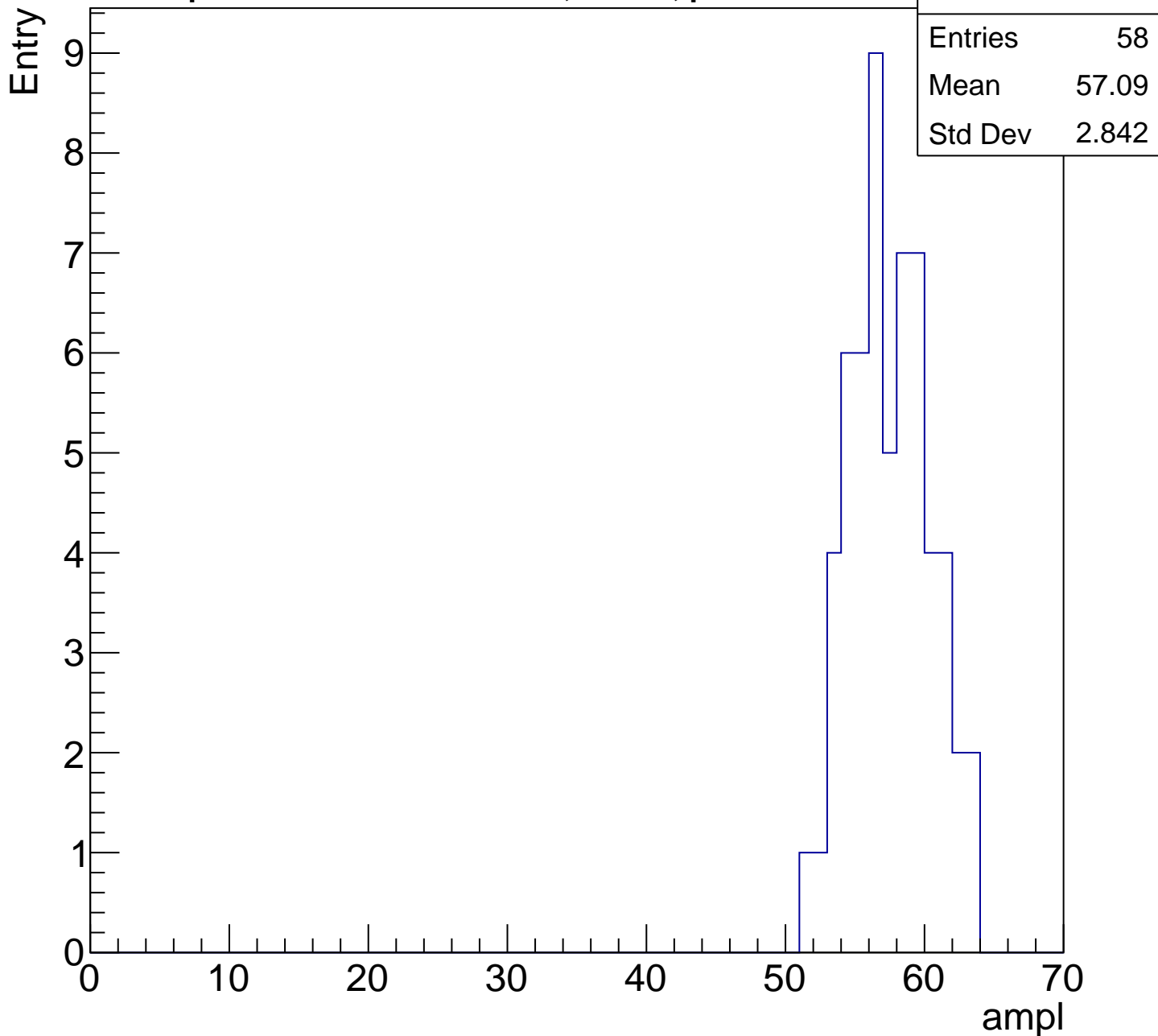
Entry

Entries	50
Mean	51.86
Std Dev	3.124



# B1L002S, U18-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

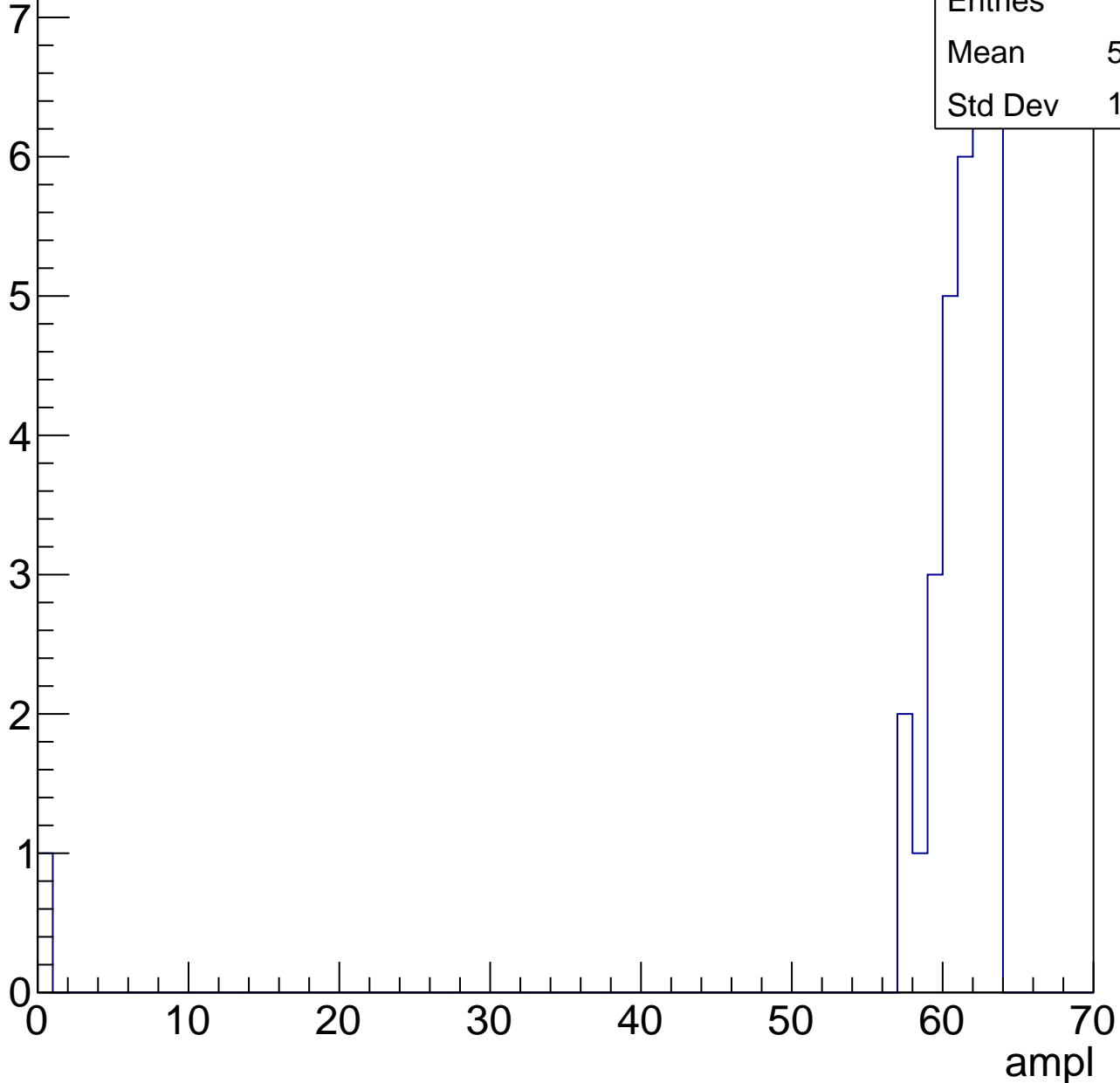


# B1L002S, U18-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.06
Std Dev	10.74

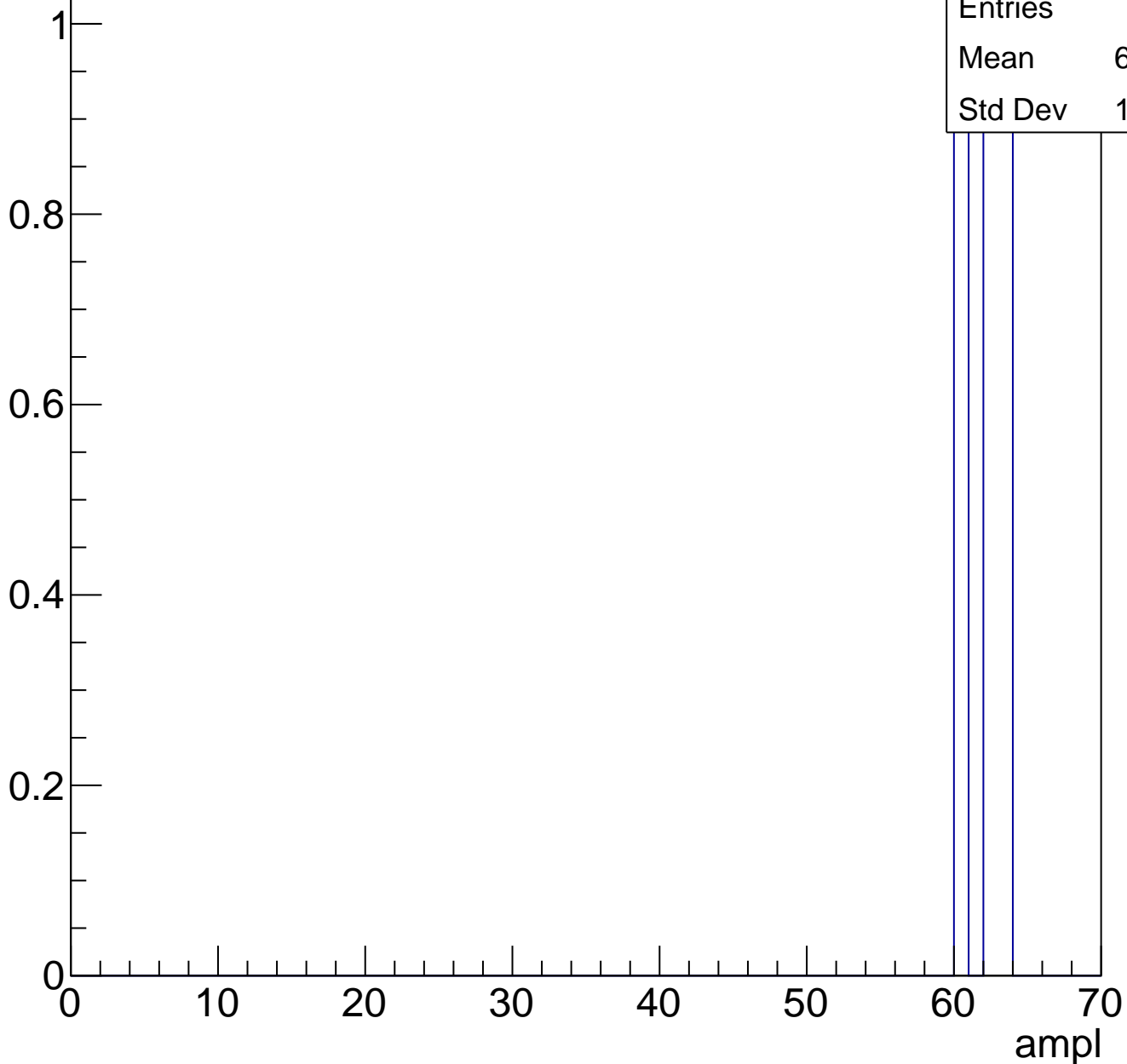




# B1L002S, U18-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch7, adc0

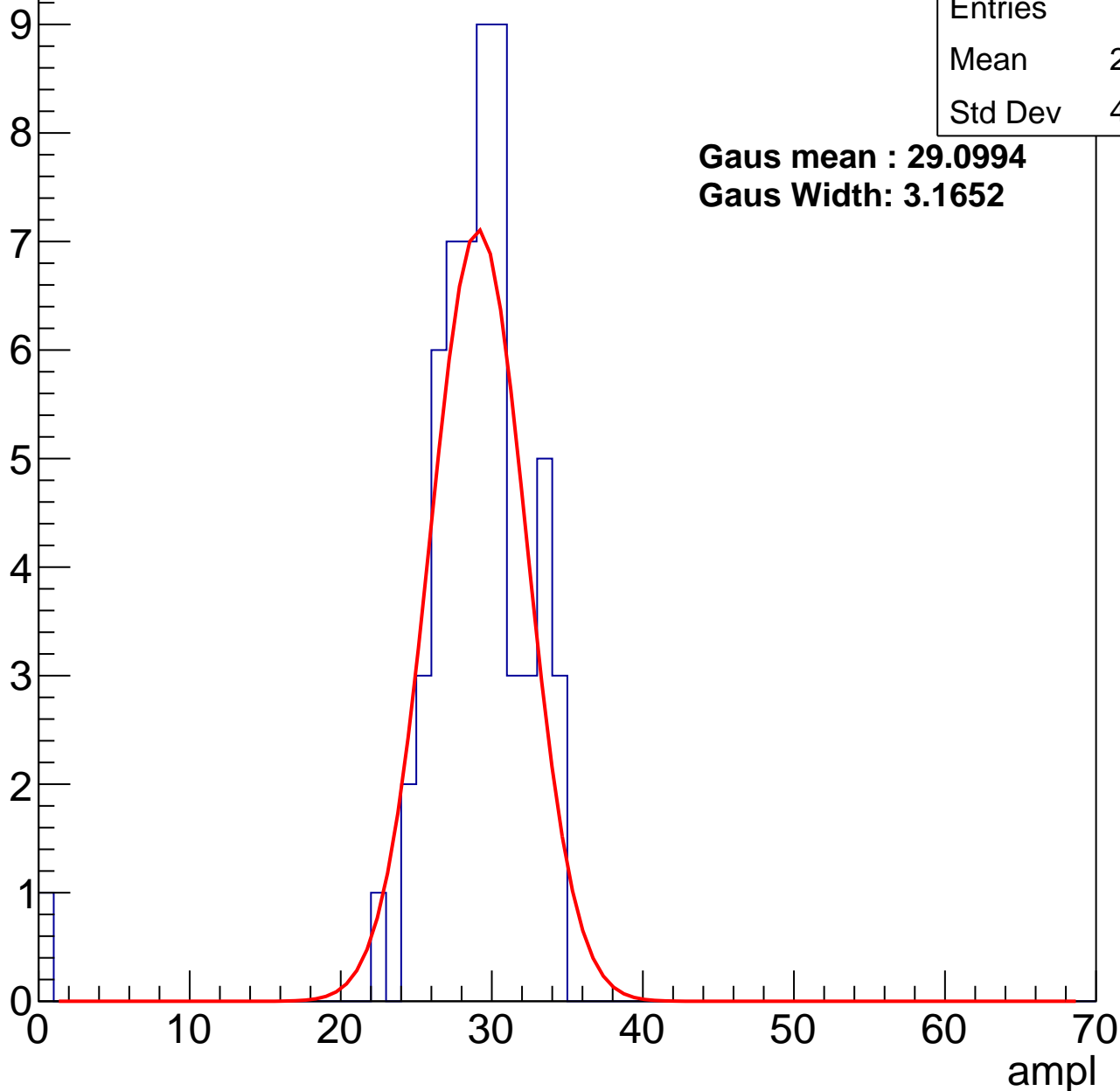
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.36
Std Dev	4.613

**Gaus mean : 29.0994**

**Gaus Width: 3.1652**



# B1L002S, U18-ch7, adc1

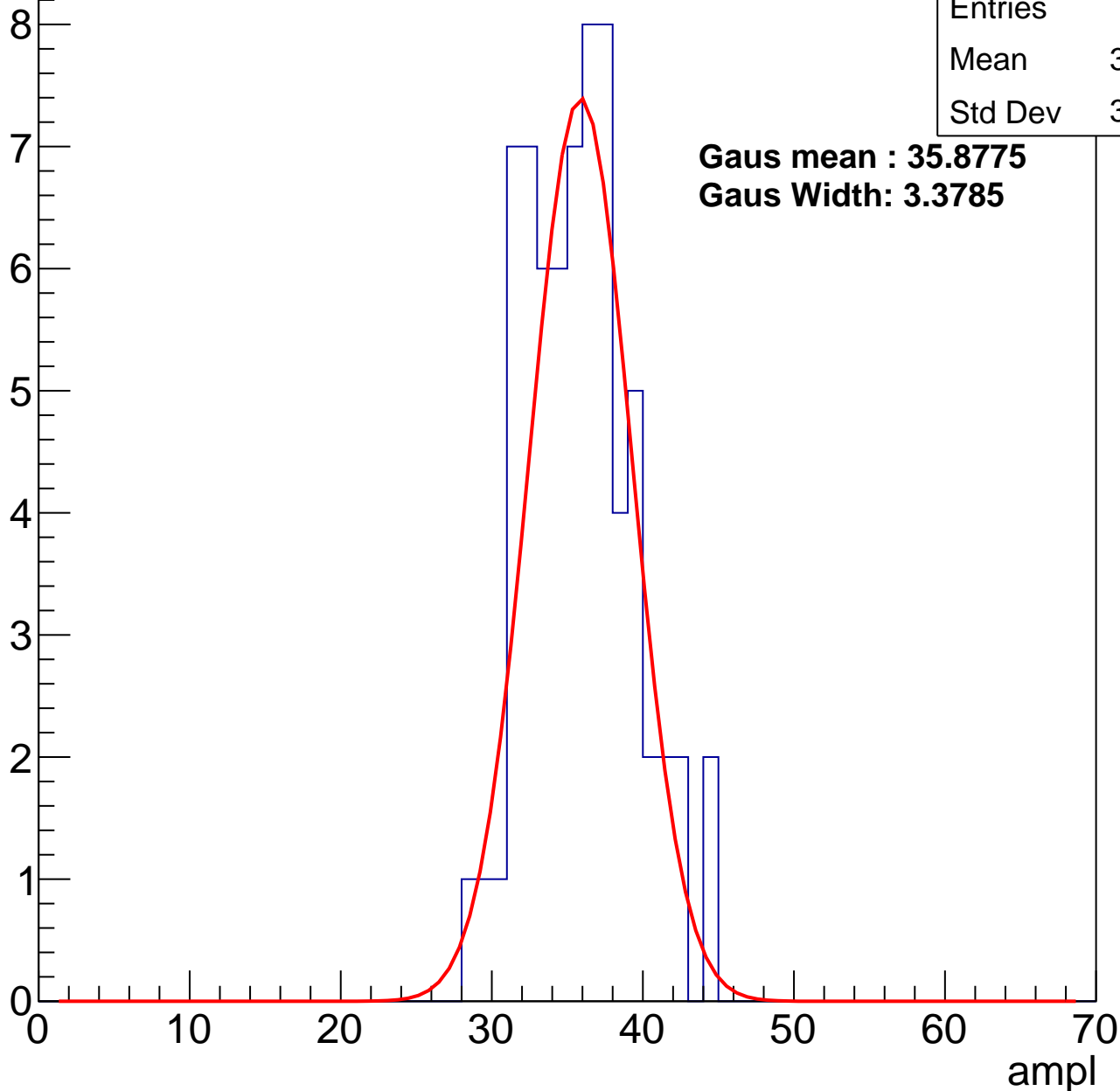
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	35.36
Std Dev	3.497

**Gaus mean : 35.8775**

**Gaus Width: 3.3785**



# B1L002S, U18-ch7, adc2

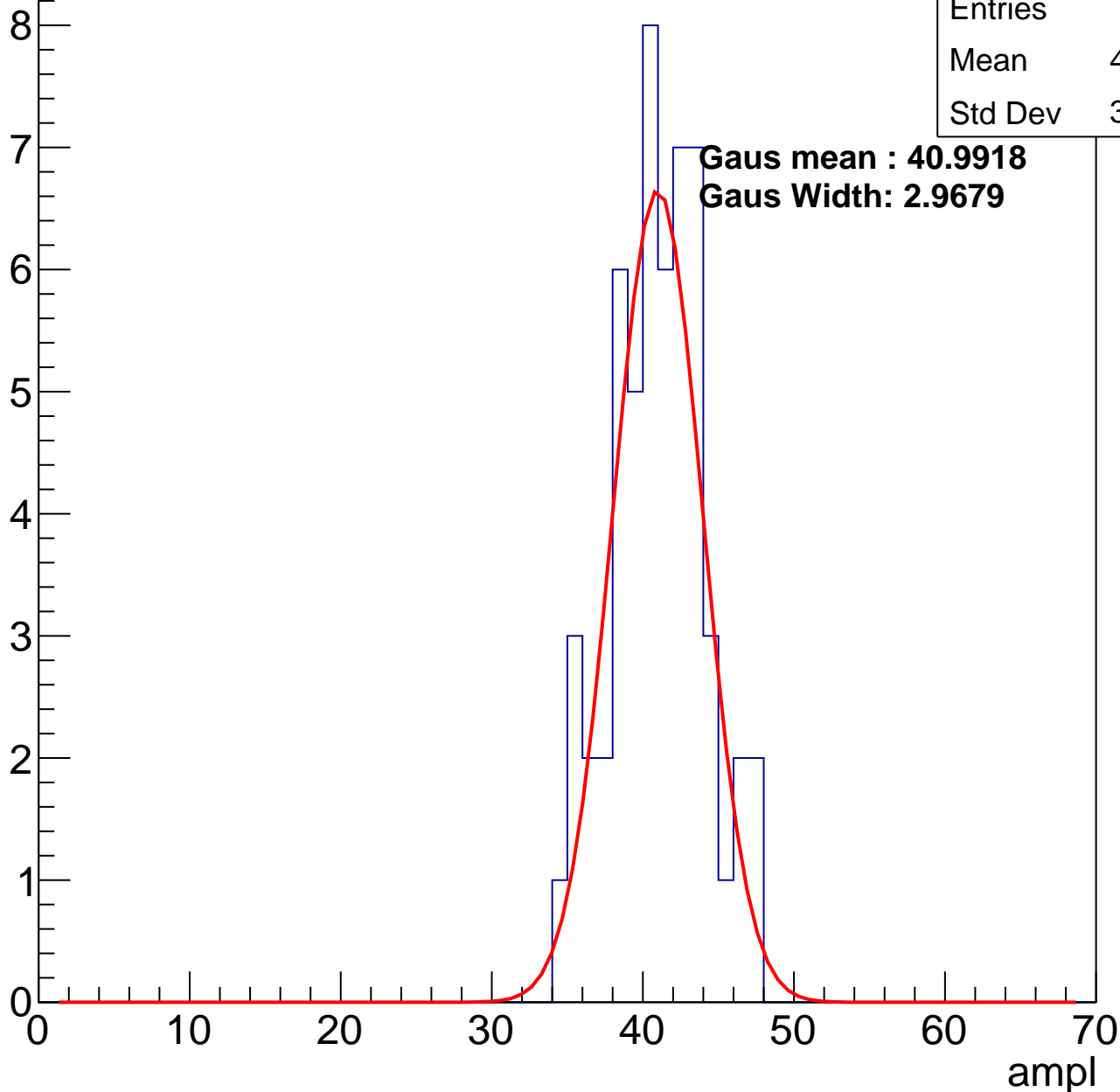
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.58
Std Dev	3.049

**Gaus mean : 40.9918**

**Gaus Width: 2.9679**



# B1L002S, U18-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	47.88
Std Dev	3.191

Entry

10

8

6

4

2

0

0

10

20

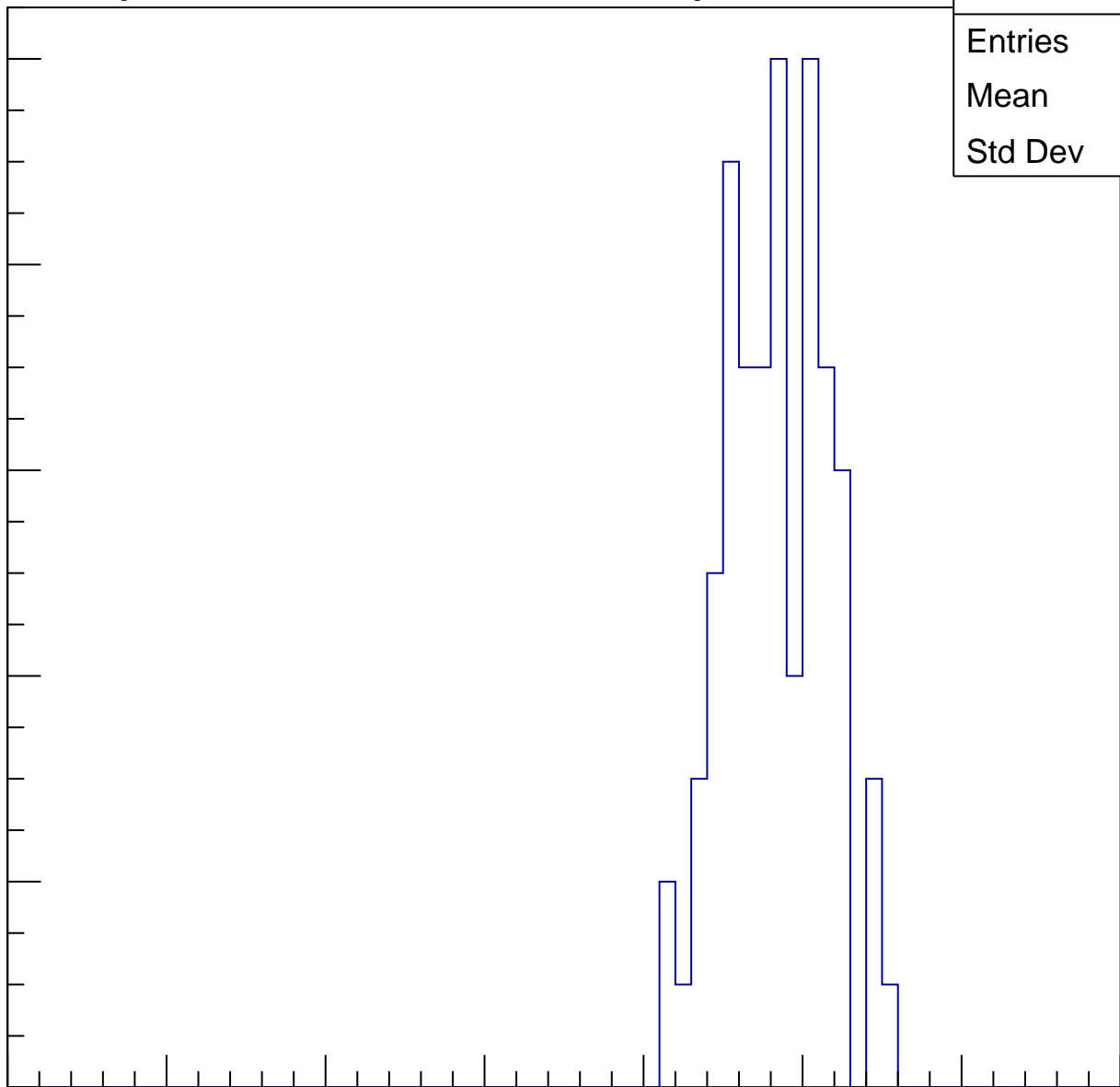
30

40

50

60

ampl

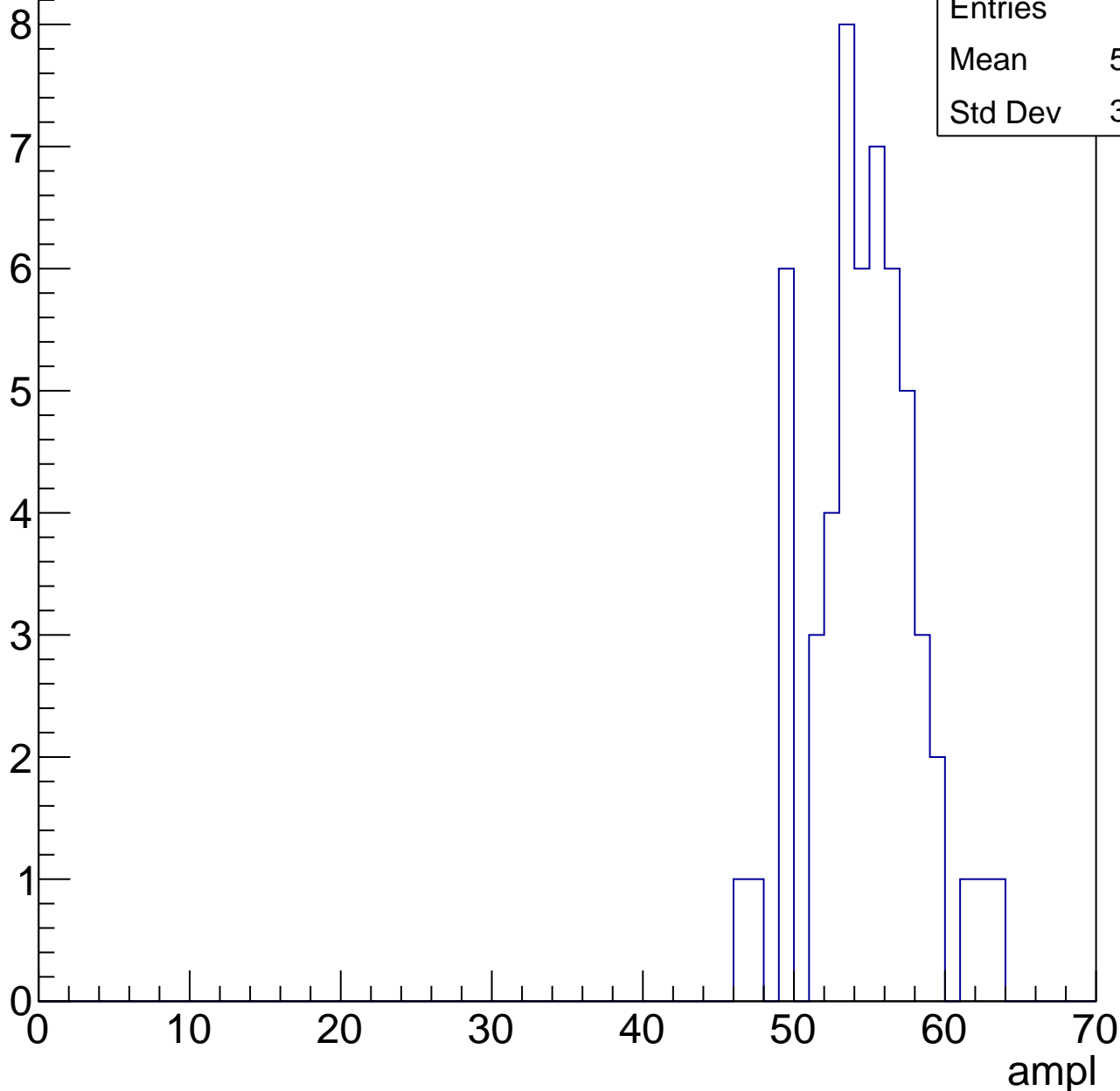


# B1L002S, U18-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	54.18
Std Dev	3.517

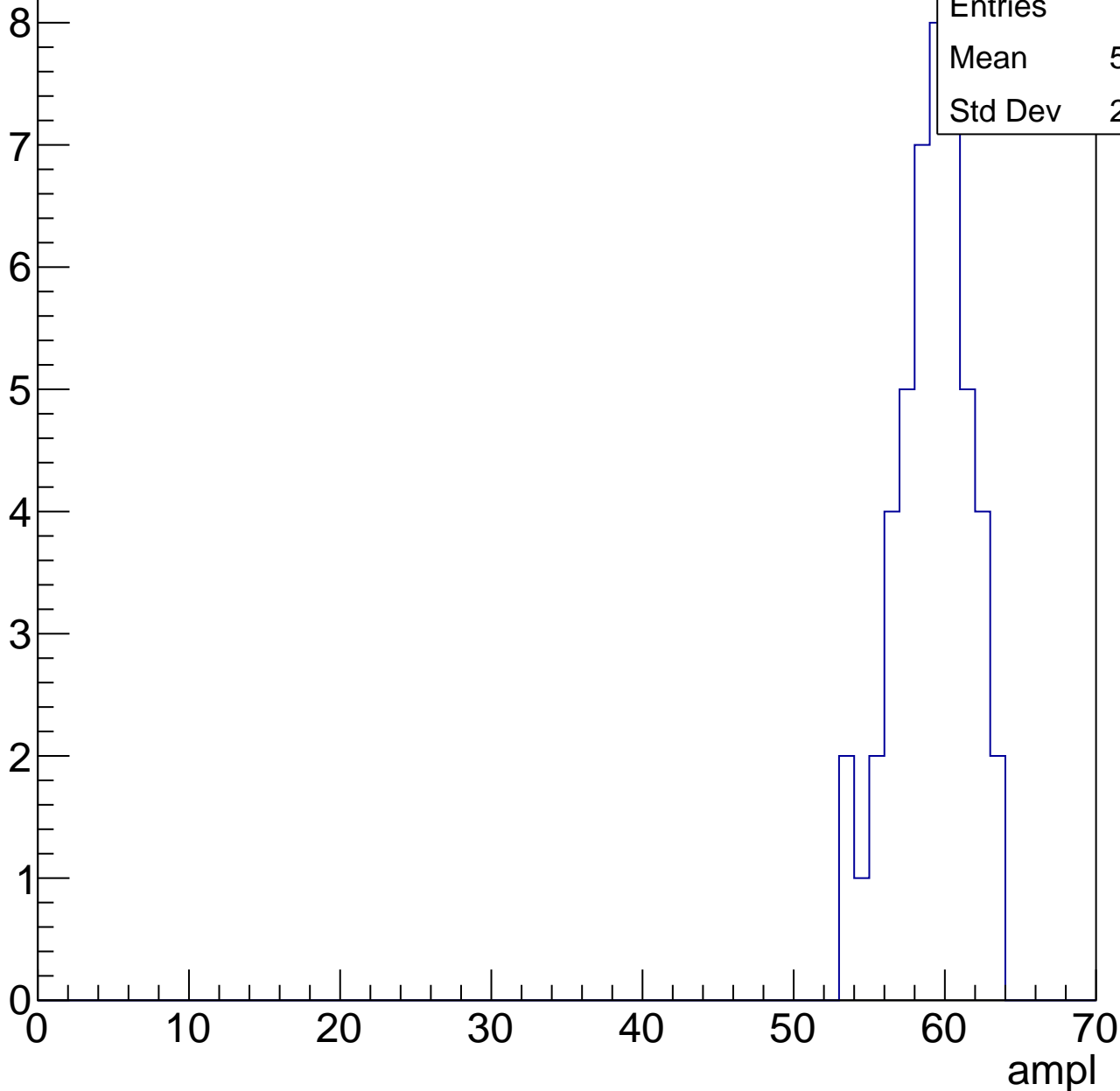


# B1L002S, U18-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.67
Std Dev	2.427

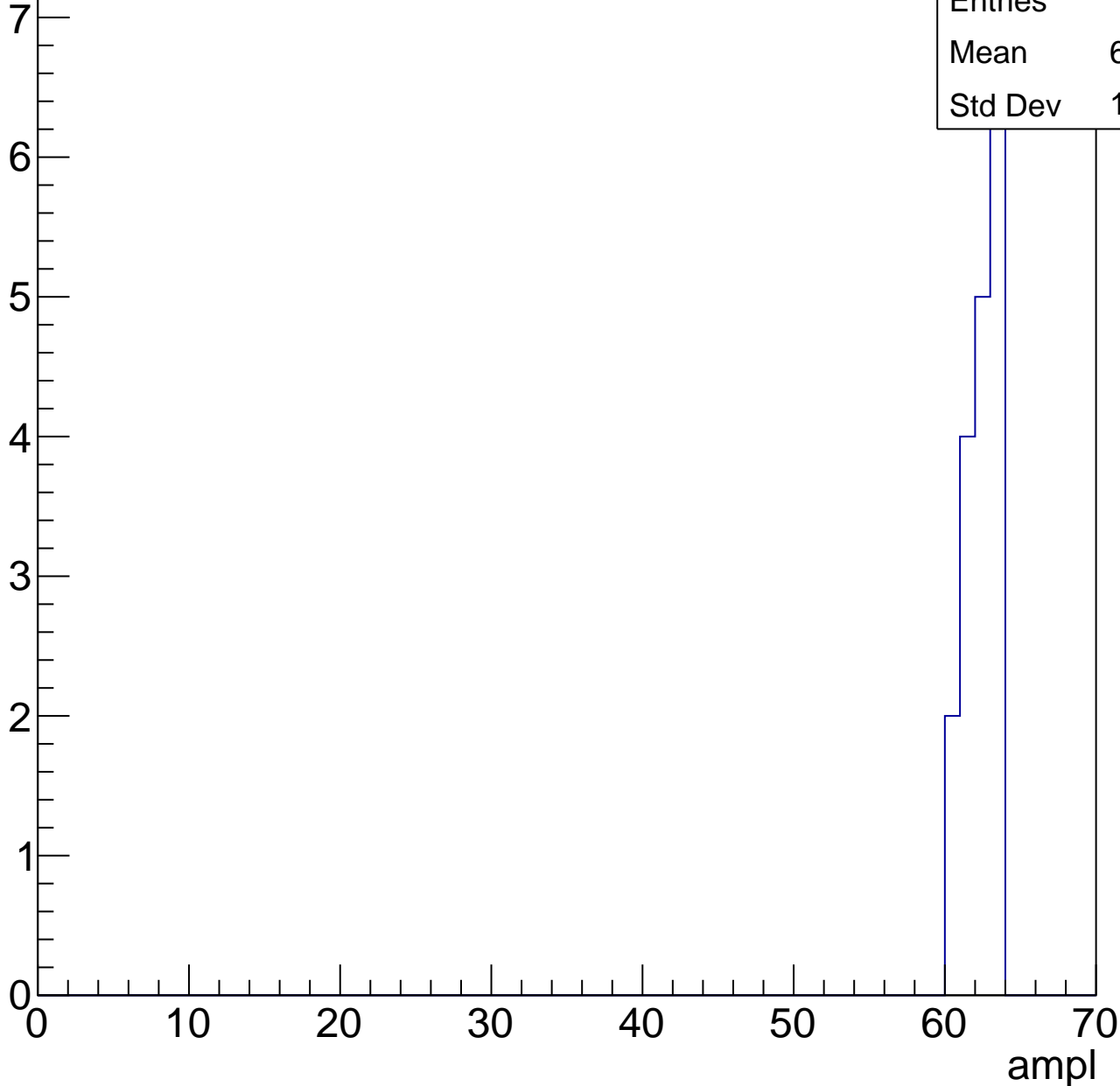


# B1L002S, U18-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.94
Std Dev	1.026





# B1L002S, U18-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U18-ch8, adc0

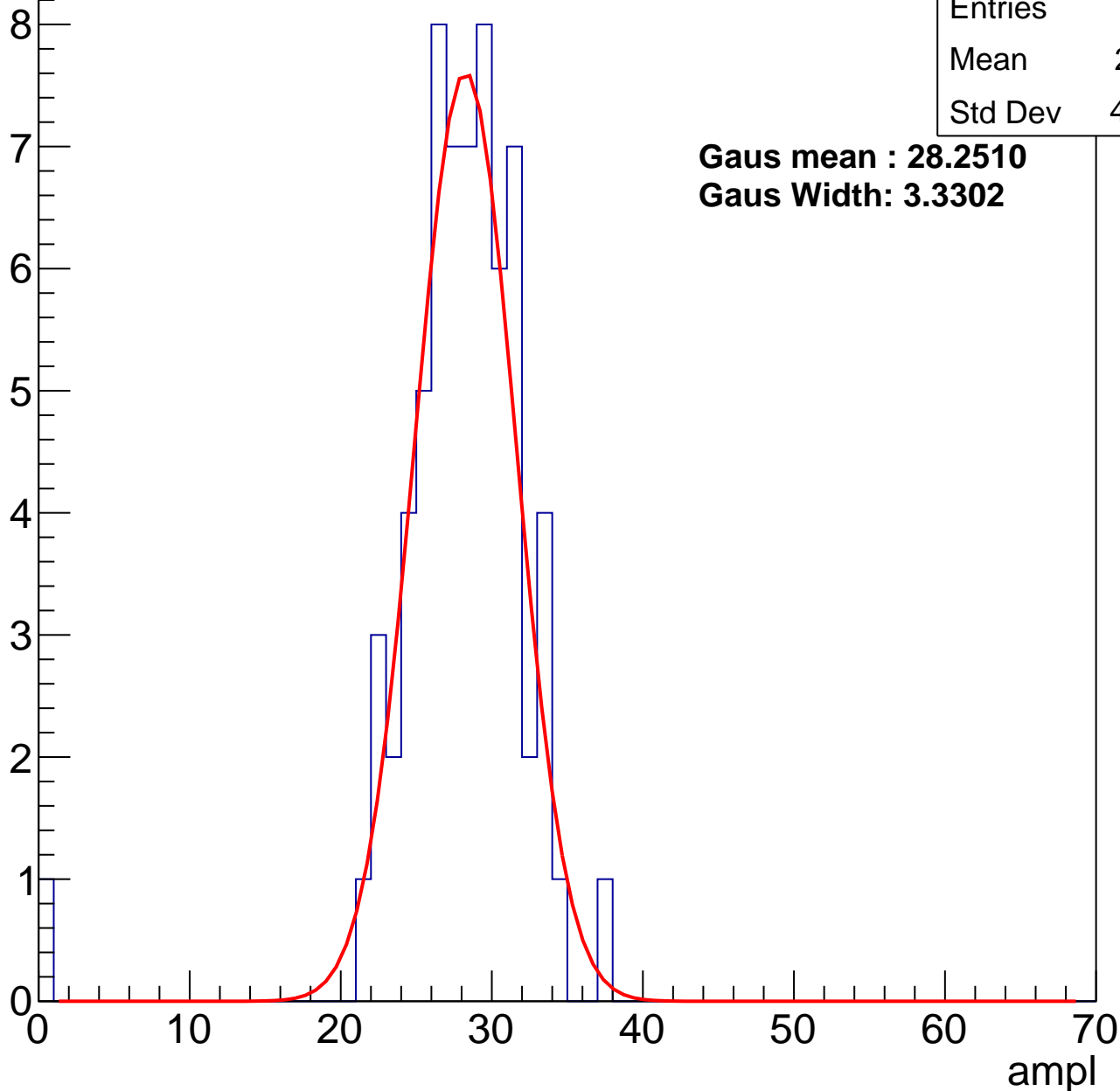
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.51
Std Dev	4.679

**Gaus mean : 28.2510**

**Gaus Width: 3.3302**



# B1L002S, U18-ch8, adc1

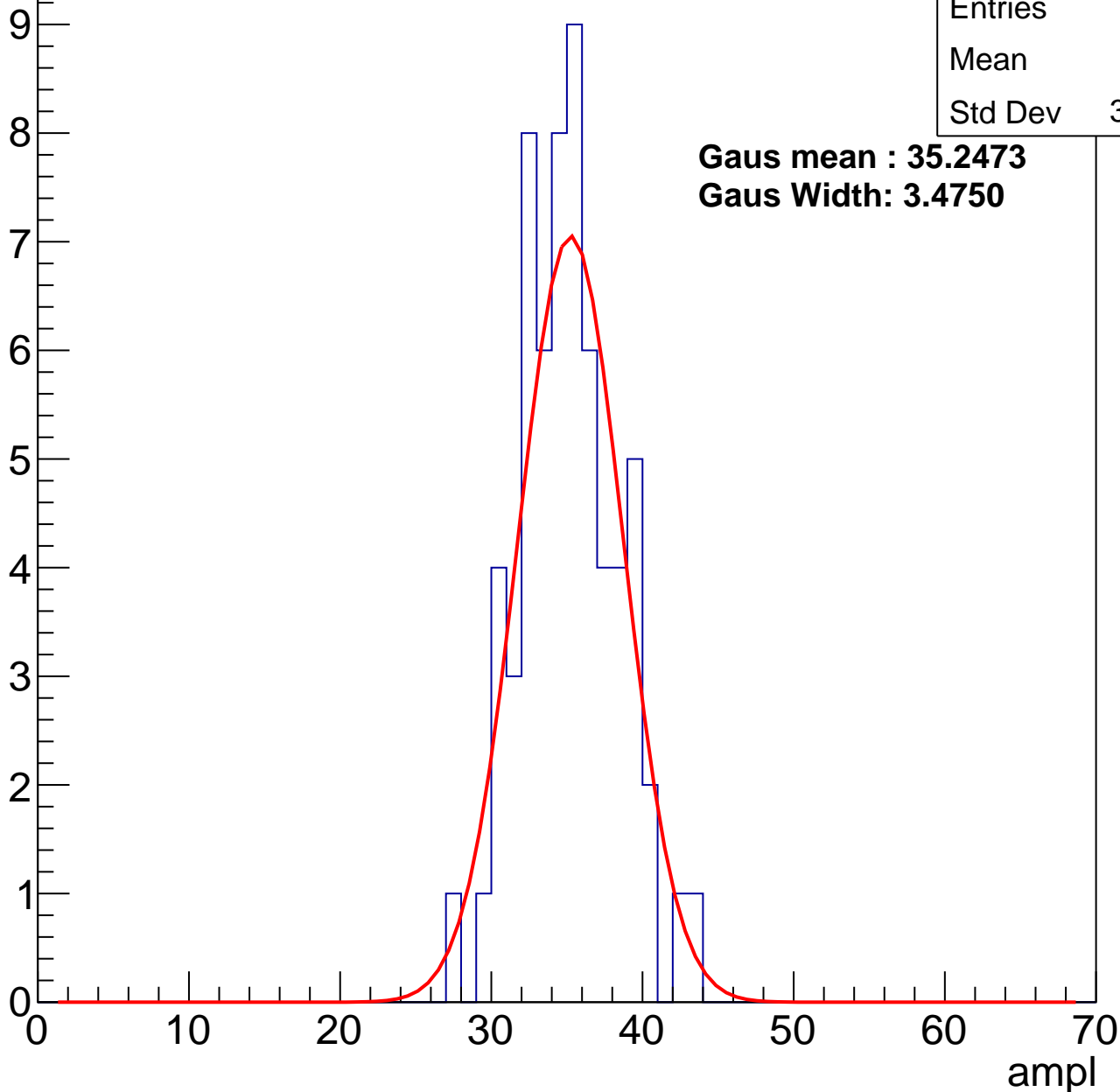
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.7
Std Dev	3.205

**Gaus mean : 35.2473**

**Gaus Width: 3.4750**



# B1L002S, U18-ch8, adc2

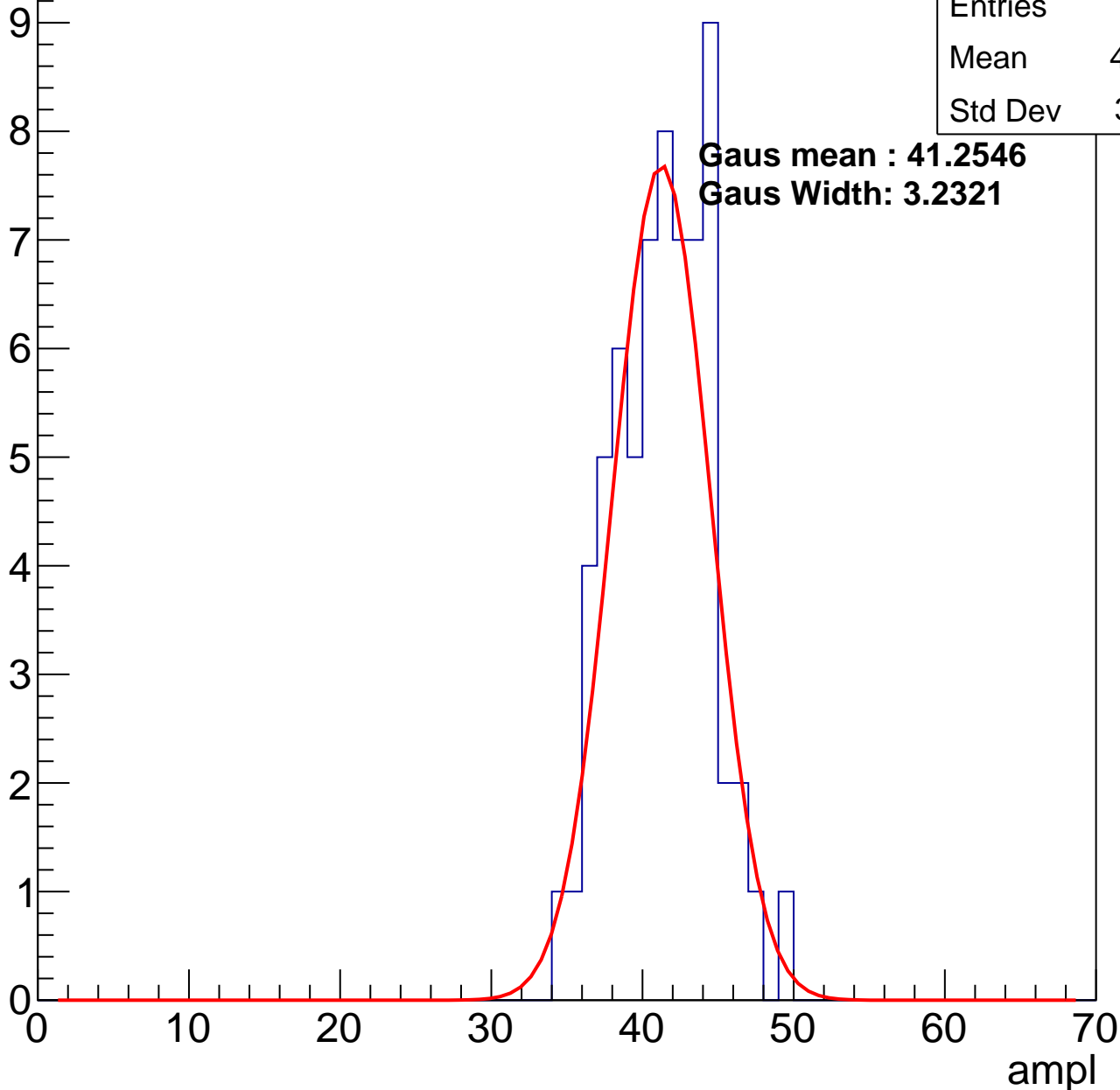
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.88
Std Dev	3.121

**Gaus mean : 41.2546**

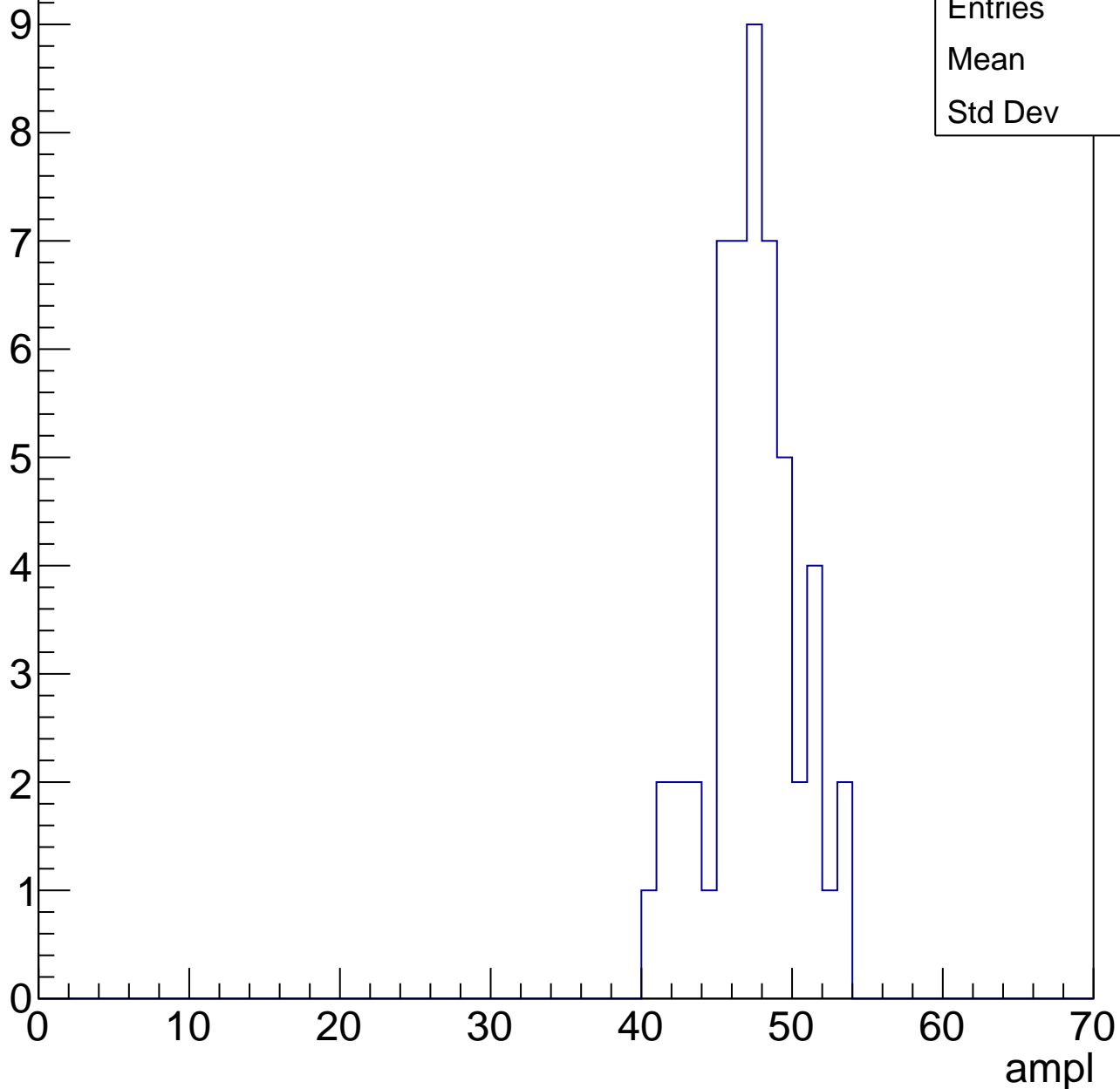
**Gaus Width: 3.2321**



# B1L002S, U18-ch8, adc3

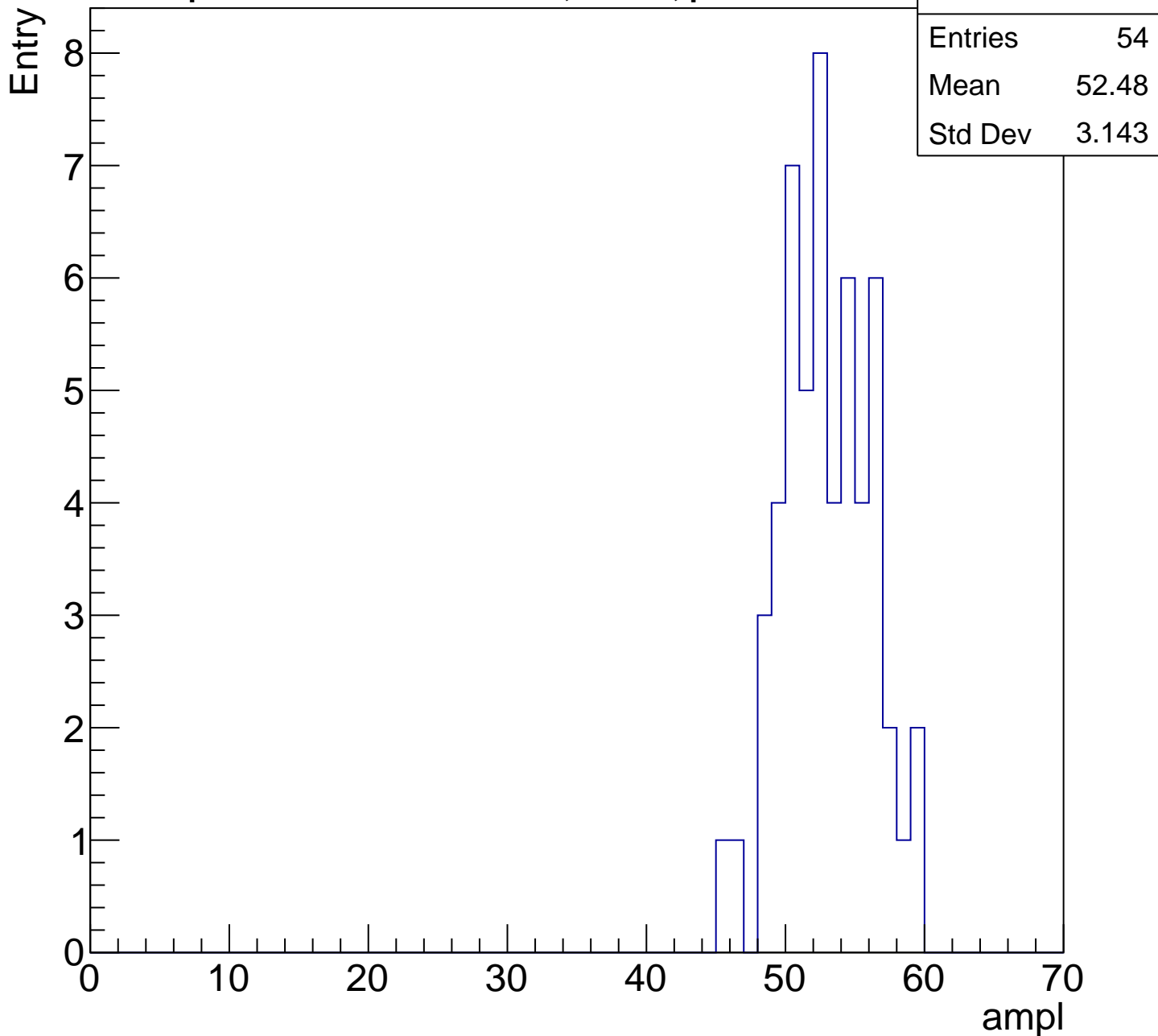
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

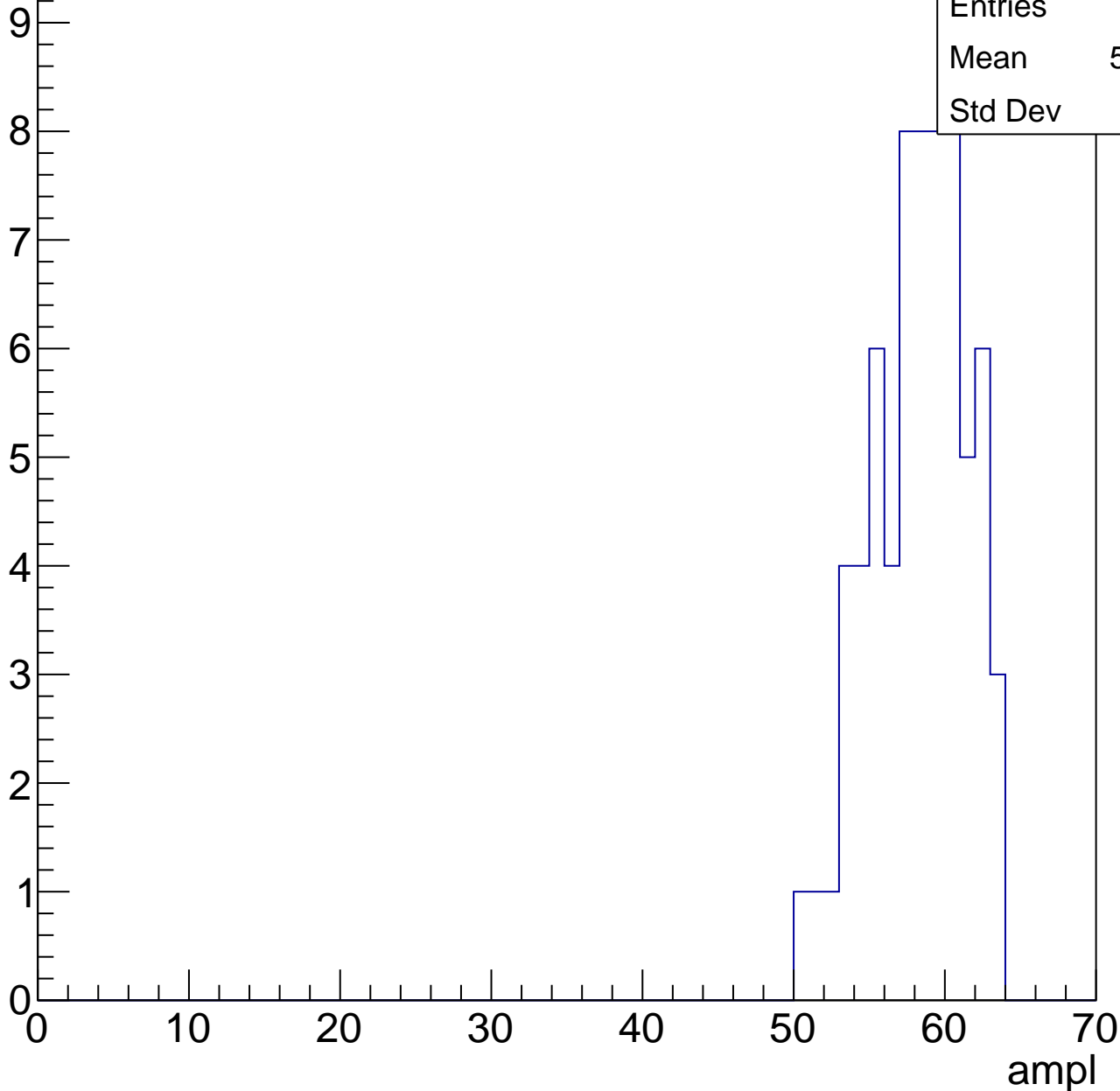


# B1L002S, U18-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	57.84
Std Dev	3.09

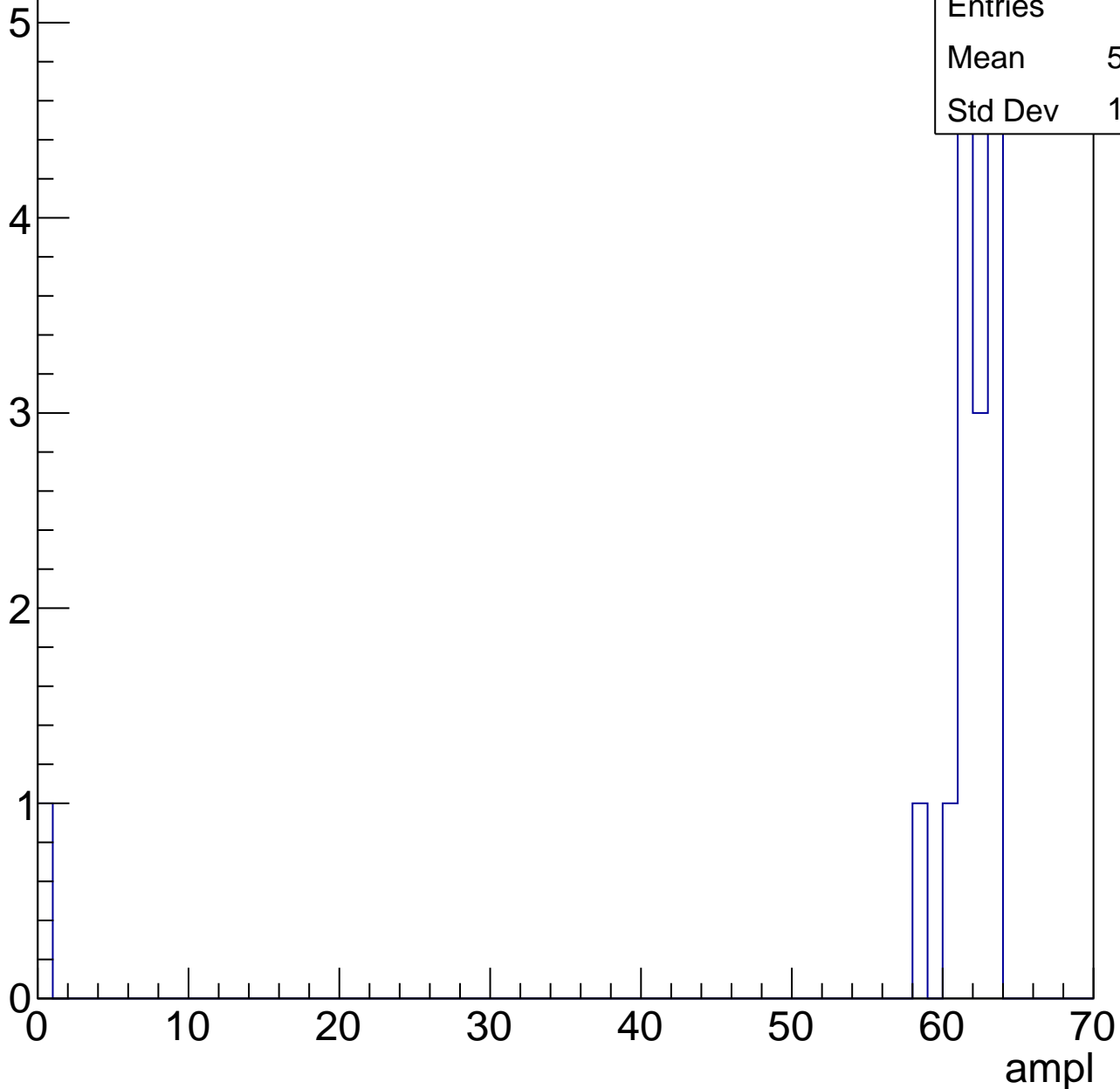


# B1L002S, U18-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.75
Std Dev	14.97

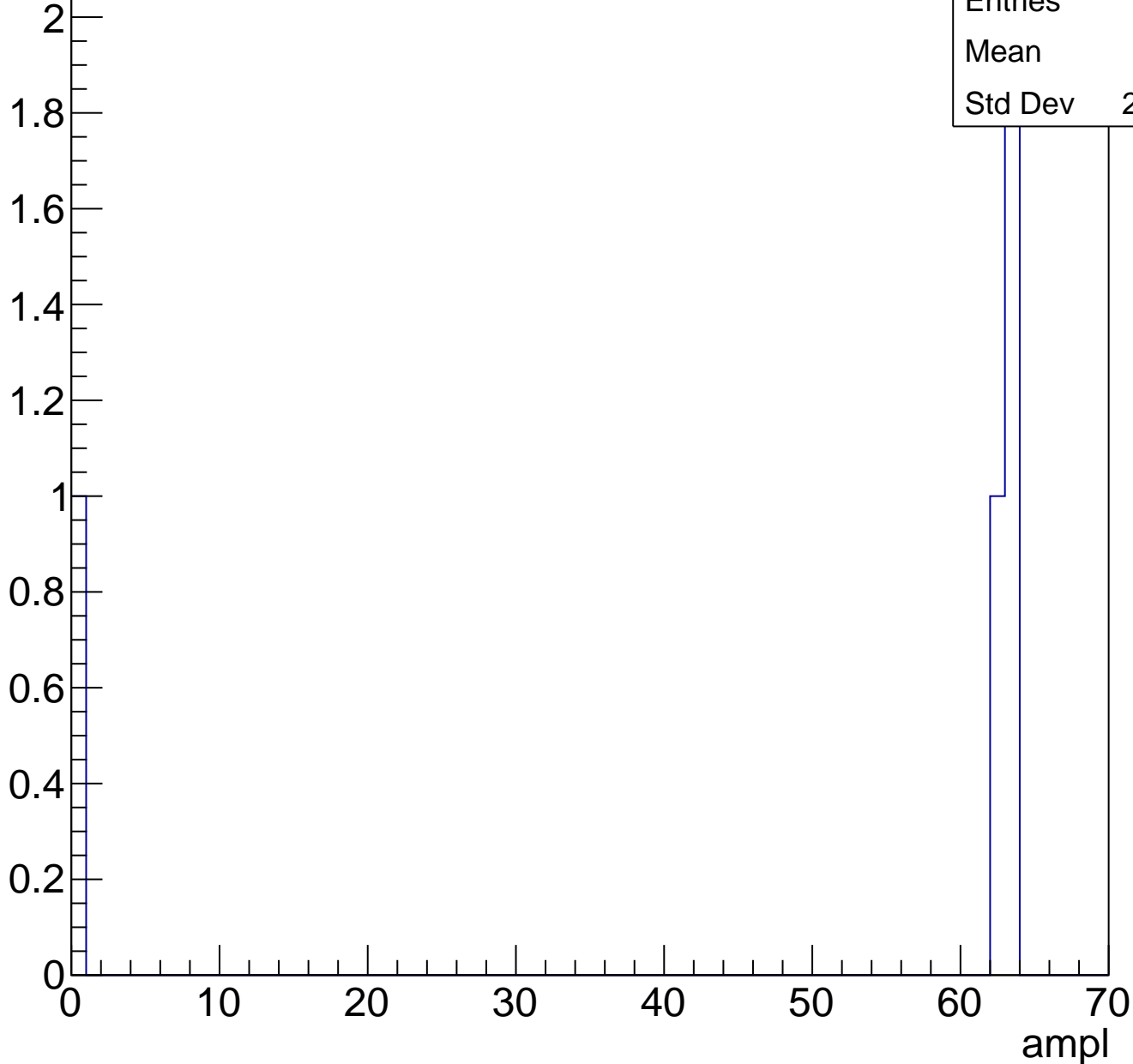




# B1L002S, U18-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch9, adc0

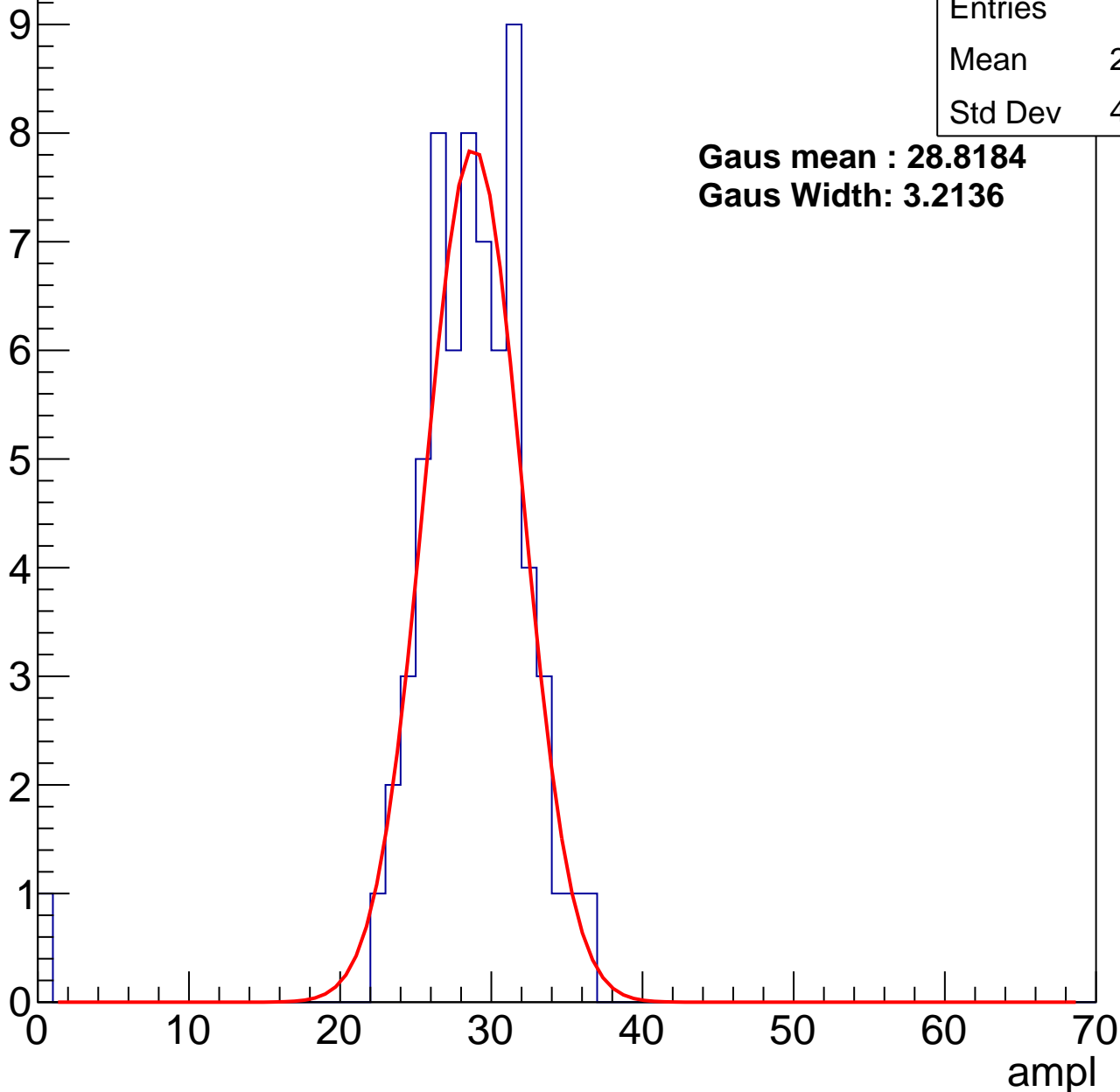
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	28.08
Std Dev	4.607

**Gaus mean : 28.8184**

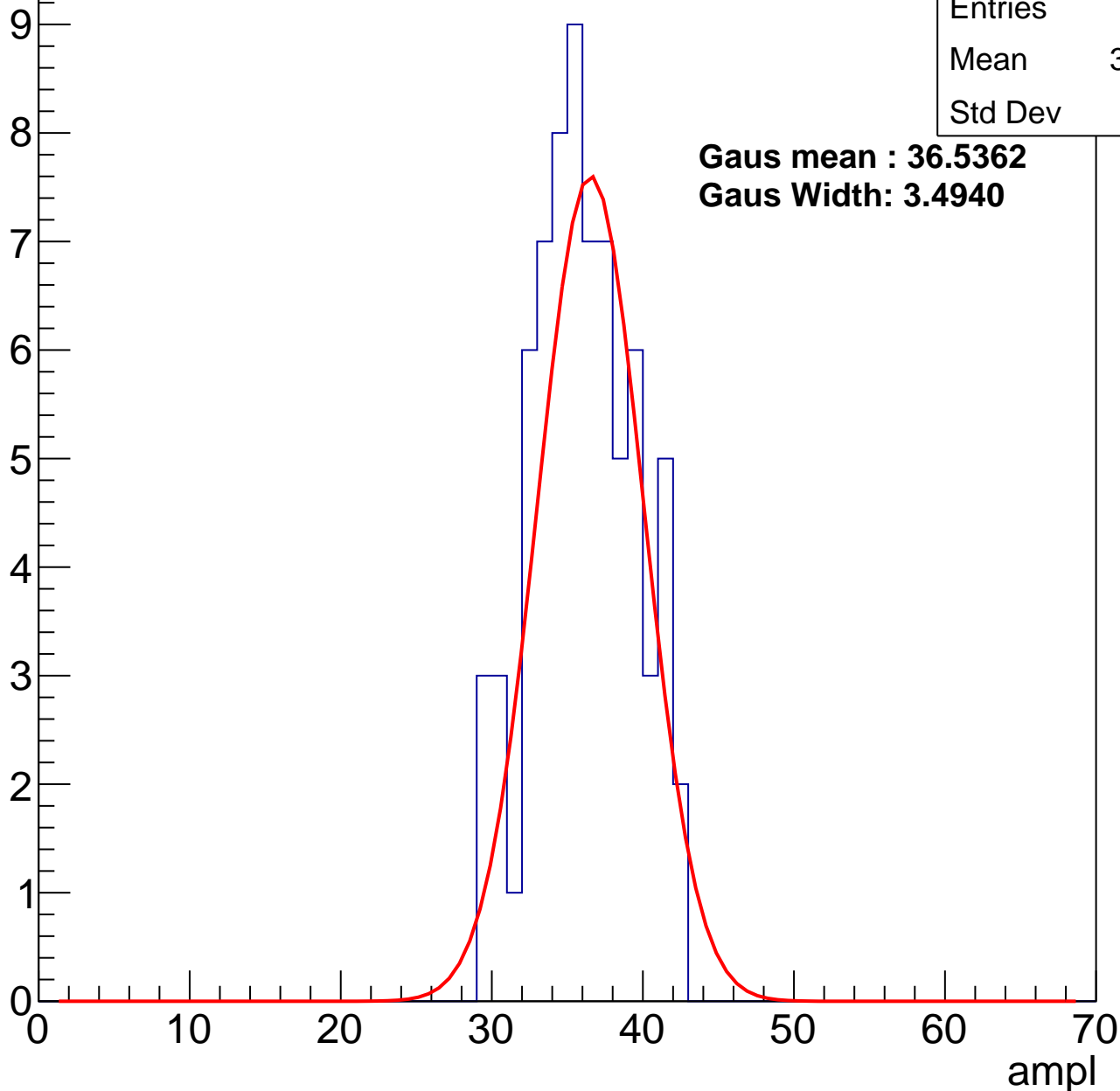
**Gaus Width: 3.2136**



# B1L002S, U18-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch9, adc2

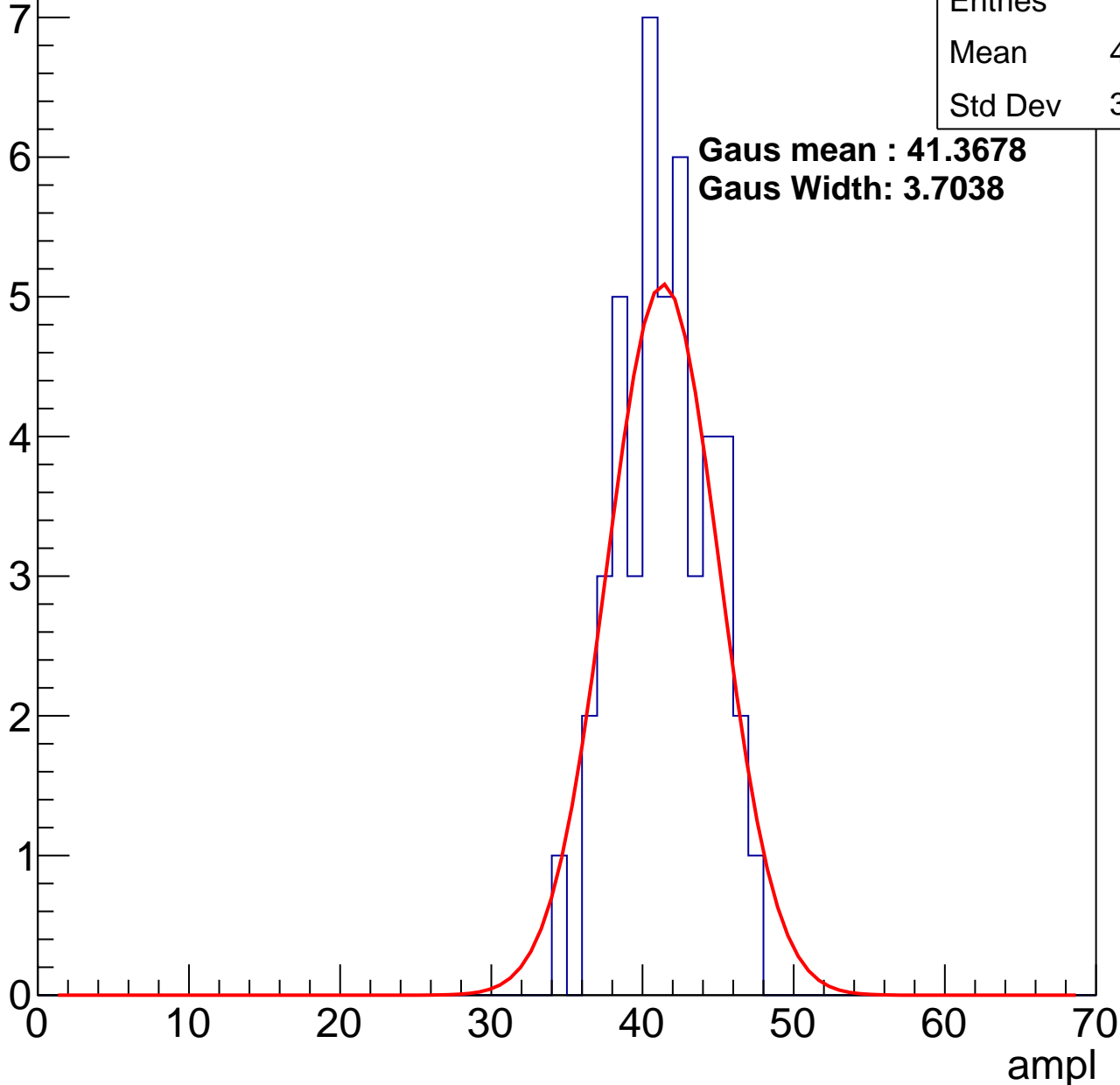
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	40.98
Std Dev	3.004

**Gaus mean : 41.3678**

**Gaus Width: 3.7038**

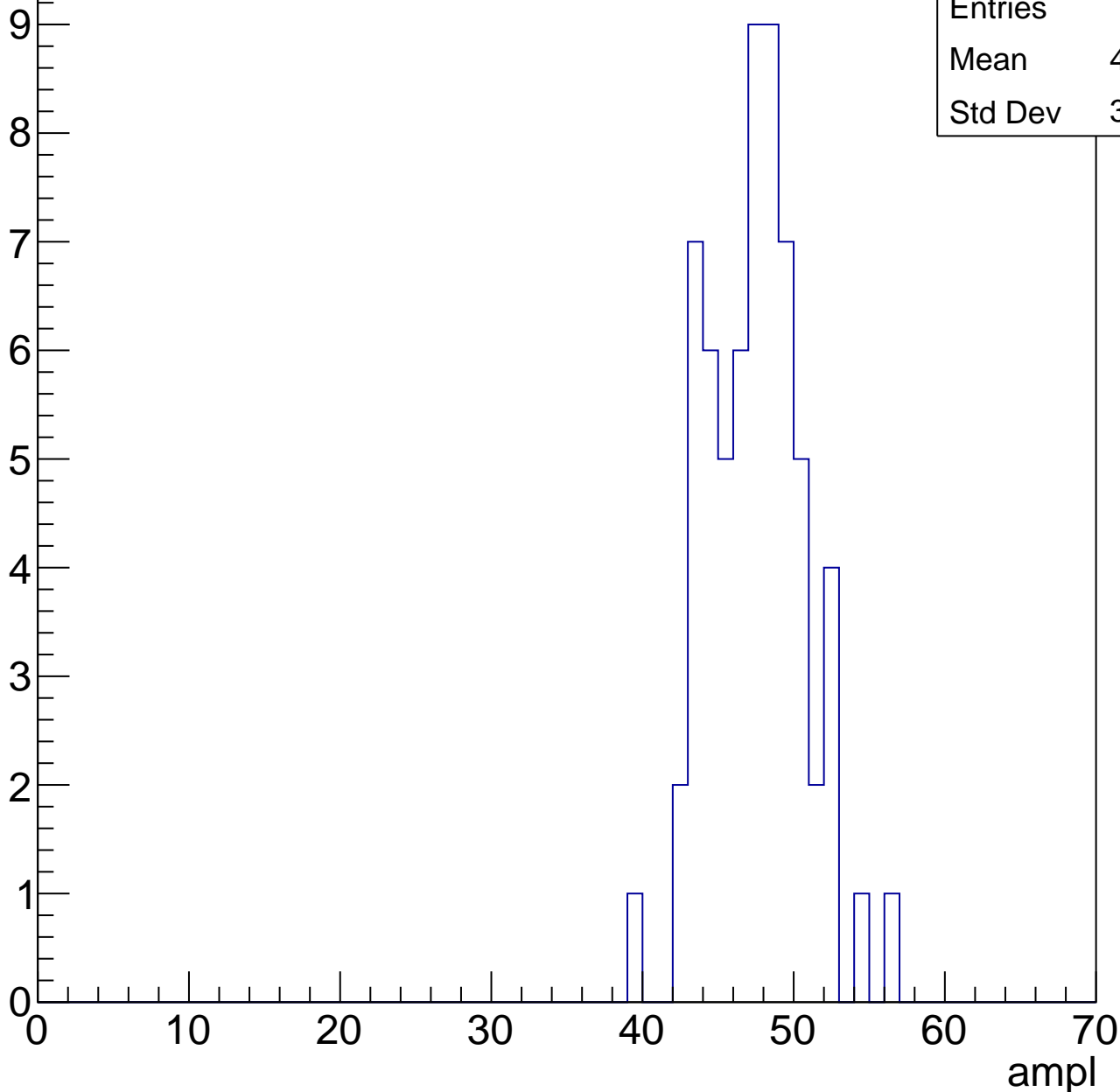


# B1L002S, U18-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

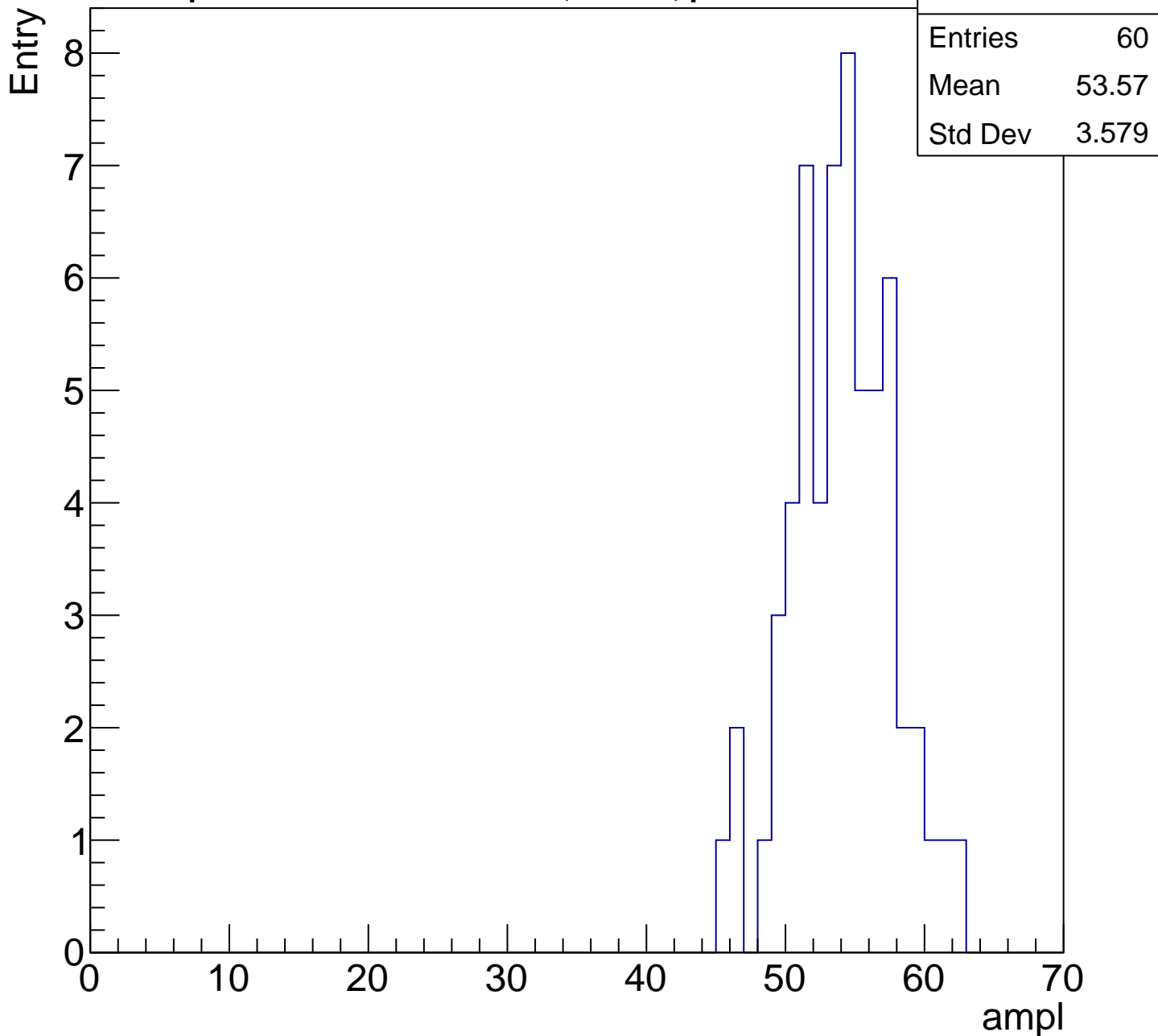
Entry

Entries	65
Mean	47.03
Std Dev	3.162



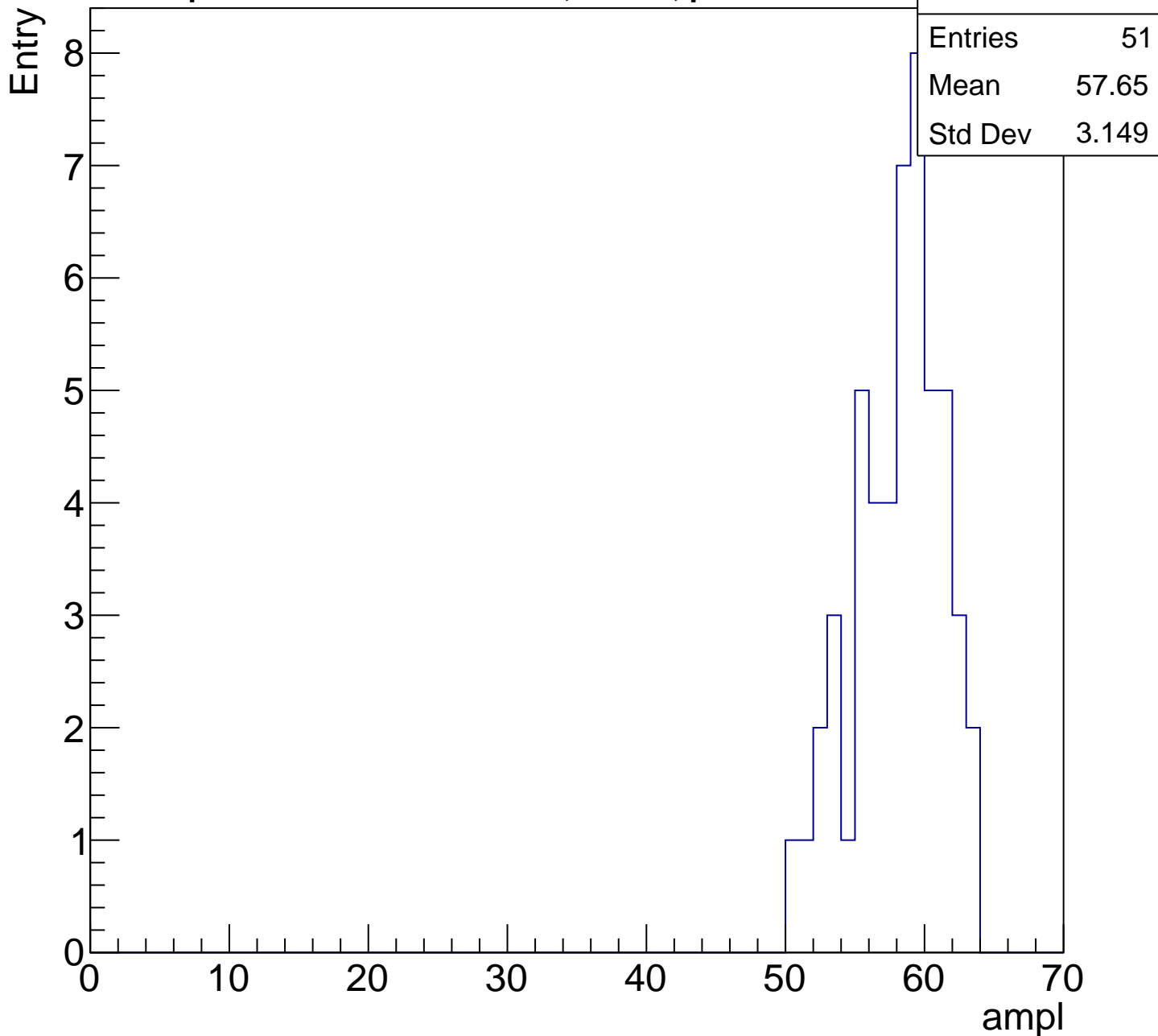
# B1L002S, U18-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

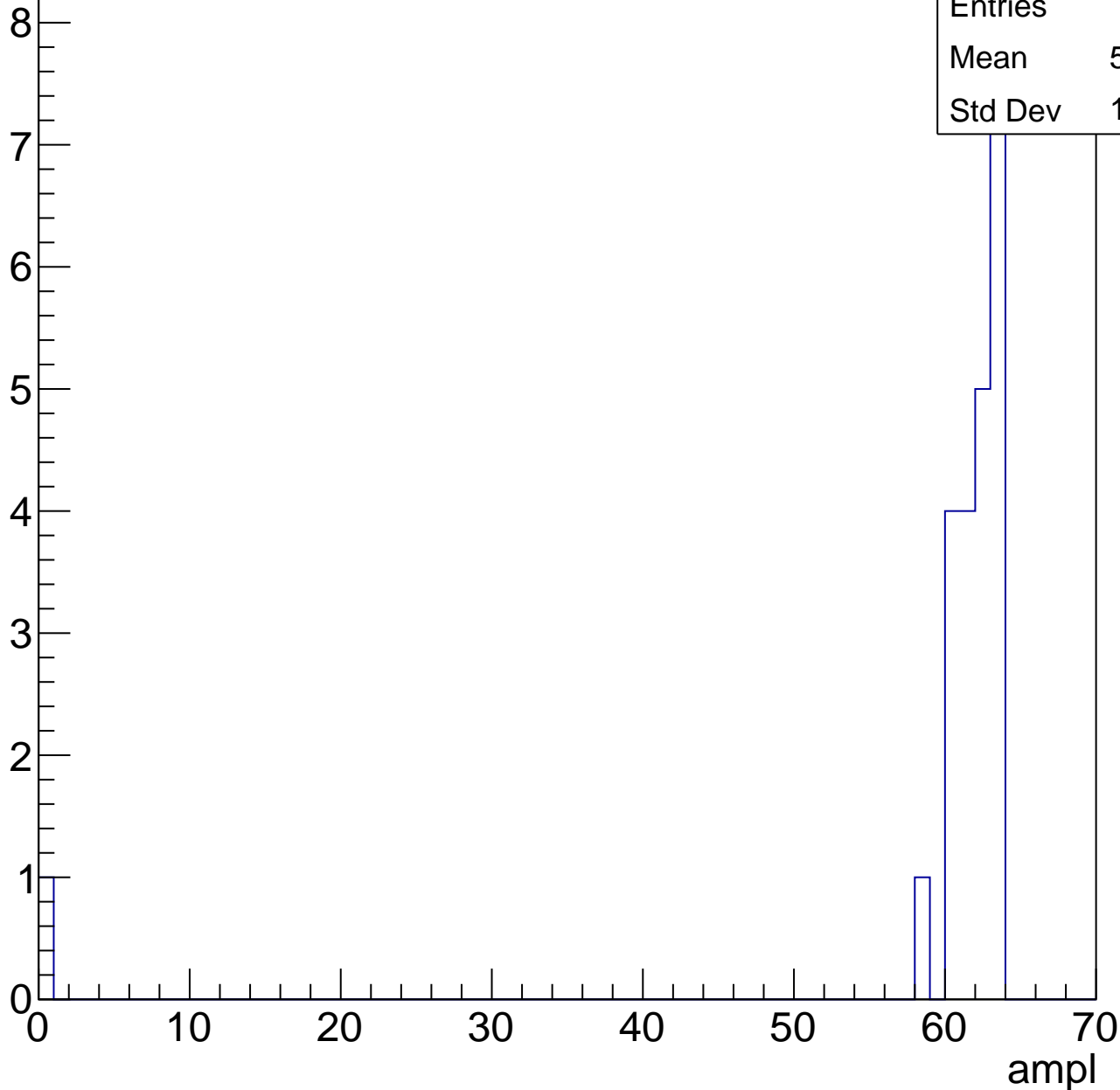


# B1L002S, U18-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.96
Std Dev	12.64

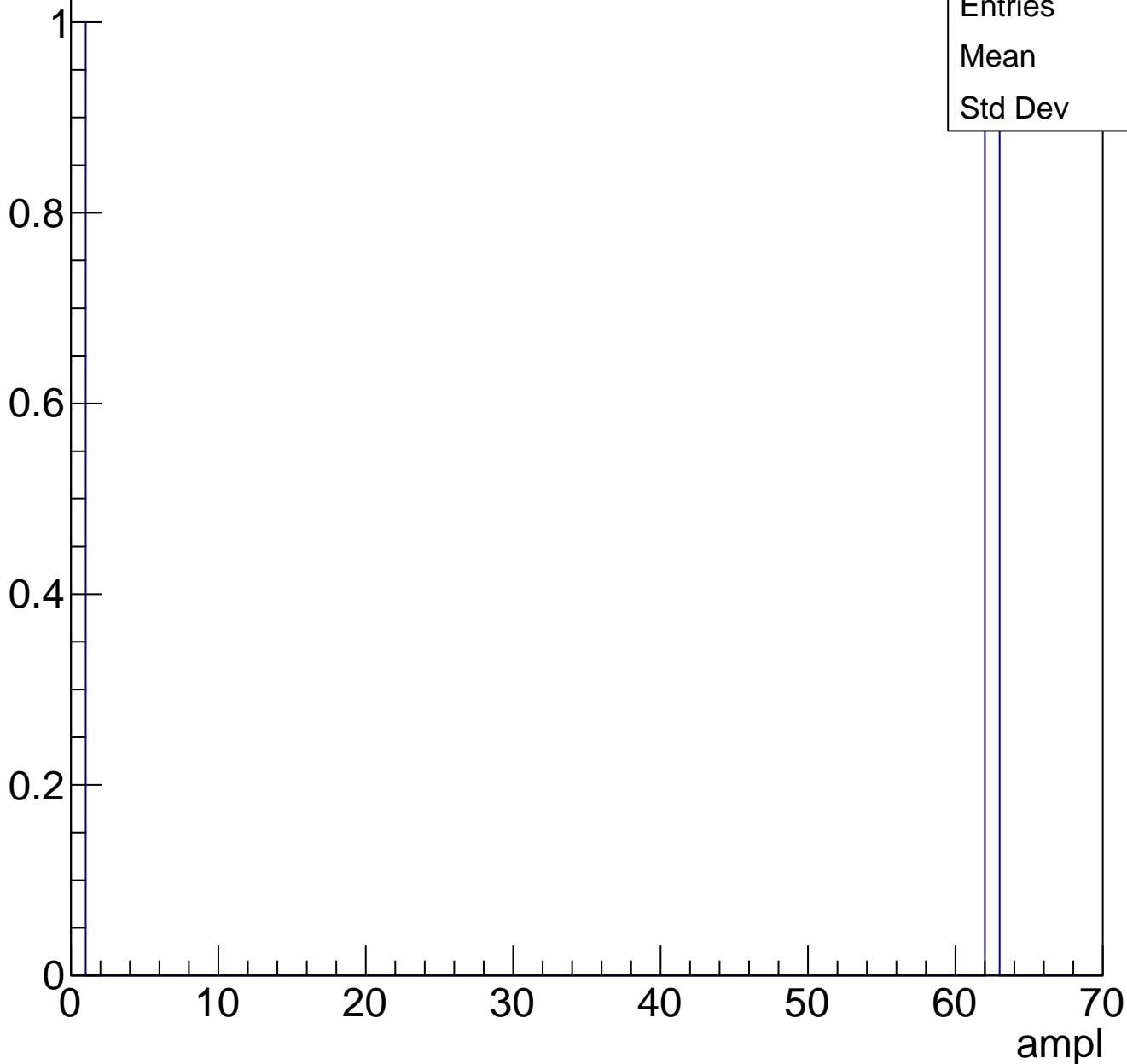




# B1L002S, U18-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch10, adc0

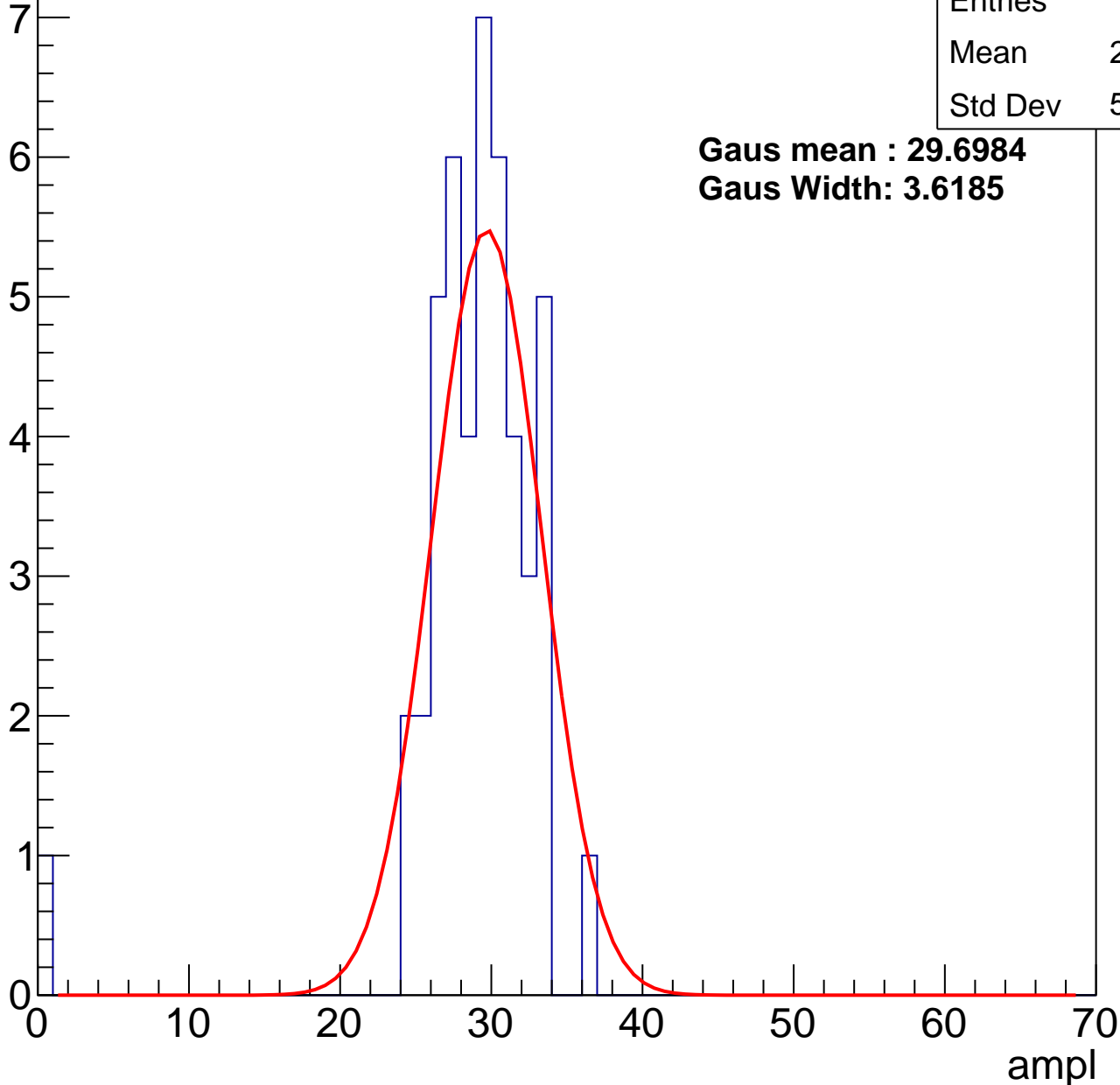
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	28.39
Std Dev	5.015

**Gaus mean : 29.6984**

**Gaus Width: 3.6185**



# B1L002S, U18-ch10, adc1

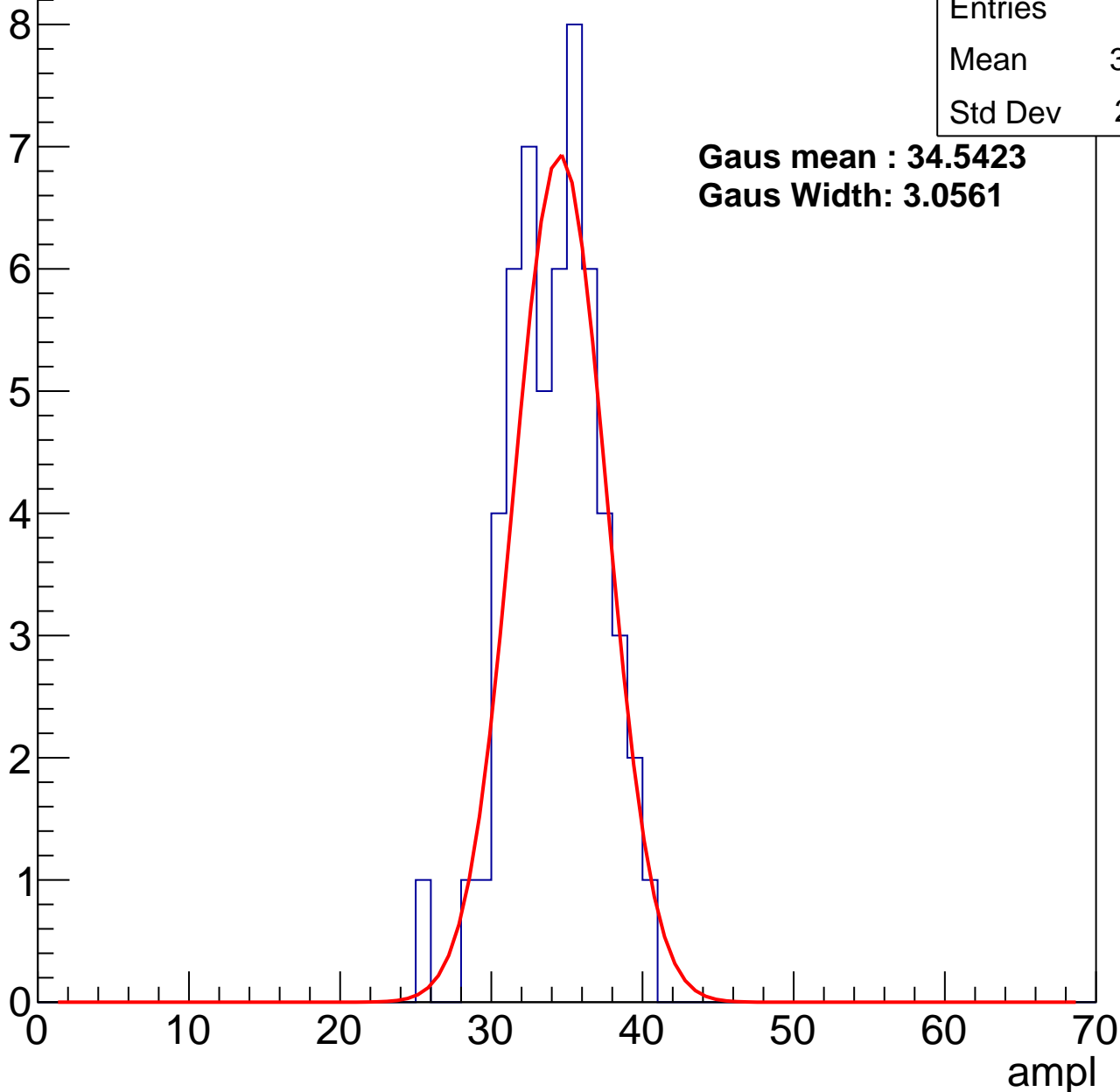
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	33.76
Std Dev	2.991

**Gaus mean : 34.5423**

**Gaus Width: 3.0561**



# B1L002S, U18-ch10, adc2

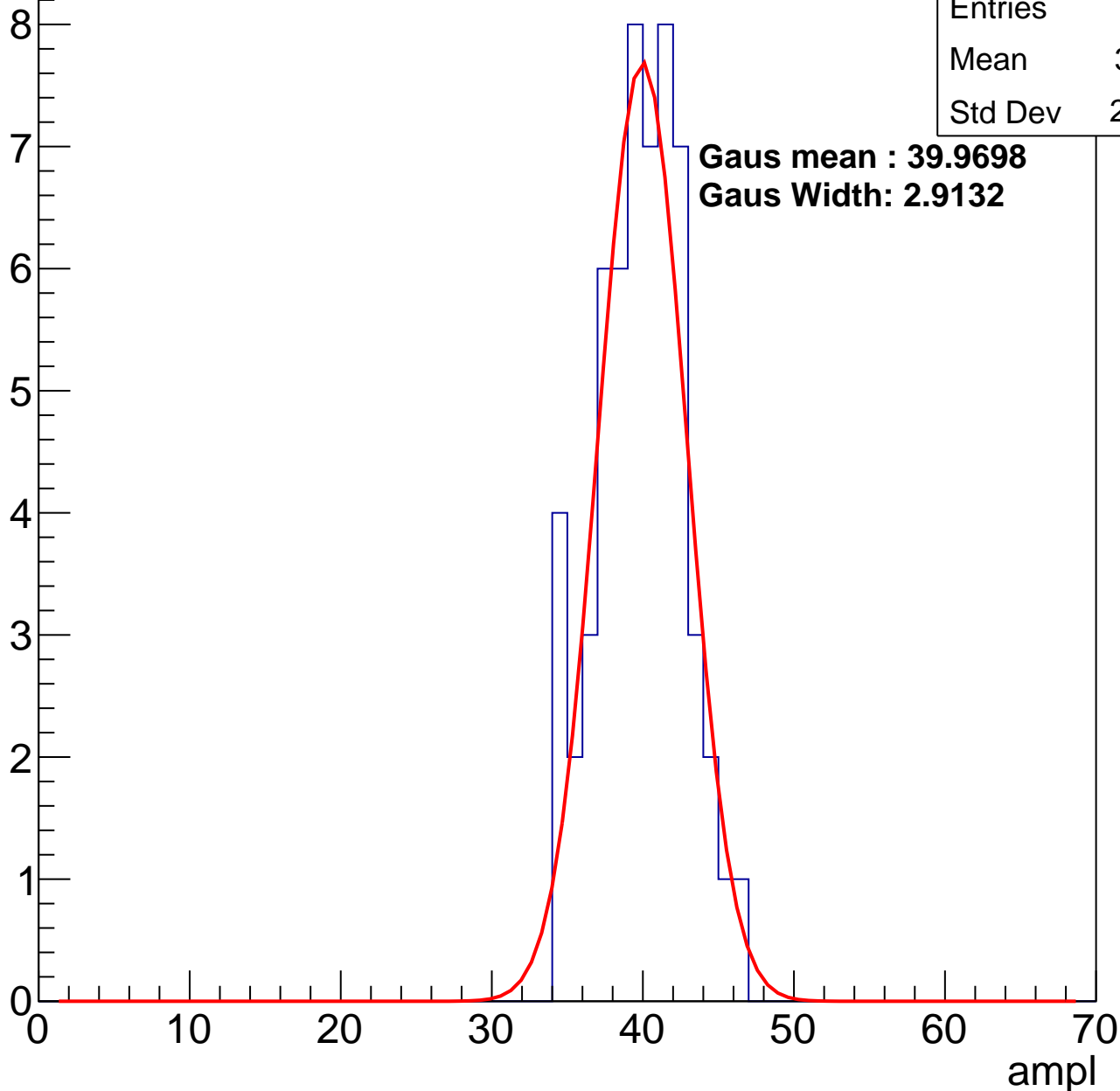
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.41
Std Dev	2.829

**Gaus mean : 39.9698**

**Gaus Width: 2.9132**

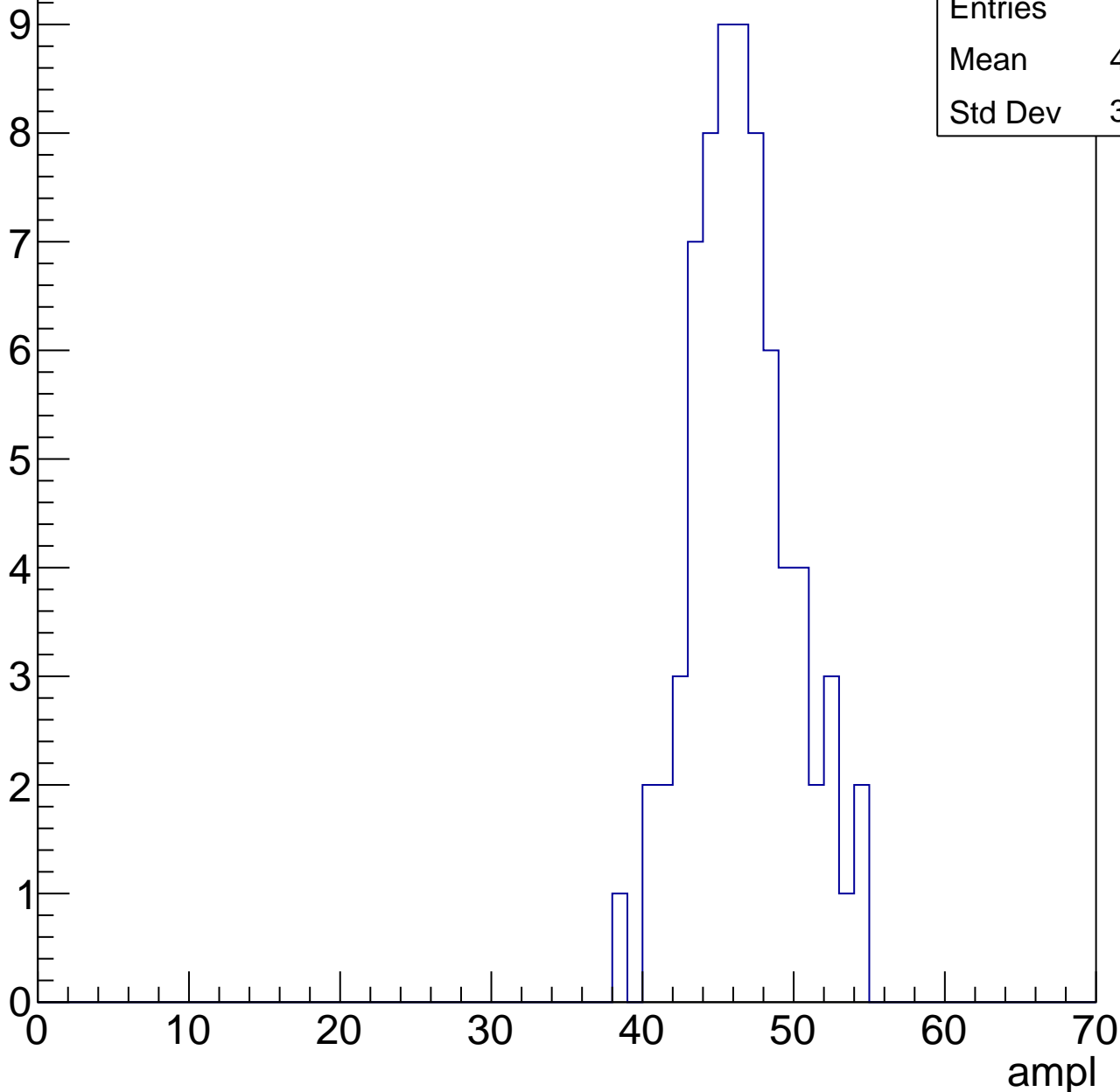


# B1L002S, U18-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

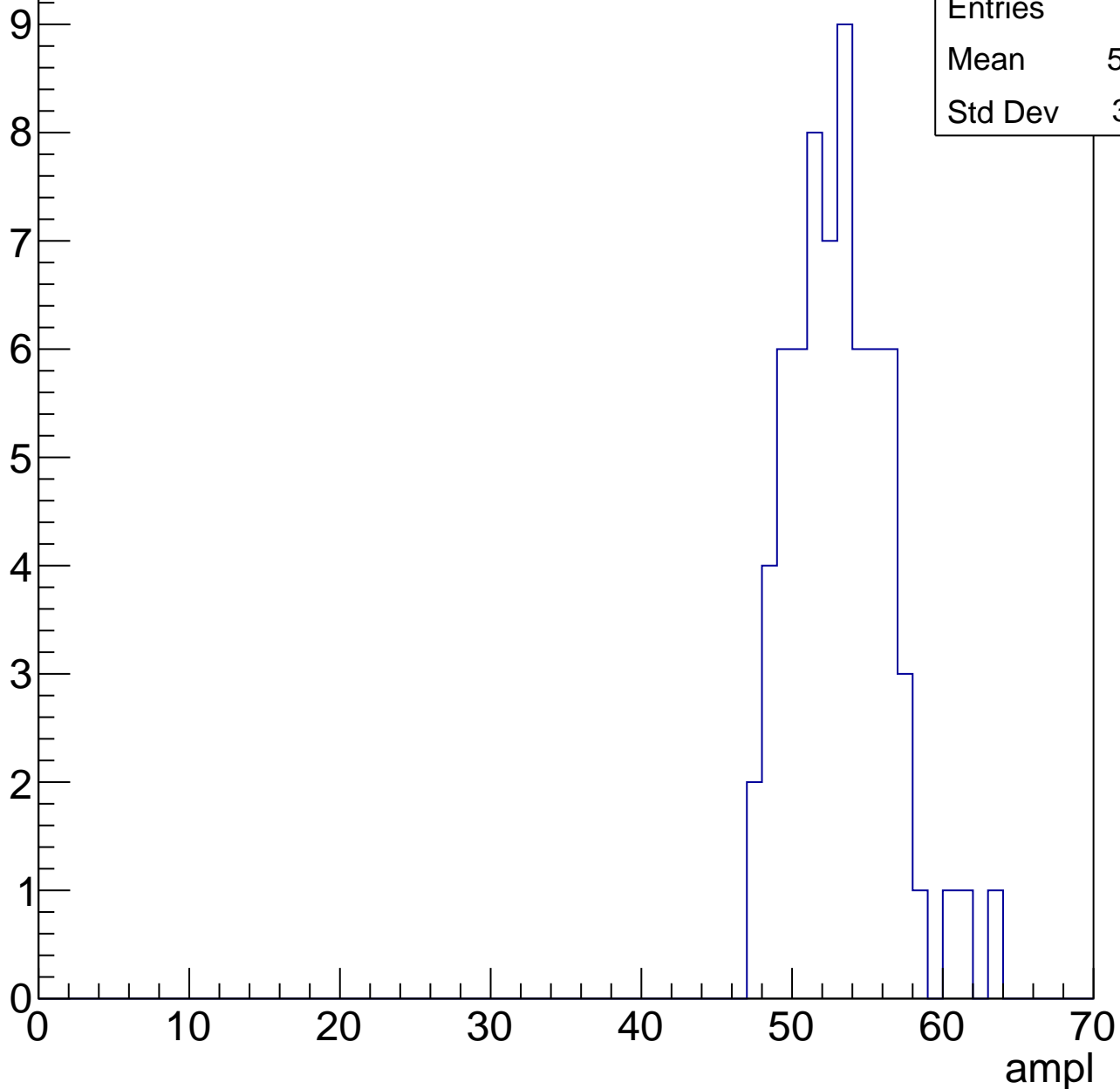
Entries	71
Mean	46.15
Std Dev	3.364



# B1L002S, U18-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

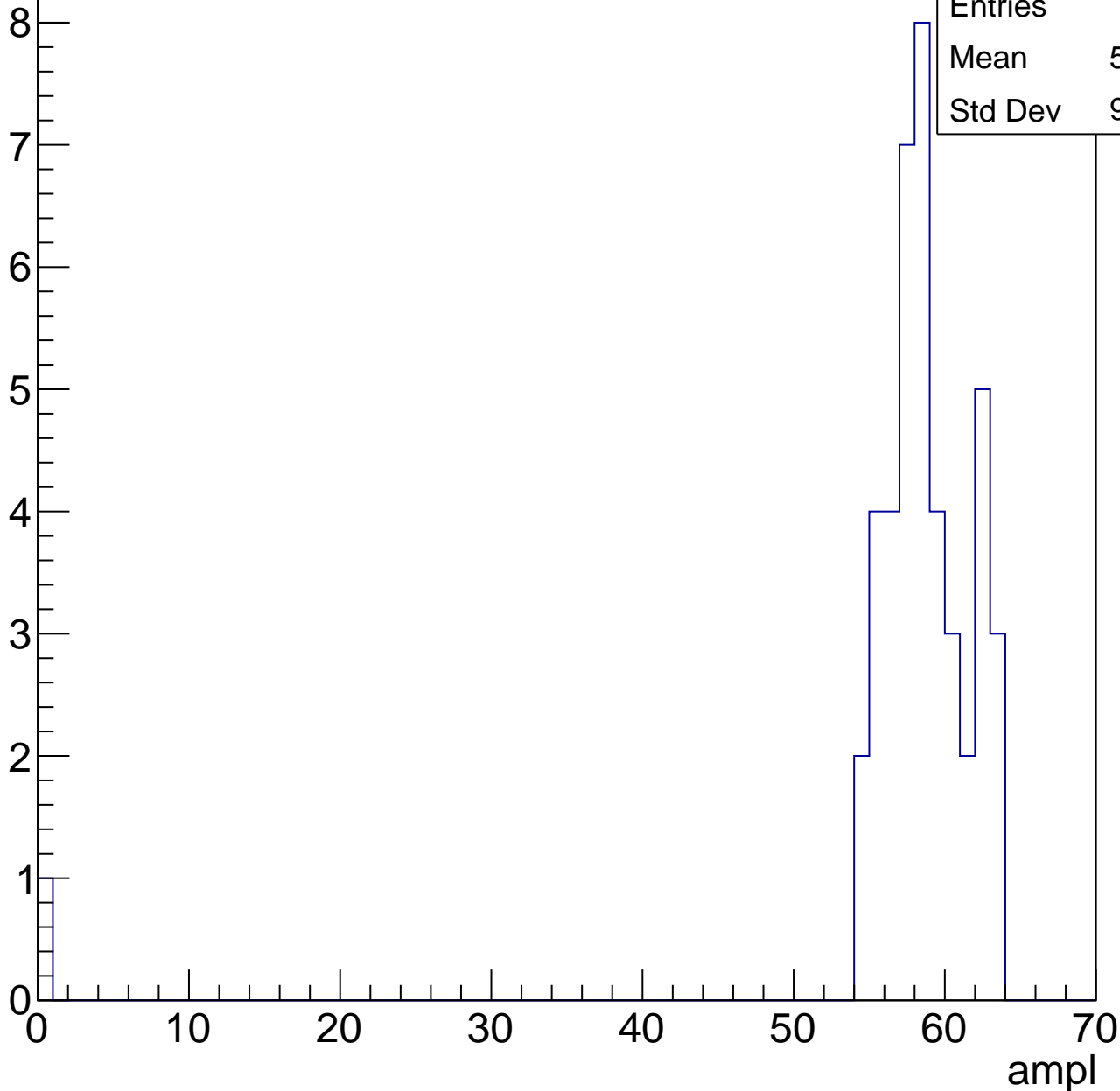


# B1L002S, U18-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

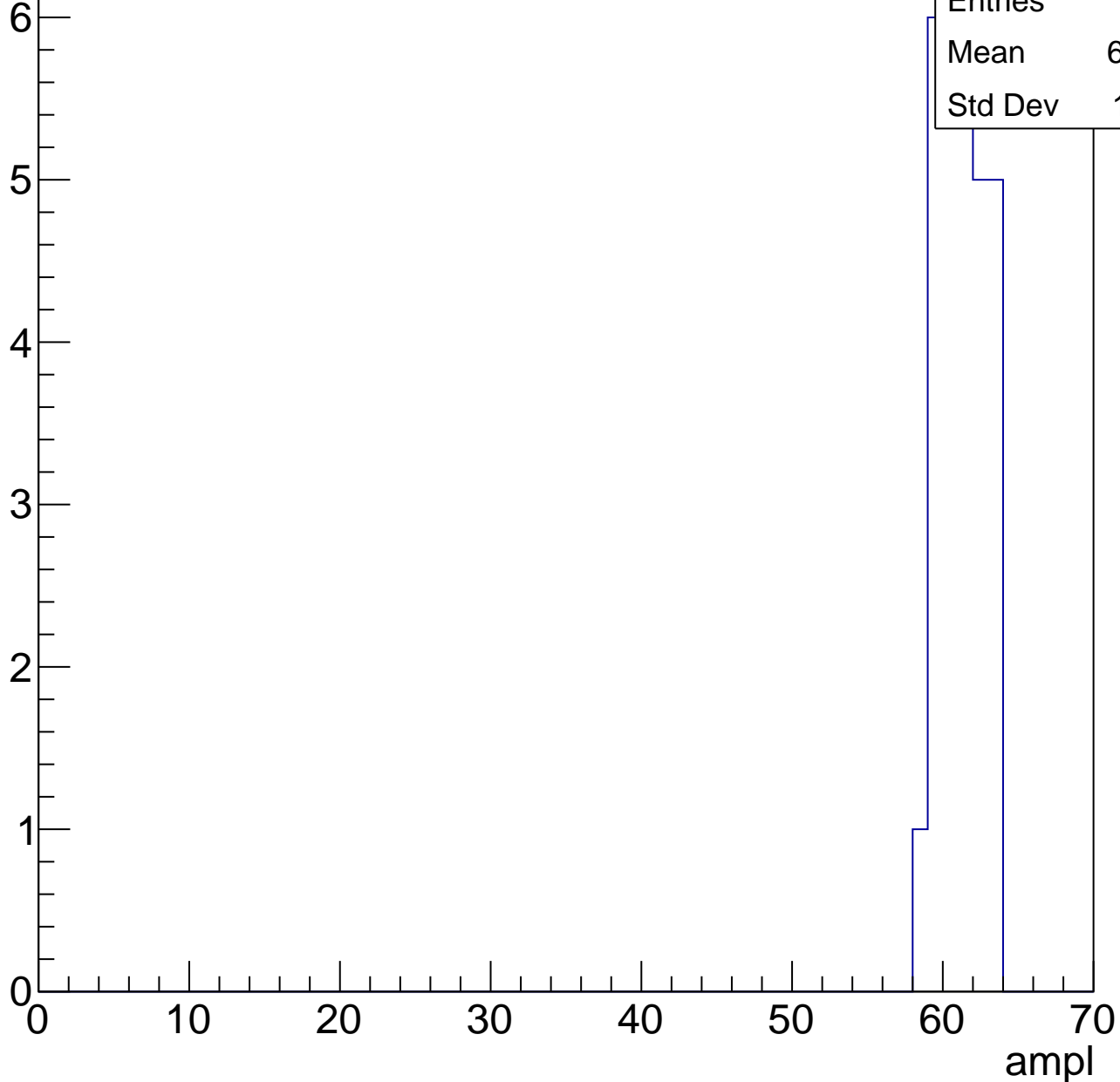
Entries	43
Mean	57.02
Std Dev	9.154



# B1L002S, U18-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

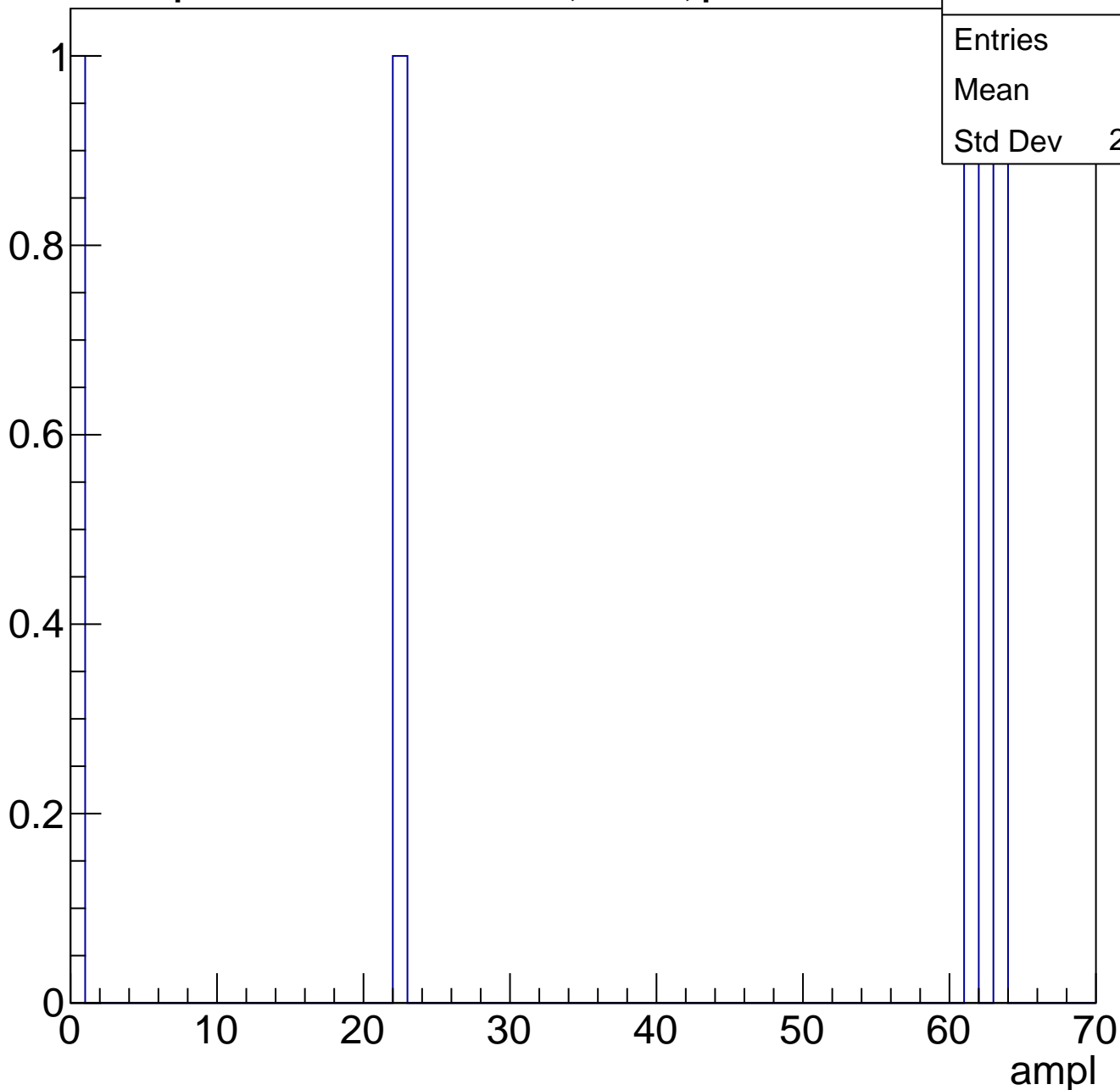




# B1L002S, U18-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	36.5
Std Dev	26.67

# B1L002S, U18-ch11, adc0

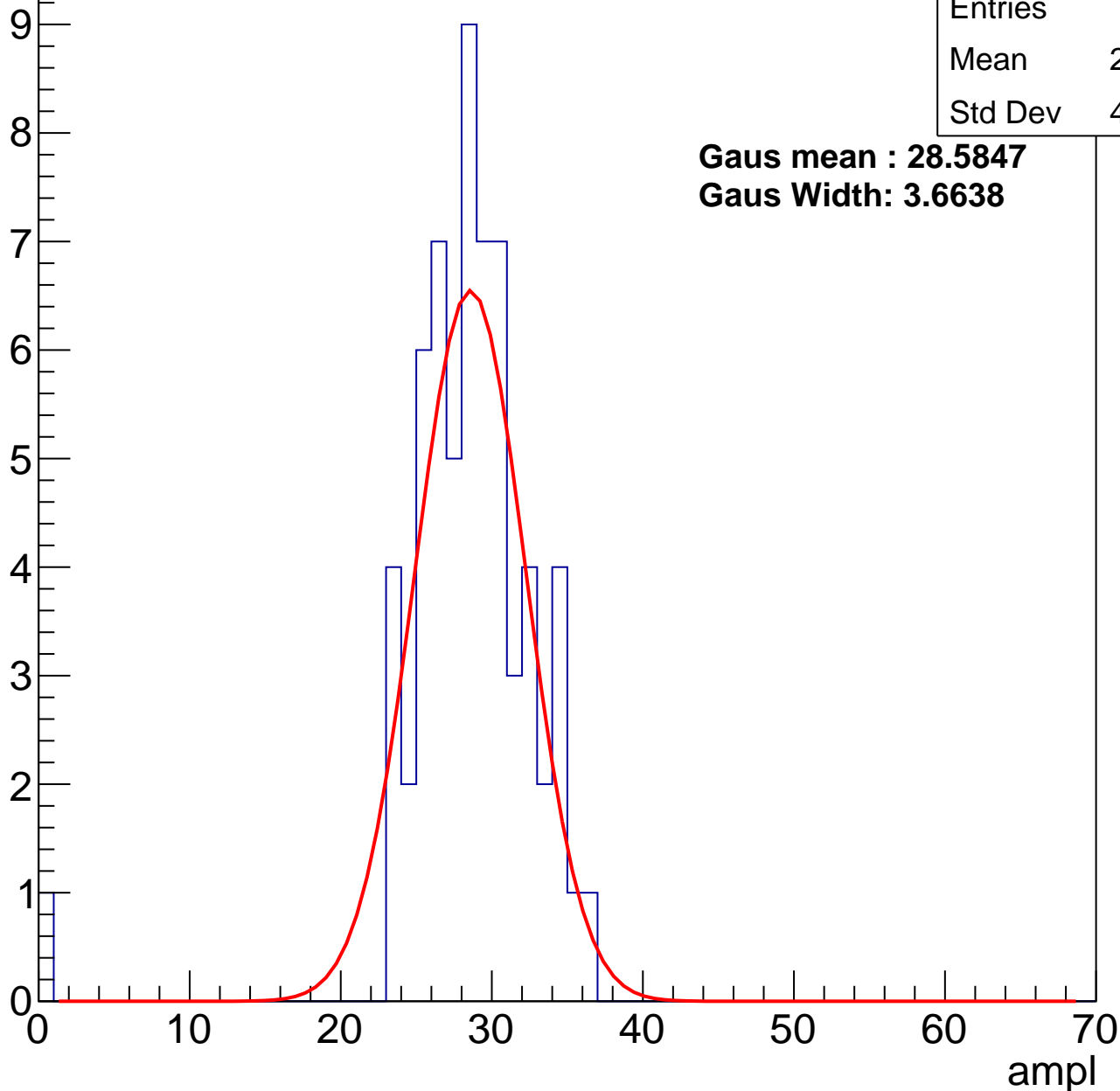
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.03
Std Dev	4.774

**Gaus mean : 28.5847**

**Gaus Width: 3.6638**



# B1L002S, U18-ch11, adc1

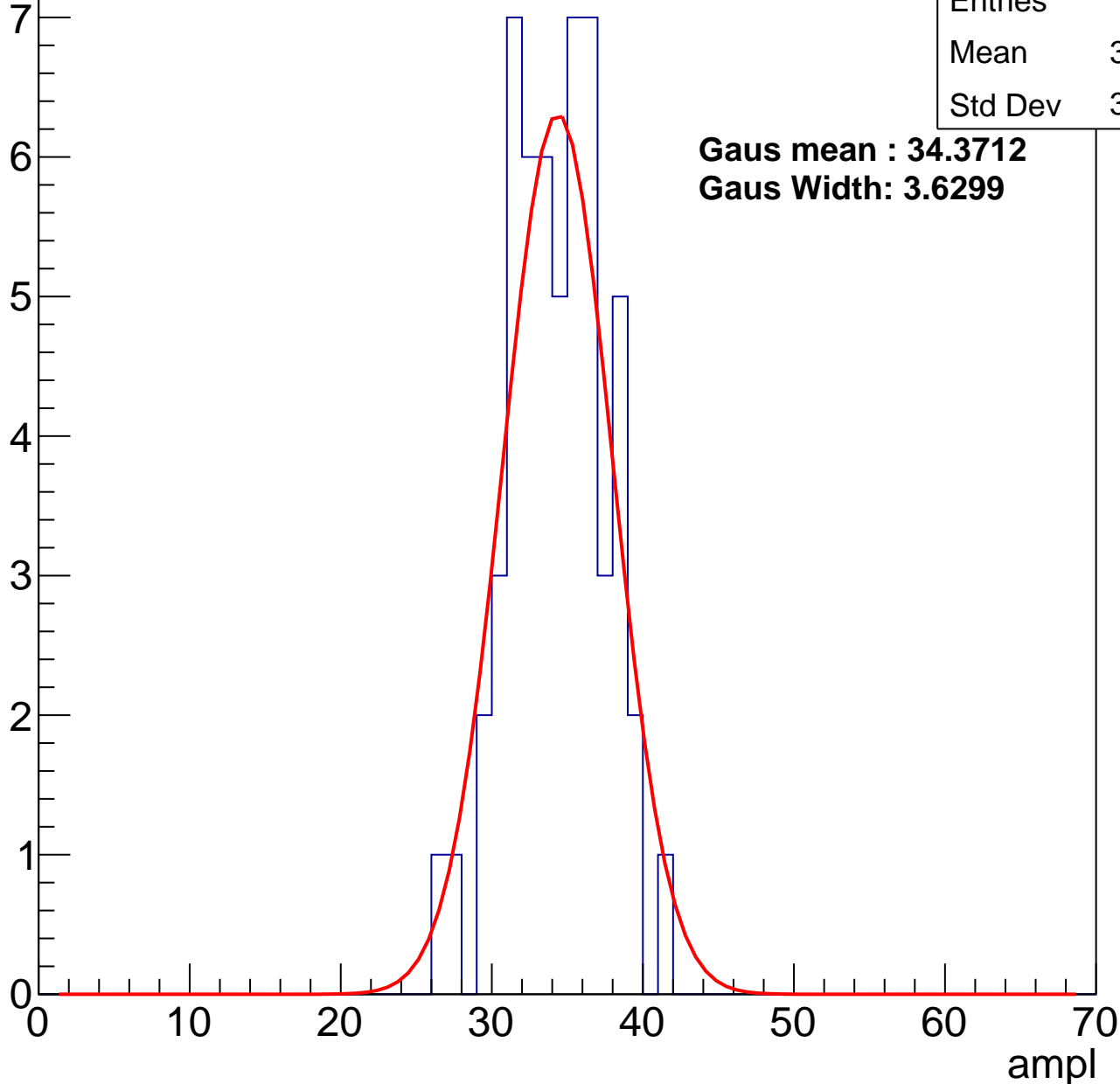
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.84
Std Dev	3.115

**Gaus mean : 34.3712**

**Gaus Width: 3.6299**



# B1L002S, U18-ch11, adc2

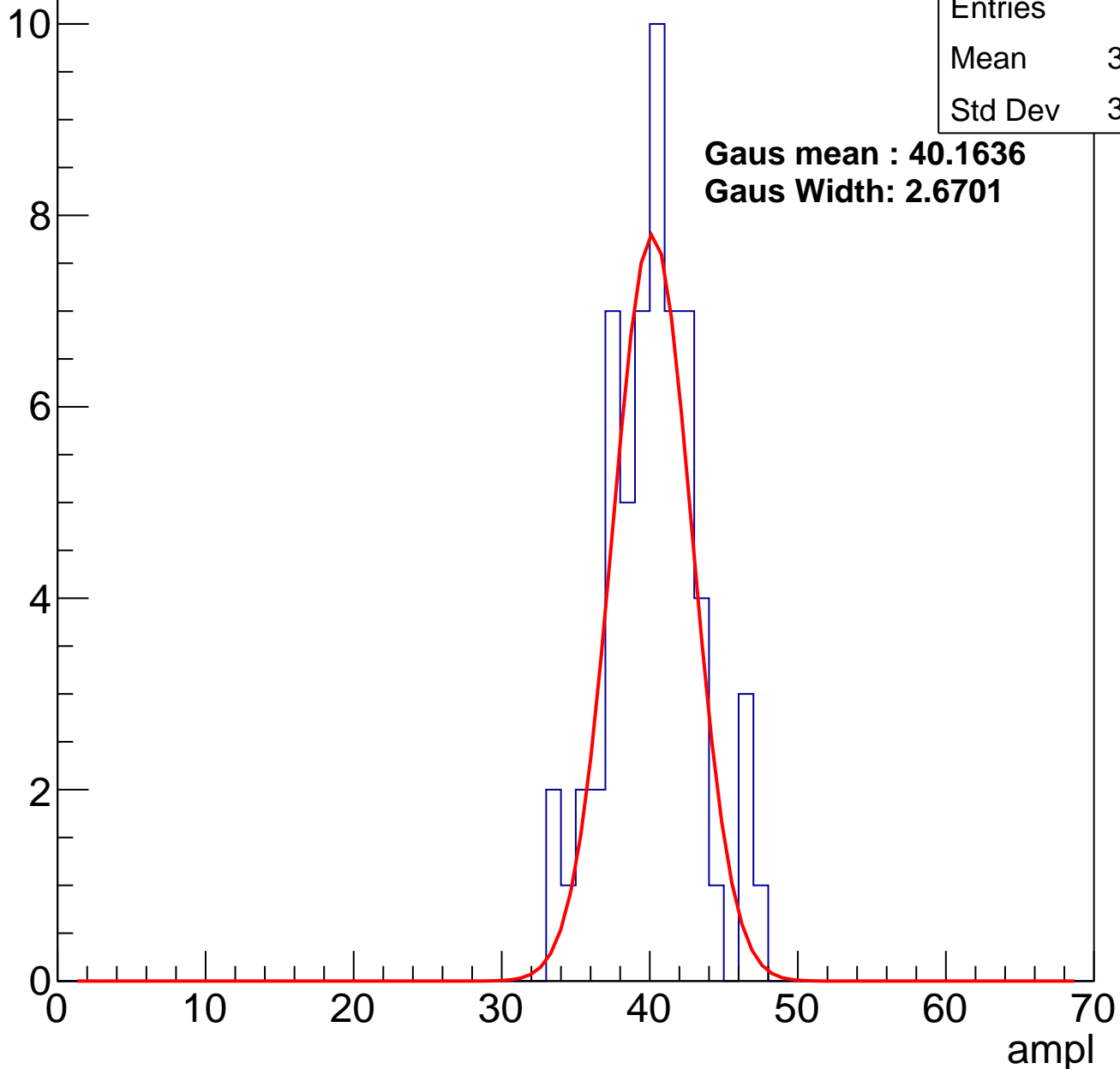
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	39.76
Std Dev	3.044

**Gaus mean : 40.1636**

**Gaus Width: 2.6701**

Entry

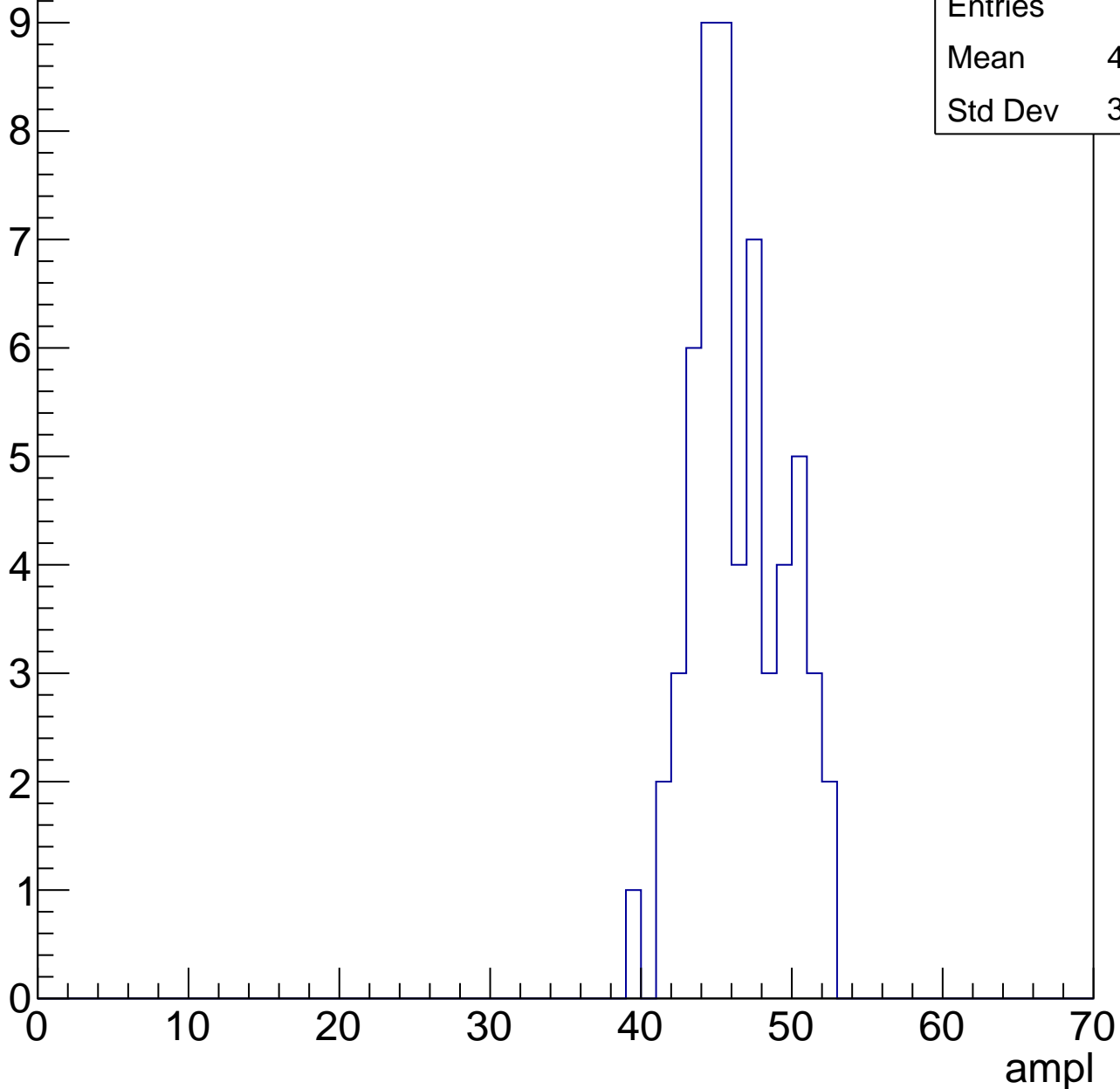


# B1L002S, U18-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.97
Std Dev	3.017

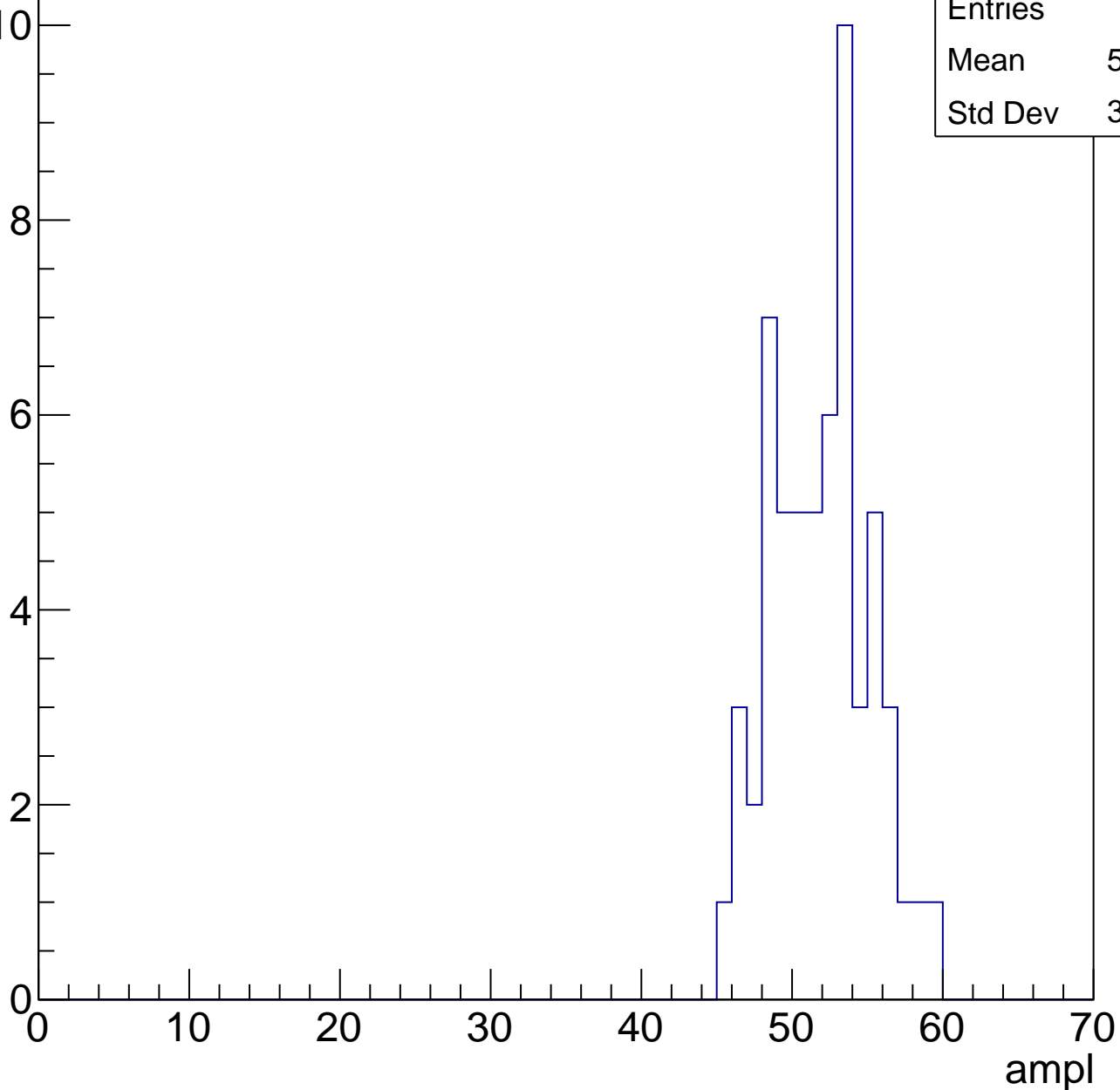


# B1L002S, U18-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	51.45
Std Dev	3.207

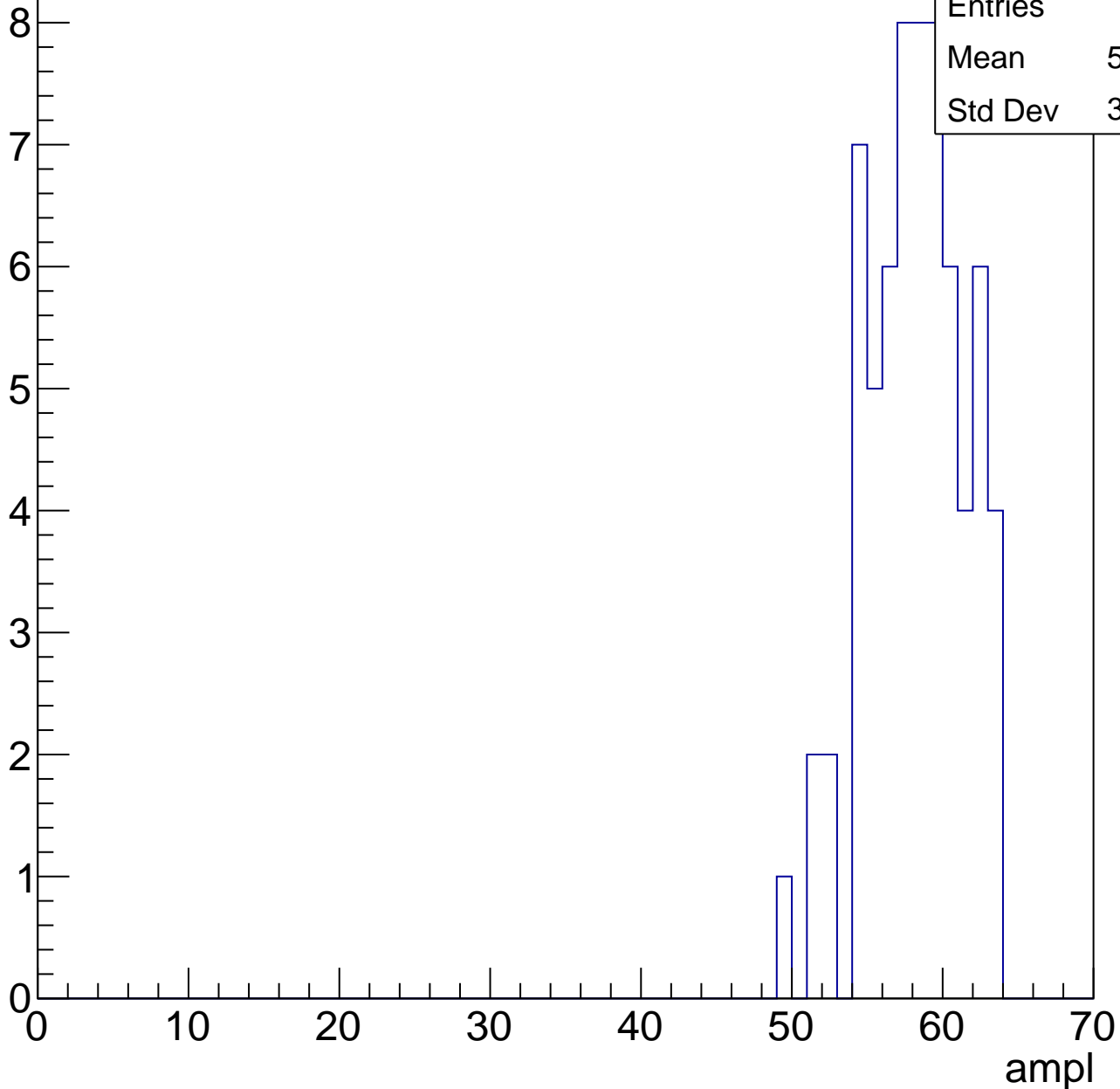


# B1L002S, U18-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	57.67
Std Dev	3.225

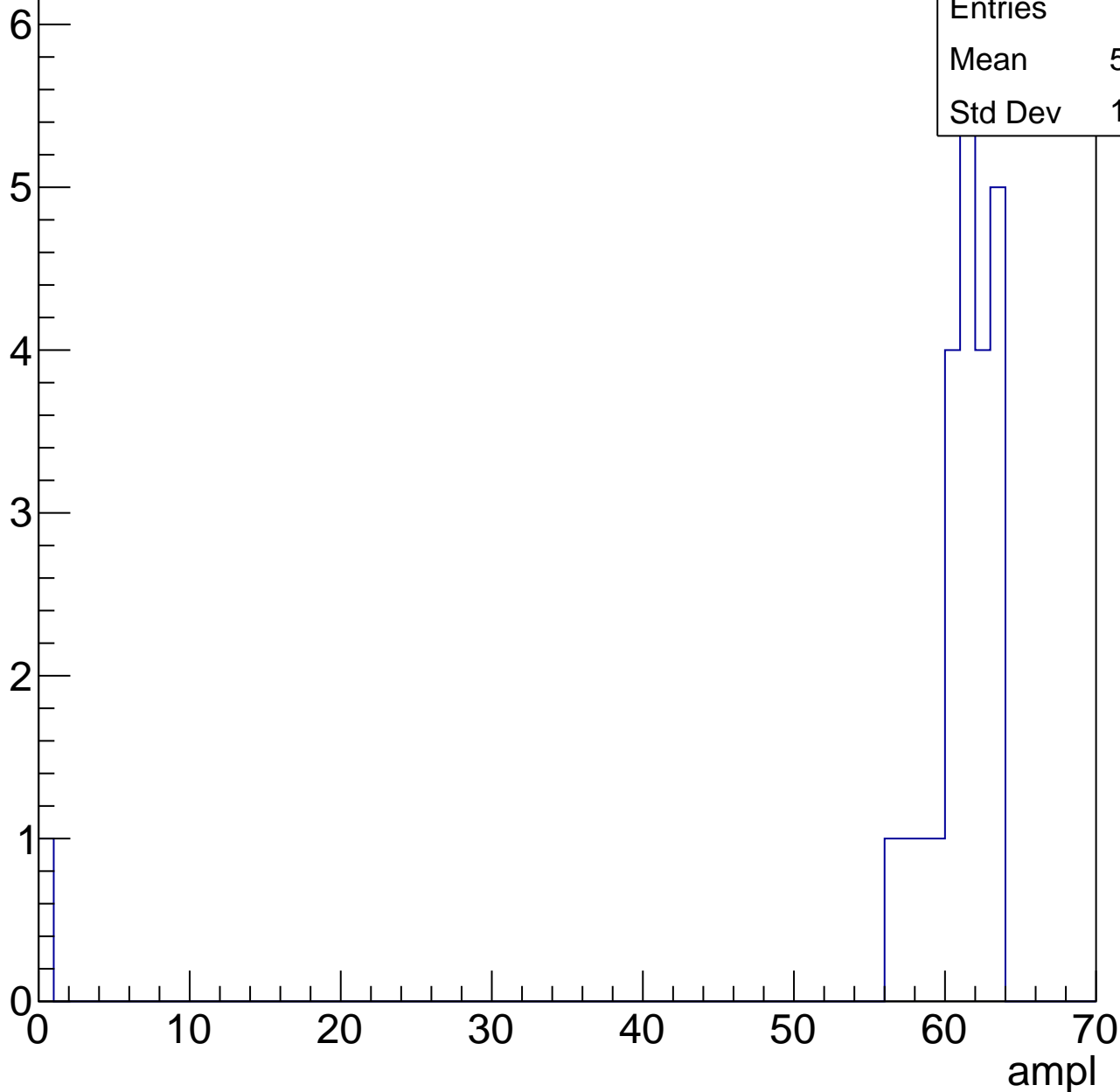


# B1L002S, U18-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.29
Std Dev	12.29





# B1L002S, U18-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch12, adc0

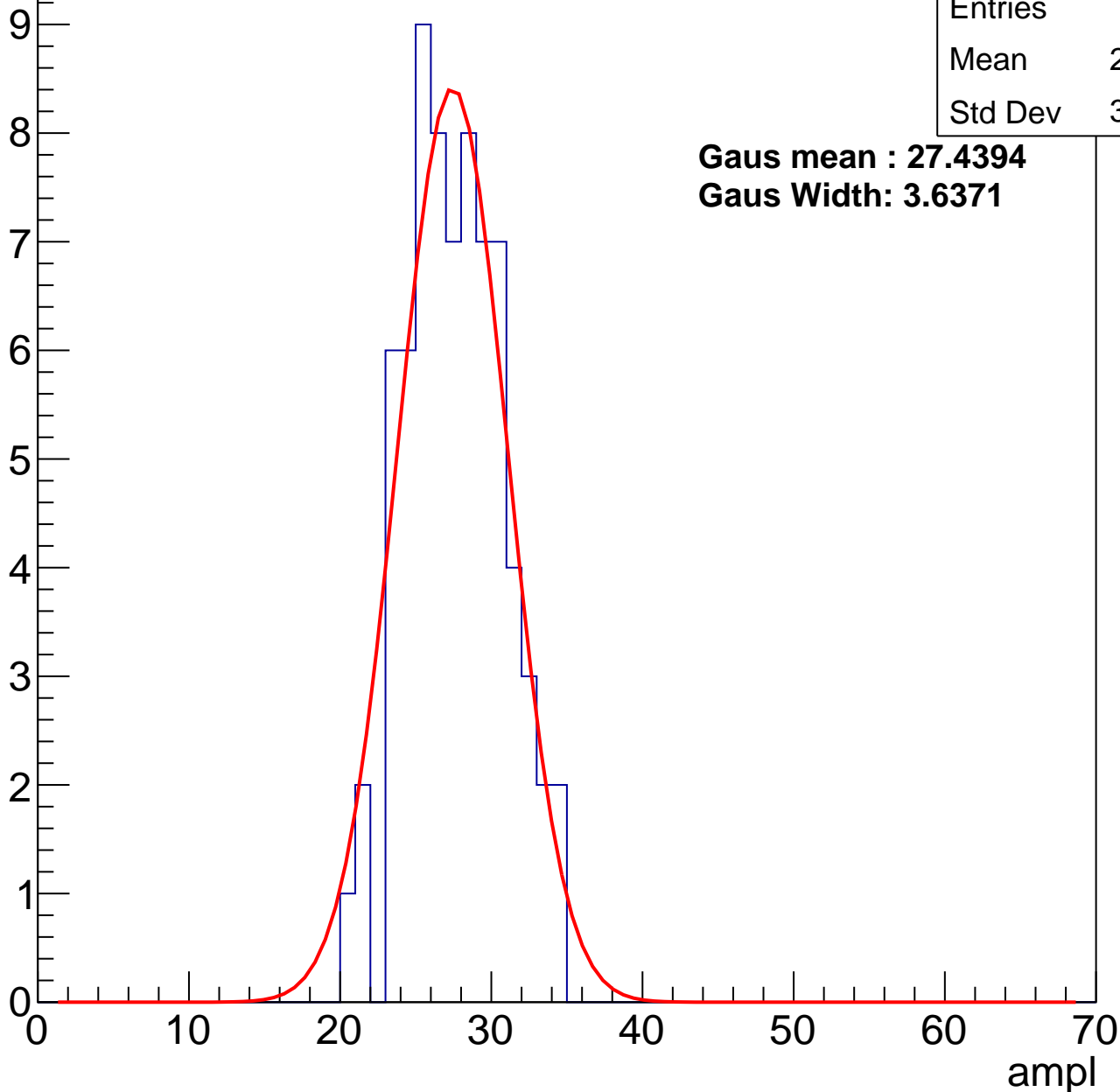
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.18
Std Dev	3.164

**Gaus mean : 27.4394**

**Gaus Width: 3.6371**



# B1L002S, U18-ch12, adc1

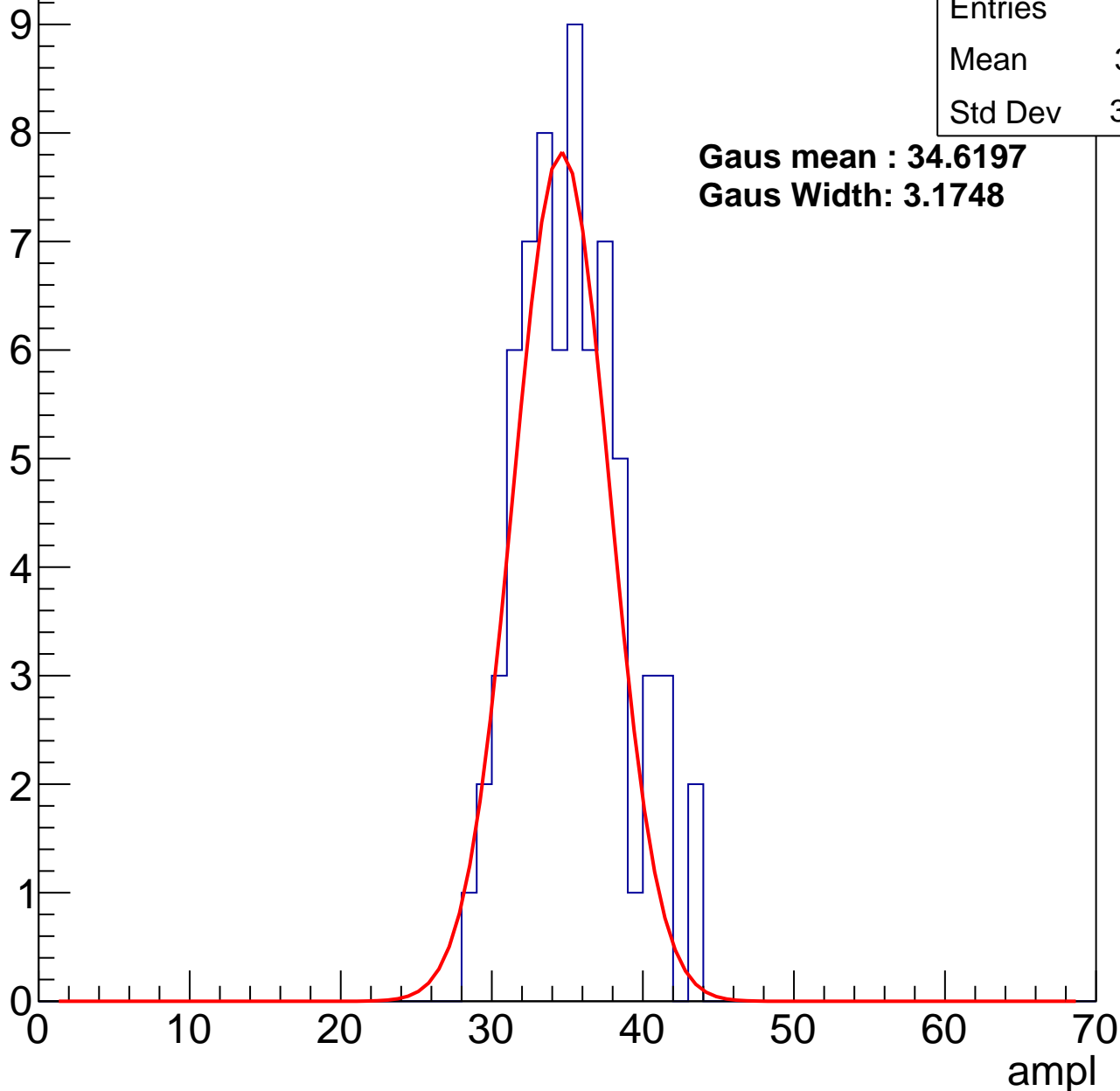
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.81
Std Dev	3.398

**Gaus mean : 34.6197**

**Gaus Width: 3.1748**



# B1L002S, U18-ch12, adc2

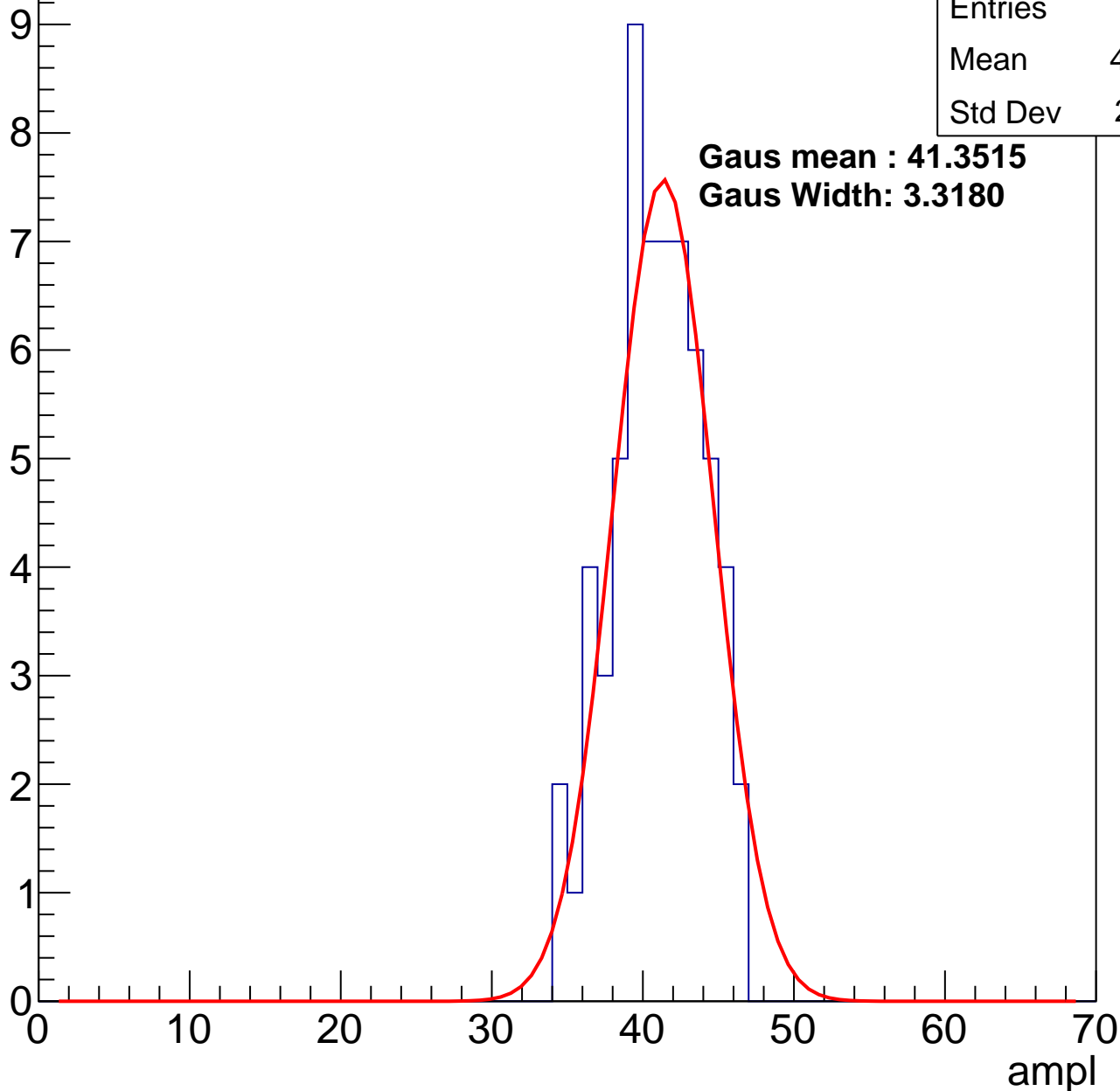
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	40.48
Std Dev	2.961

**Gaus mean : 41.3515**

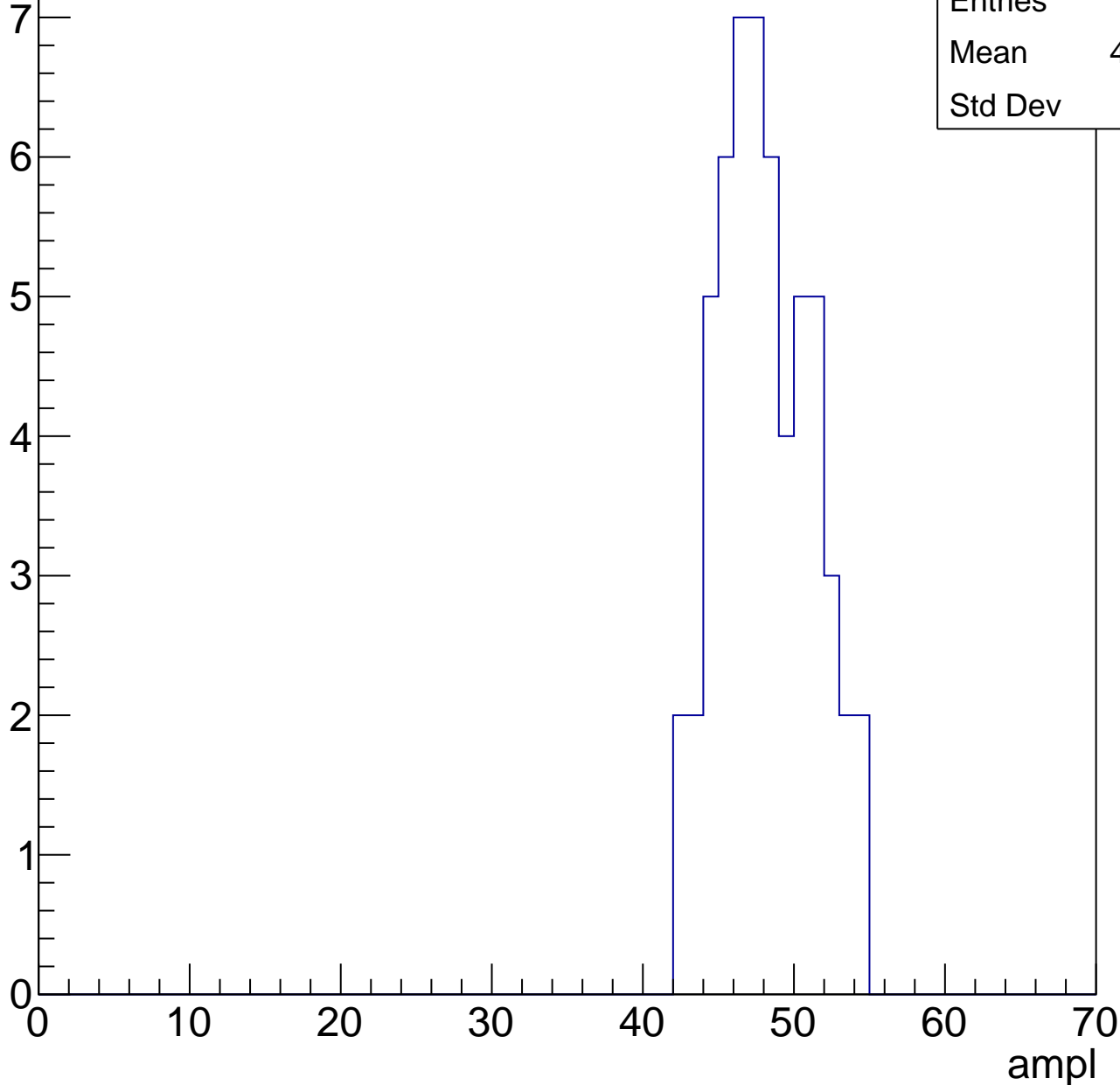
**Gaus Width: 3.3180**



# B1L002S, U18-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



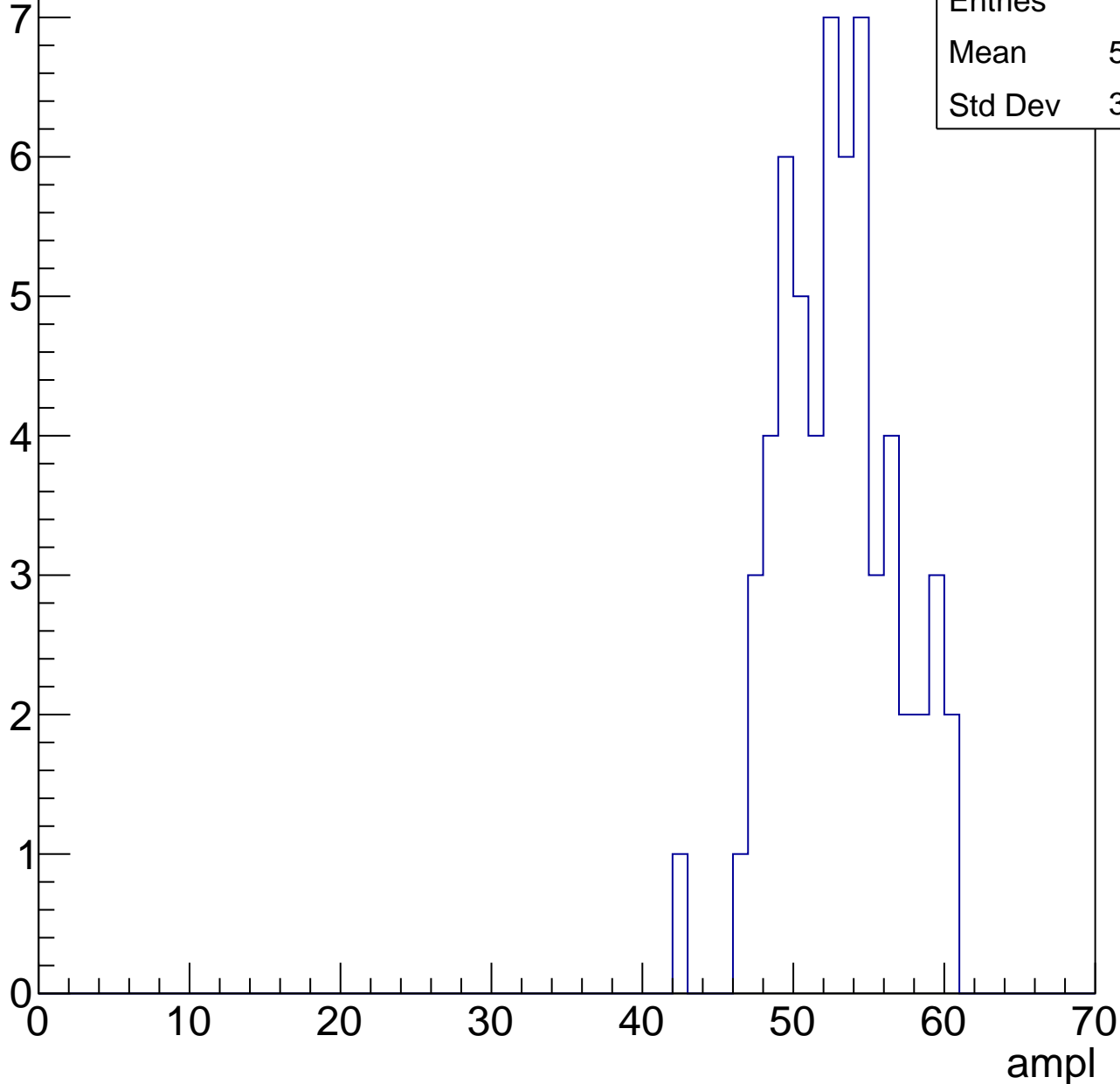
Entries	56
Mean	47.68
Std Dev	3.06

# B1L002S, U18-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

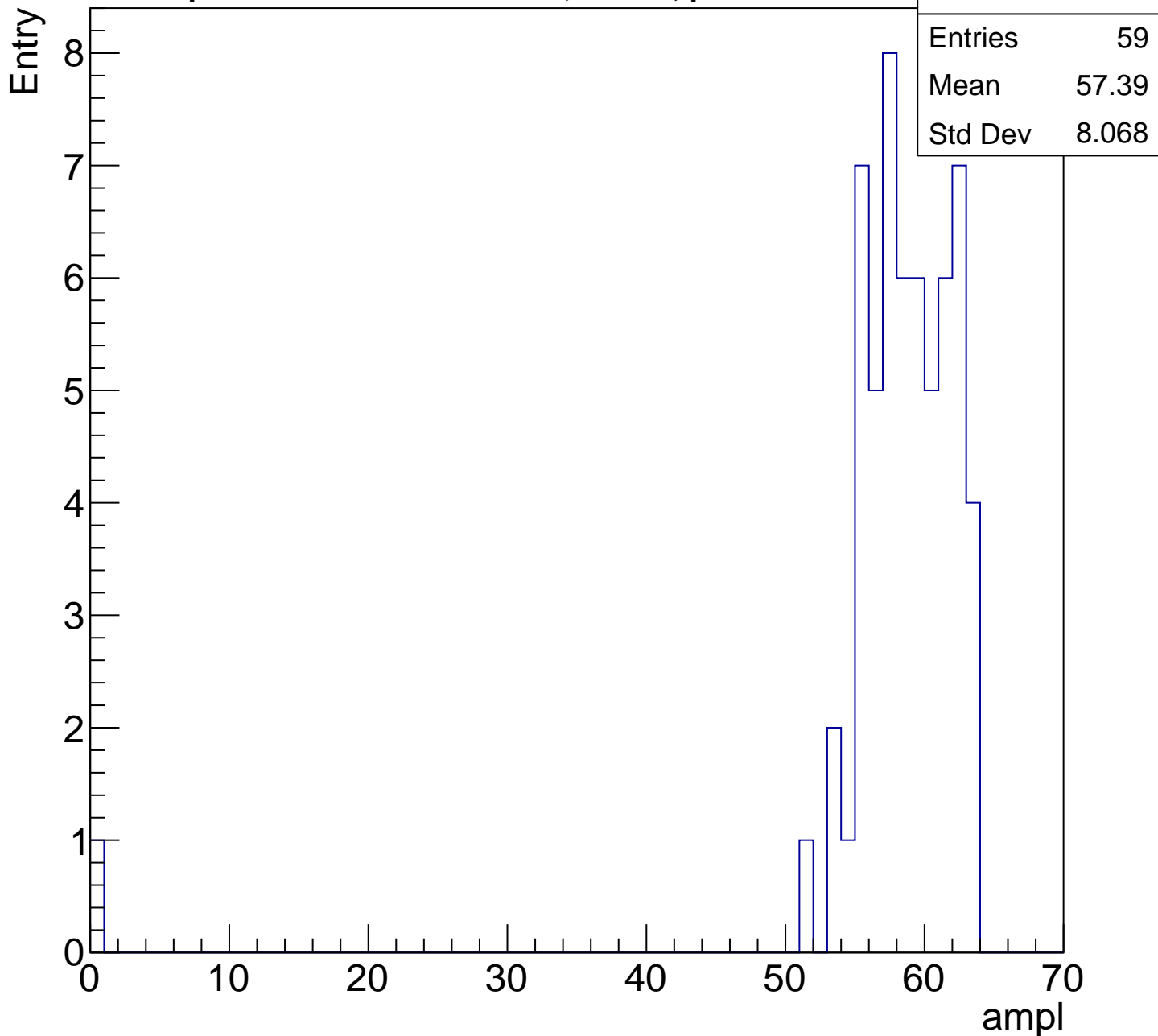
Entry

Entries	60
Mean	52.42
Std Dev	3.796



# B1L002S, U18-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

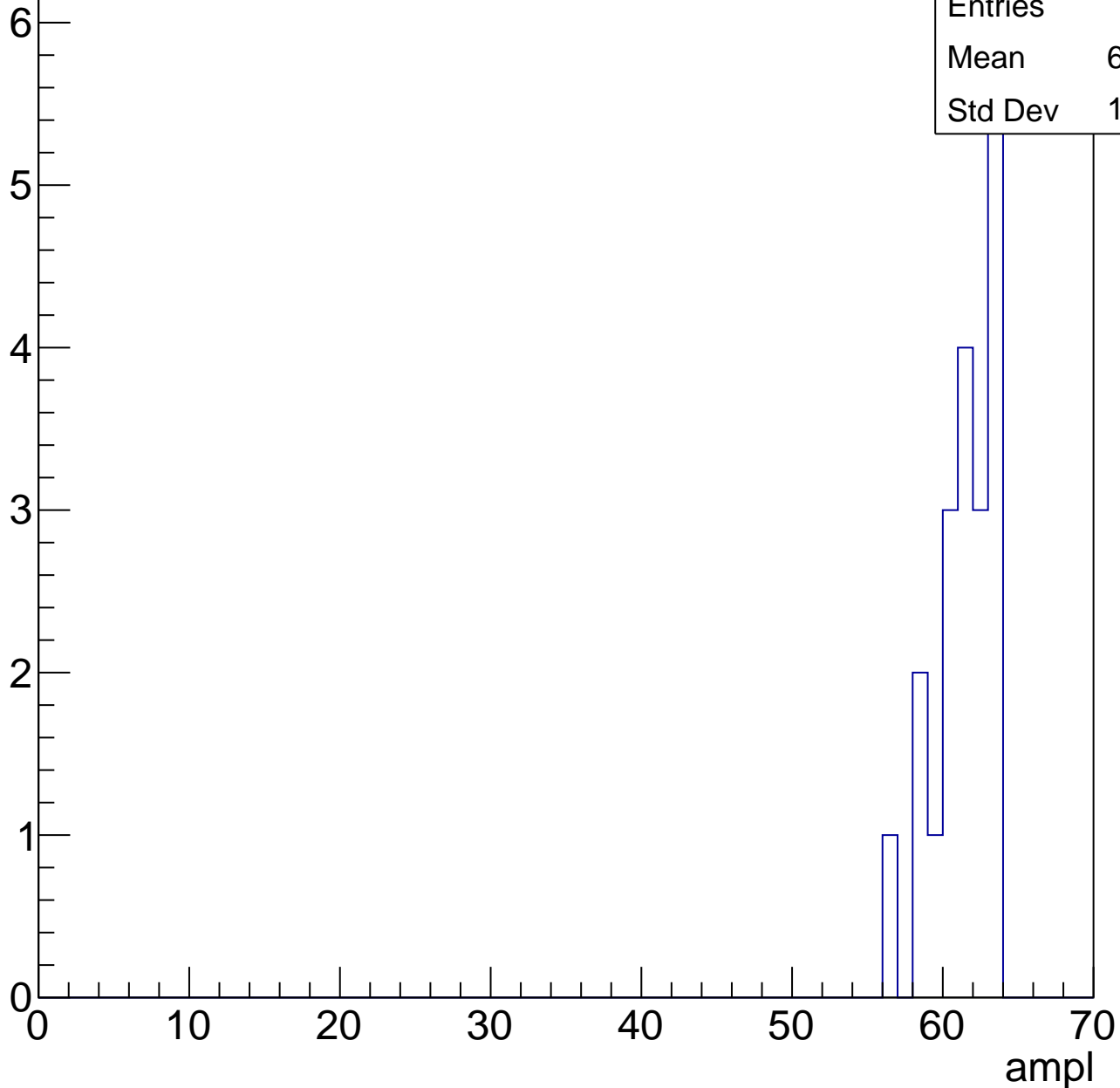


# B1L002S, U18-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	60.95
Std Dev	1.962





# B1L002S, U18-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U18-ch13, adc0

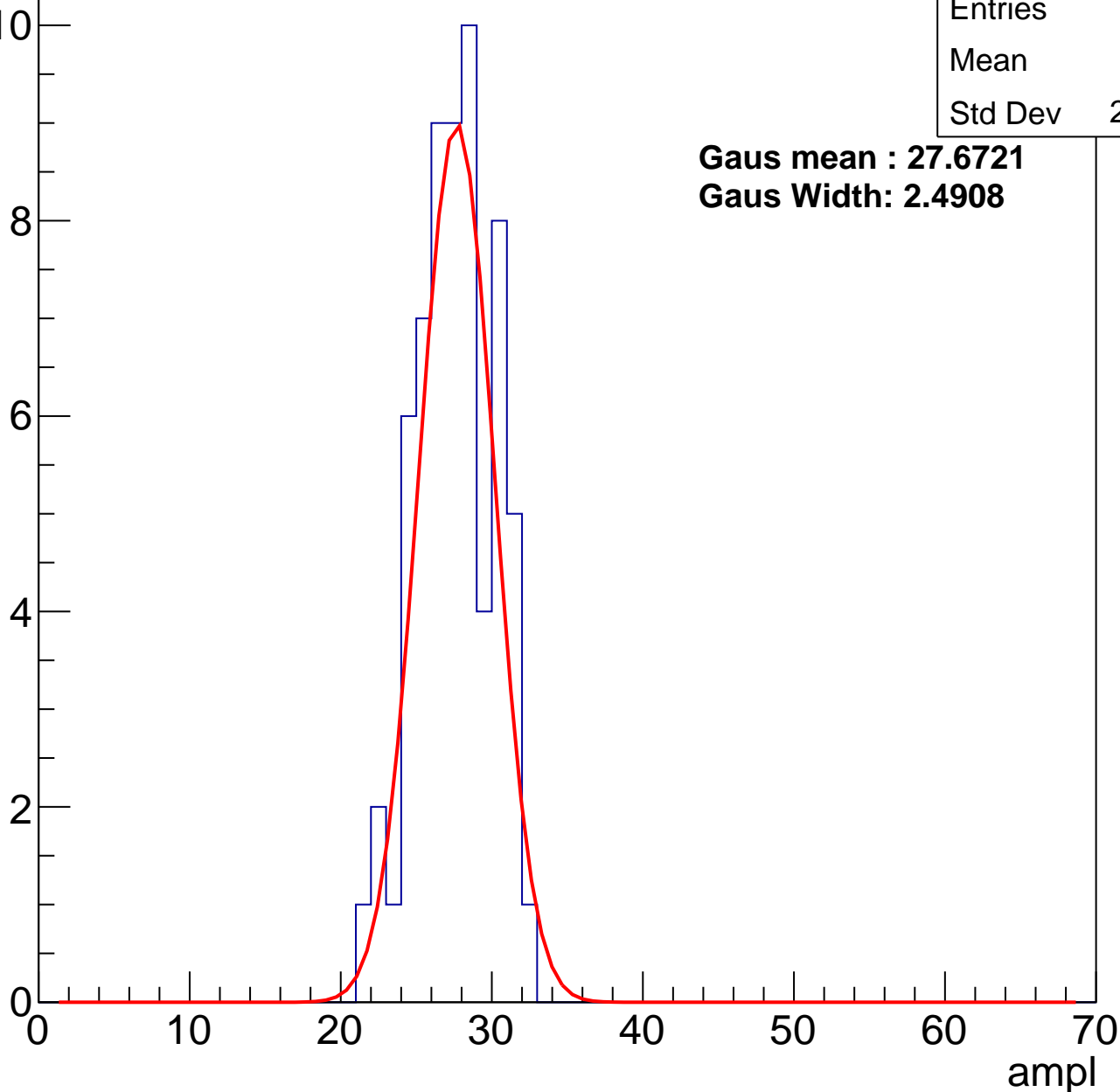
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.1
Std Dev	2.505

**Gaus mean : 27.6721**

**Gaus Width: 2.4908**



# B1L002S, U18-ch13, adc1

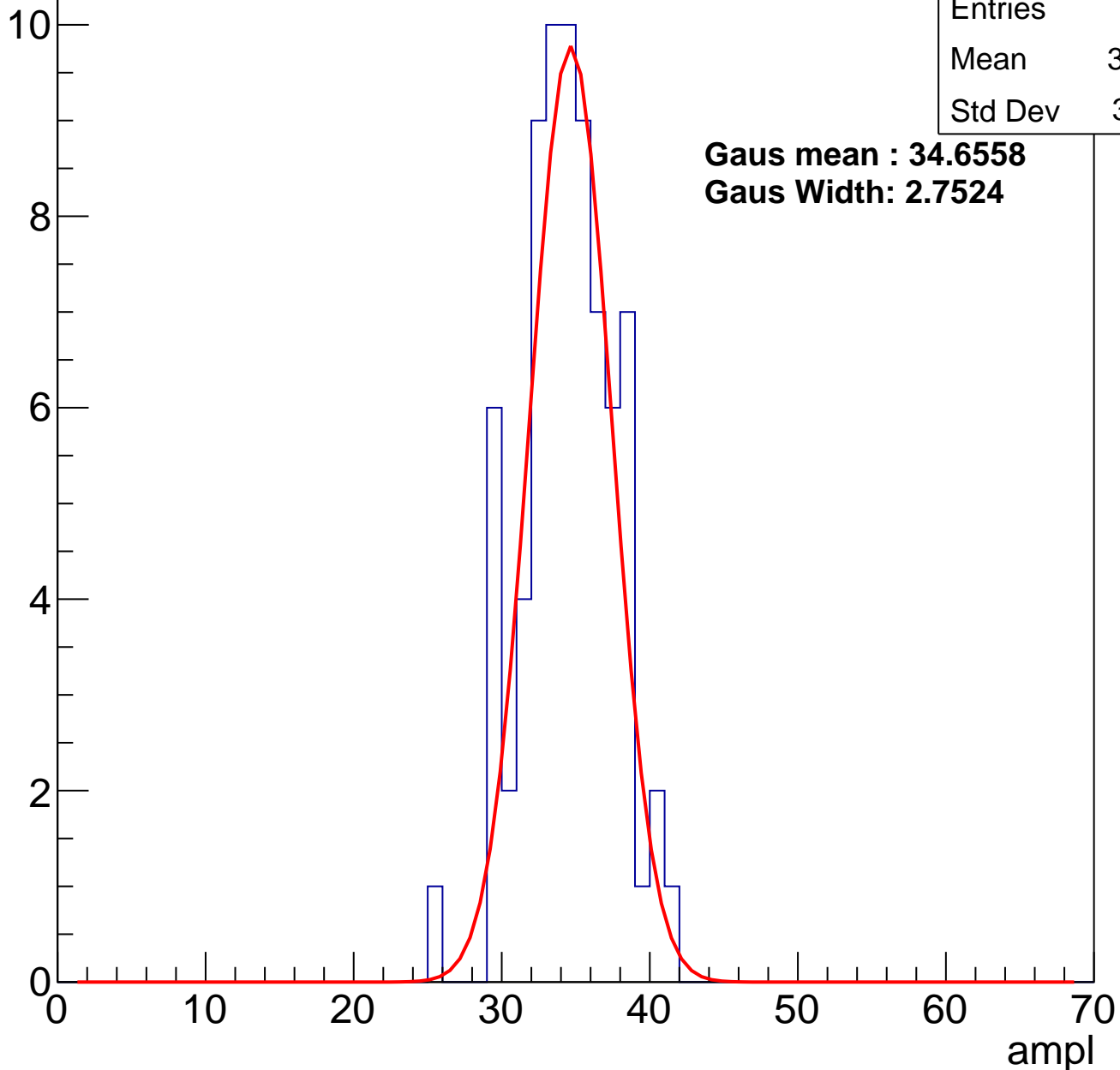
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	34.08
Std Dev	3.041

**Gaus mean : 34.6558**

**Gaus Width: 2.7524**

Entry



# B1L002S, U18-ch13, adc2

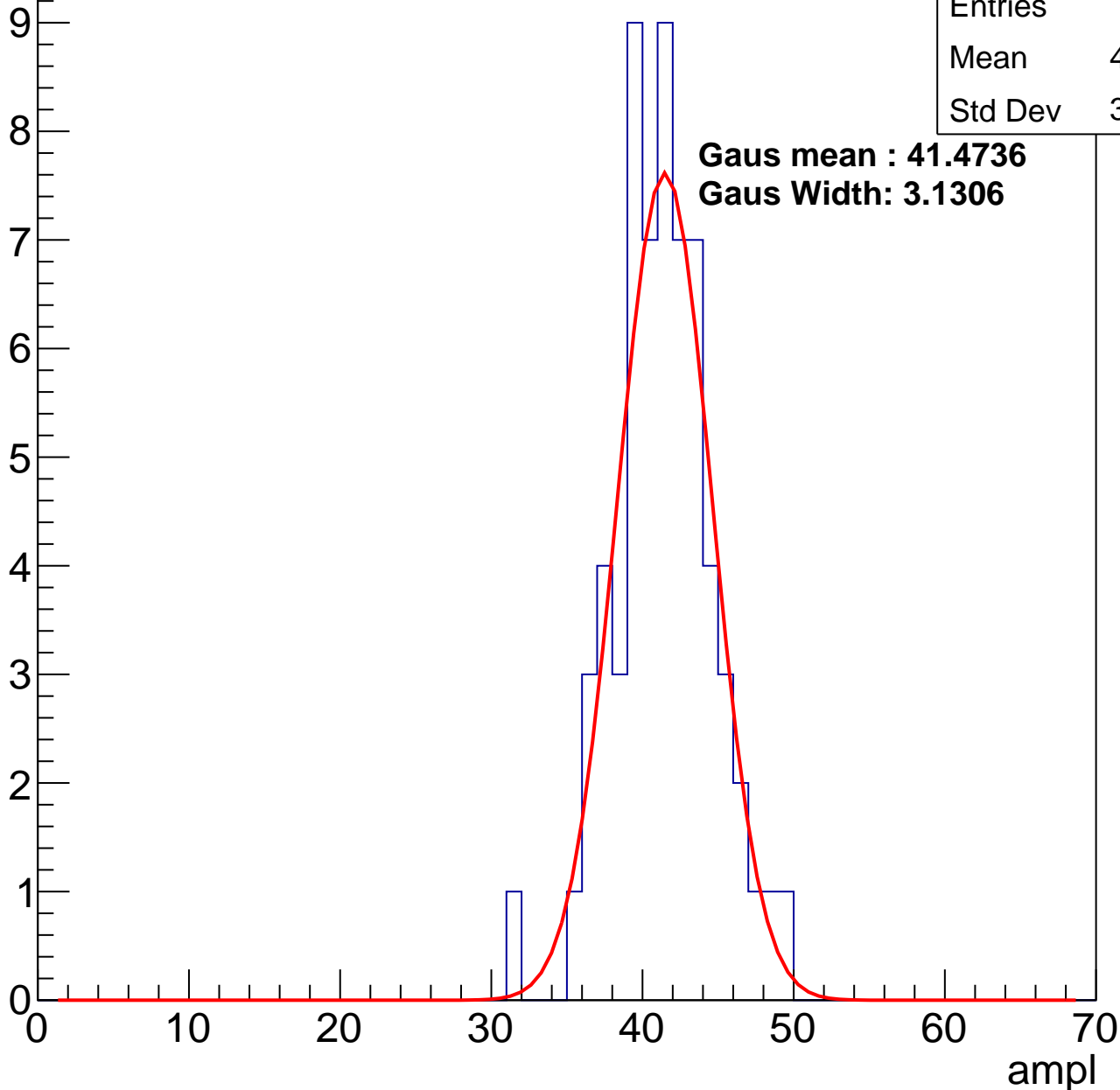
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.92
Std Dev	3.243

**Gaus mean : 41.4736**

**Gaus Width: 3.1306**

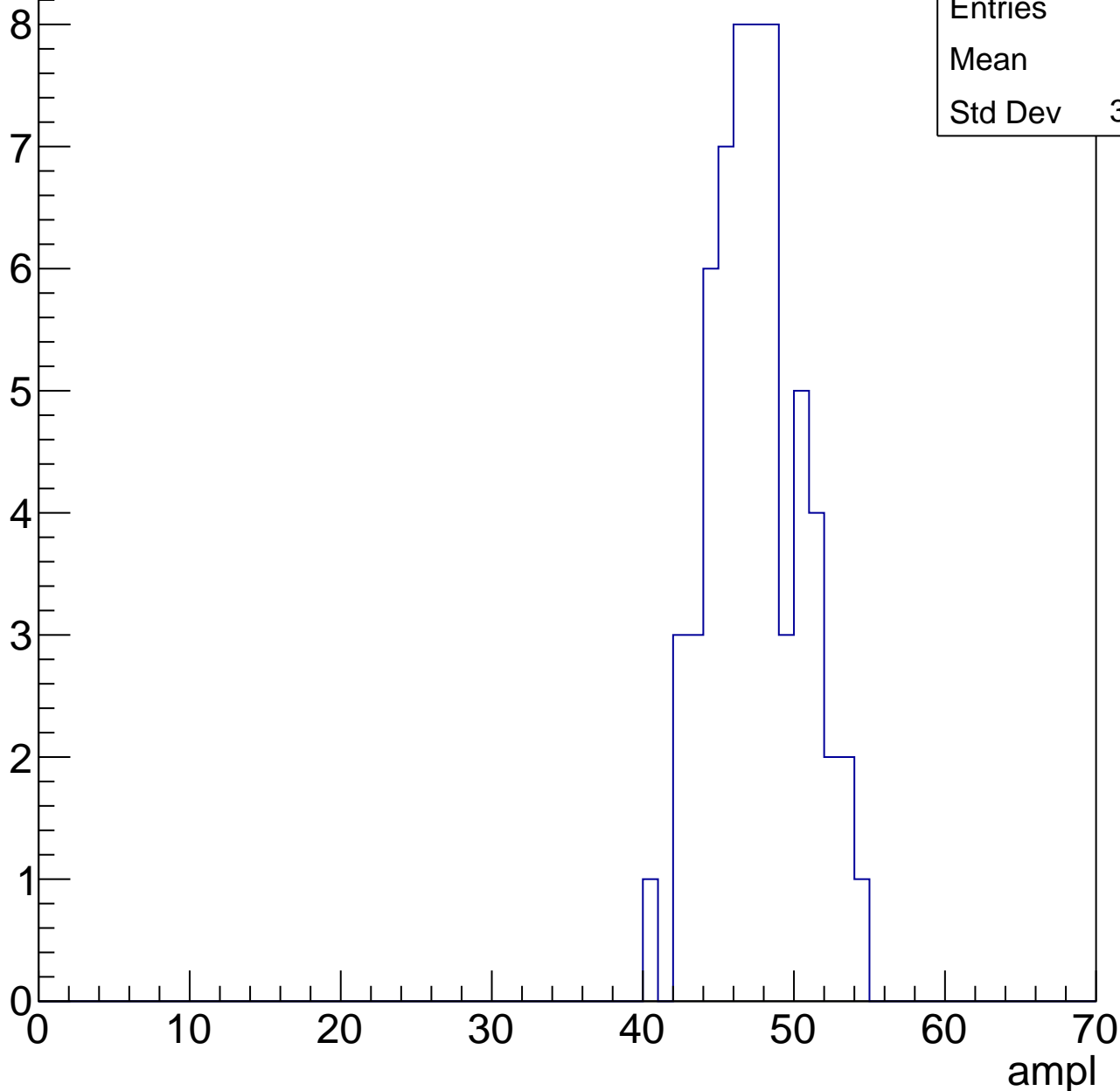


# B1L002S, U18-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	47
Std Dev	3.035

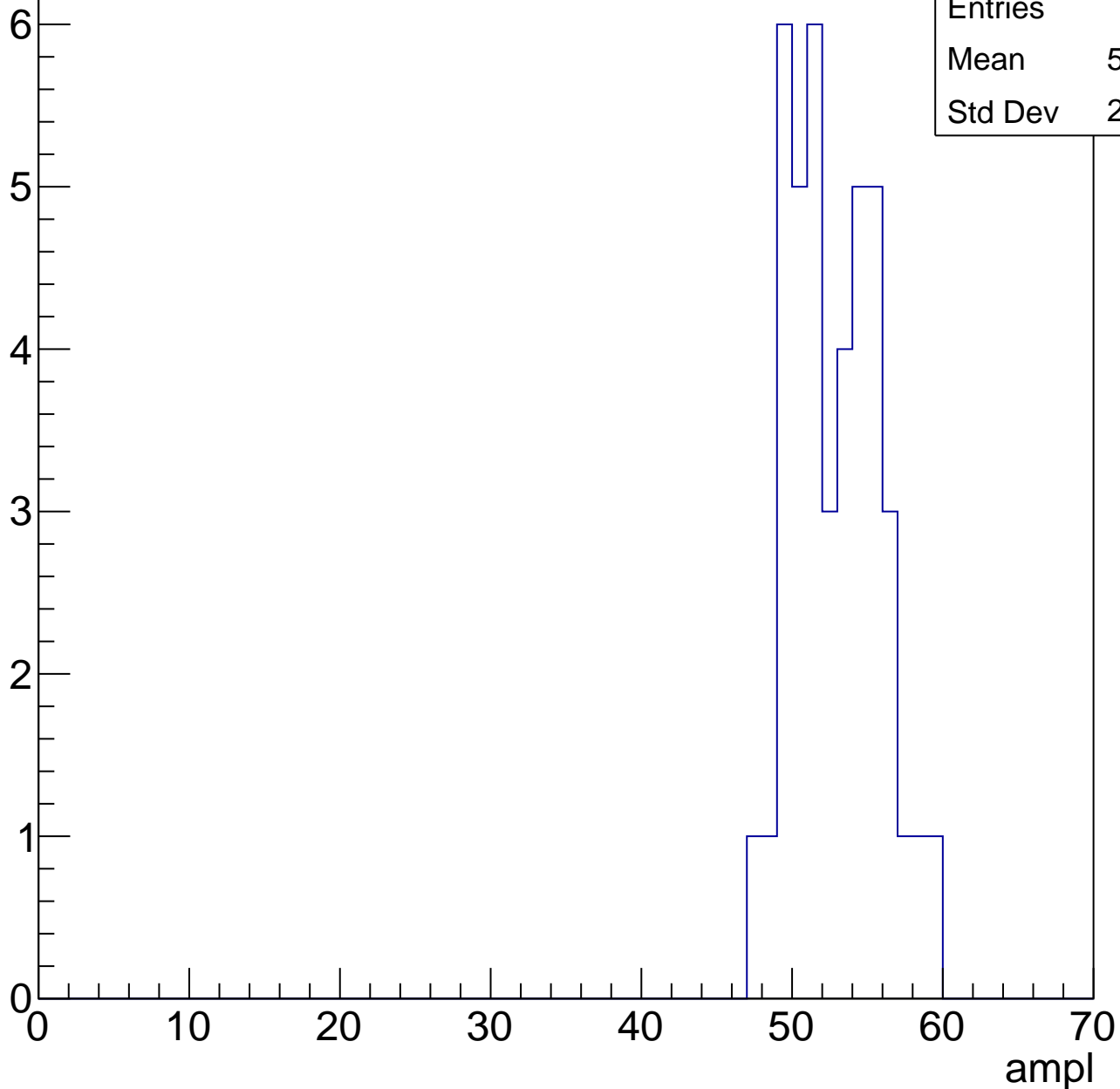


# B1L002S, U18-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	52.38
Std Dev	2.853

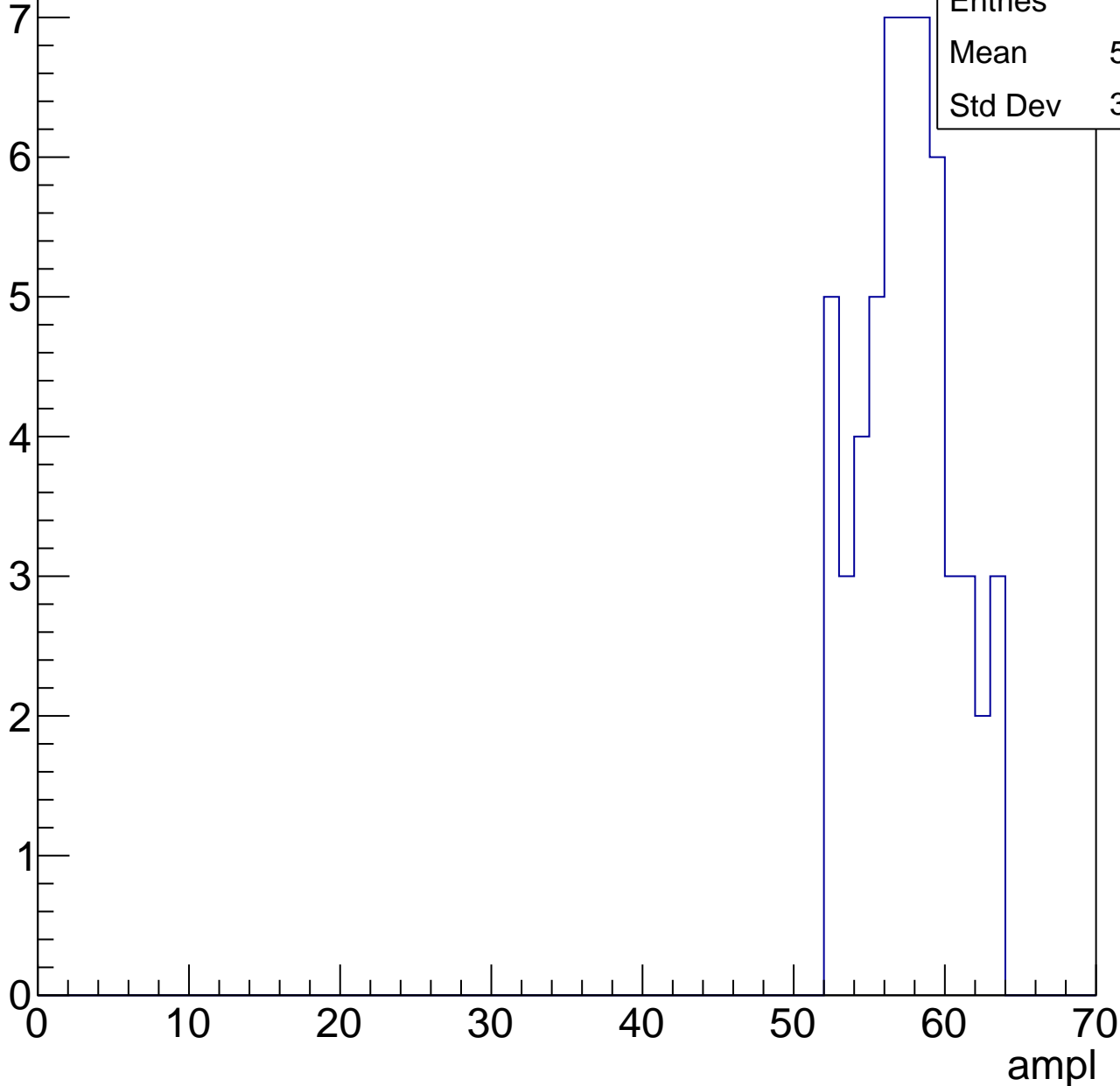


# B1L002S, U18-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.04
Std Dev	3.015

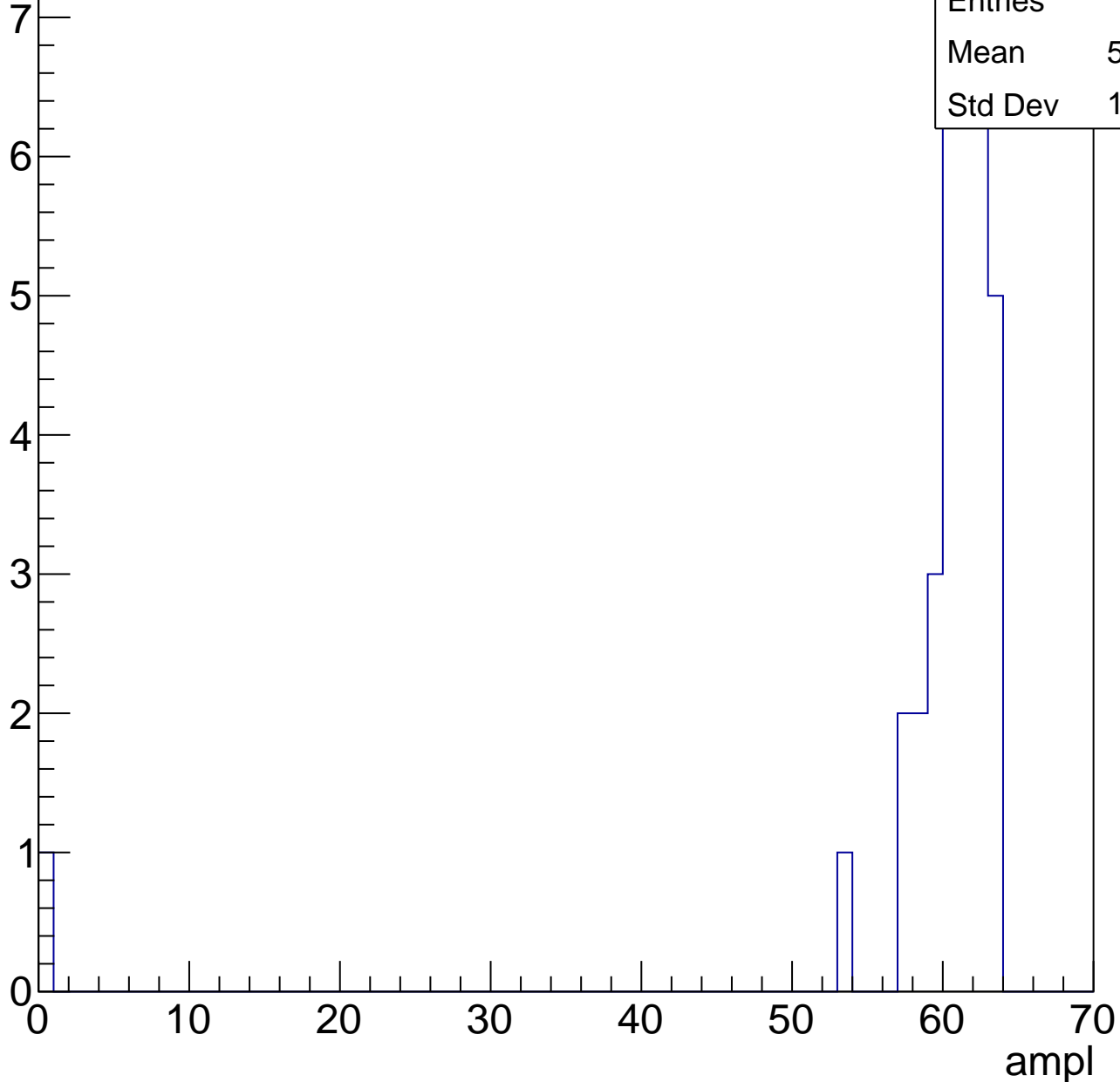


# B1L002S, U18-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.74
Std Dev	10.29

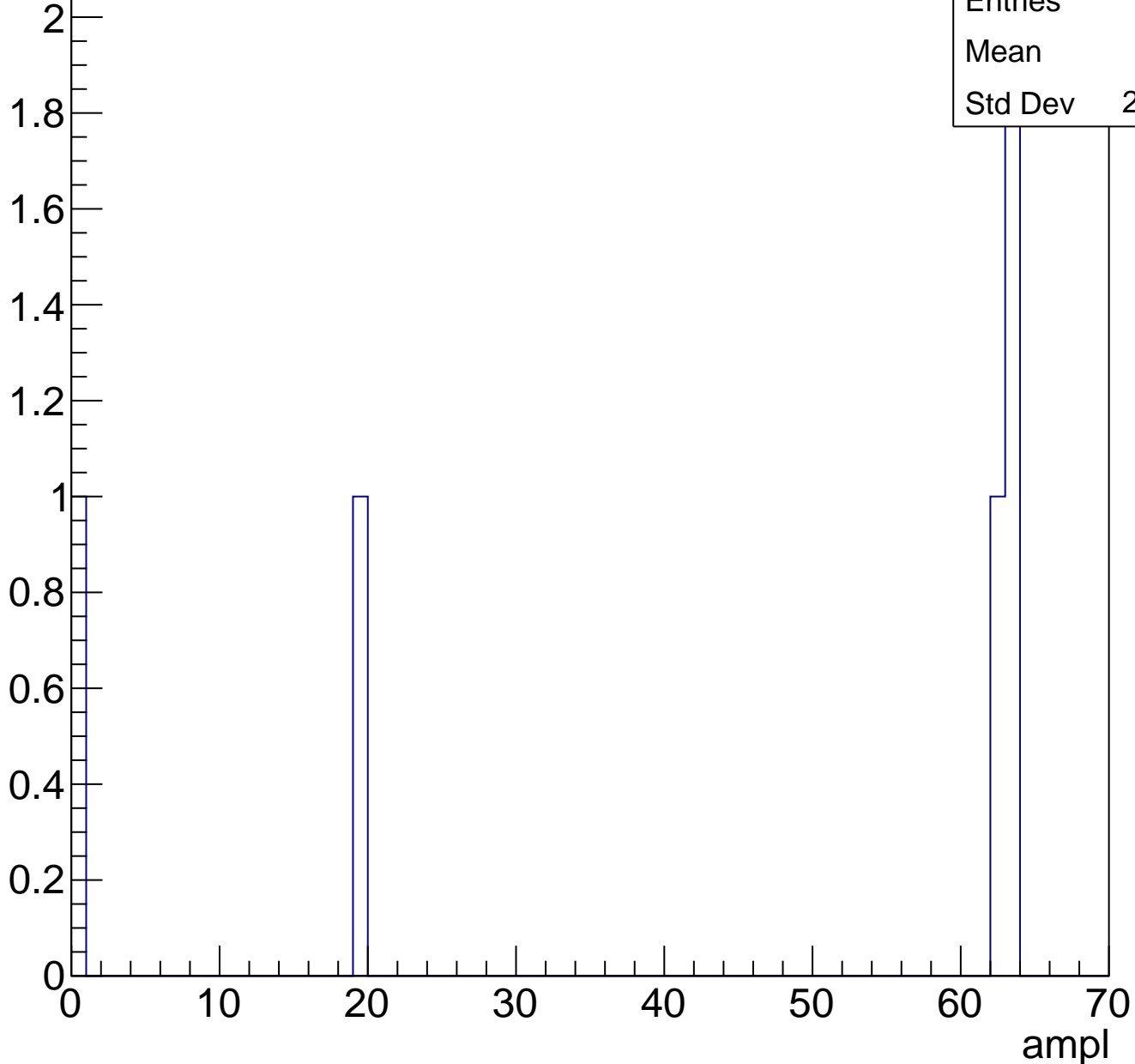




# B1L002S, U18-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	41.4
Std Dev	26.73

# B1L002S, U18-ch14, adc0

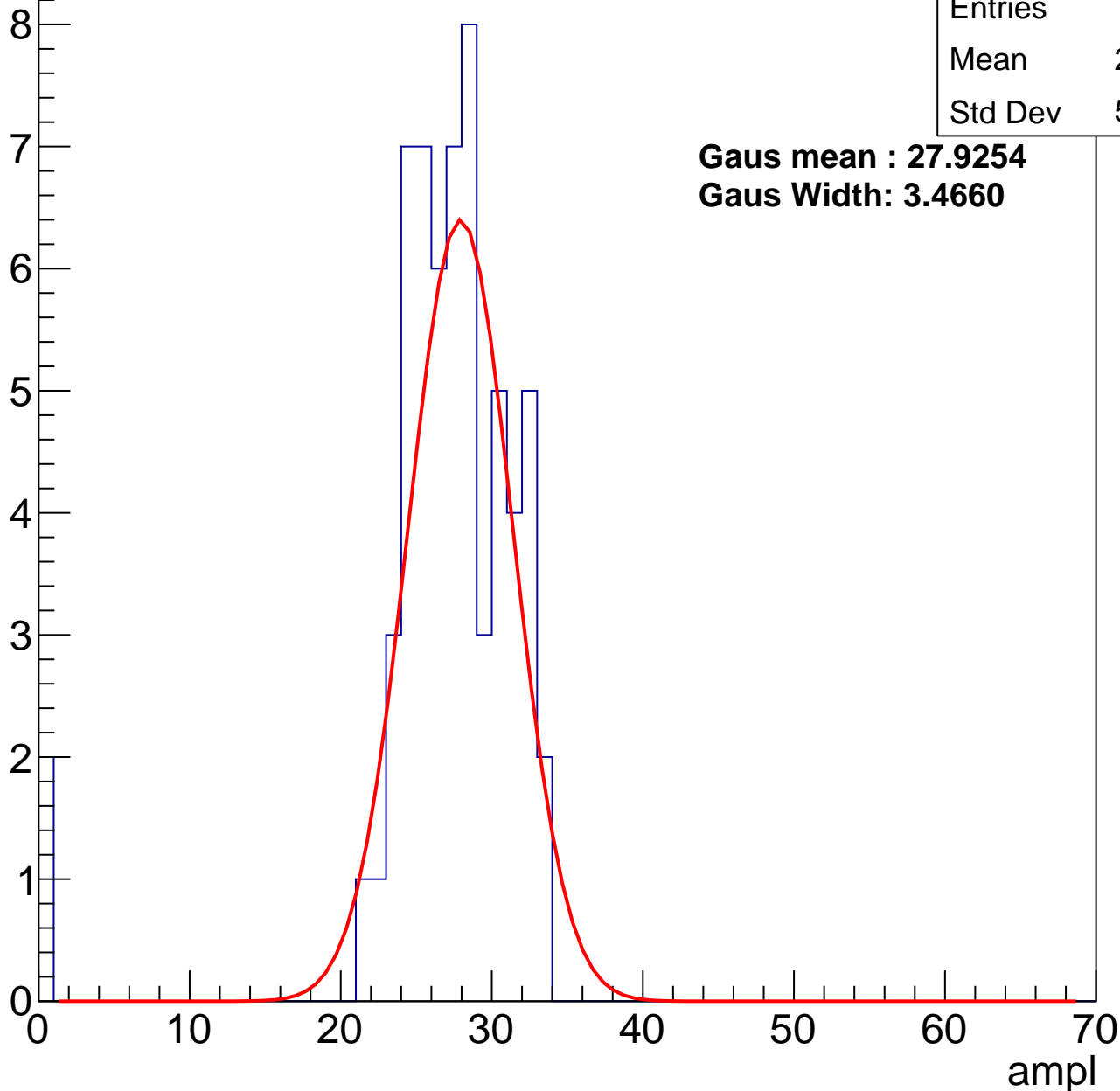
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	26.41
Std Dev	5.681

**Gaus mean : 27.9254**

**Gaus Width: 3.4660**



# B1L002S, U18-ch14, adc1

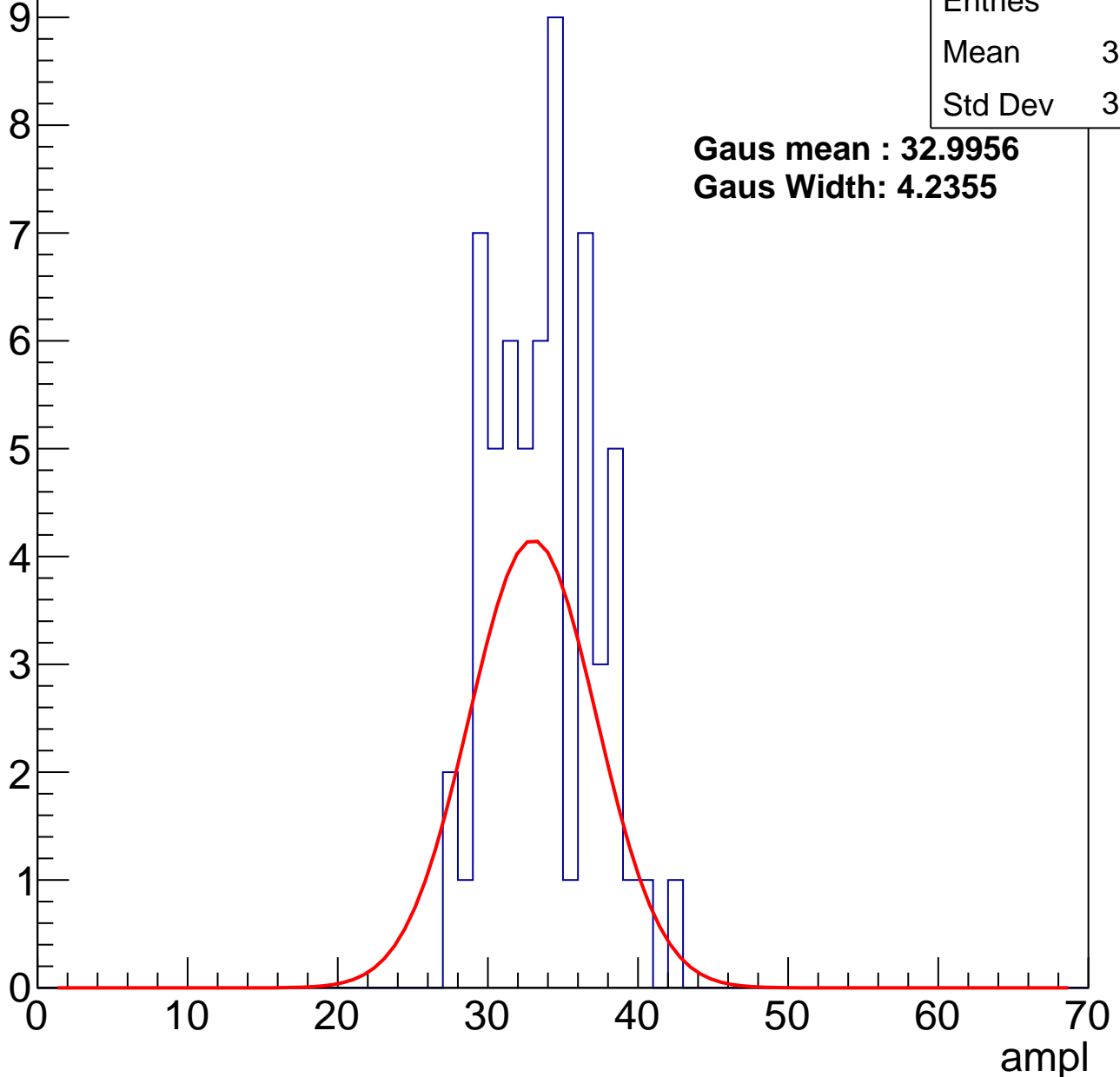
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.23
Std Dev	3.403

**Gaus mean : 32.9956**

**Gaus Width: 4.2355**



# B1L002S, U18-ch14, adc2

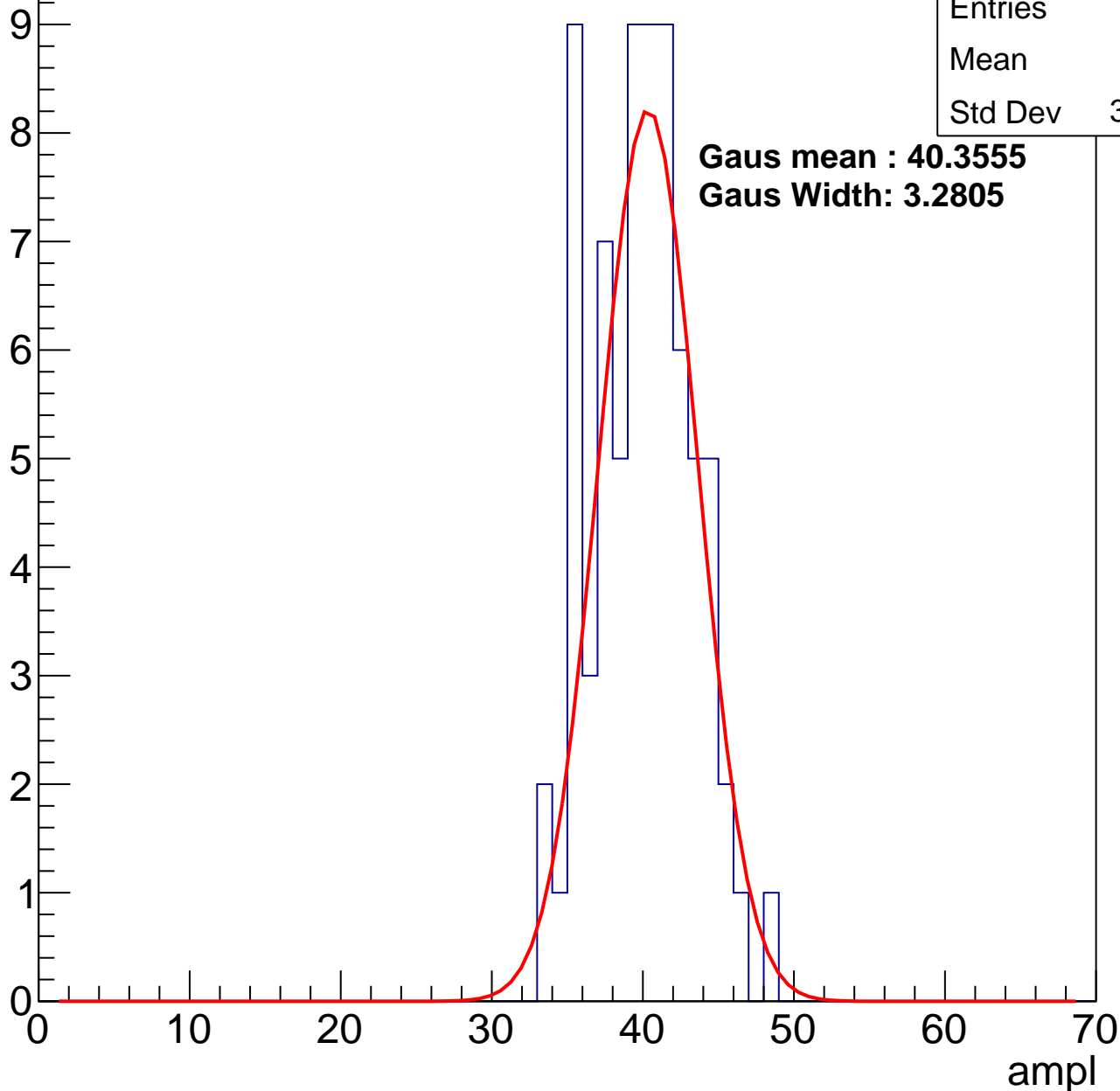
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	39.5
Std Dev	3.264

**Gaus mean : 40.3555**

**Gaus Width: 3.2805**

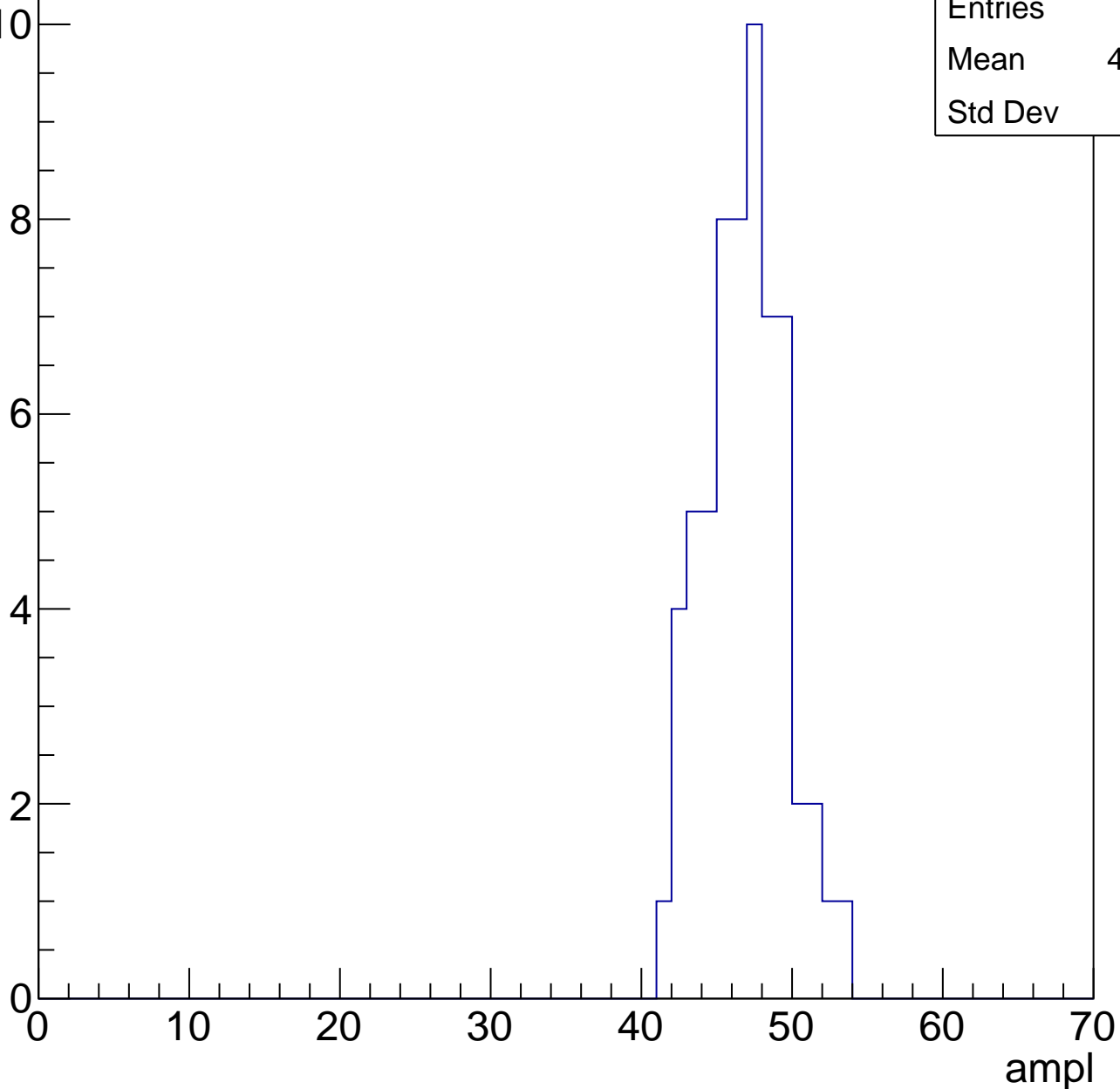


# B1L002S, U18-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.36
Std Dev	2.63

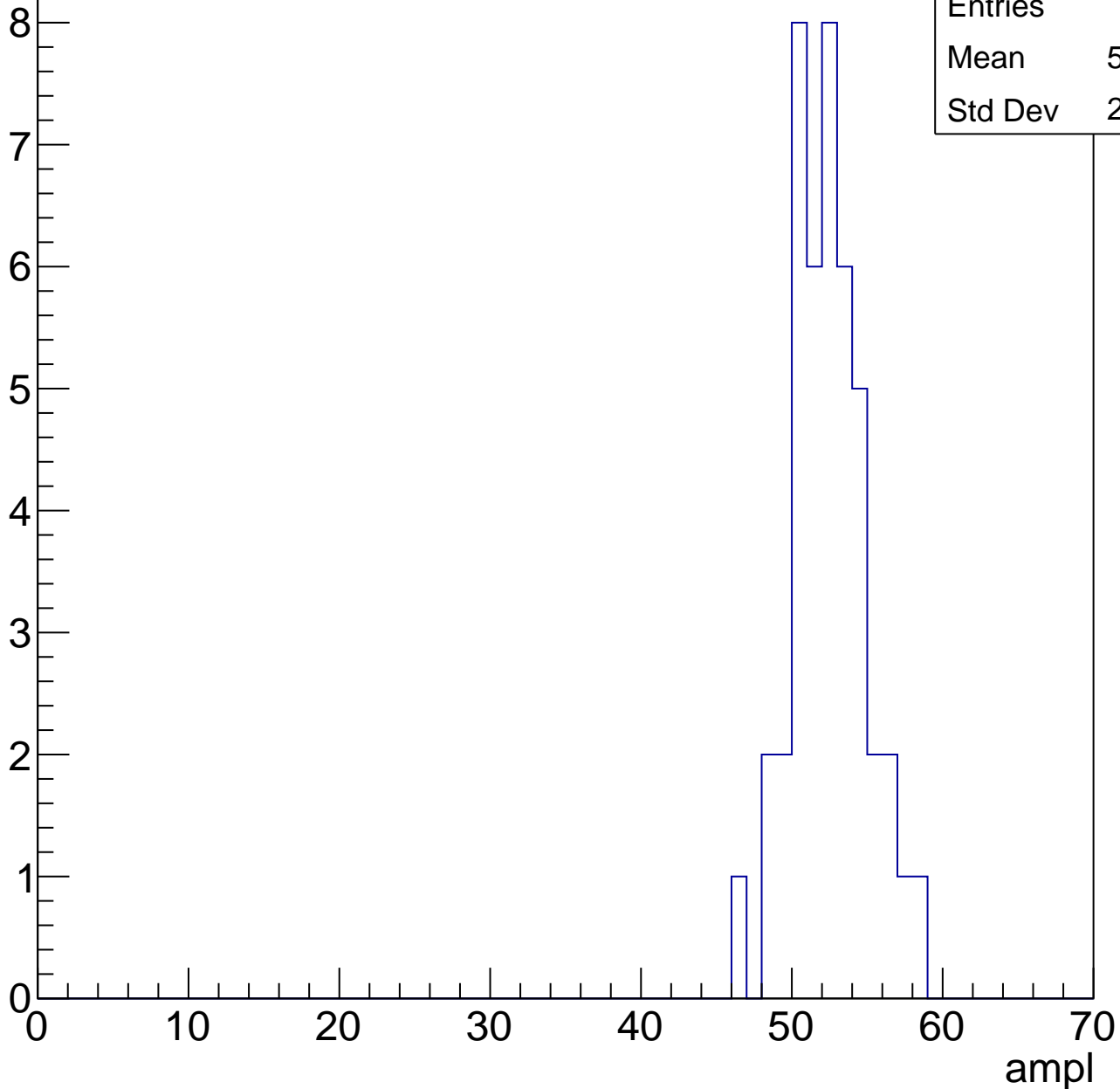


# B1L002S, U18-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.98
Std Dev	2.435

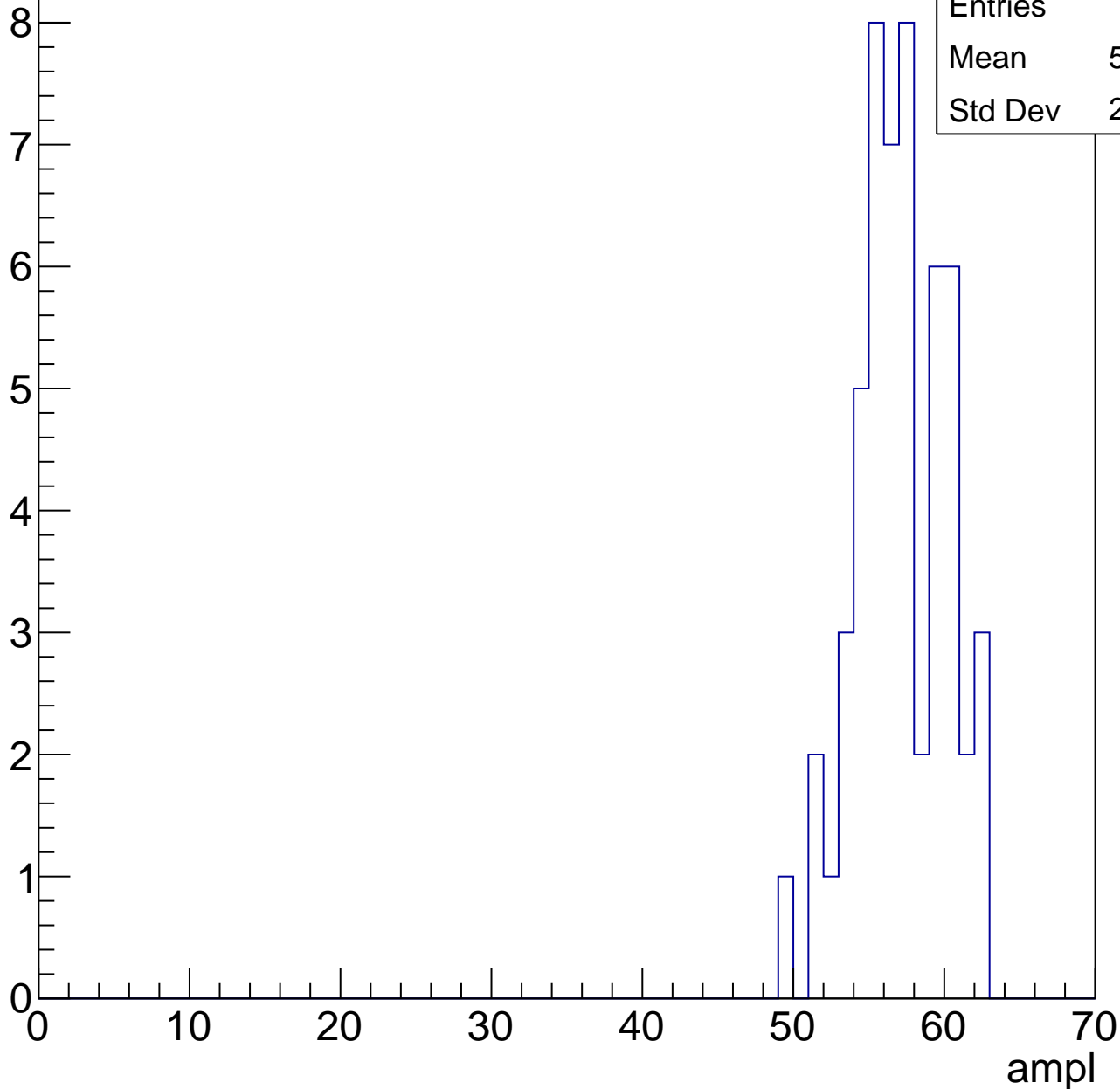


# B1L002S, U18-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	56.63
Std Dev	2.958

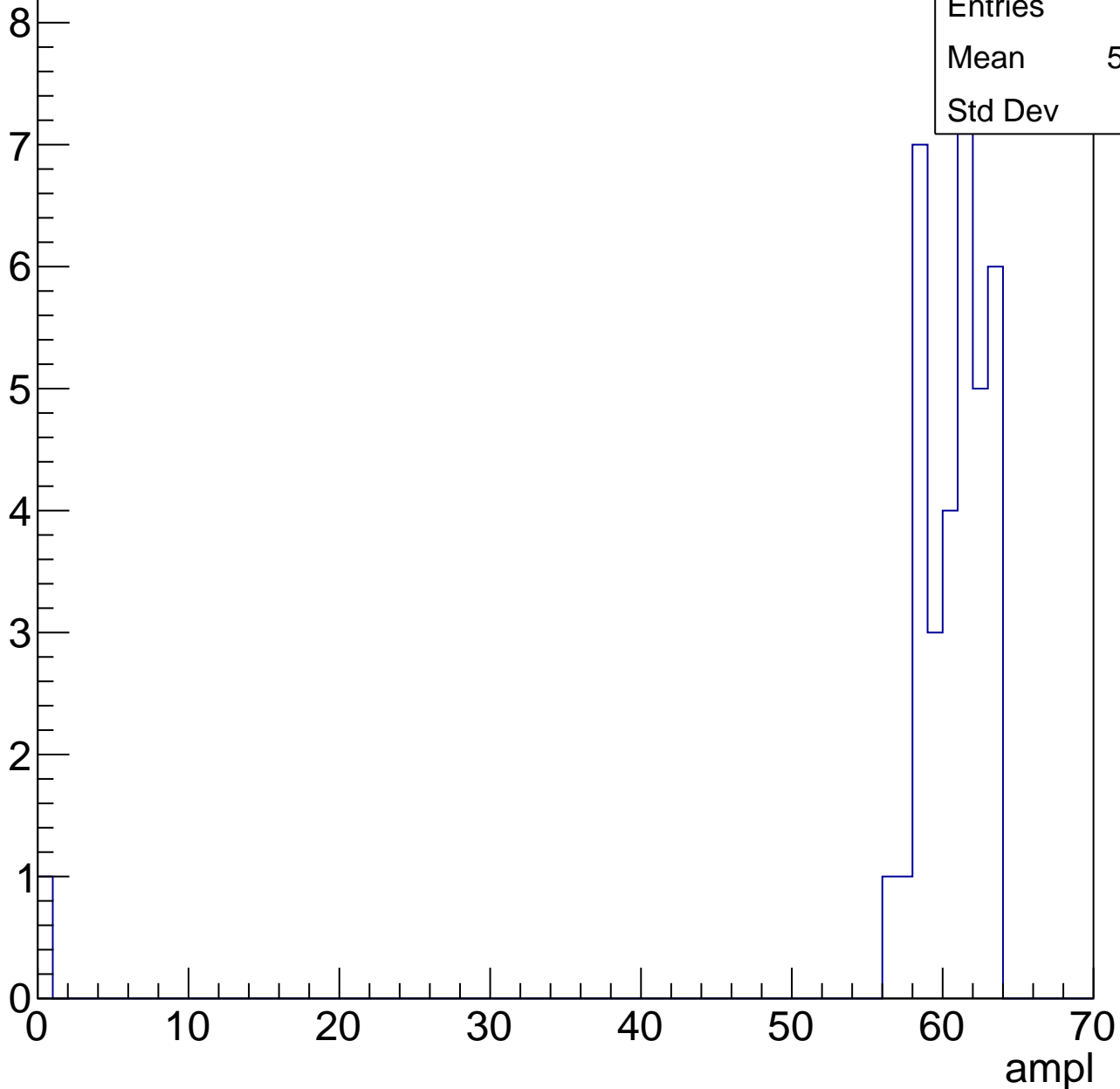


# B1L002S, U18-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.67
Std Dev	10.1

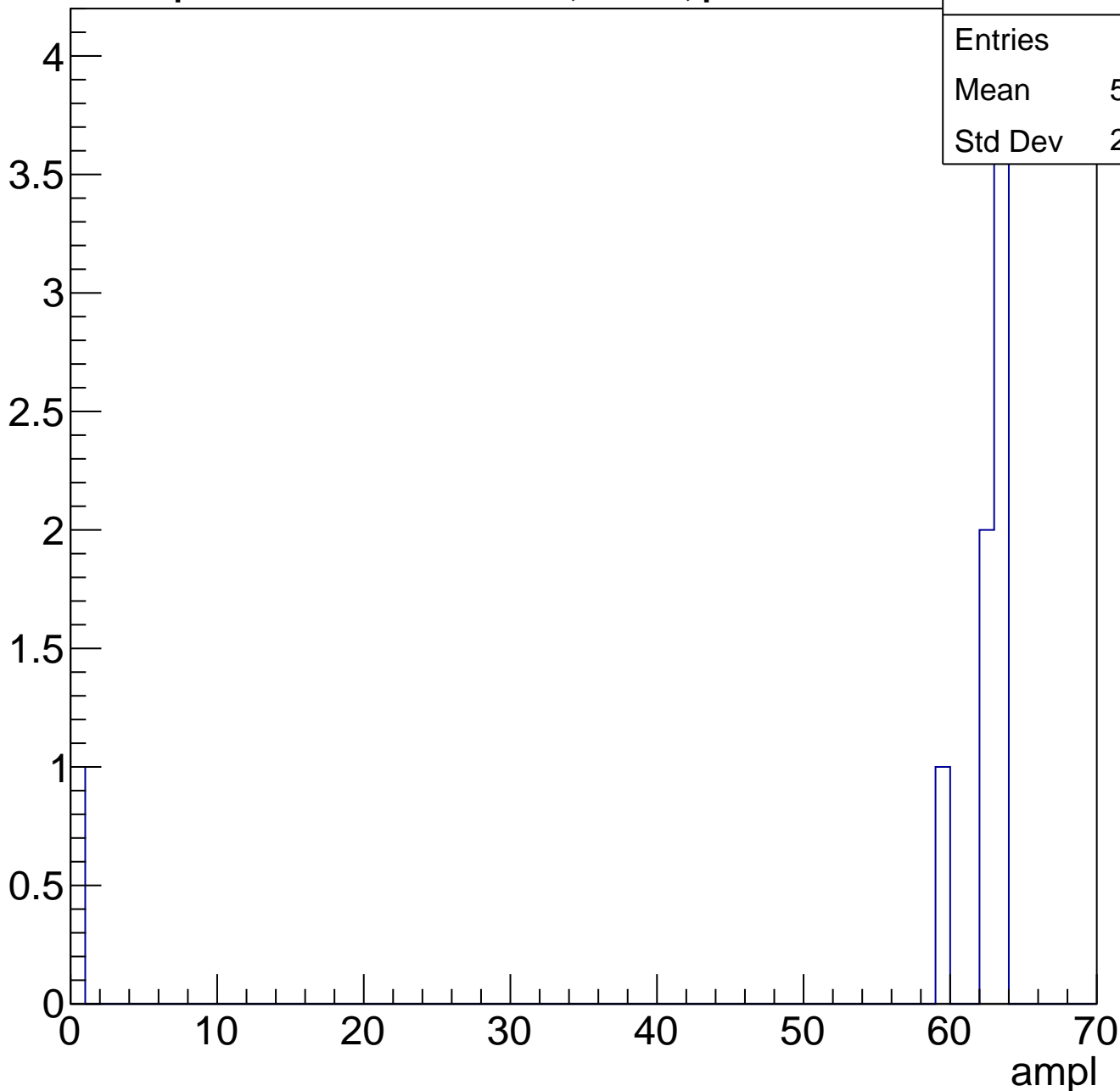




# B1L002S, U18-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	27.2
Std Dev	4.468

**Gaus mean : 28.0228**

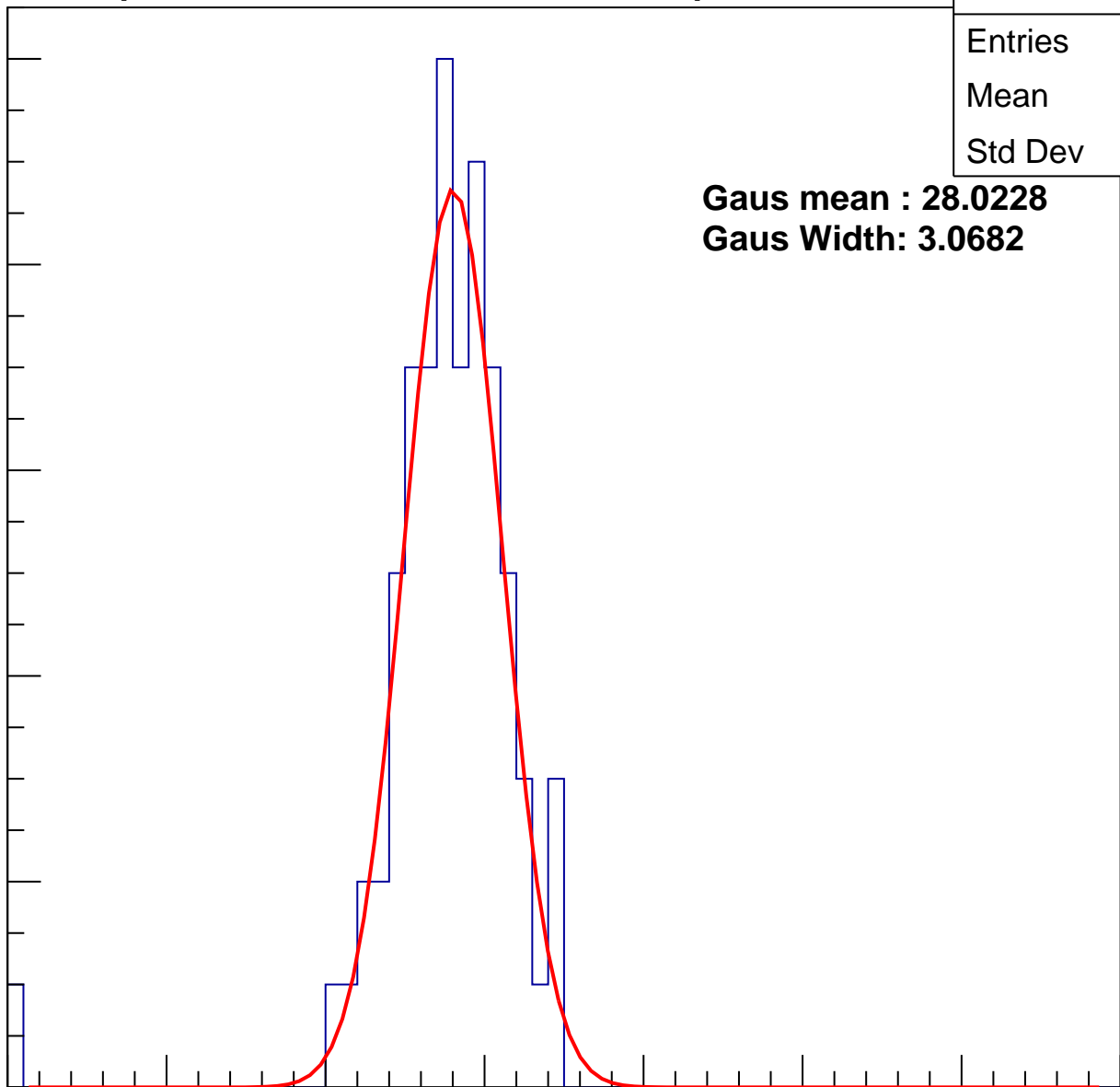
**Gaus Width: 3.0682**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch15, adc1

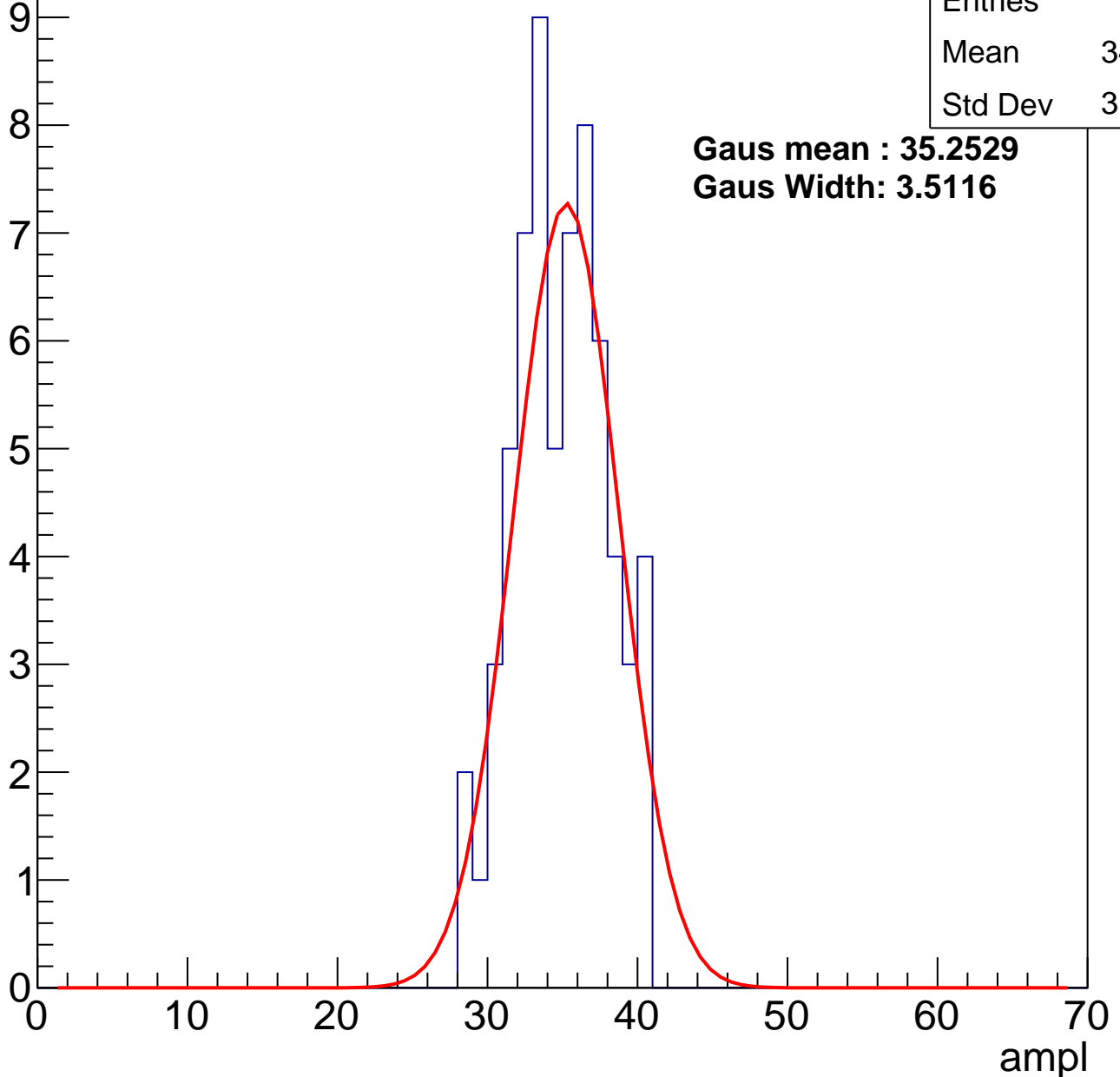
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.45
Std Dev	3.036

**Gaus mean : 35.2529**

**Gaus Width: 3.5116**



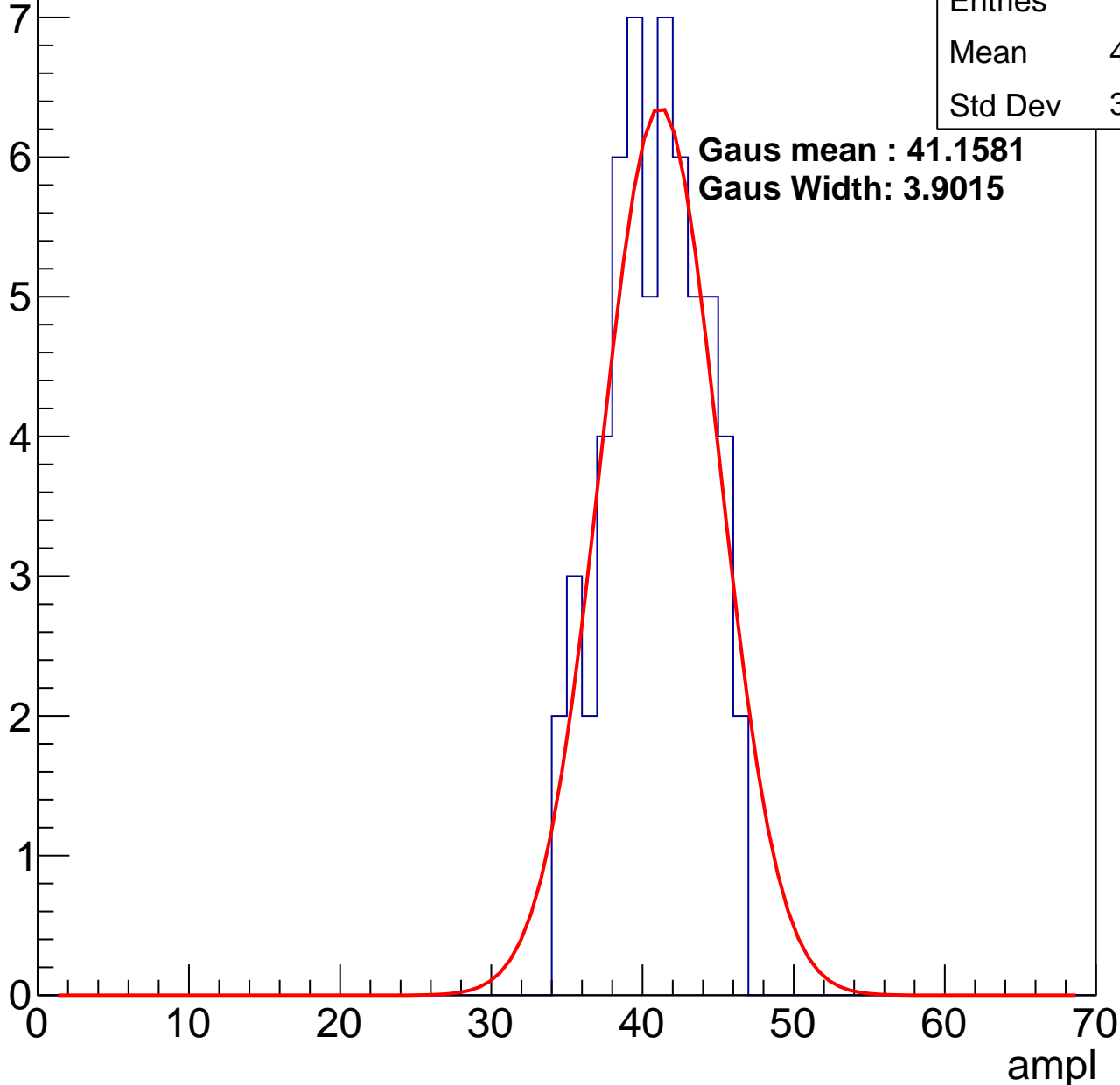
# B1L002S, U18-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.34
Std Dev	3.127

**Gaus mean : 41.1581**  
**Gaus Width: 3.9015**

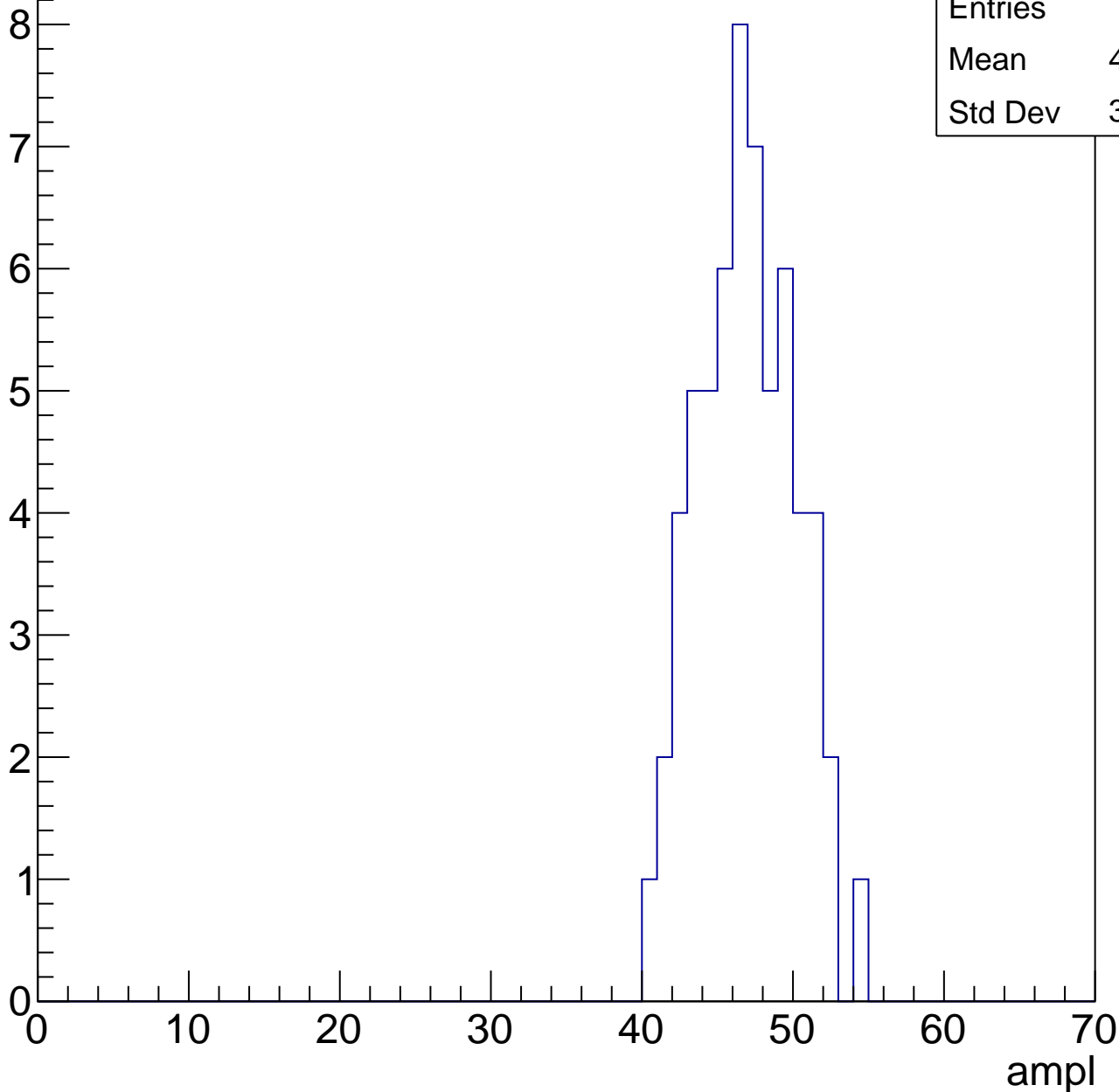


# B1L002S, U18-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

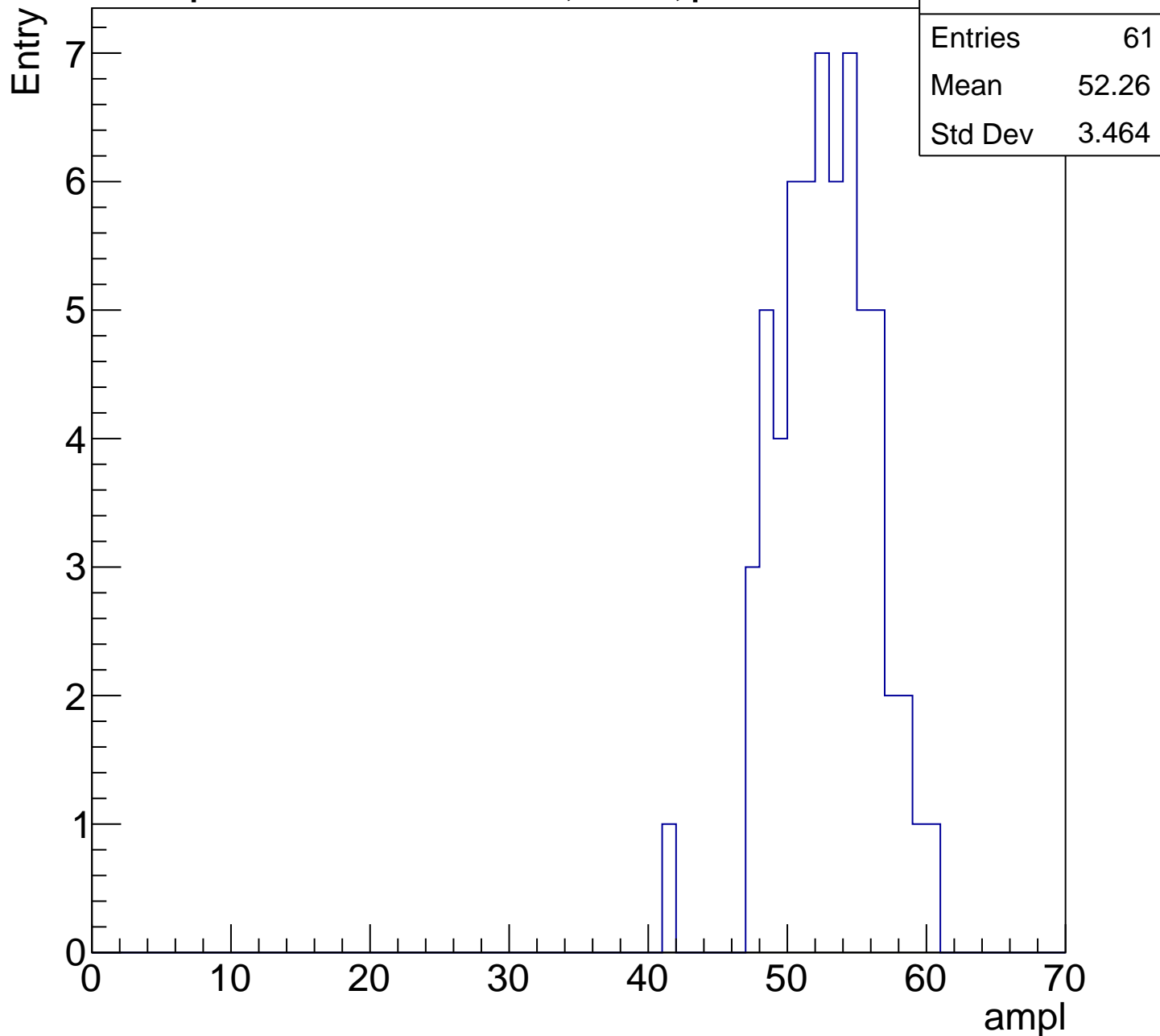
Entry

Entries	60
Mean	46.47
Std Dev	3.133



# B1L002S, U18-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

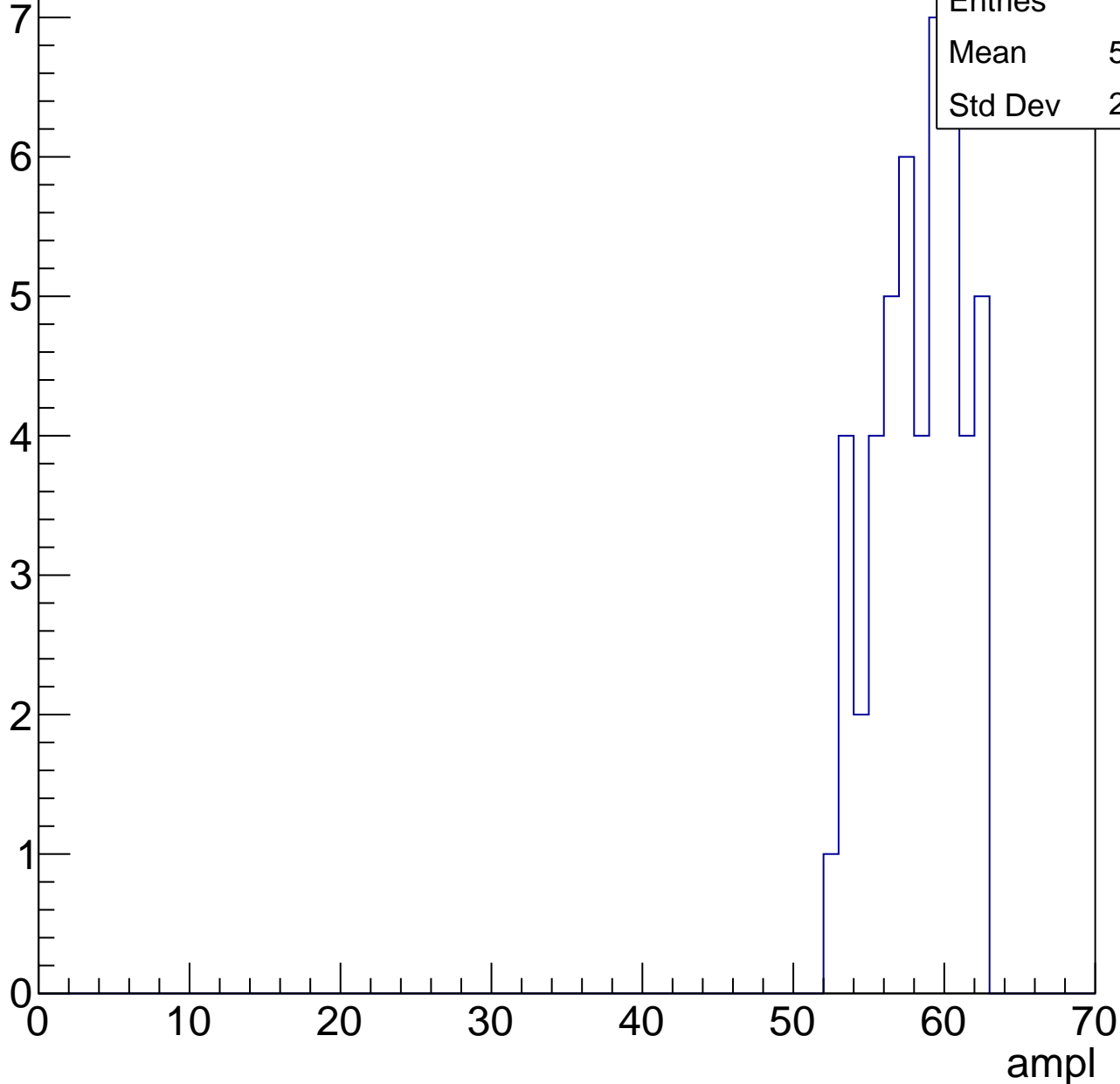


# B1L002S, U18-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	57.82
Std Dev	2.782



# B1L002S, U18-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	33
Mean	58.94
Std Dev	10.65

Entry

10

8

6

4

2

0

0

10

20

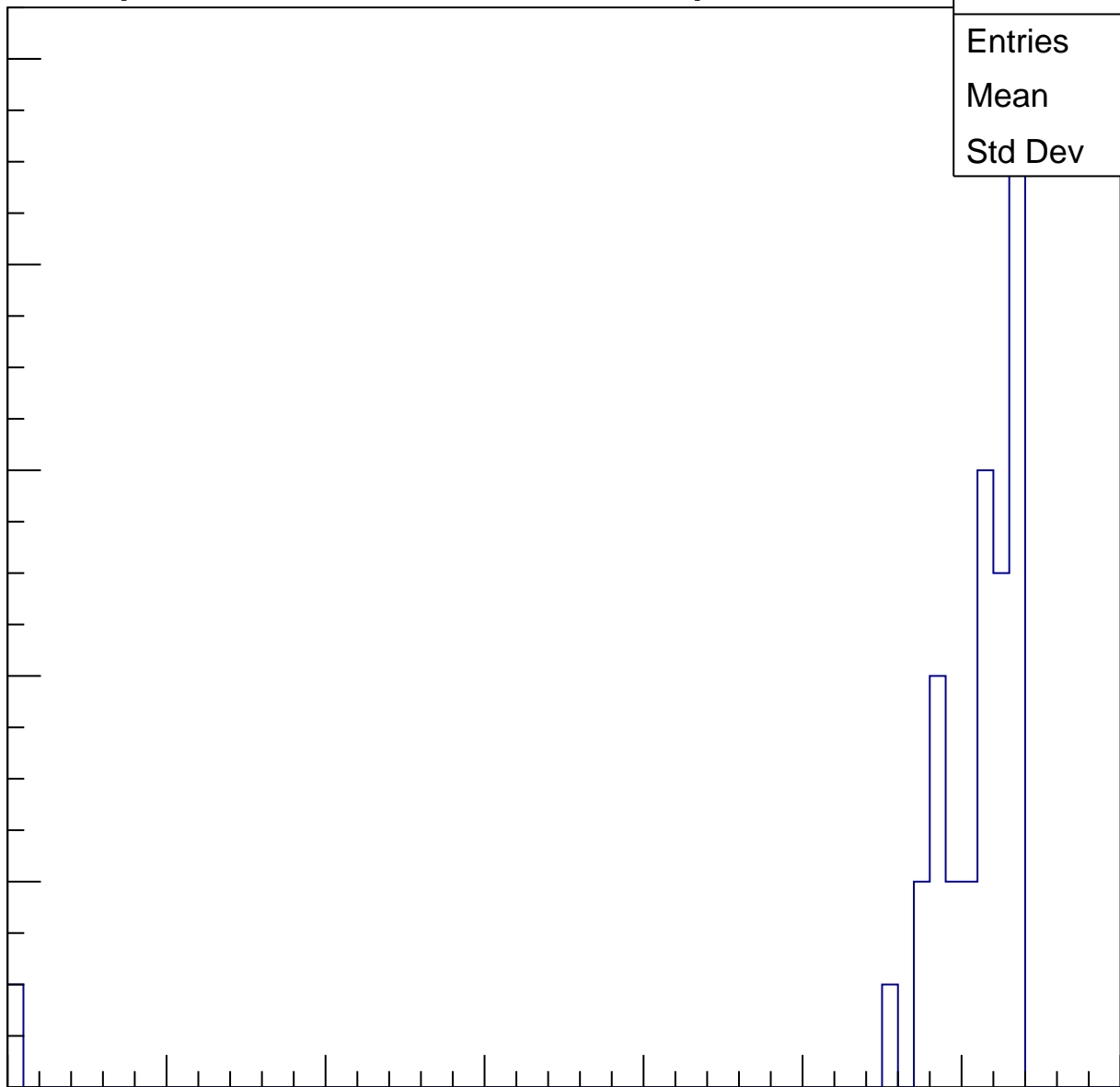
30

40

50

60

ampl





# B1L002S, U18-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U18-ch16, adc0

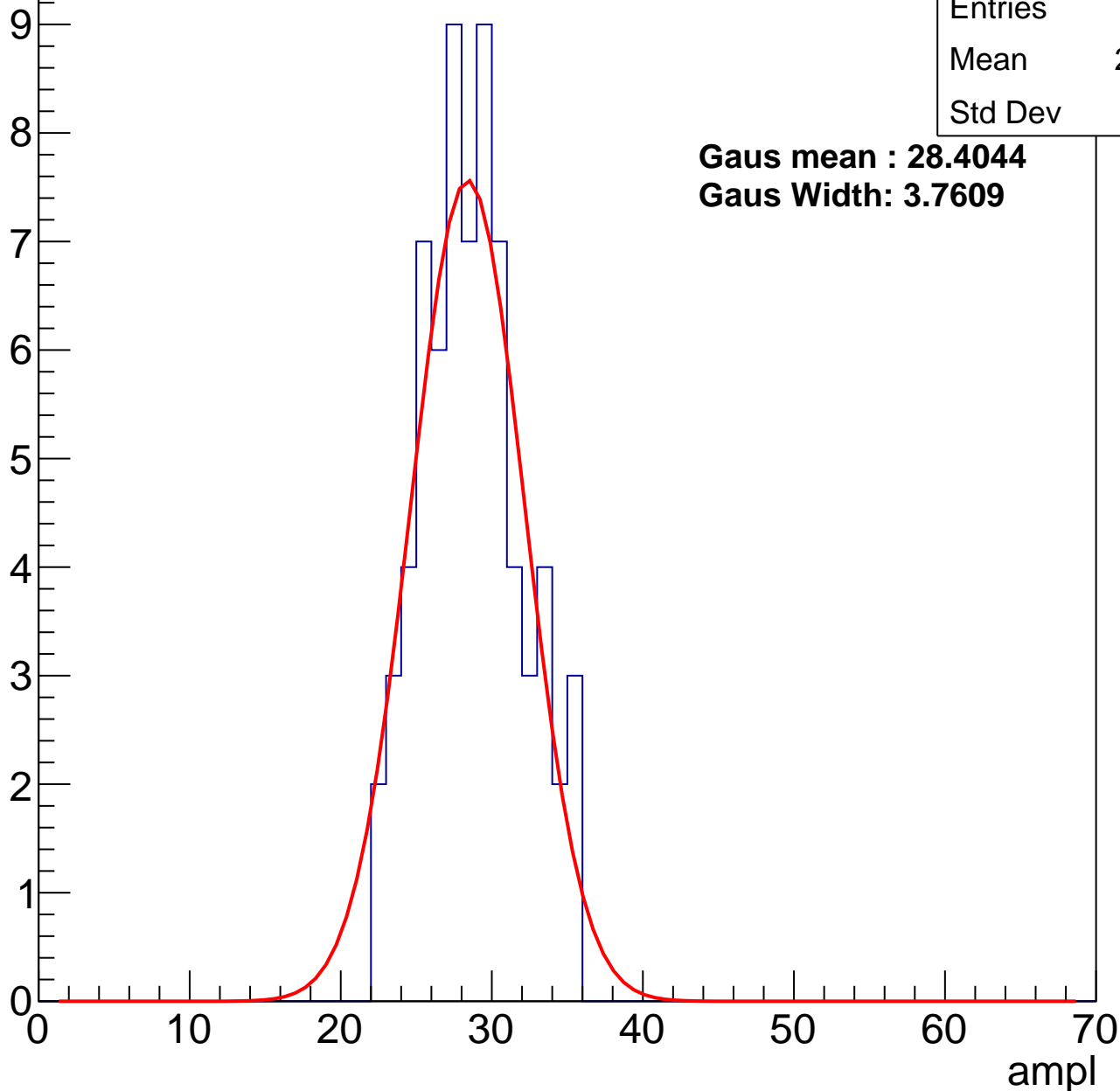
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.21
Std Dev	3.26

**Gaus mean : 28.4044**

**Gaus Width: 3.7609**



# B1L002S, U18-ch16, adc1

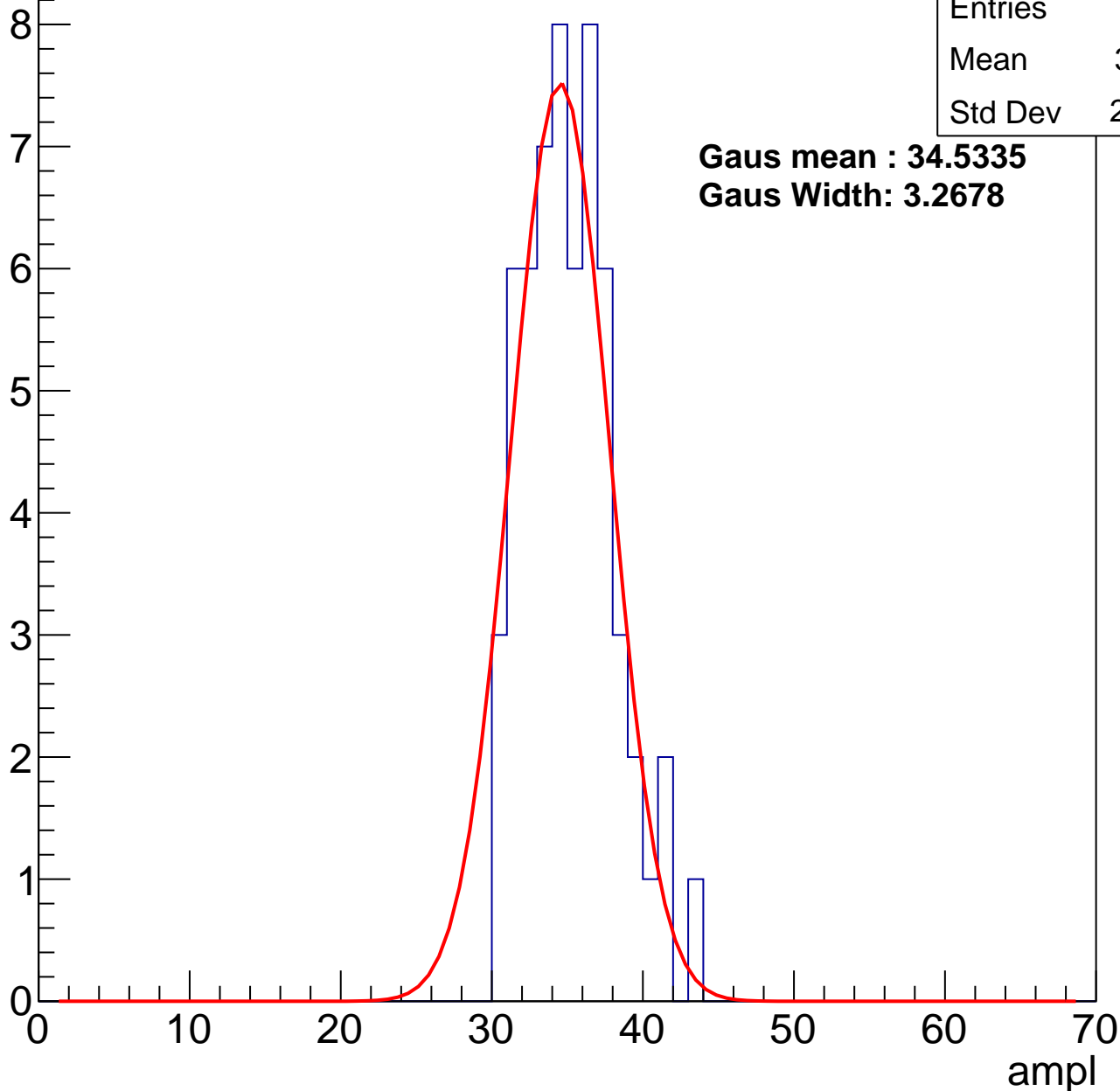
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.71
Std Dev	2.935

**Gaus mean : 34.5335**

**Gaus Width: 3.2678**



# B1L002S, U18-ch16, adc2

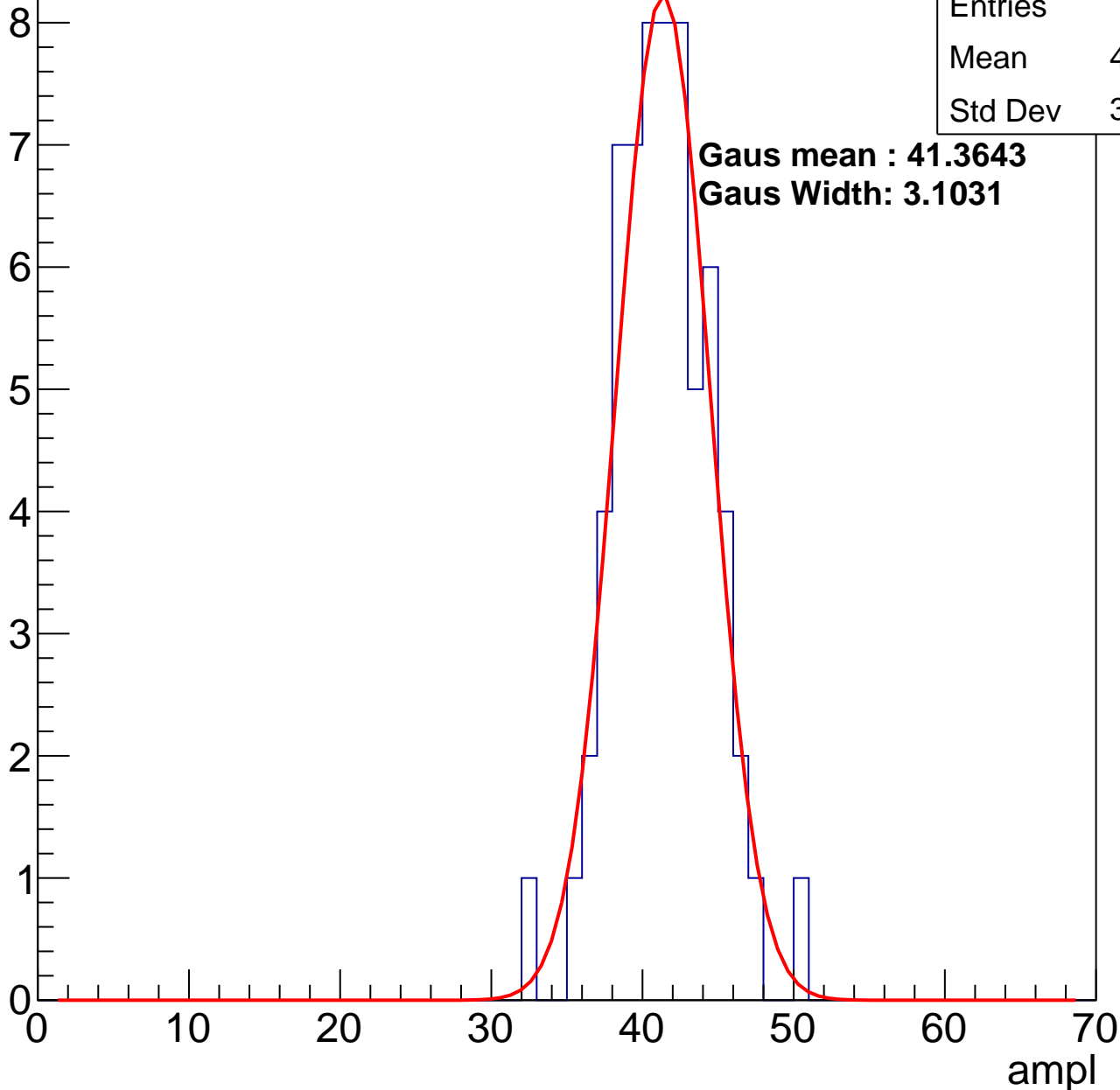
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.89
Std Dev	3.143

**Gaus mean : 41.3643**

**Gaus Width: 3.1031**

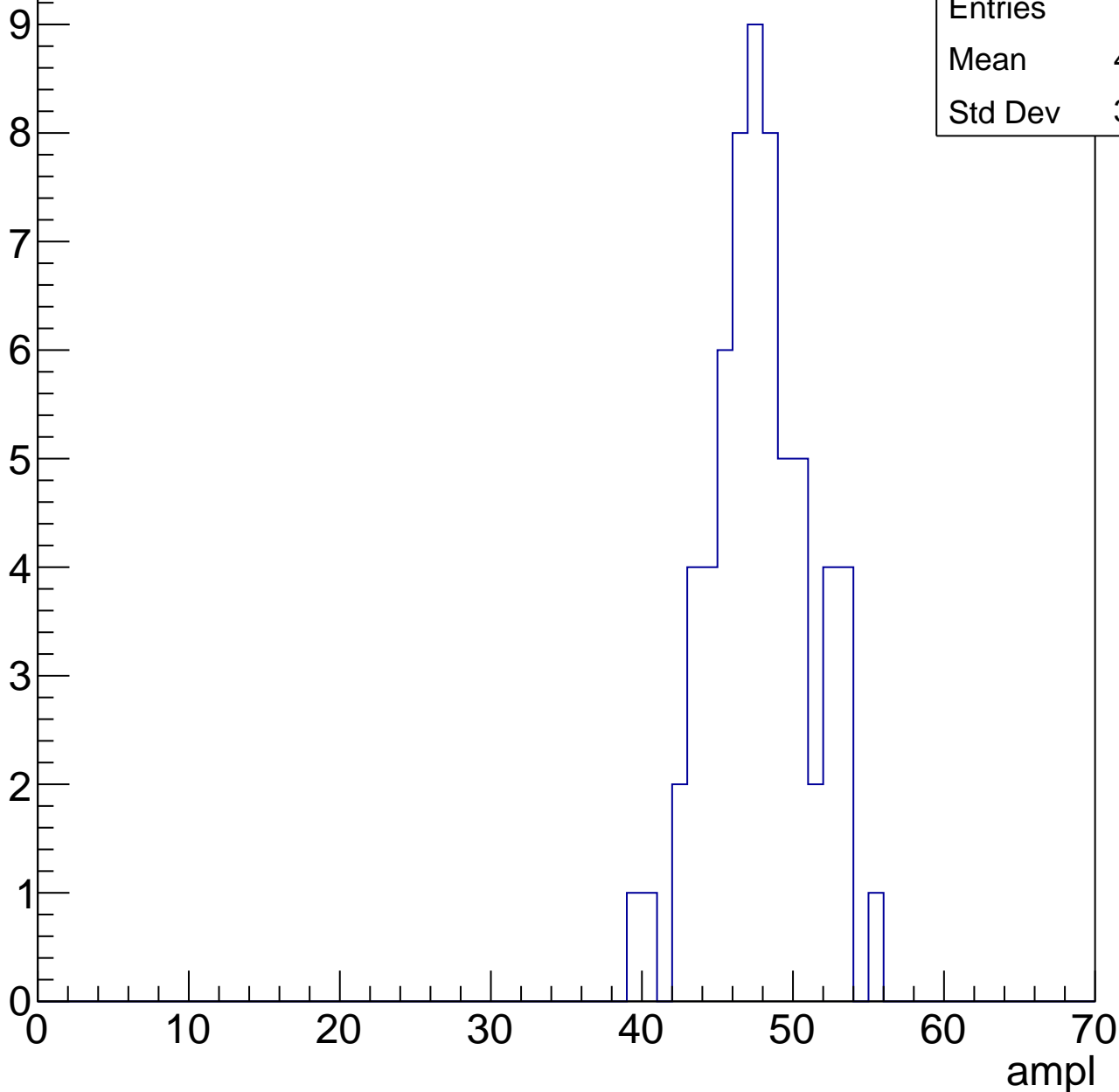


# B1L002S, U18-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

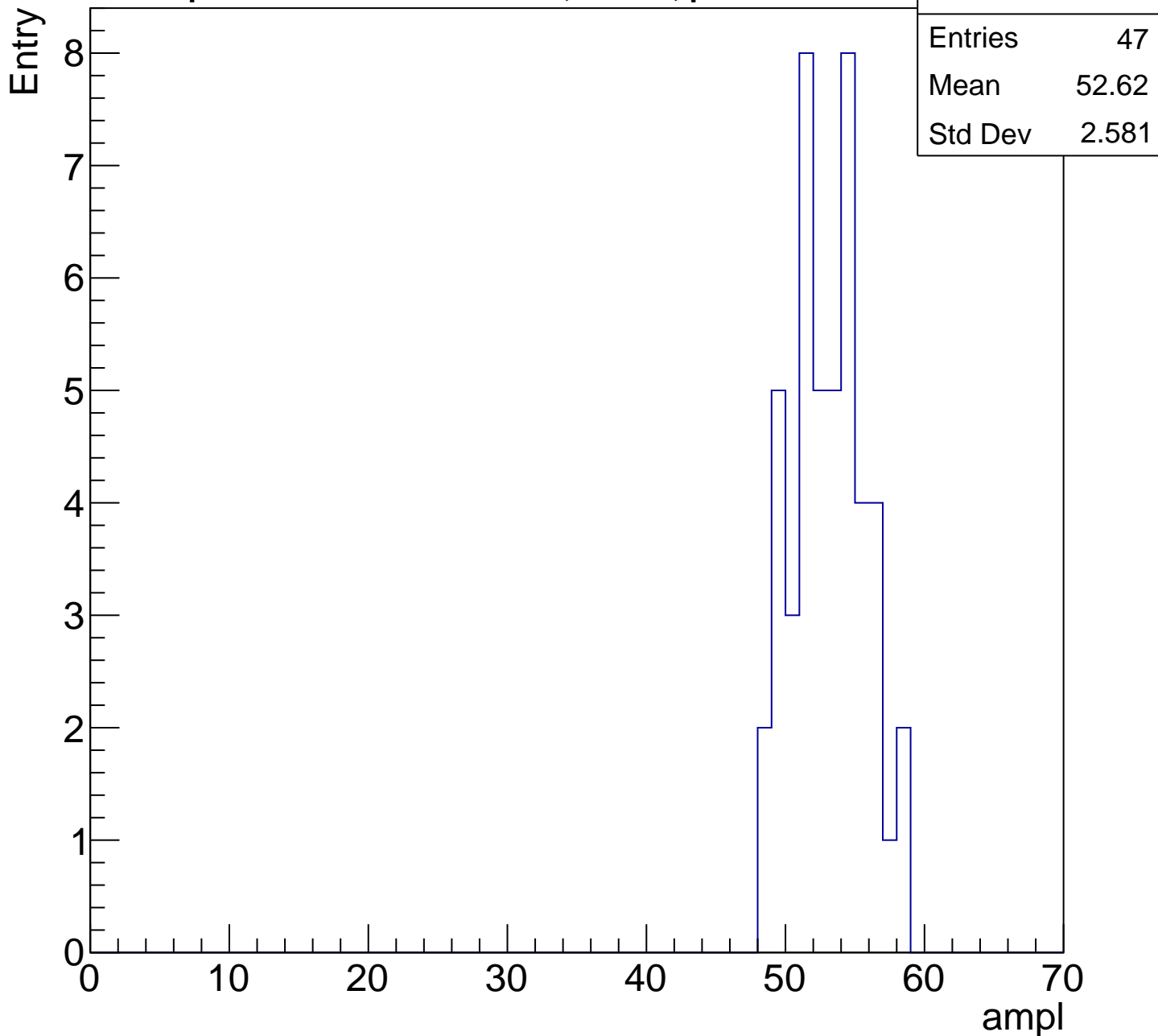
Entry

Entries	64
Mean	47.31
Std Dev	3.311



# B1L002S, U18-ch16, adc4

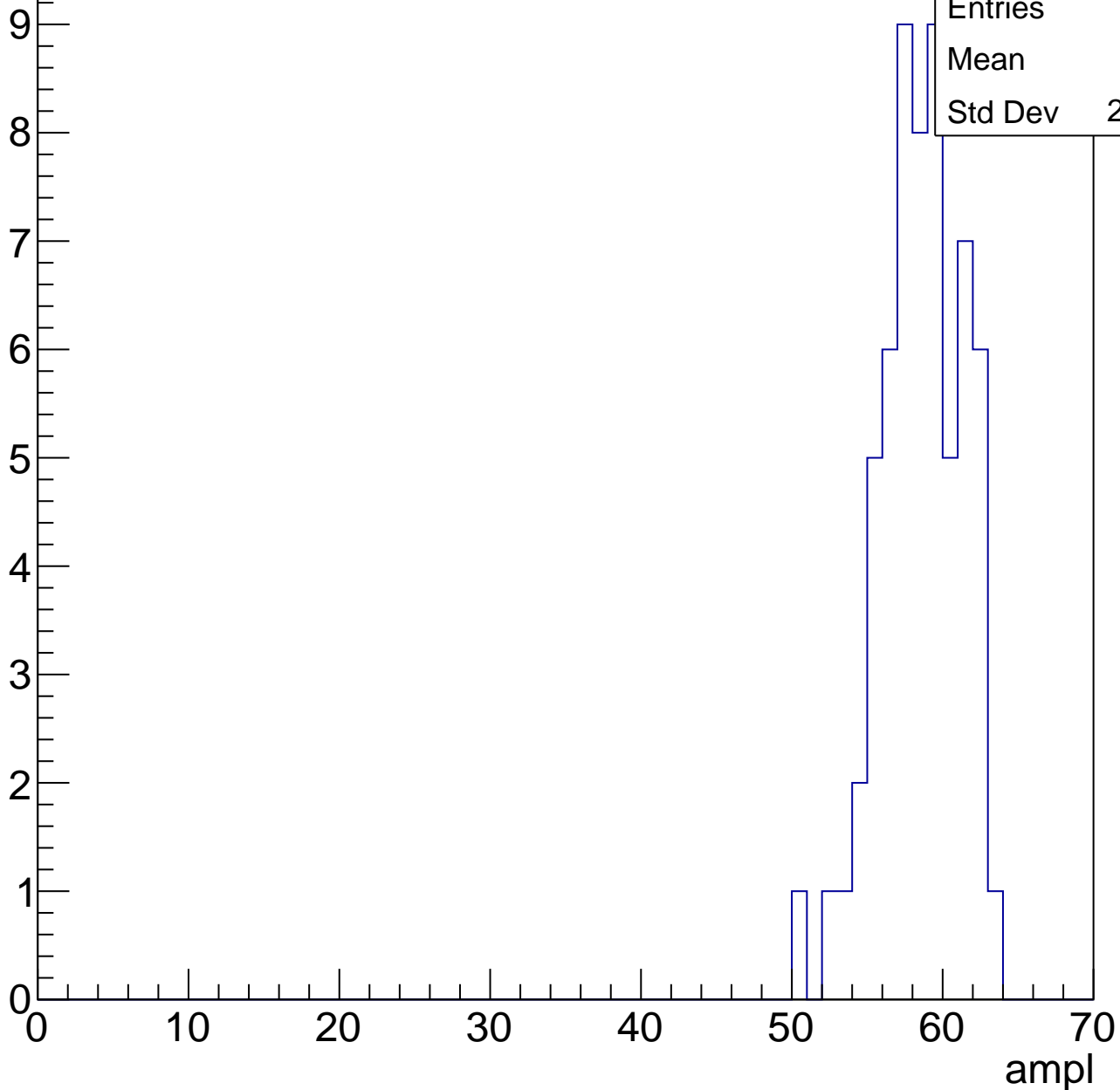
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



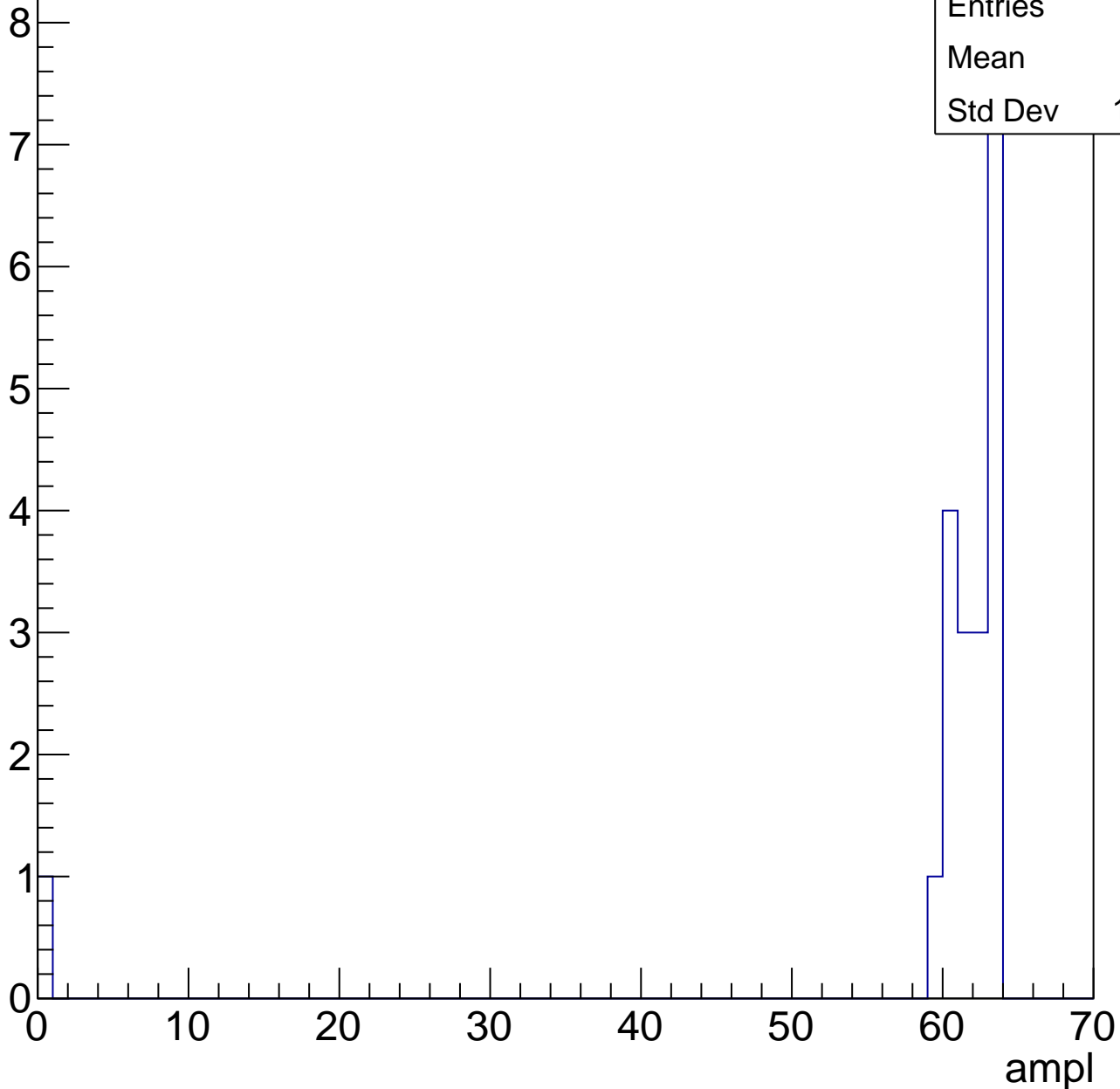
Entries	61
Mean	58.1
Std Dev	2.708

# B1L002S, U18-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.6
Std Dev	13.51

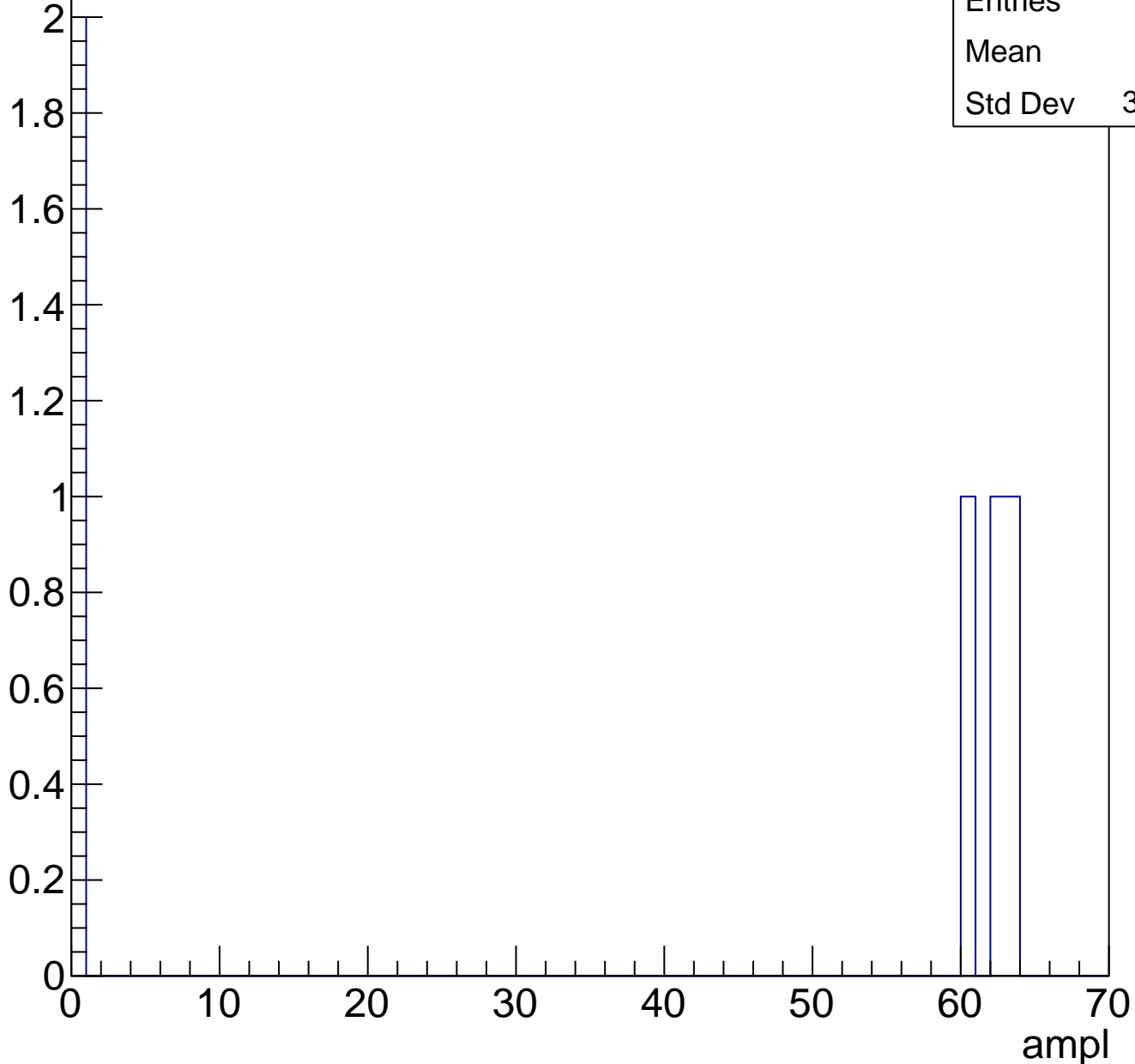




# B1L002S, U18-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	37
Std Dev	30.23

# B1L002S, U18-ch17, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	29.54
Std Dev	2.736

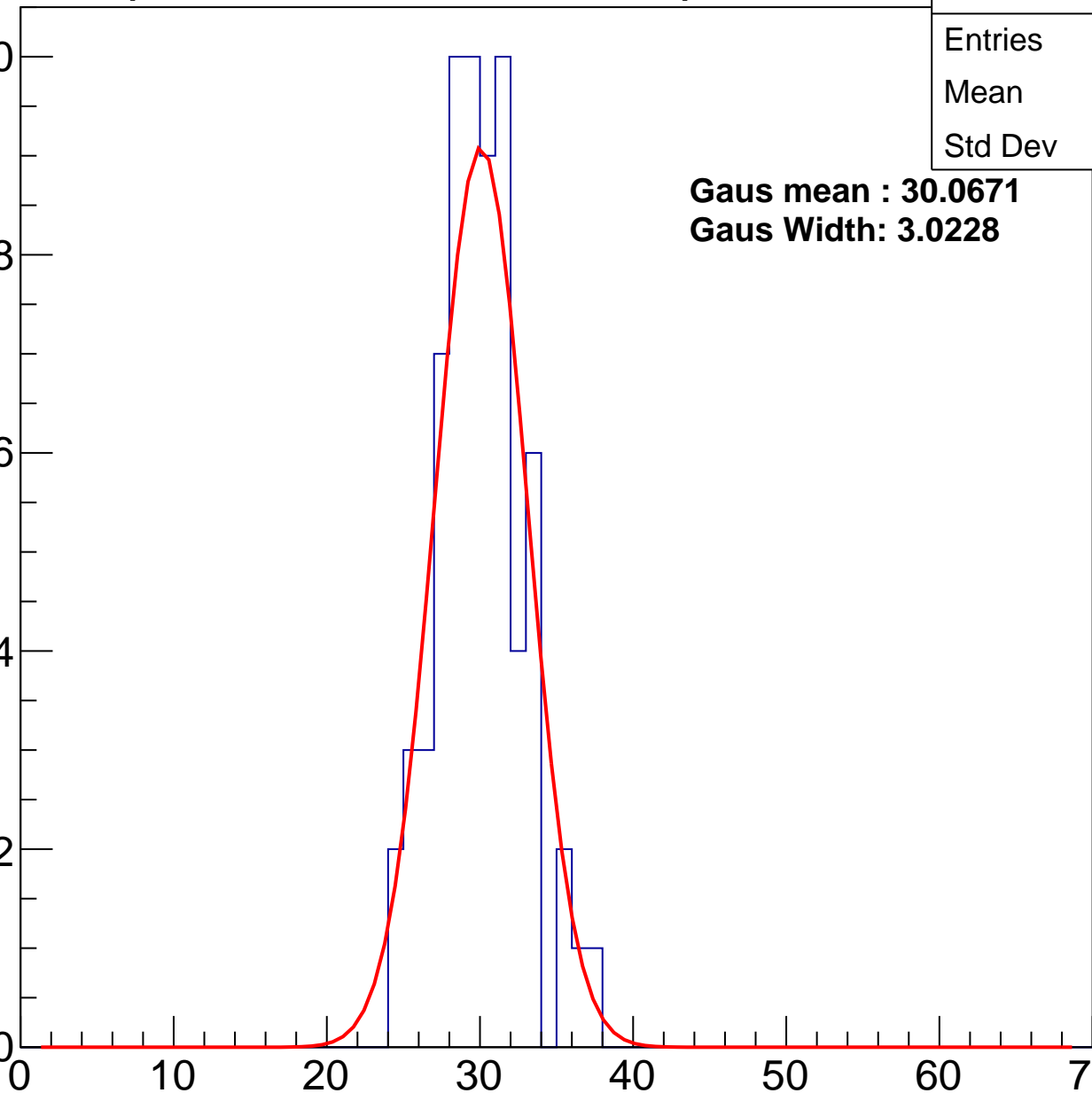
**Gaus mean : 30.0671**

**Gaus Width: 3.0228**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U18-ch17, adc1

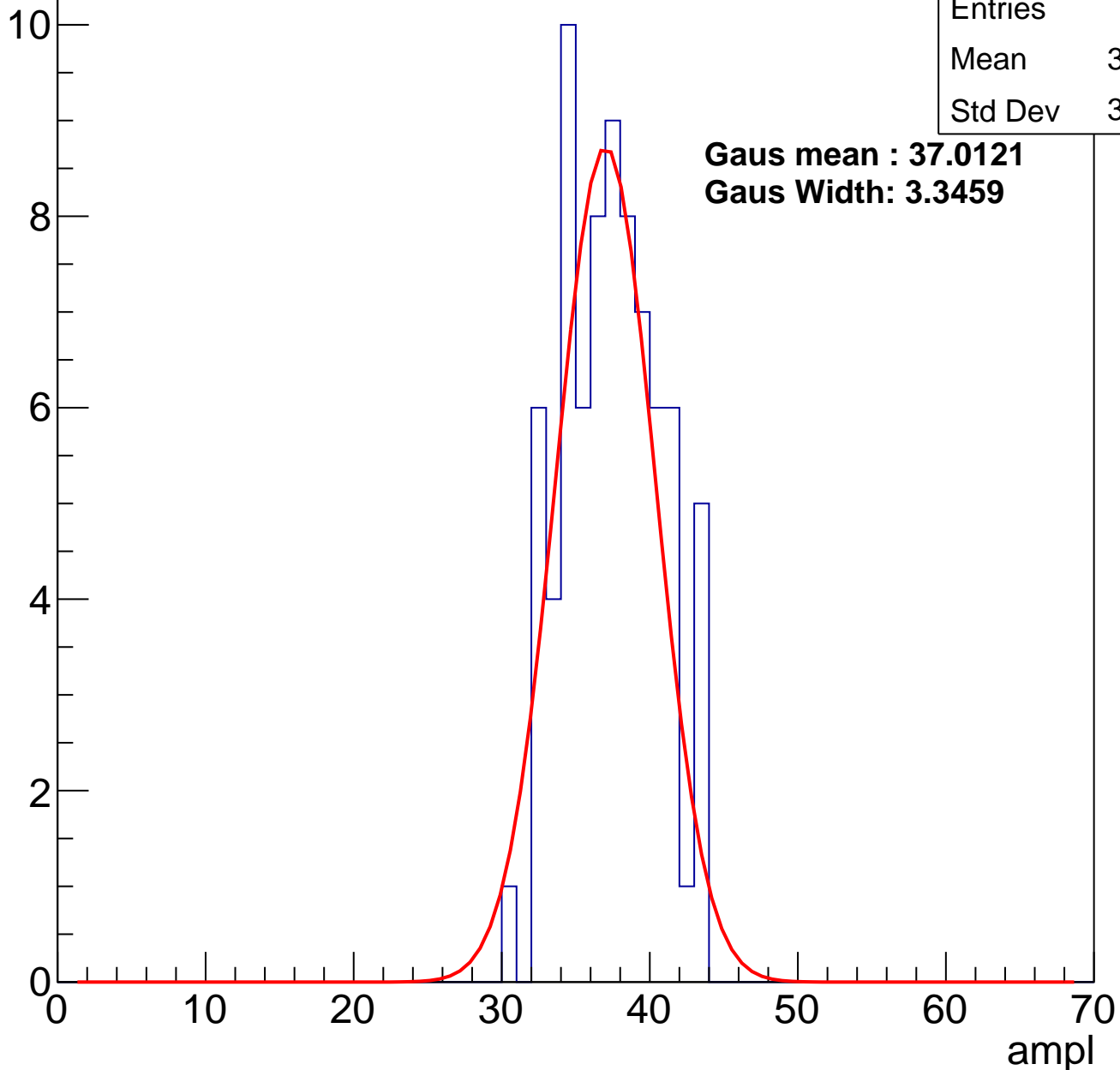
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	36.95
Std Dev	3.174

**Gaus mean : 37.0121**

**Gaus Width: 3.3459**

Entry



# B1L002S, U18-ch17, adc2

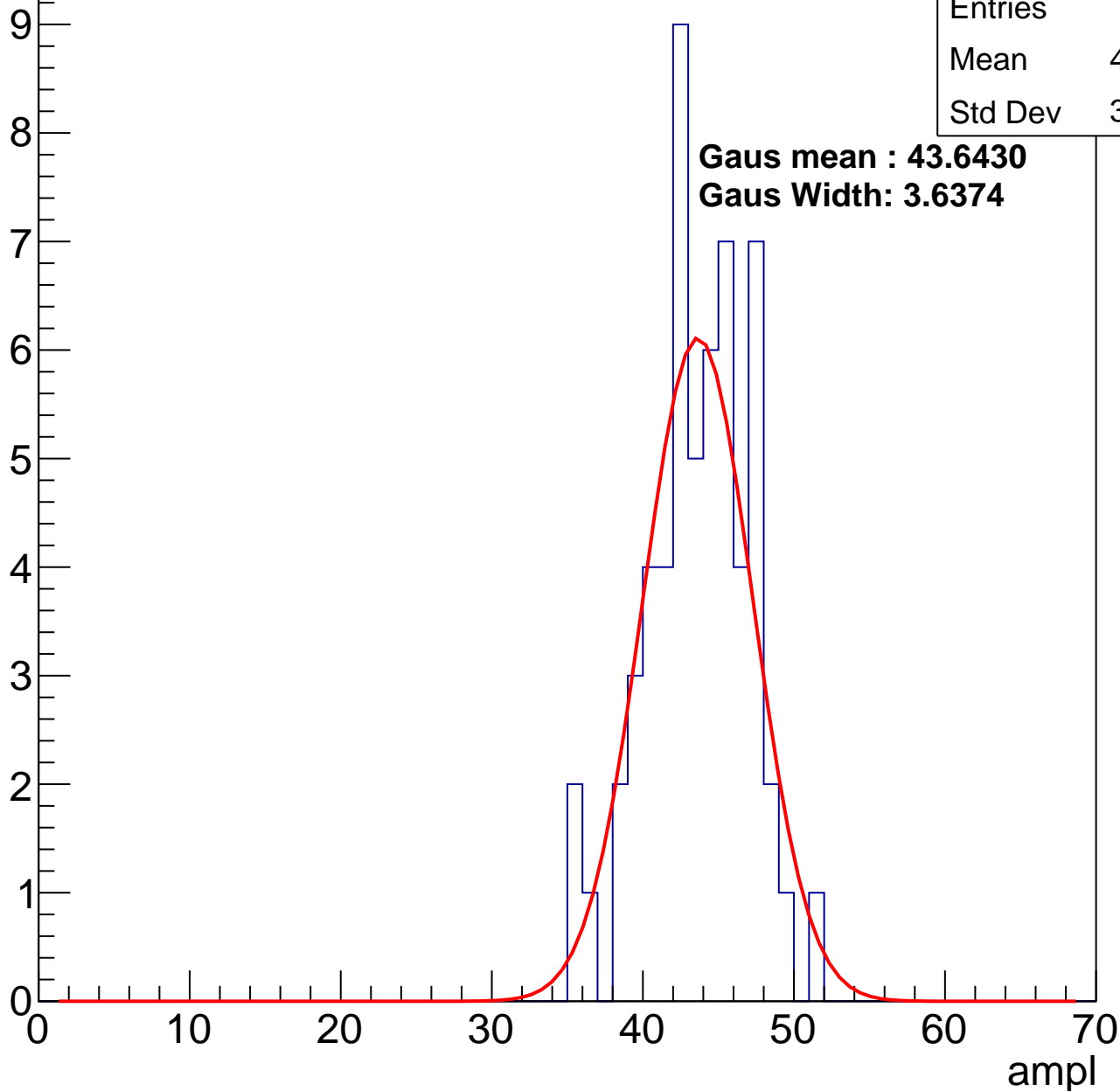
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	43.17
Std Dev	3.389

**Gaus mean : 43.6430**

**Gaus Width: 3.6374**

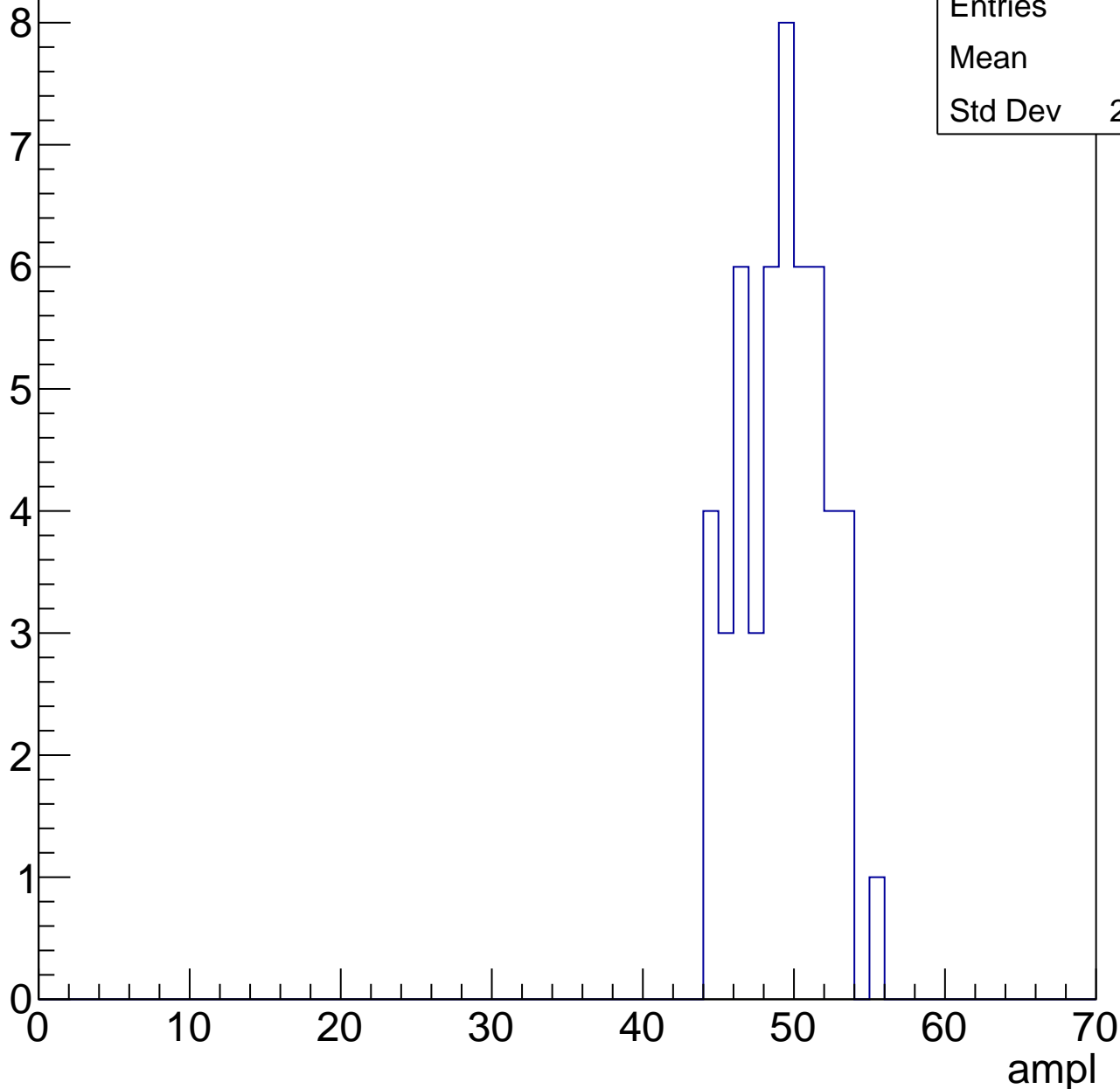


# B1L002S, U18-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	48.8
Std Dev	2.744



# B1L002S, U18-ch17, adc4

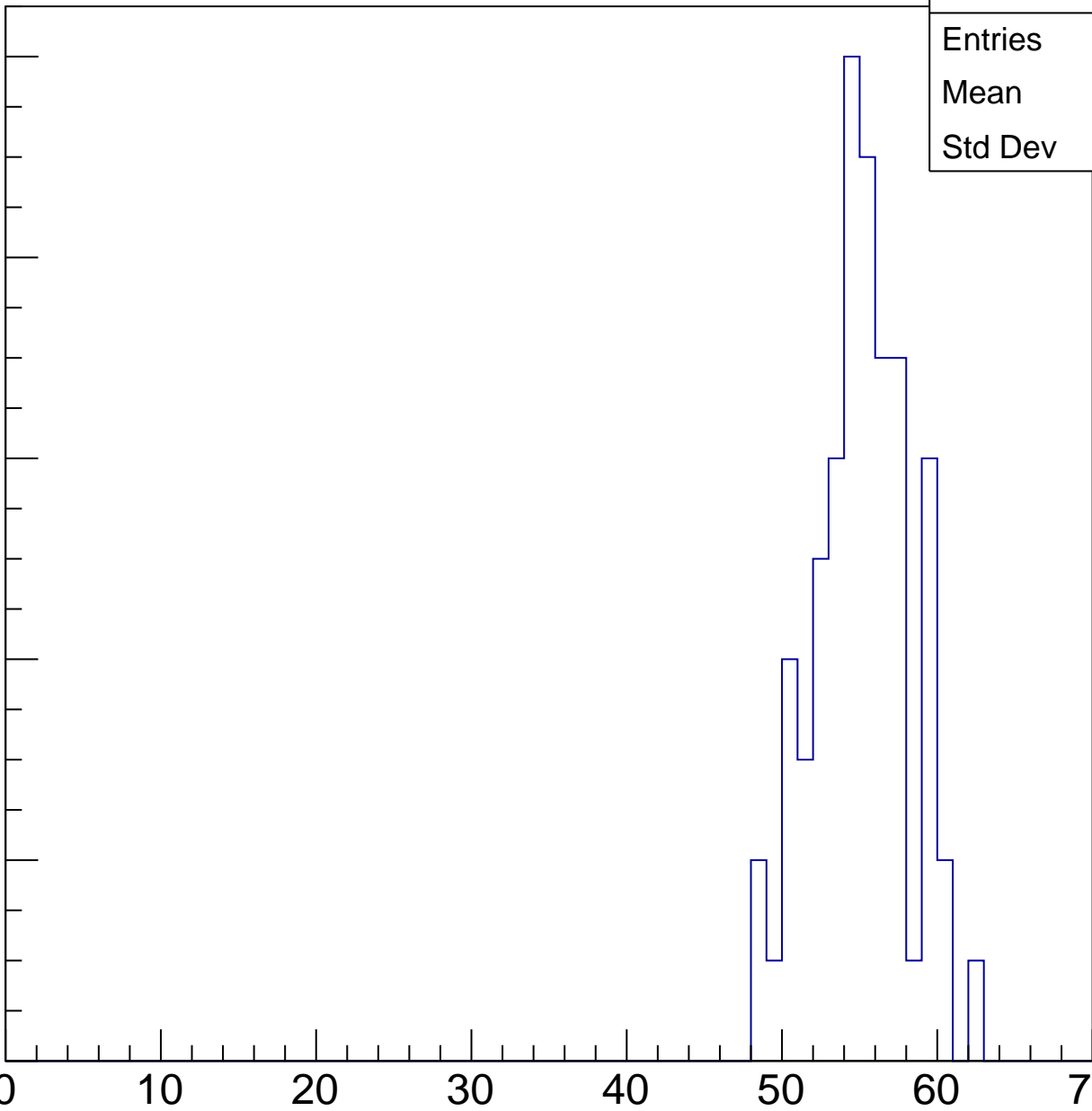
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	54.62
Std Dev	3.039

Entry

10  
8  
6  
4  
2  
0

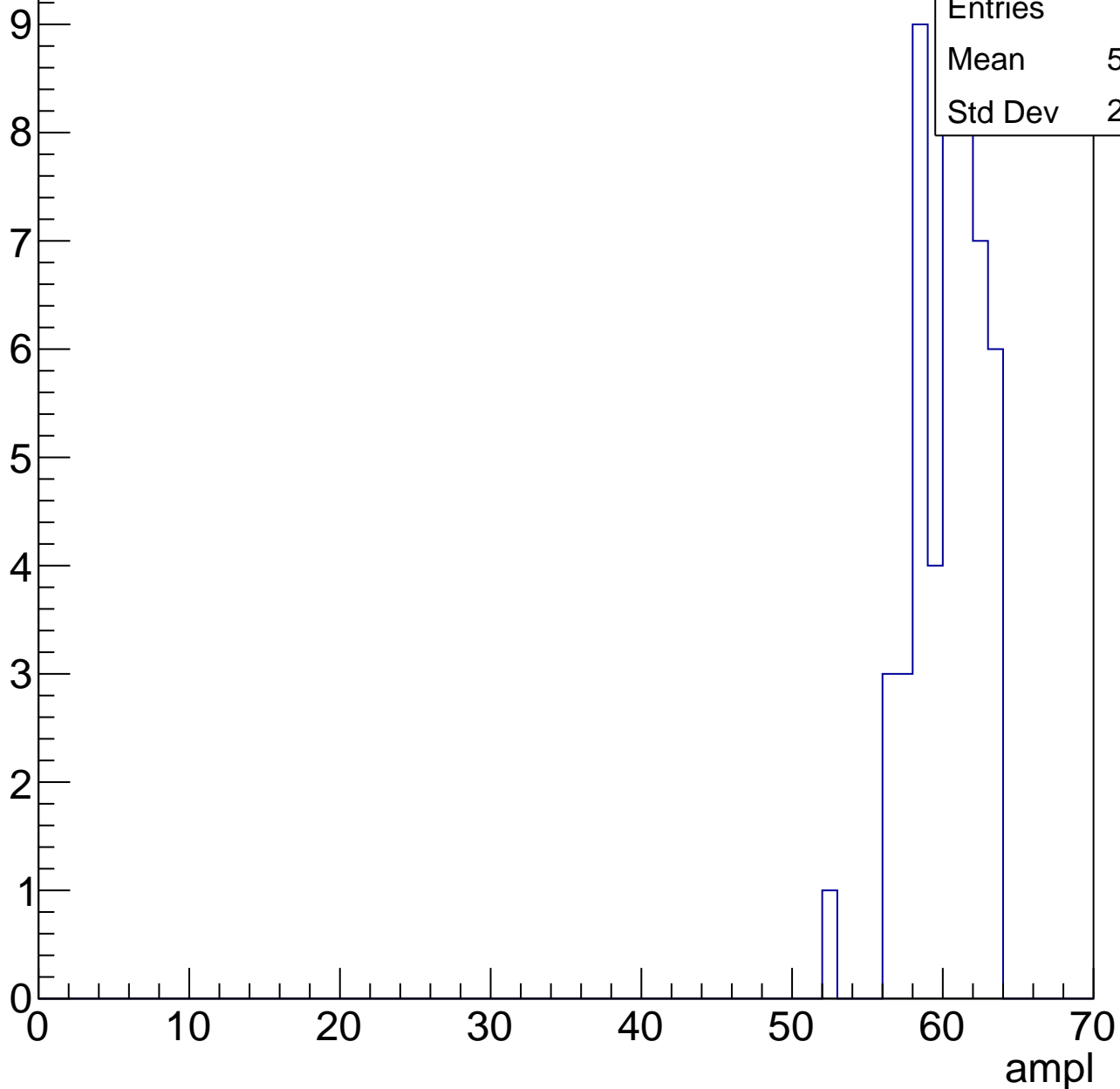
ampl



# B1L002S, U18-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

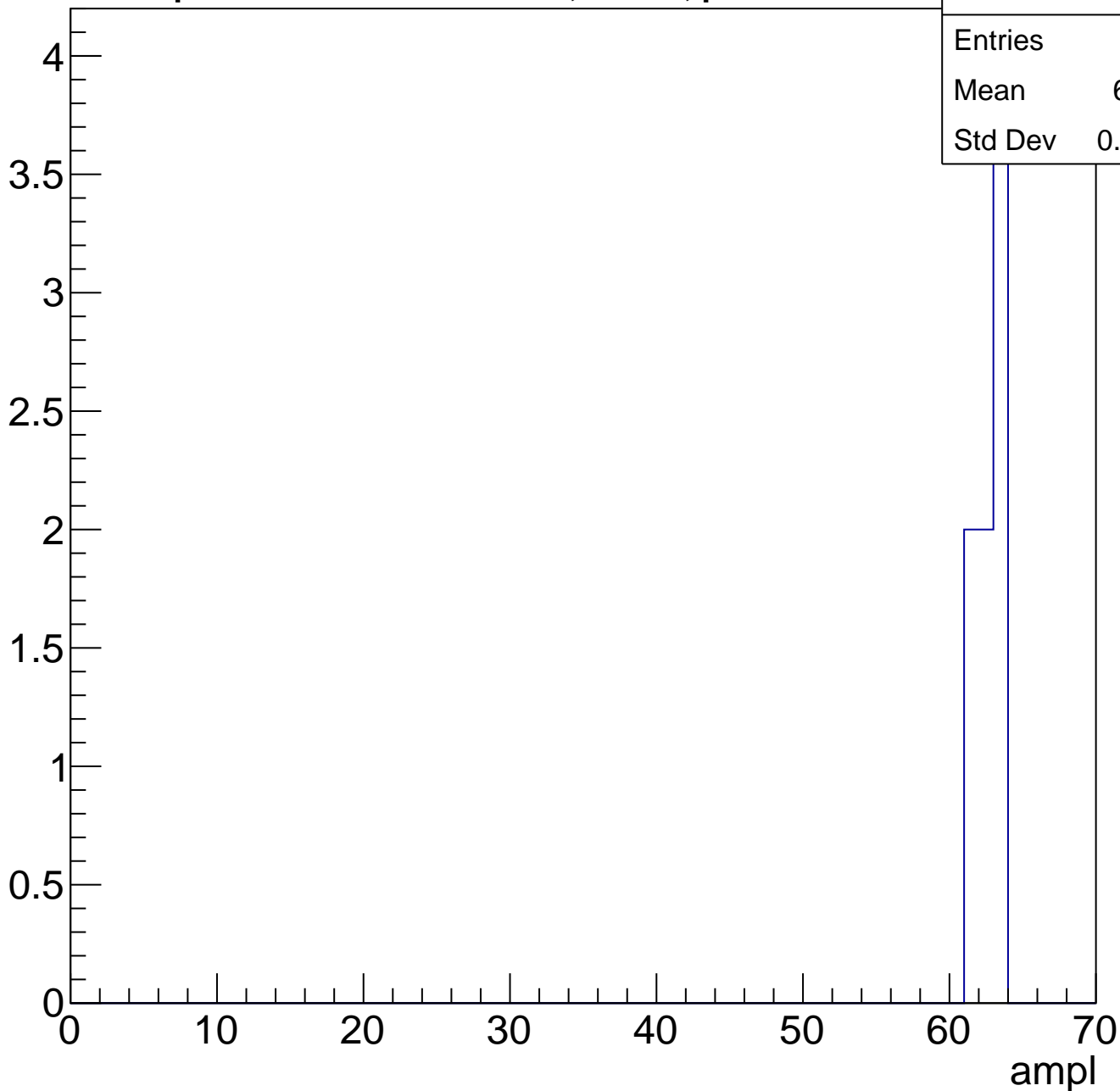
Entry



# B1L002S, U18-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

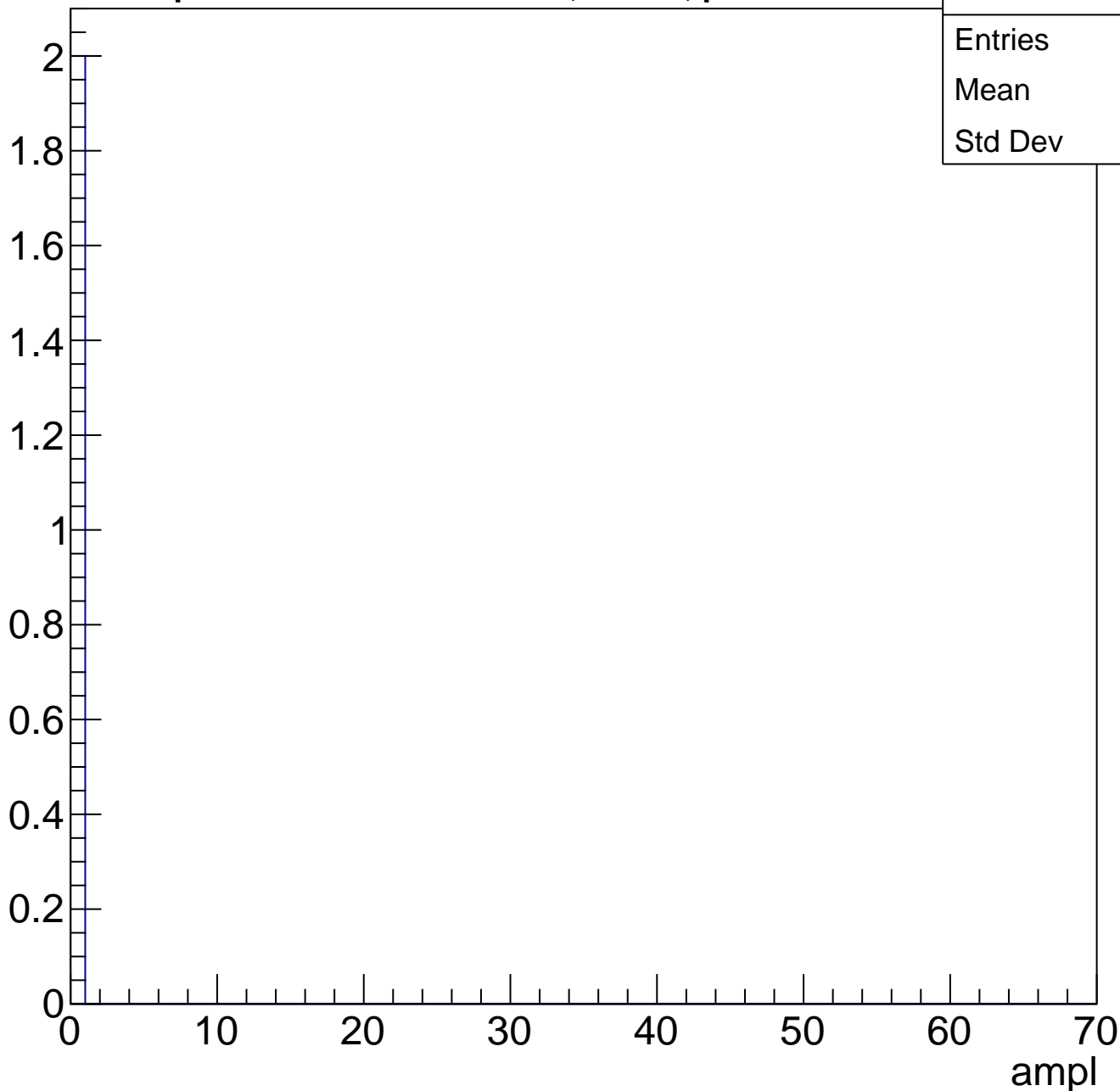




# B1L002S, U18-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U18-ch18, adc0

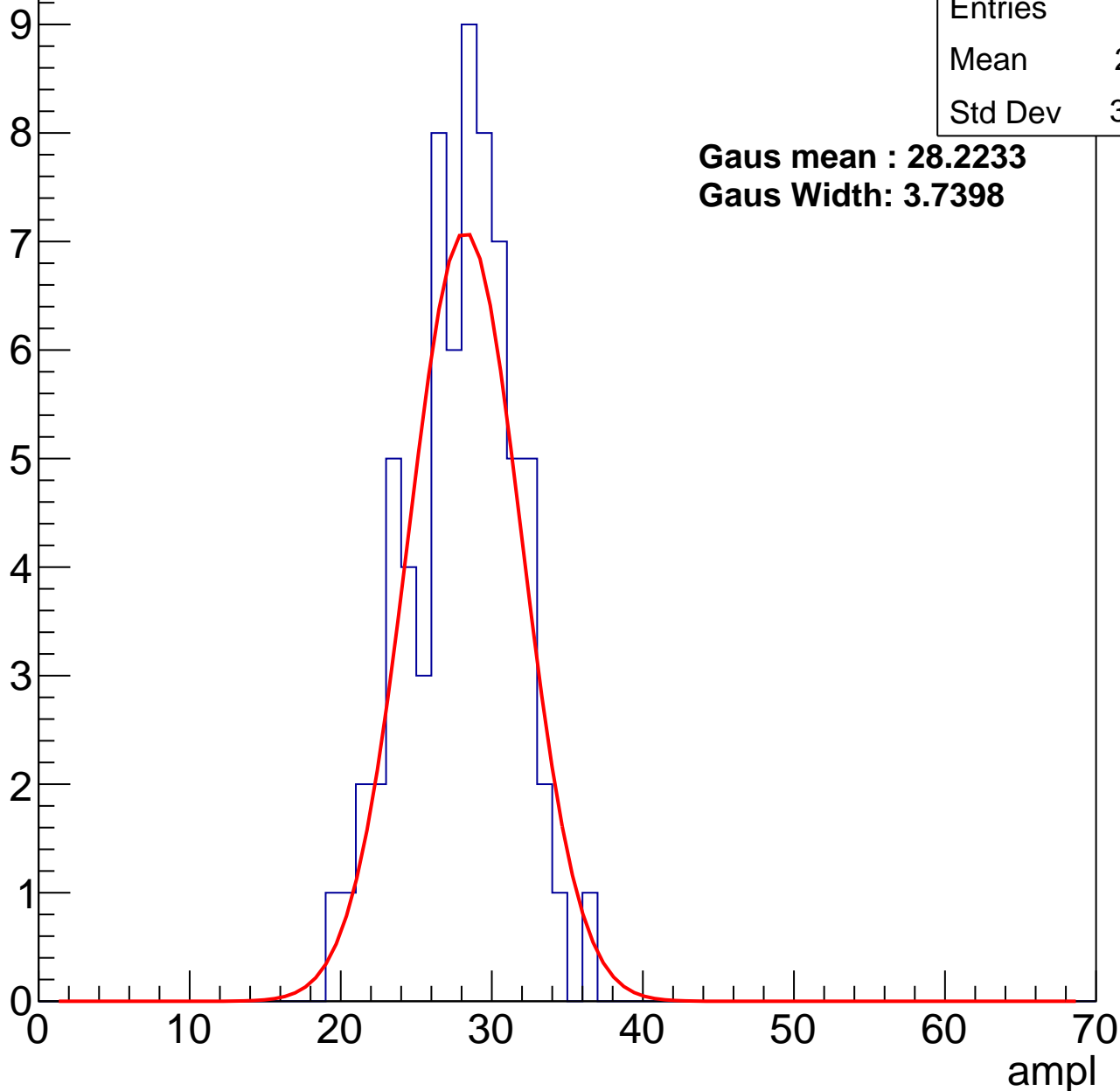
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	27.51
Std Dev	3.512

**Gaus mean : 28.2233**

**Gaus Width: 3.7398**



# B1L002S, U18-ch18, adc1

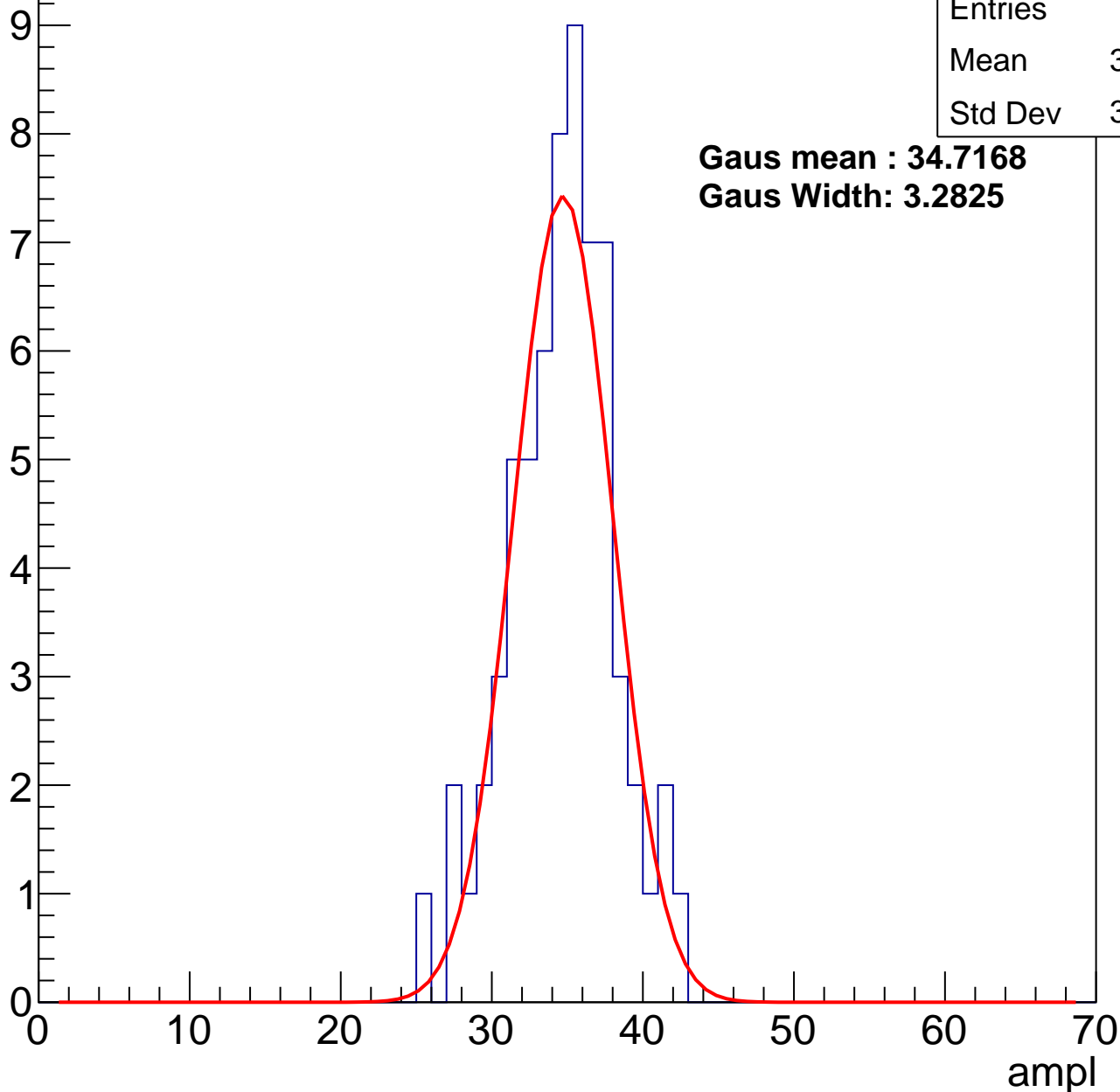
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.18
Std Dev	3.459

**Gaus mean : 34.7168**

**Gaus Width: 3.2825**



# B1L002S, U18-ch18, adc2

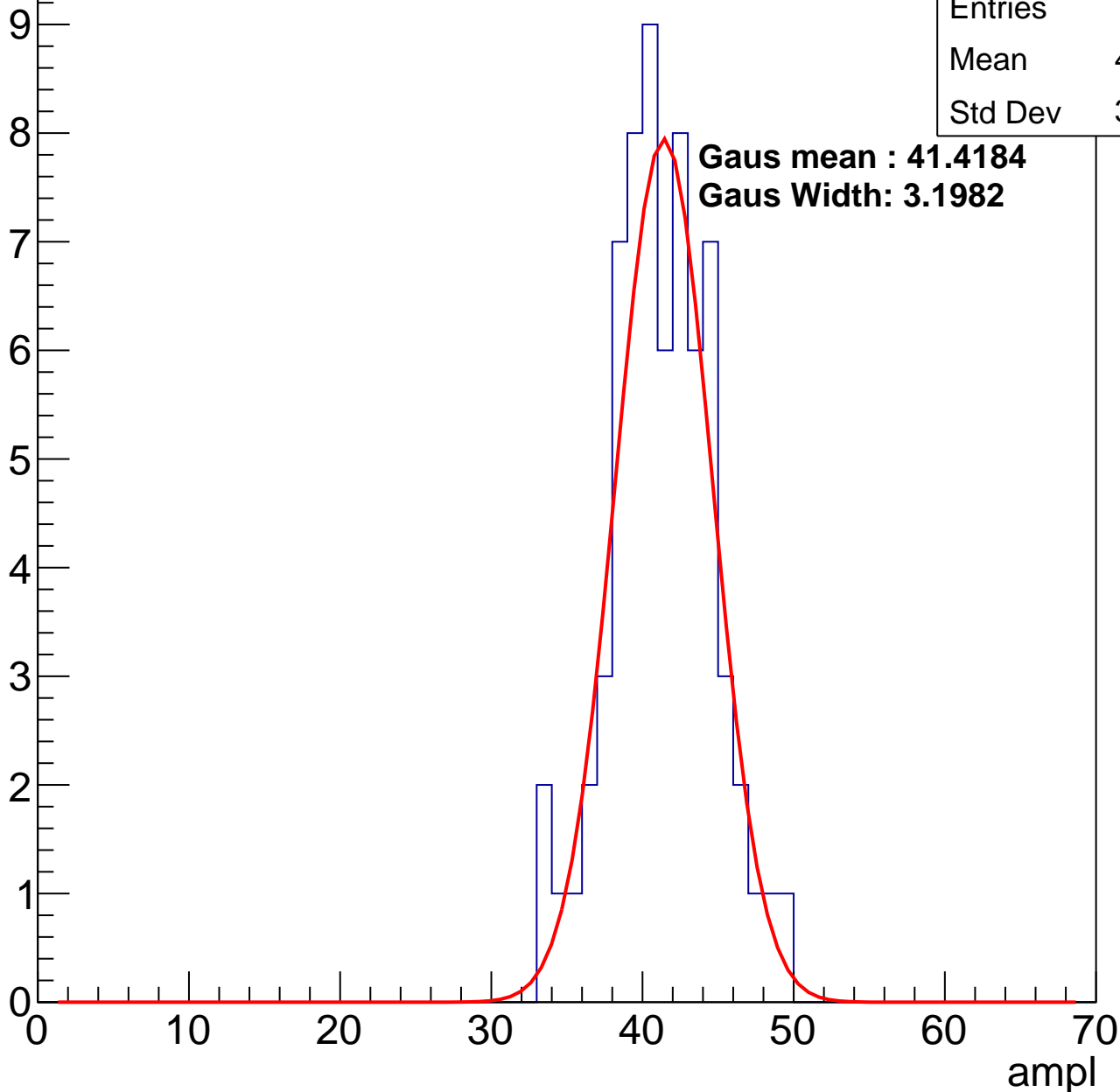
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.81
Std Dev	3.331

**Gaus mean : 41.4184**

**Gaus Width: 3.1982**

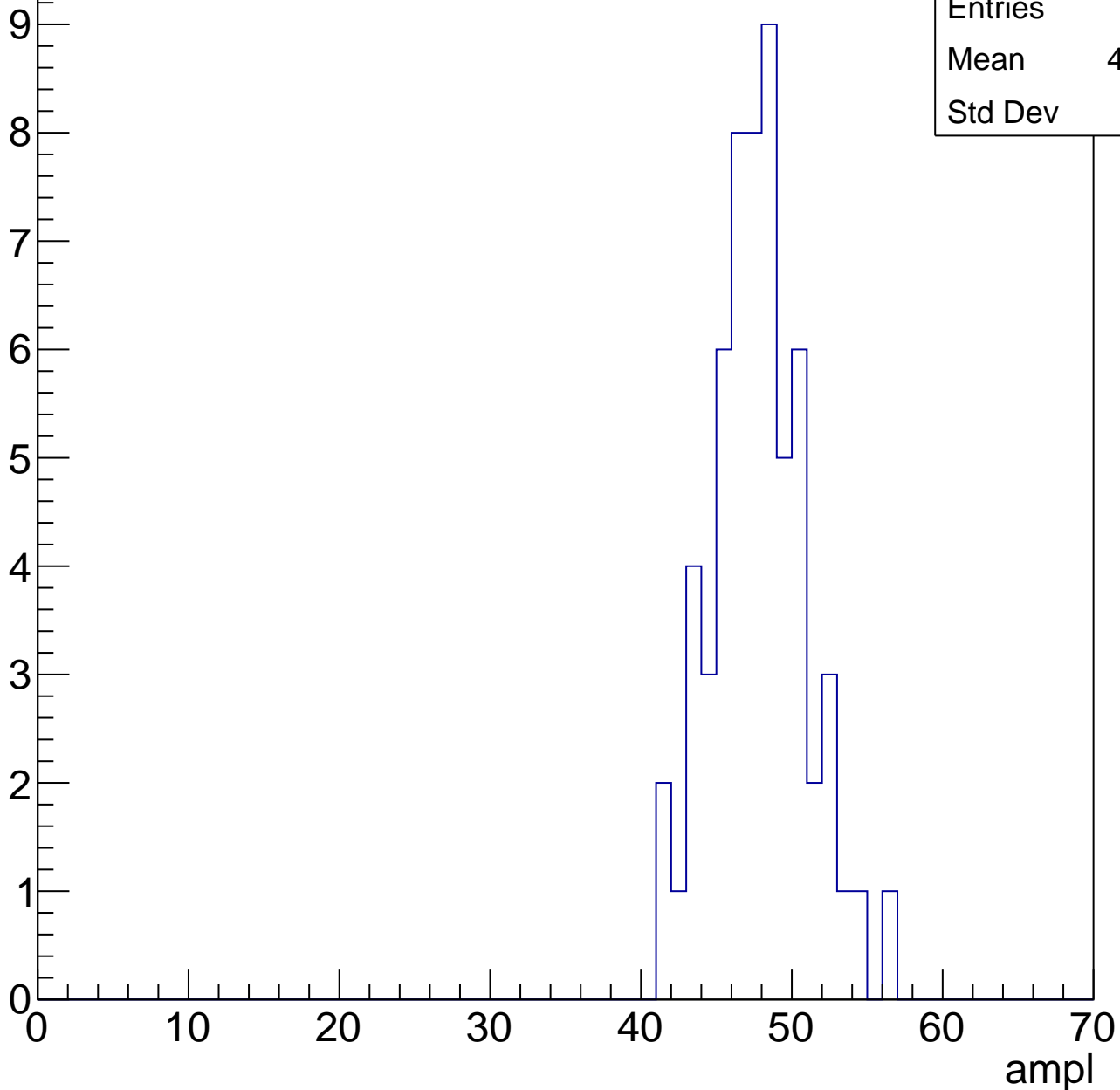


# B1L002S, U18-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	47.33
Std Dev	3.08

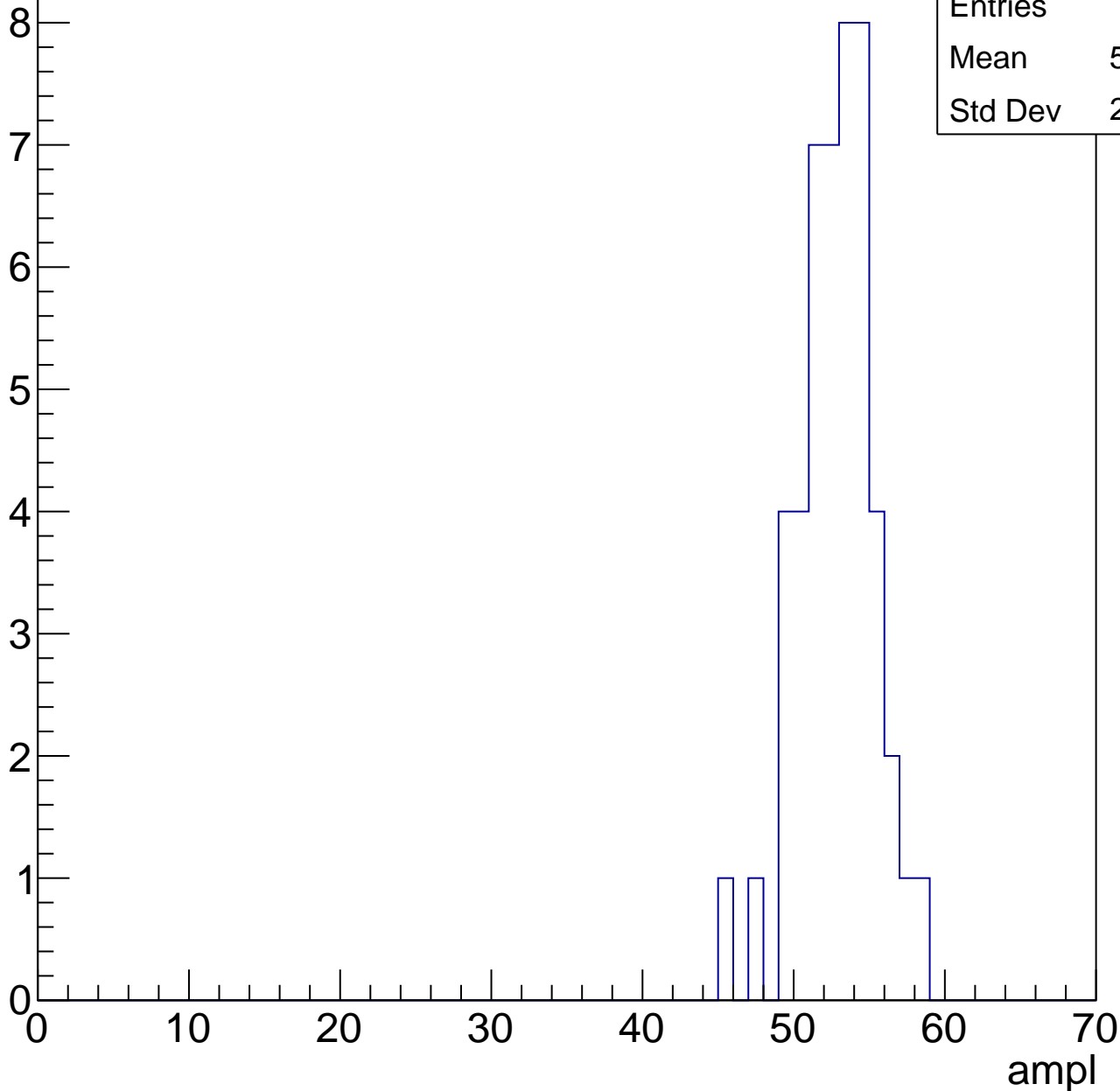


# B1L002S, U18-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	52.33
Std Dev	2.486

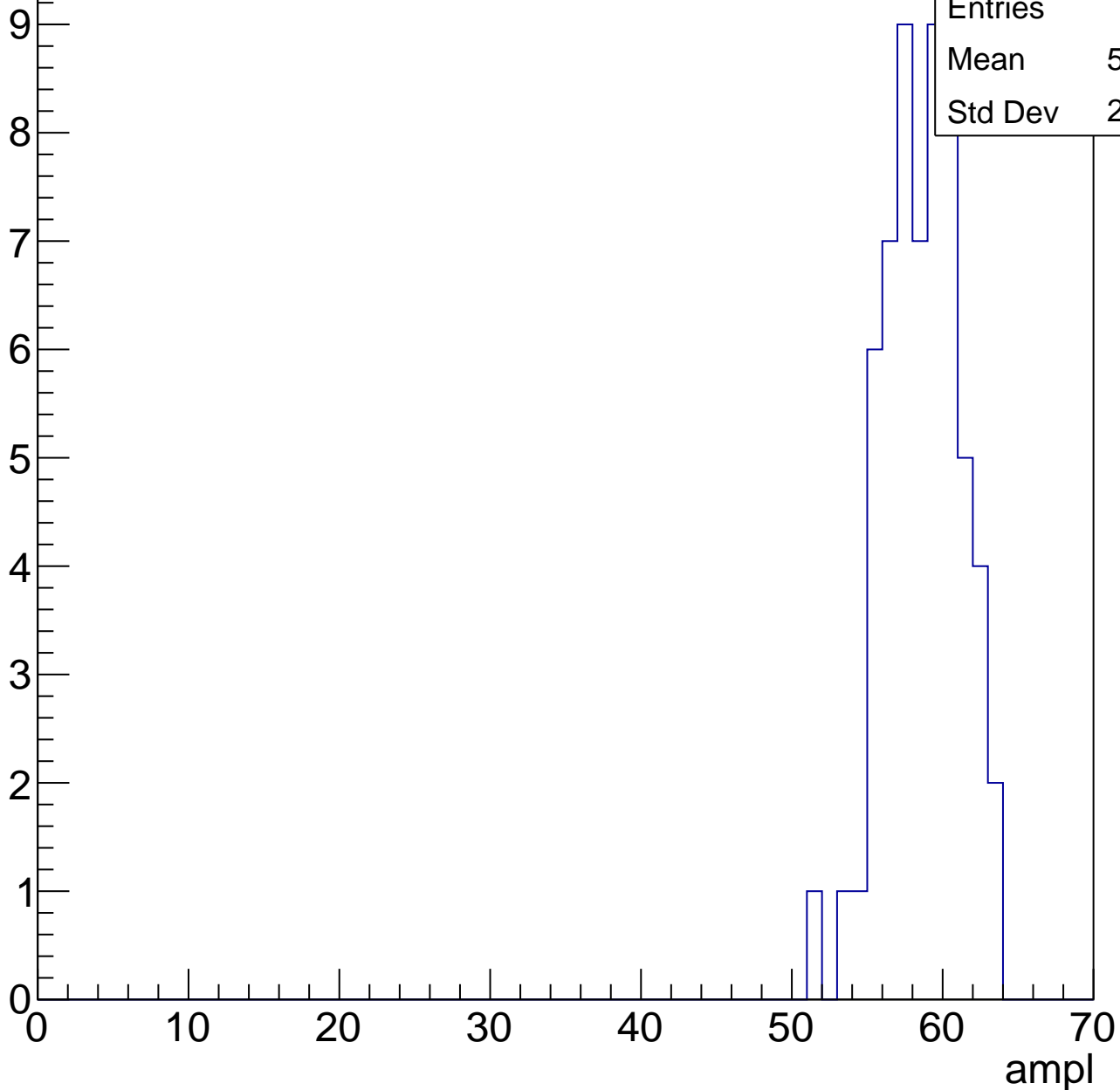


# B1L002S, U18-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.15
Std Dev	2.515

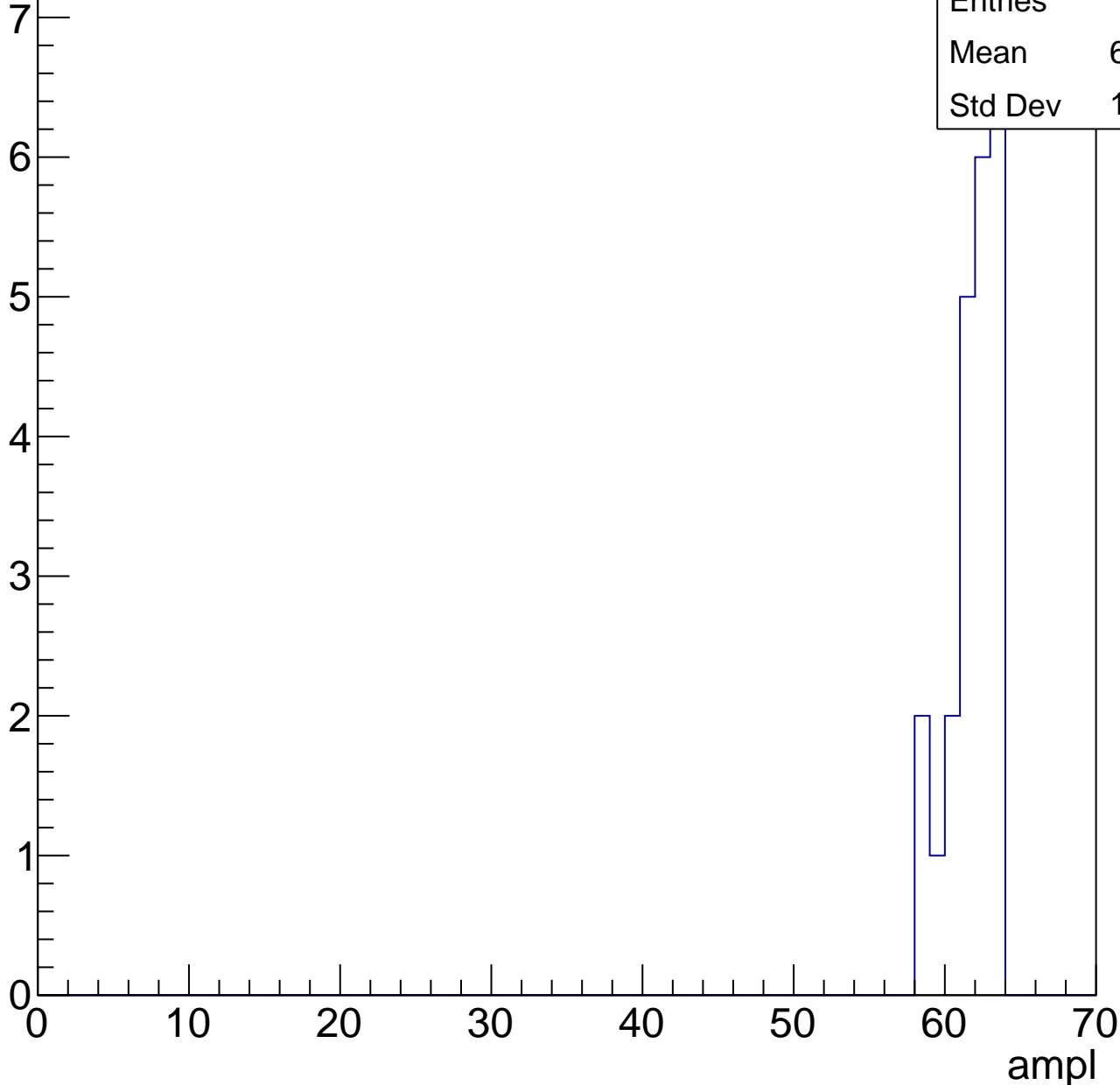


# B1L002S, U18-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.43
Std Dev	1.527

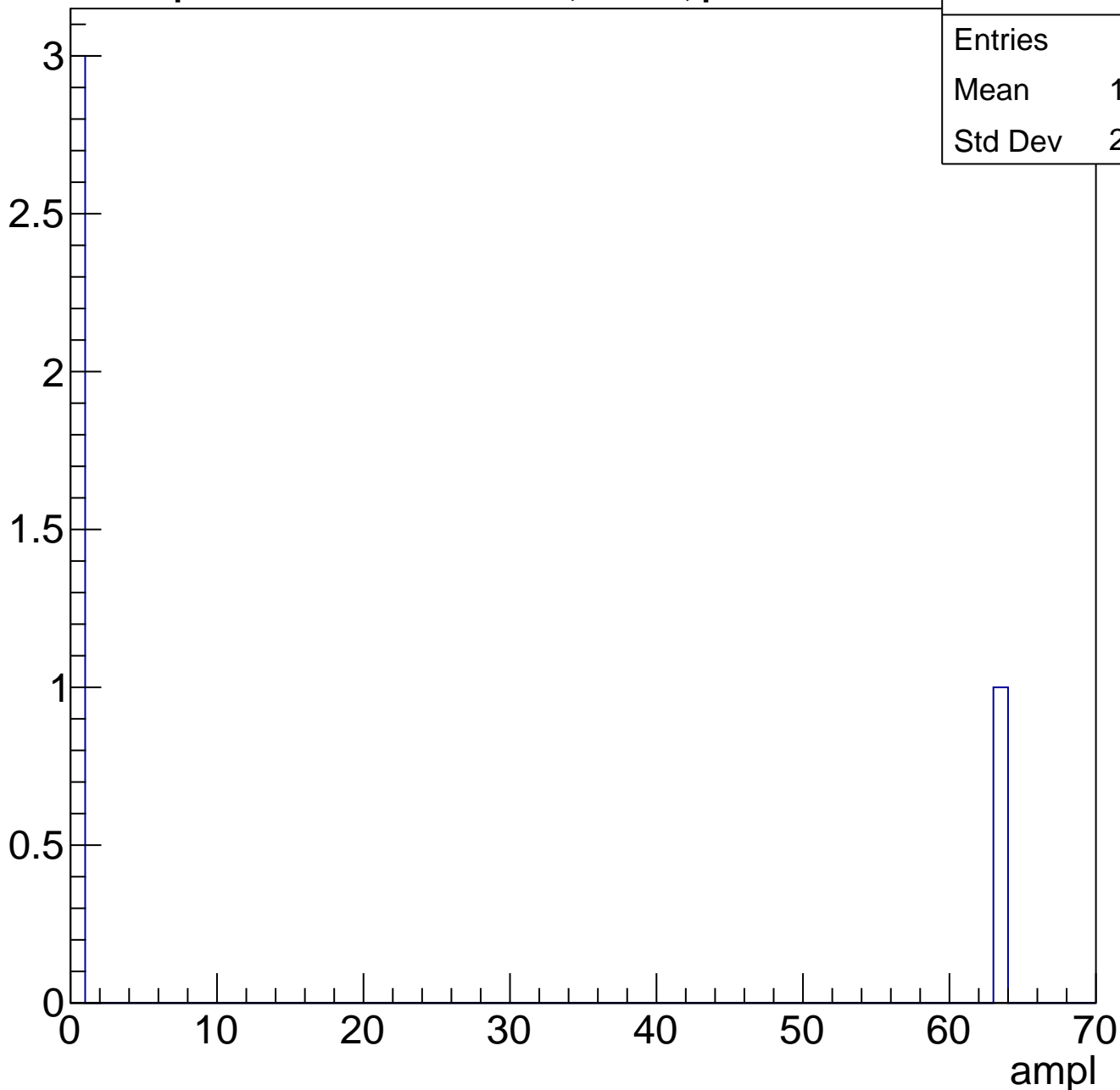




# B1L002S, U18-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	15.75
Std Dev	27.28

# B1L002S, U18-ch19, adc0

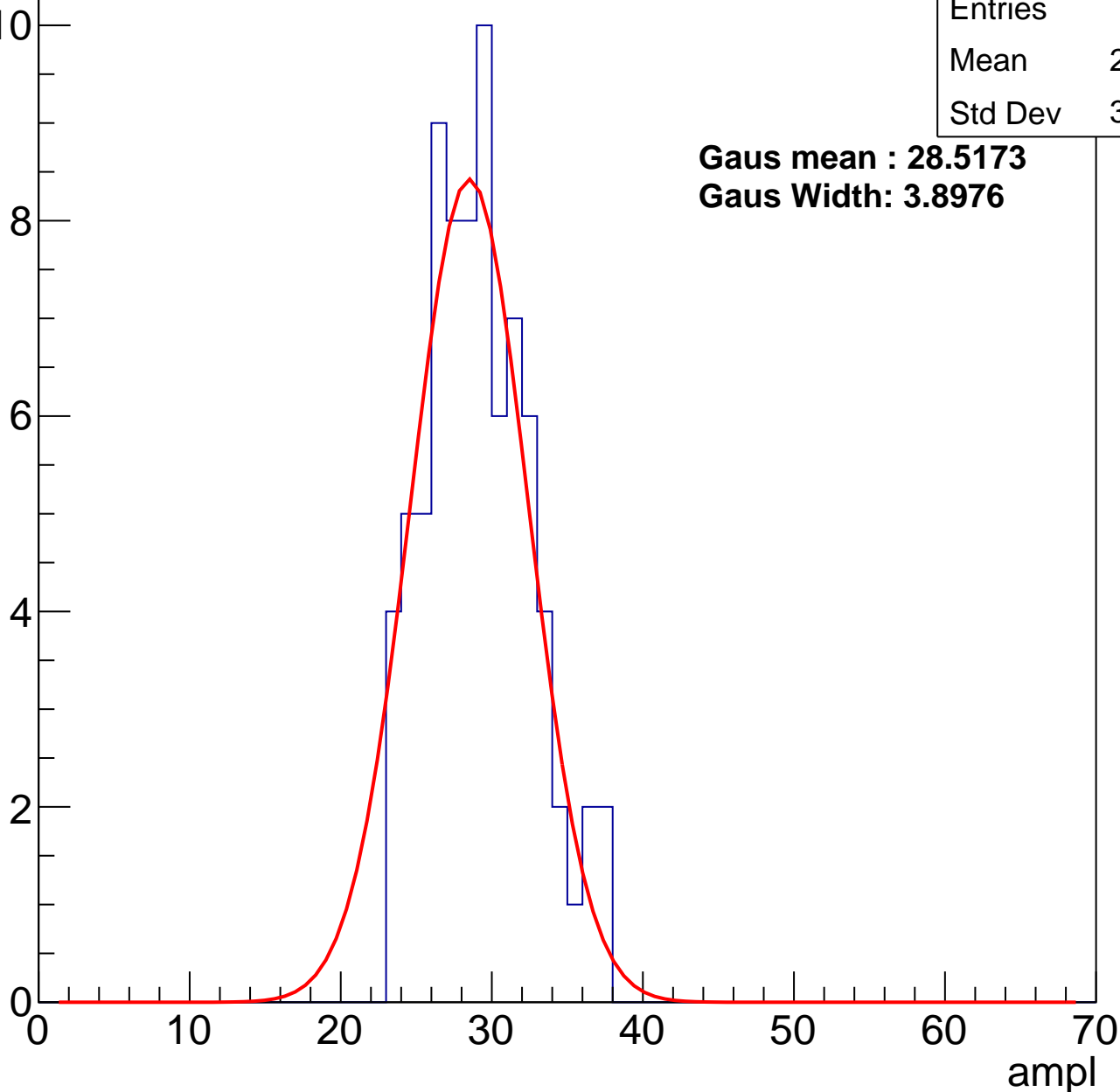
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	28.75
Std Dev	3.429

**Gaus mean : 28.5173**

**Gaus Width: 3.8976**



# B1L002S, U18-ch19, adc1

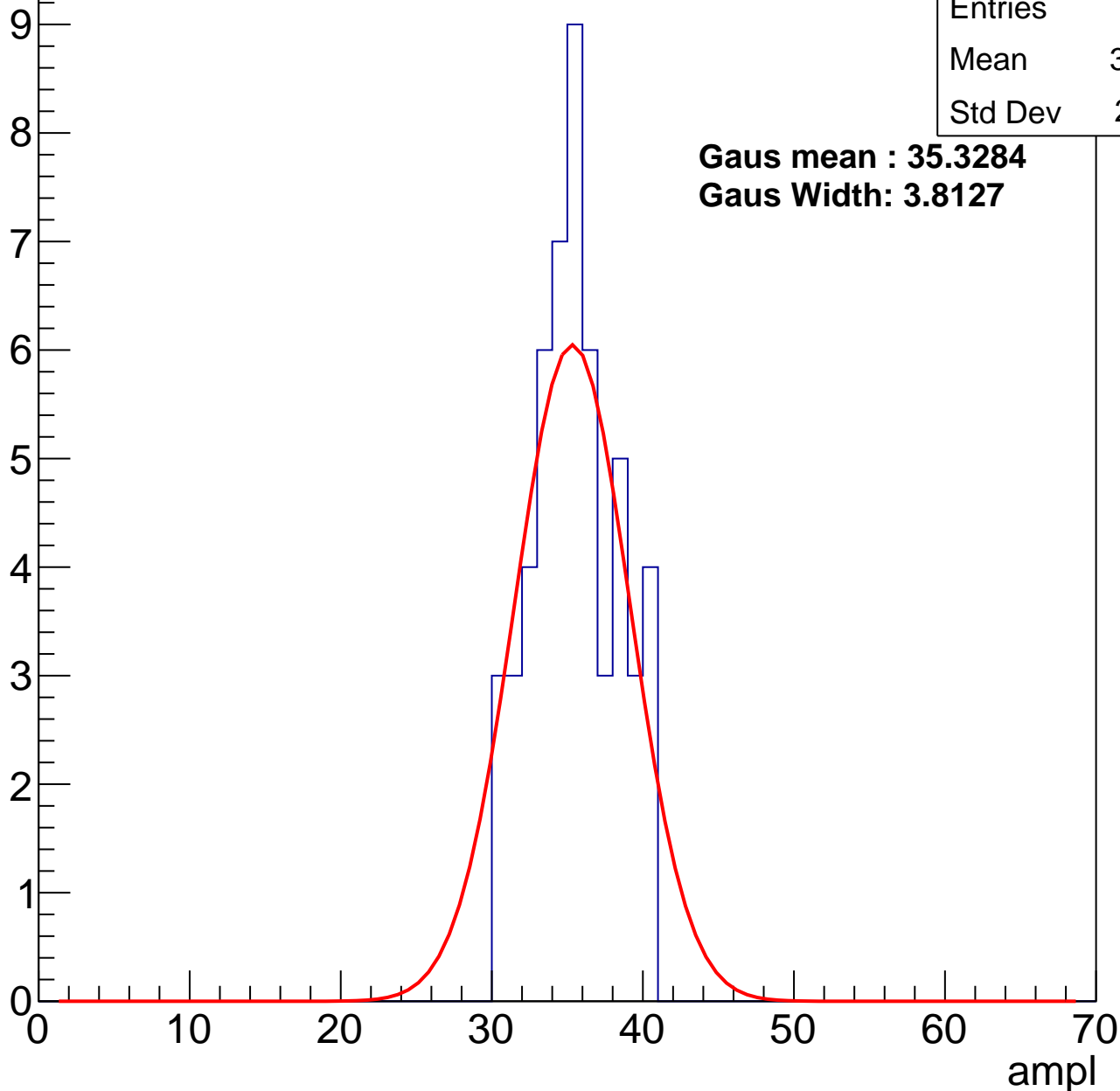
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	35.02
Std Dev	2.751

**Gaus mean : 35.3284**

**Gaus Width: 3.8127**



# B1L002S, U18-ch19, adc2

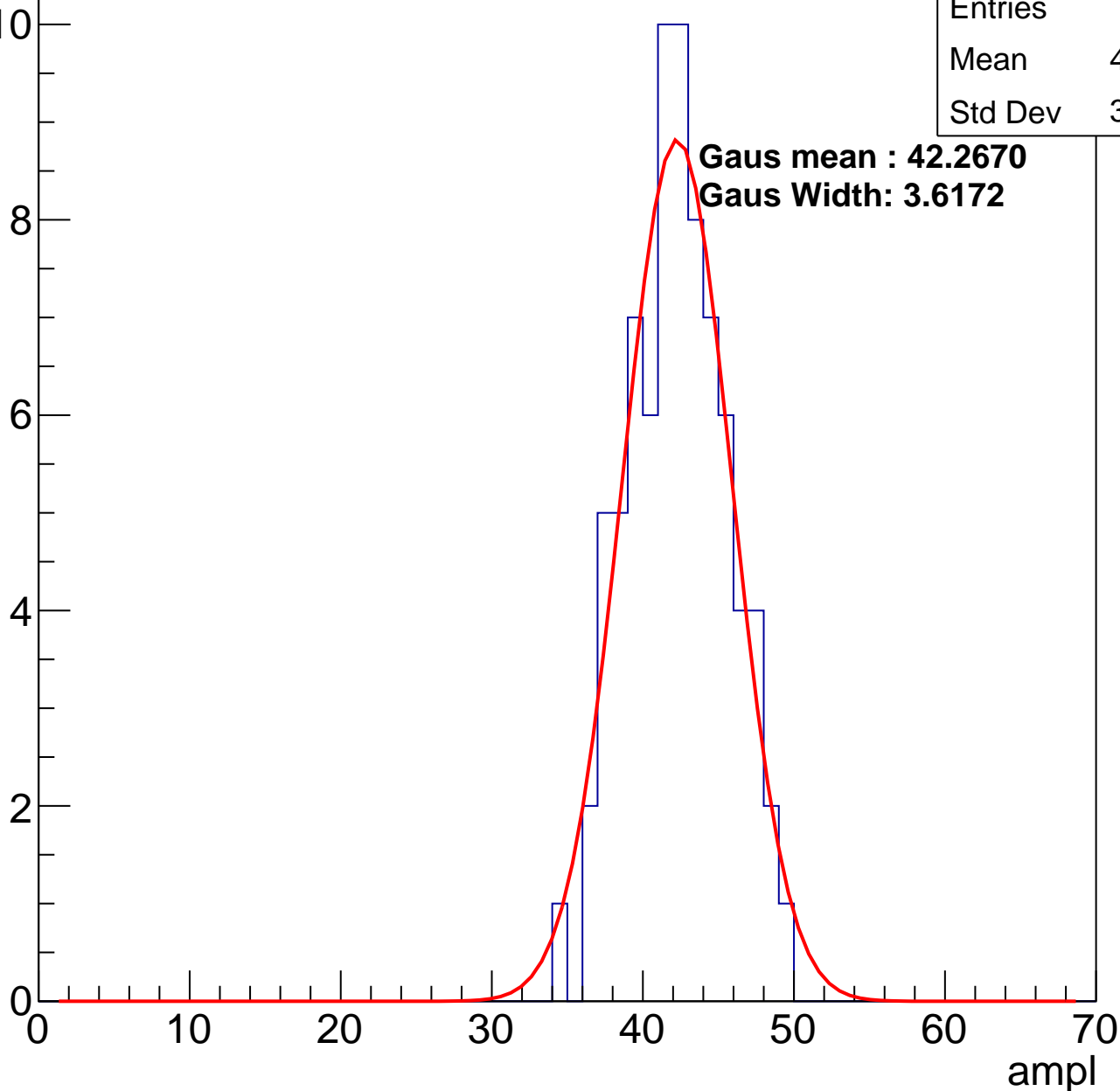
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	41.83
Std Dev	3.224

**Gaus mean : 42.2670**

**Gaus Width: 3.6172**

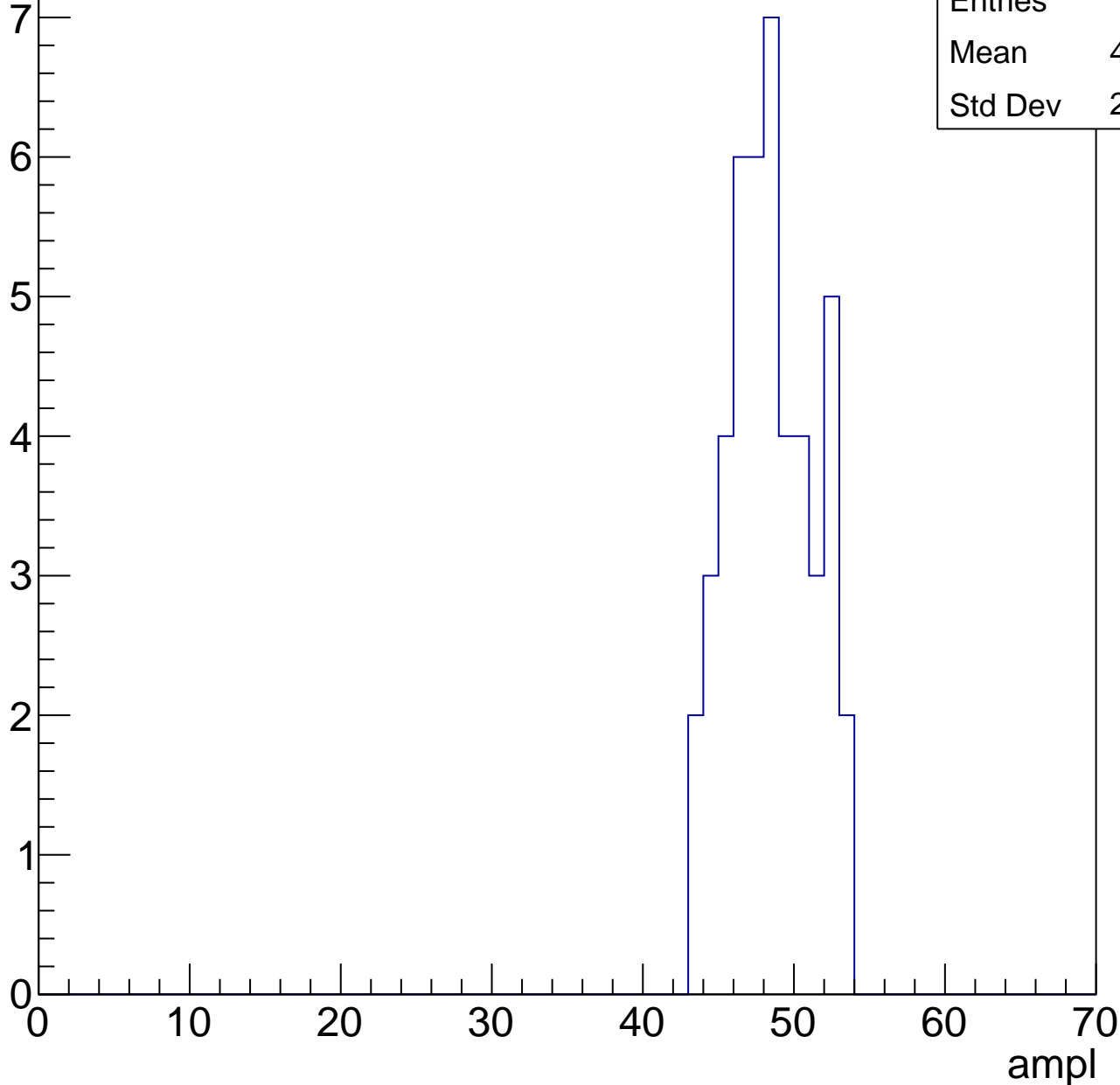


# B1L002S, U18-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	47.98
Std Dev	2.723



# B1L002S, U18-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	53.76
Std Dev	3.057

Entry

10

8

6

4

2

0

0

10

20

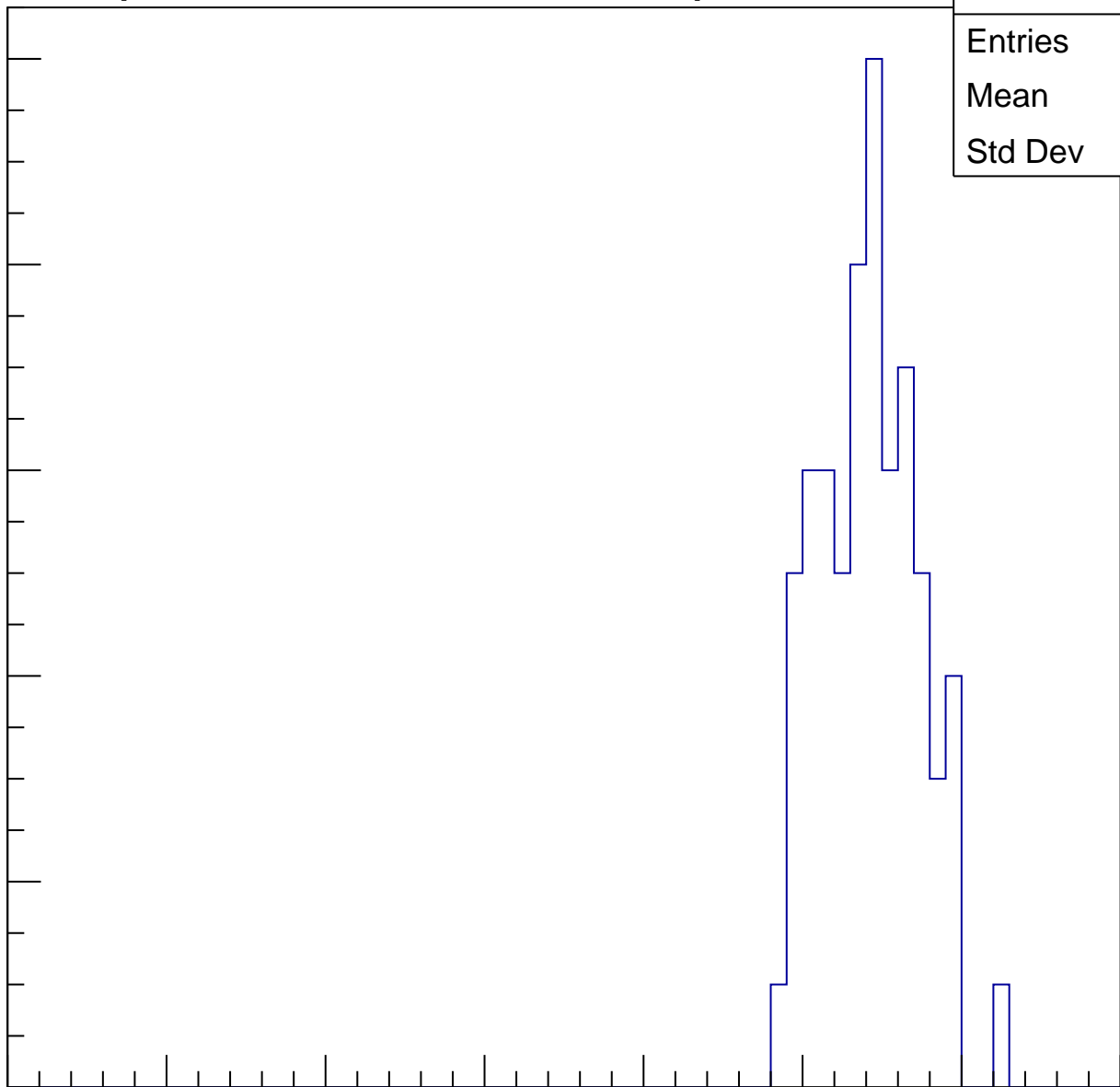
30

40

50

60

ampl

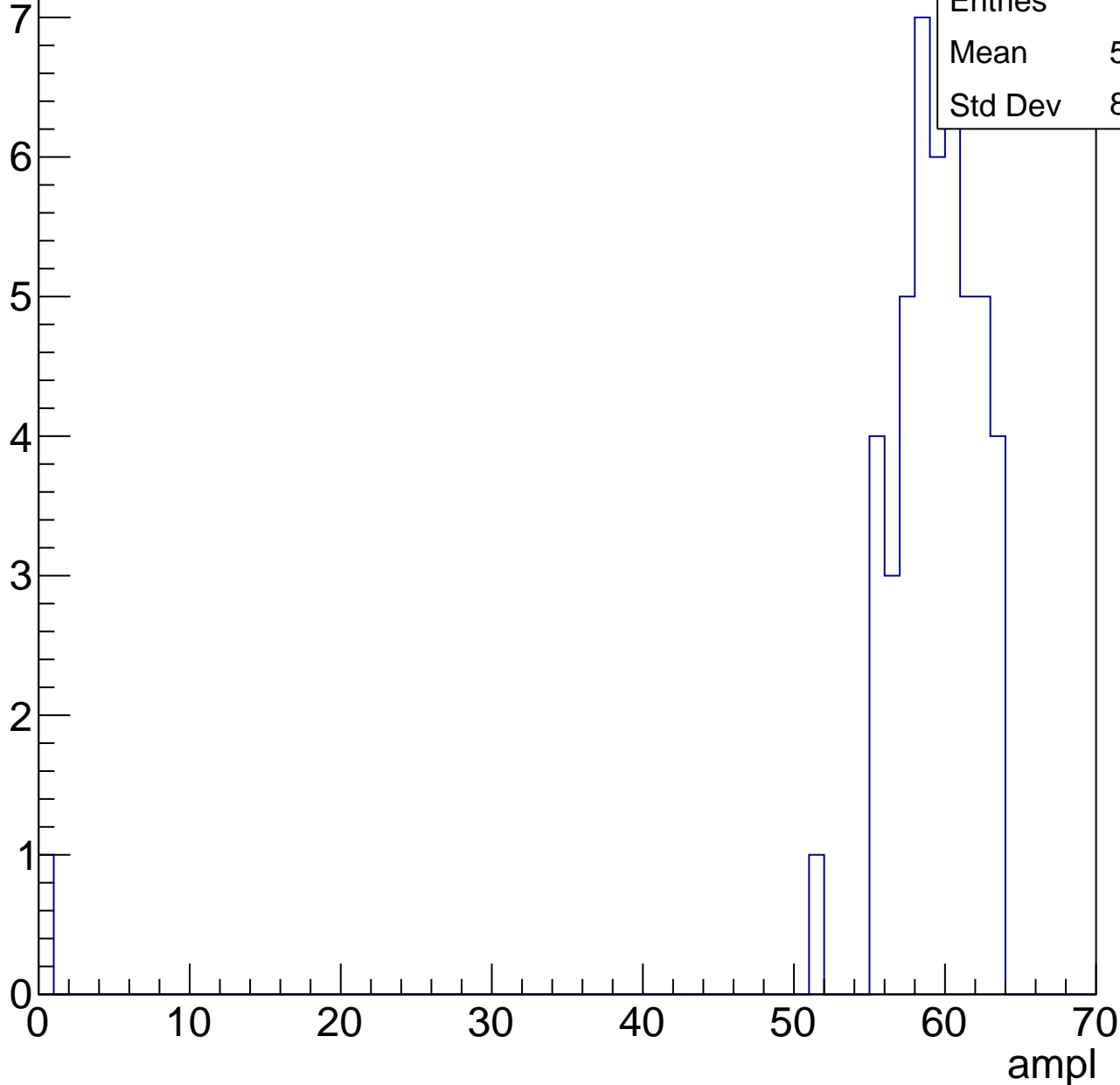


# B1L002S, U18-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	57.73
Std Dev	8.805

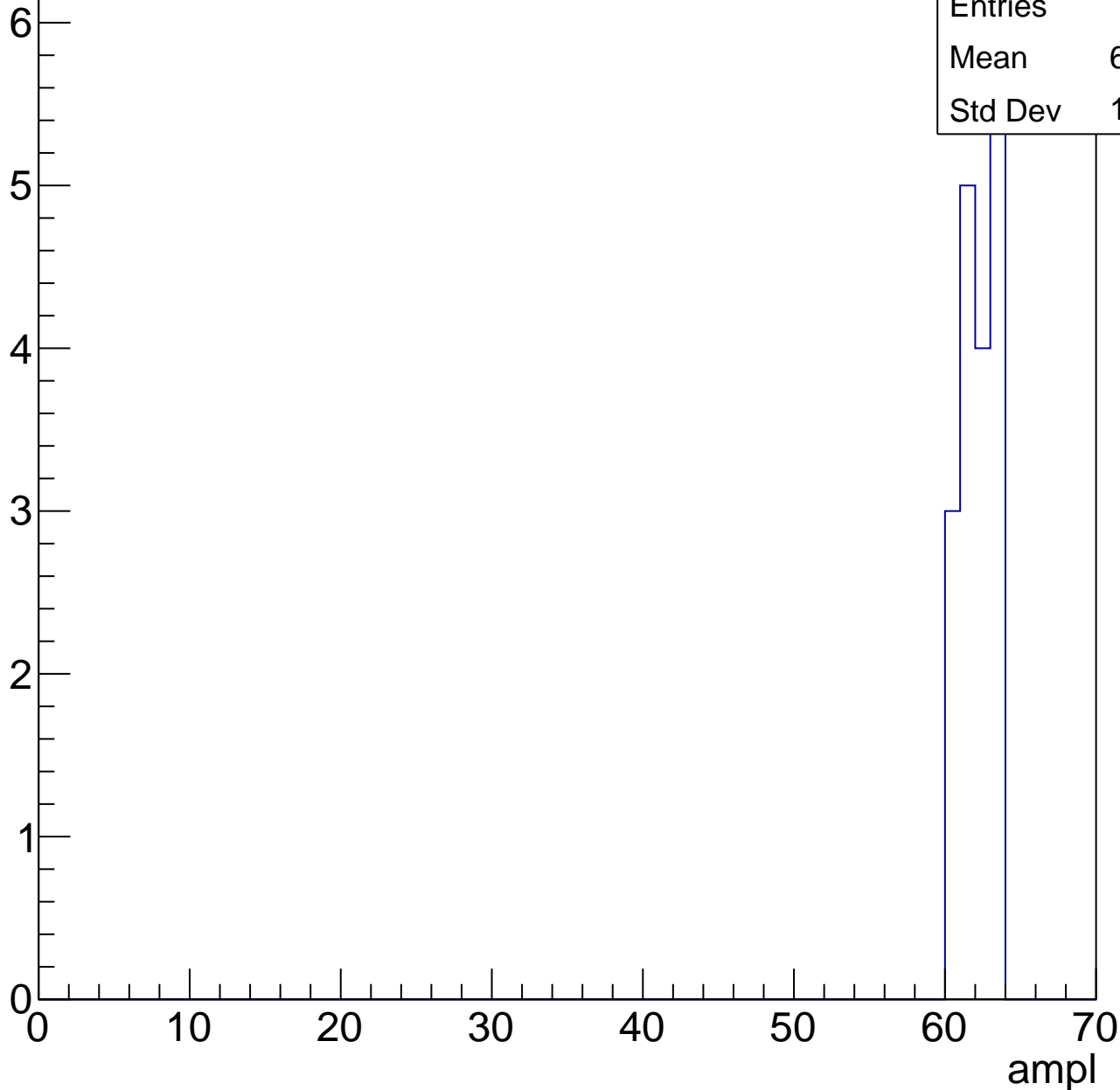


# B1L002S, U18-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.72
Std Dev	1.096





# B1L002S, U18-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch20, adc0

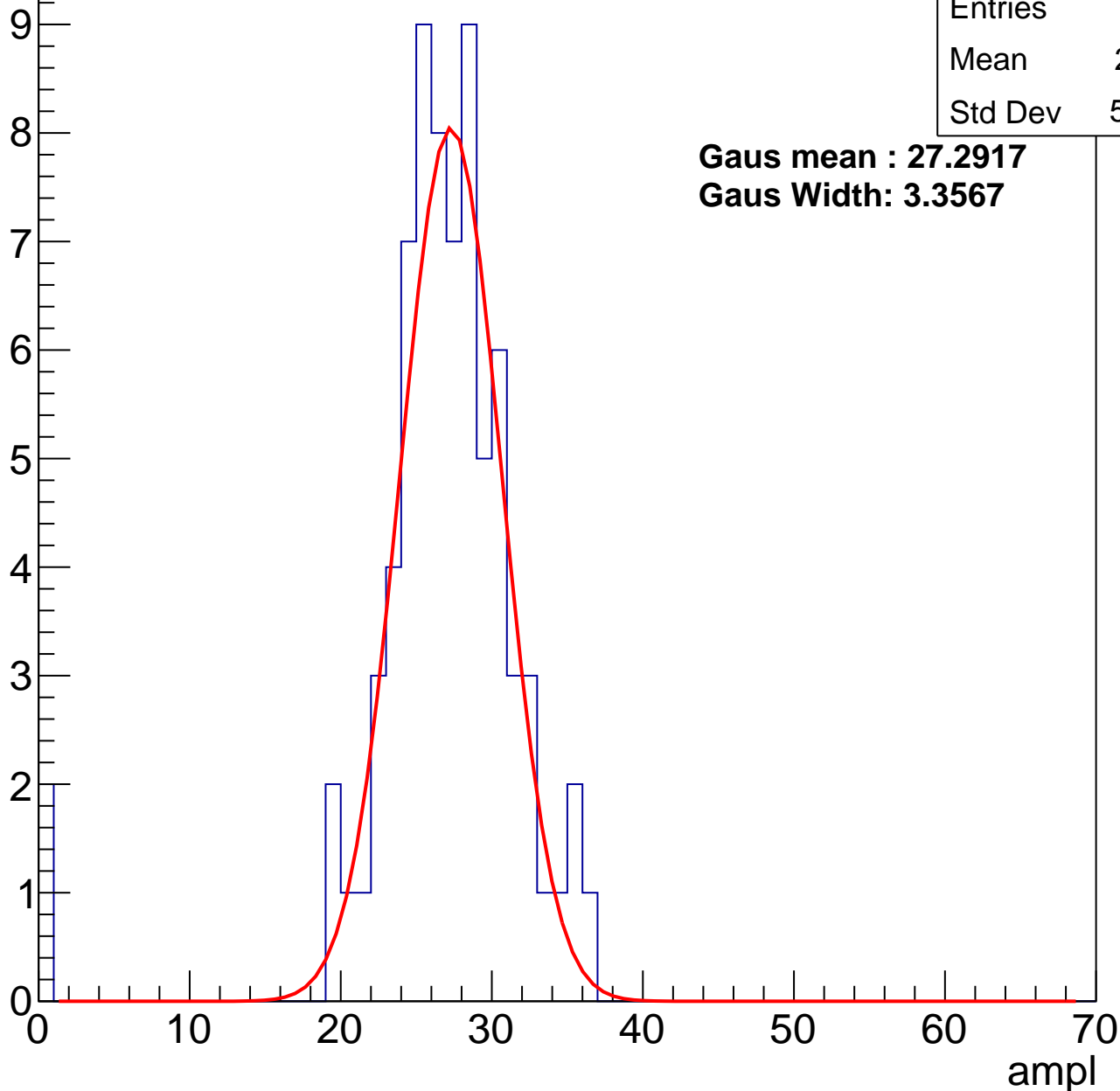
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	26.21
Std Dev	5.622

**Gaus mean : 27.2917**

**Gaus Width: 3.3567**



# B1L002S, U18-ch20, adc1

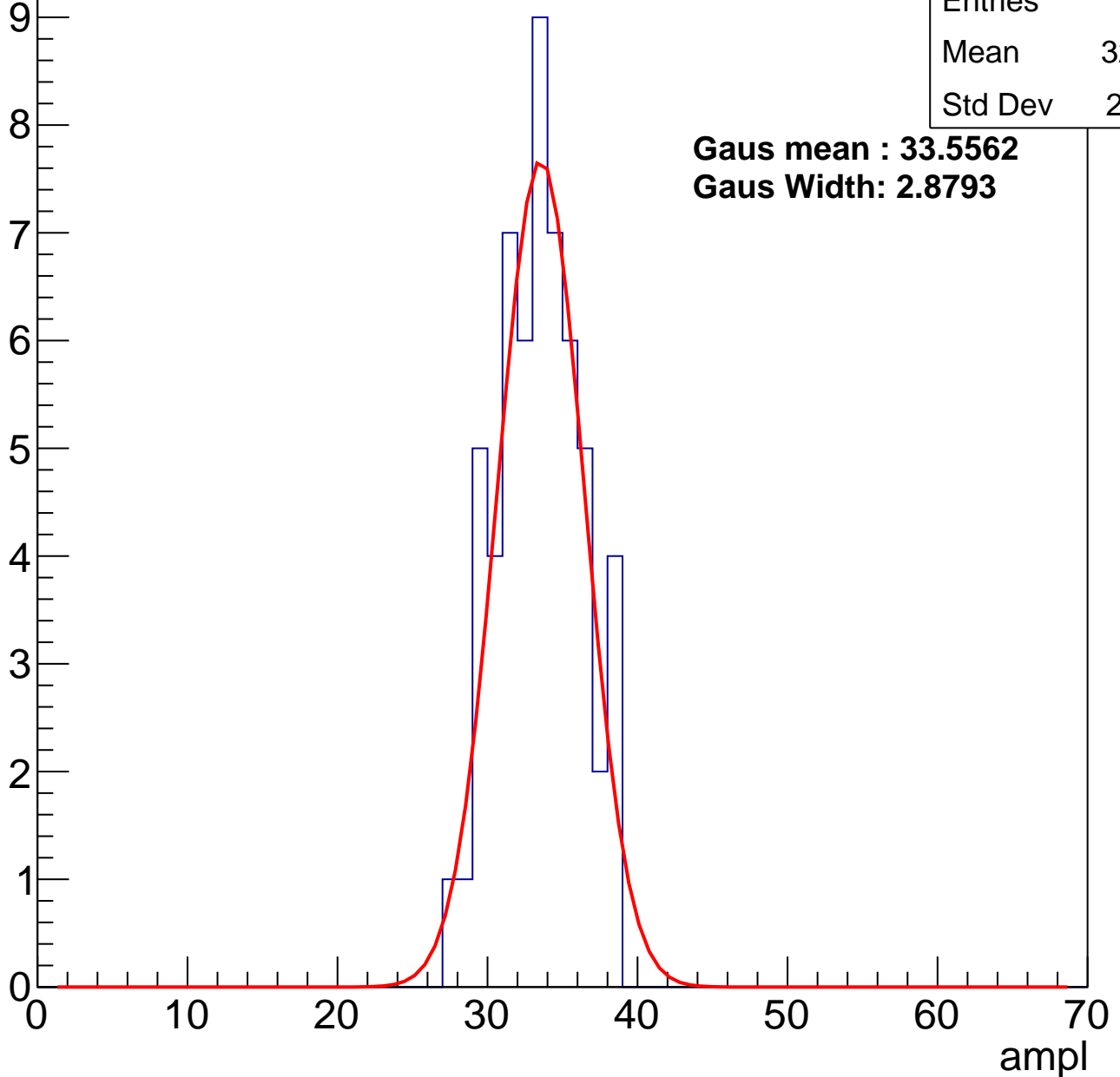
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	32.98
Std Dev	2.711

**Gaus mean : 33.5562**

**Gaus Width: 2.8793**



# B1L002S, U18-ch20, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	39.74
Std Dev	2.925

**Gaus mean : 40.1884**

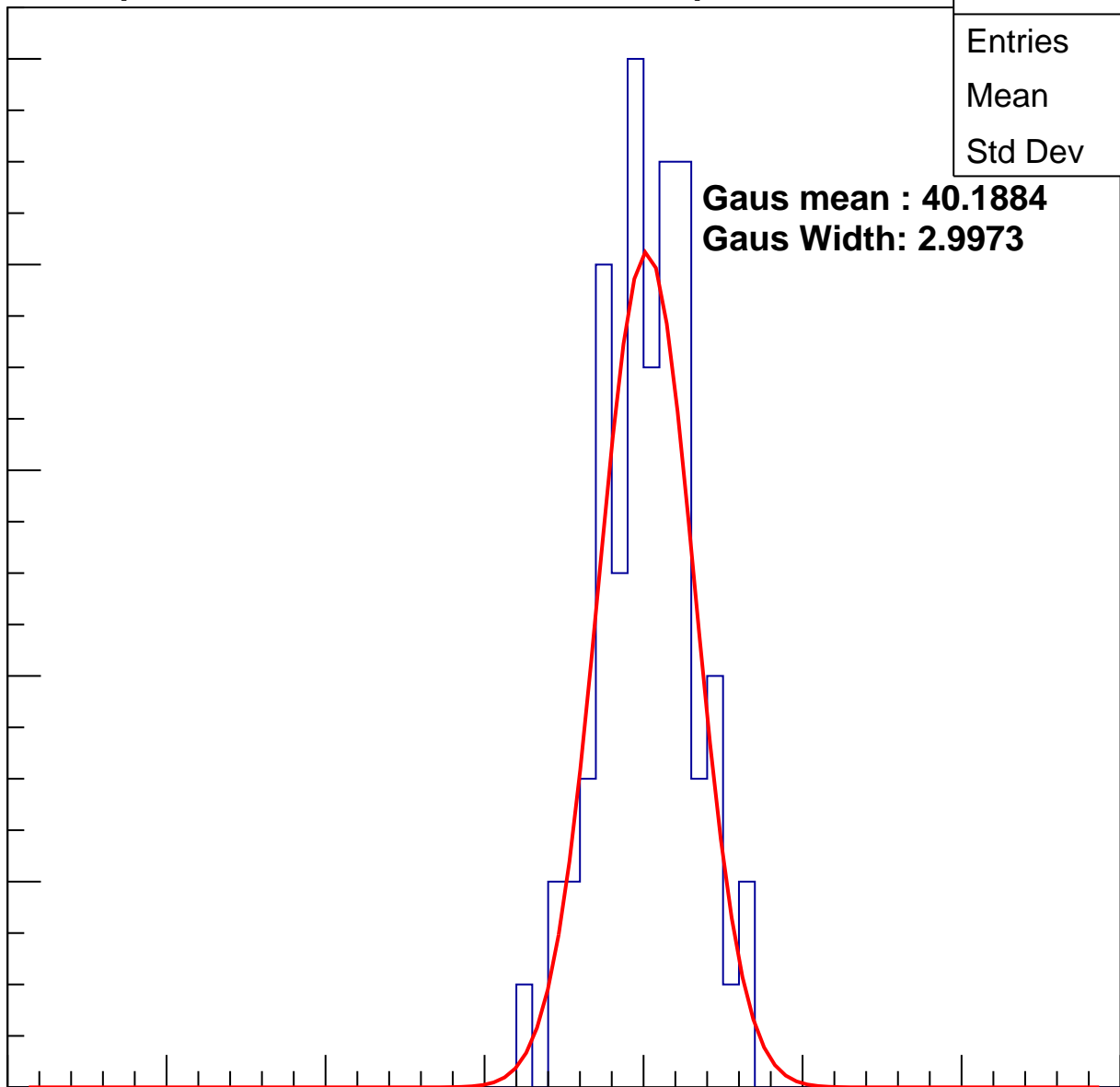
**Gaus Width: 2.9973**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

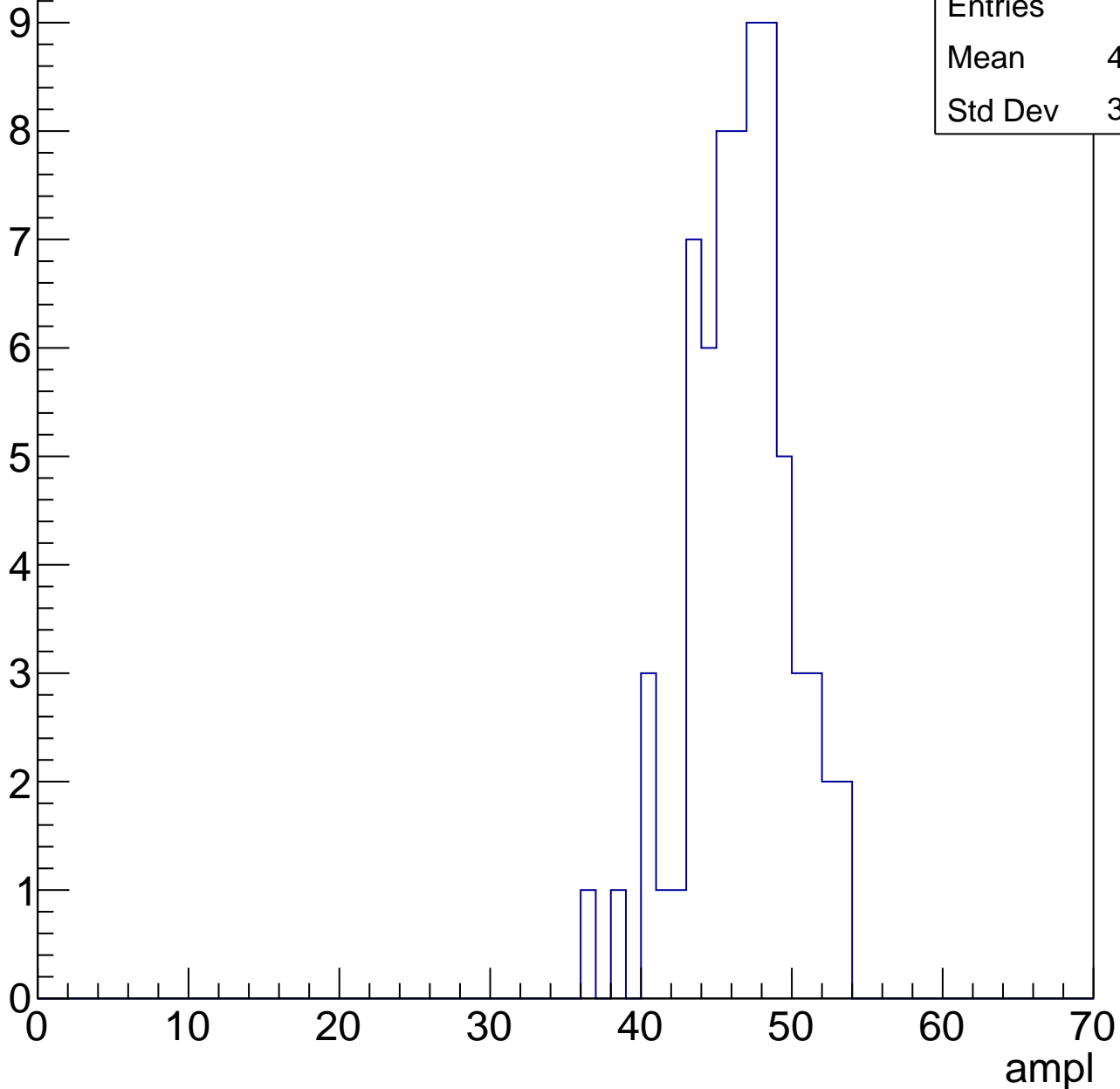


# B1L002S, U18-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.13
Std Dev	3.383

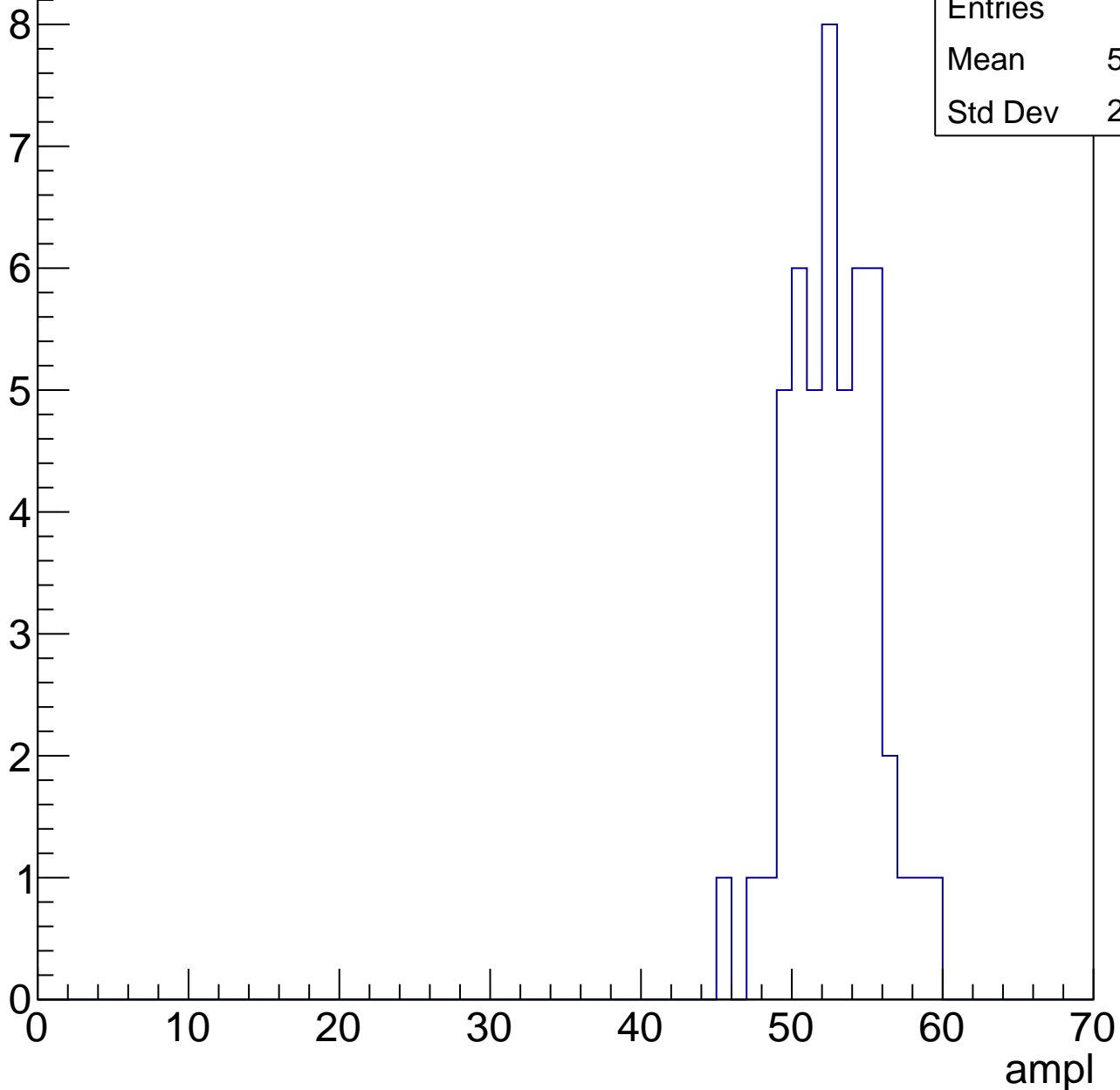


# B1L002S, U18-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

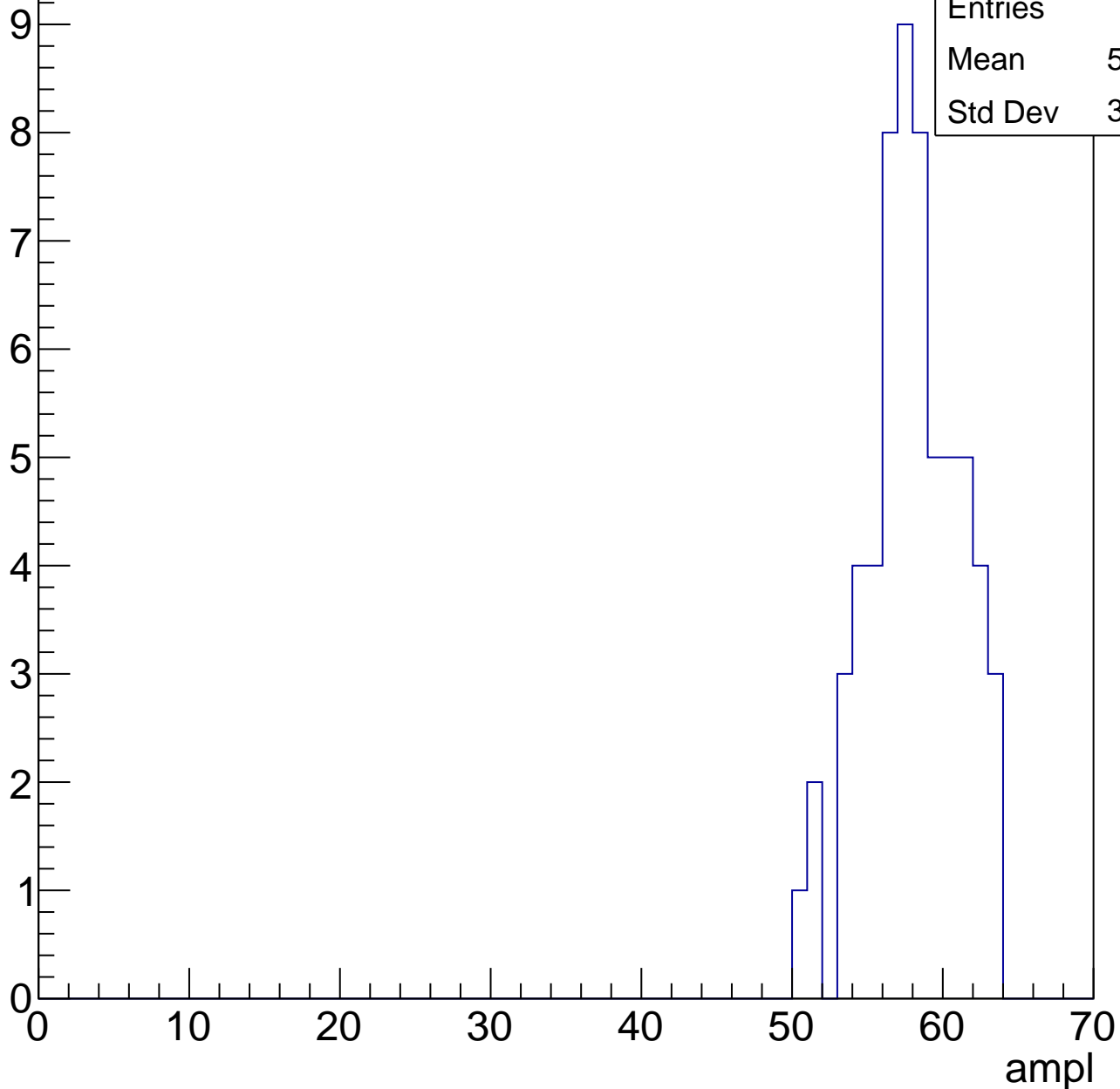
Entries	49
Mean	52.27
Std Dev	2.805



# B1L002S, U18-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



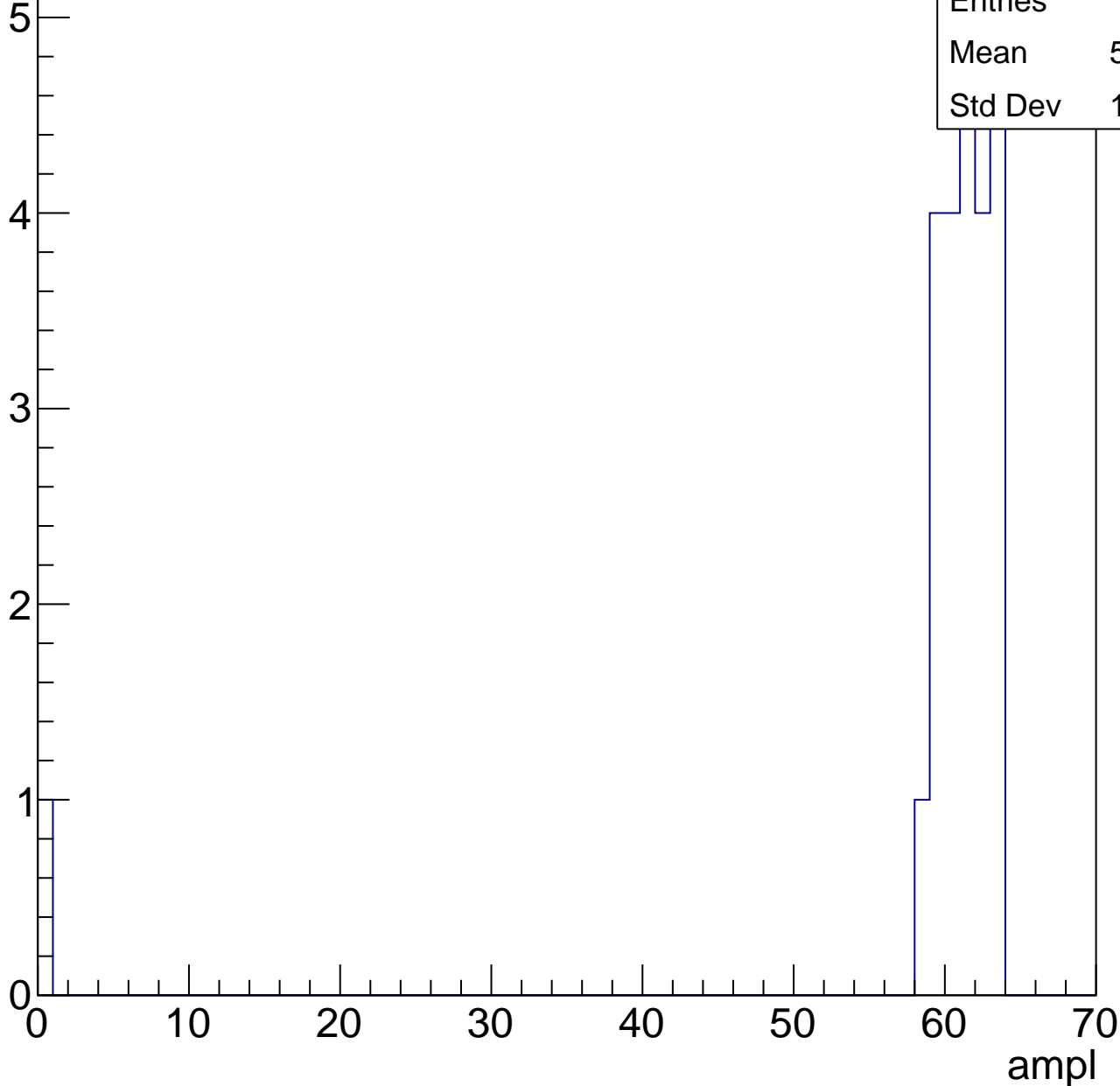
Entries	61
Mean	57.52
Std Dev	3.065

# B1L002S, U18-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.42
Std Dev	12.27

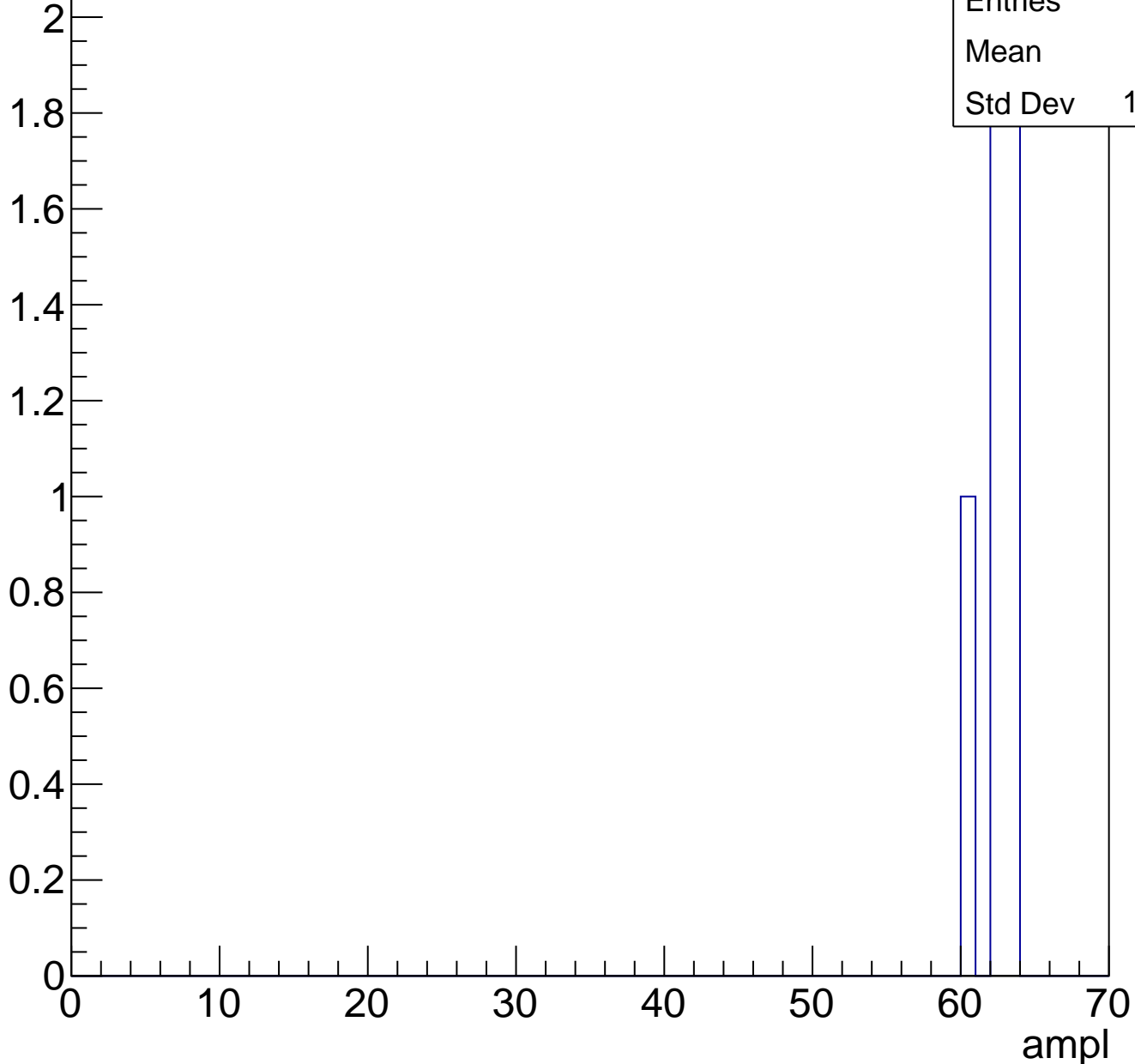




# B1L002S, U18-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch21, adc0

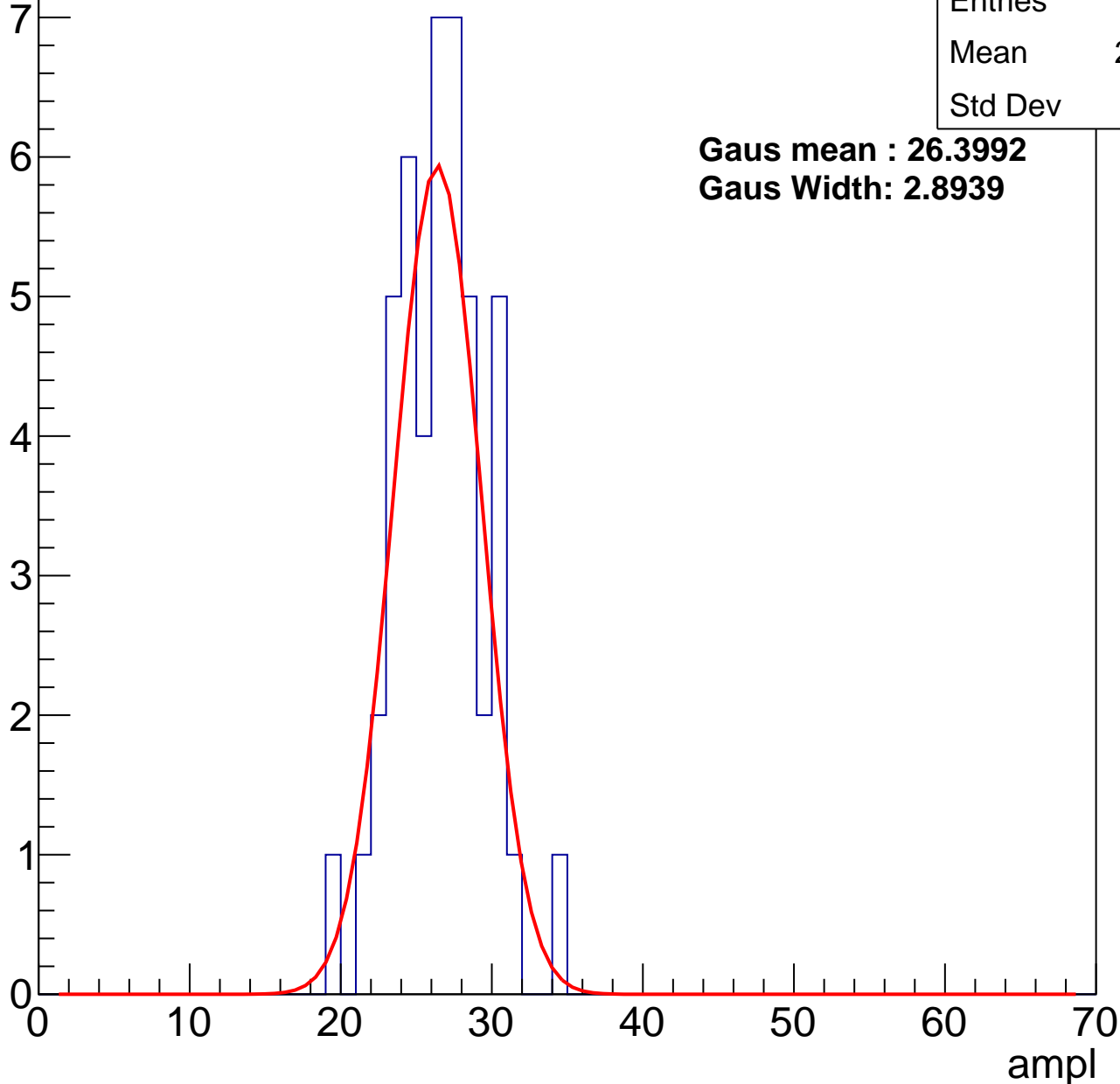
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	26.11
Std Dev	2.89

**Gaus mean : 26.3992**

**Gaus Width: 2.8939**



# B1L002S, U18-ch21, adc1

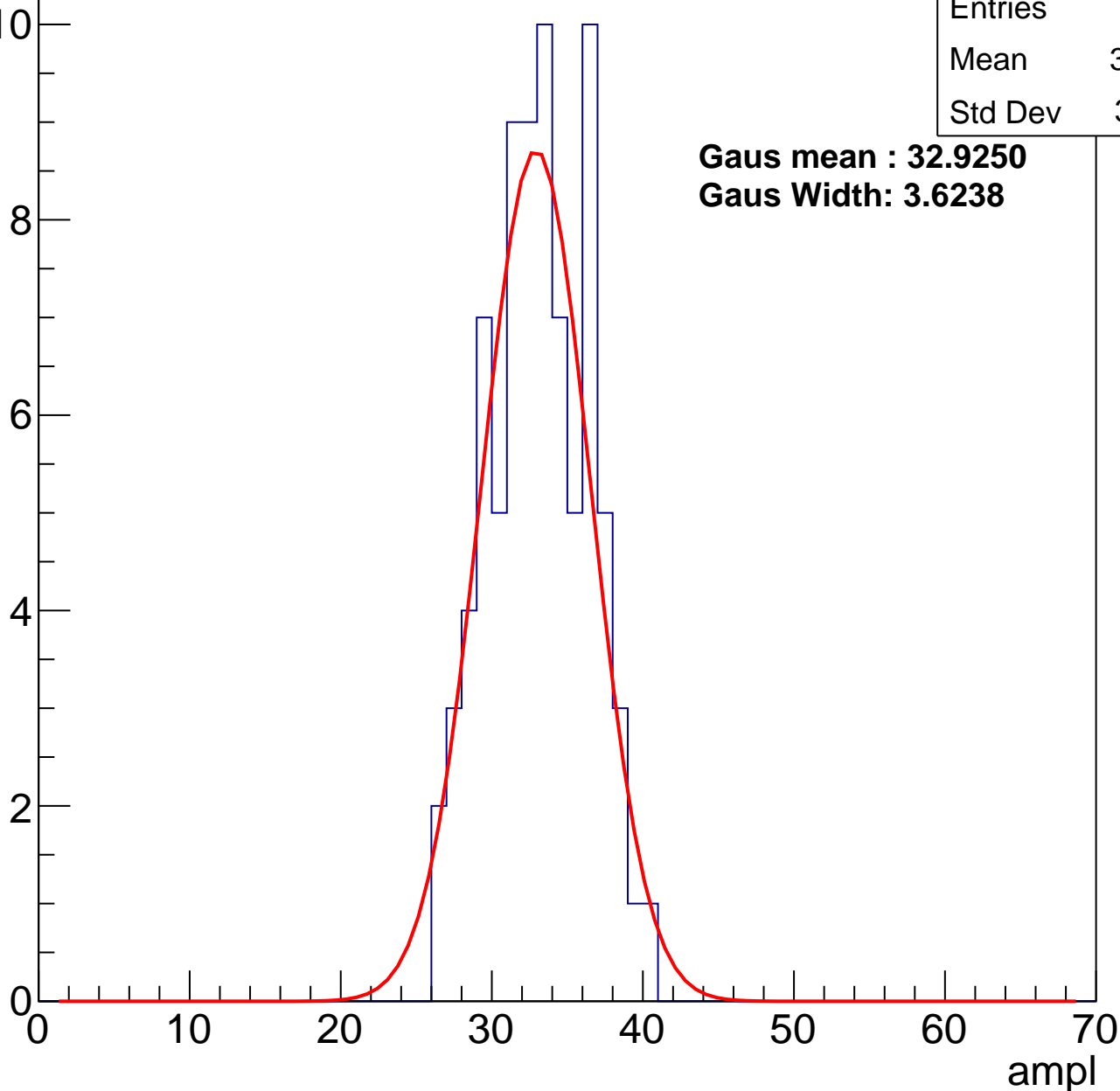
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	32.67
Std Dev	3.251

**Gaus mean : 32.9250**

**Gaus Width: 3.6238**



# B1L002S, U18-ch21, adc2

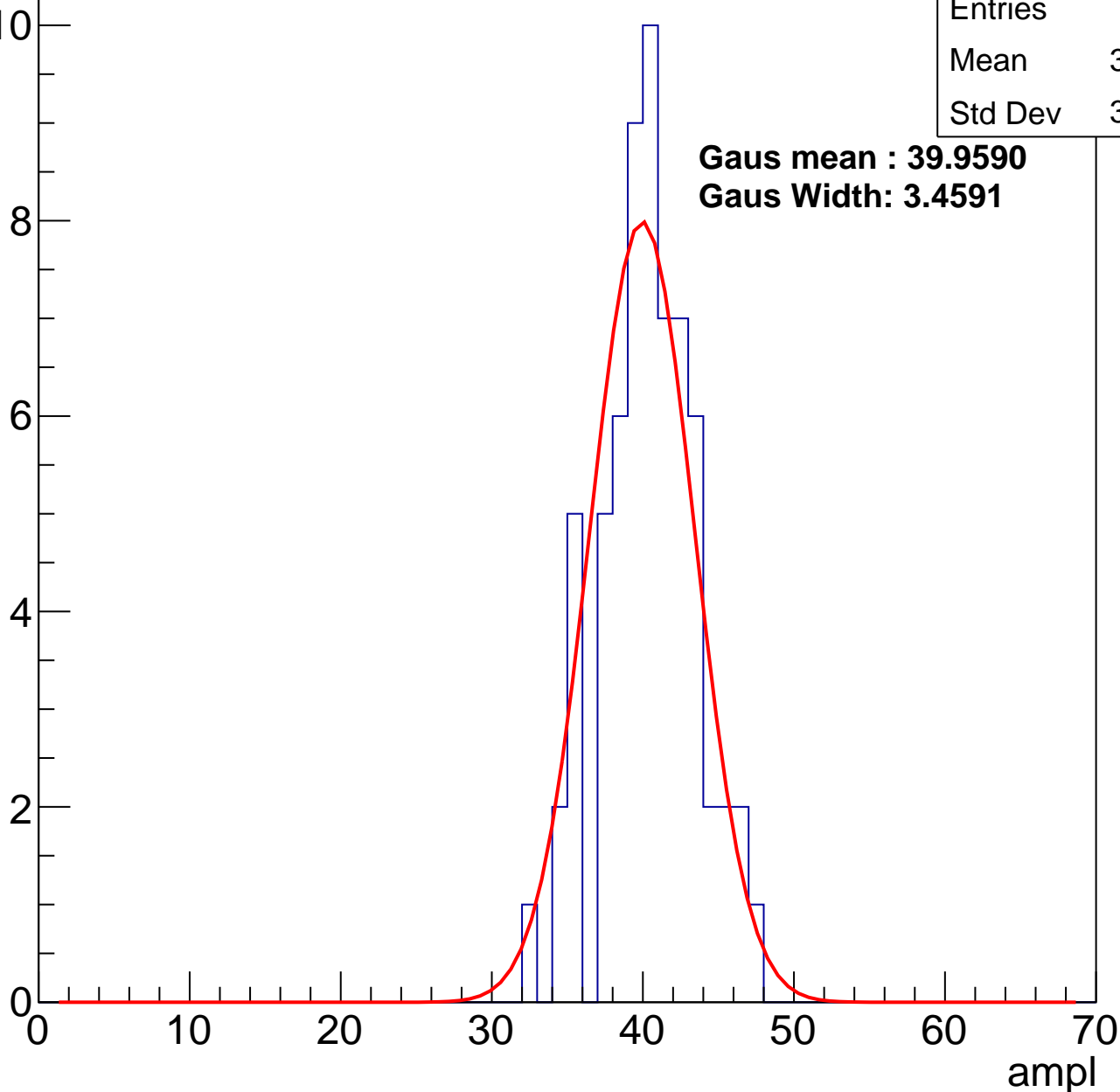
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.92
Std Dev	3.115

**Gaus mean : 39.9590**

**Gaus Width: 3.4591**

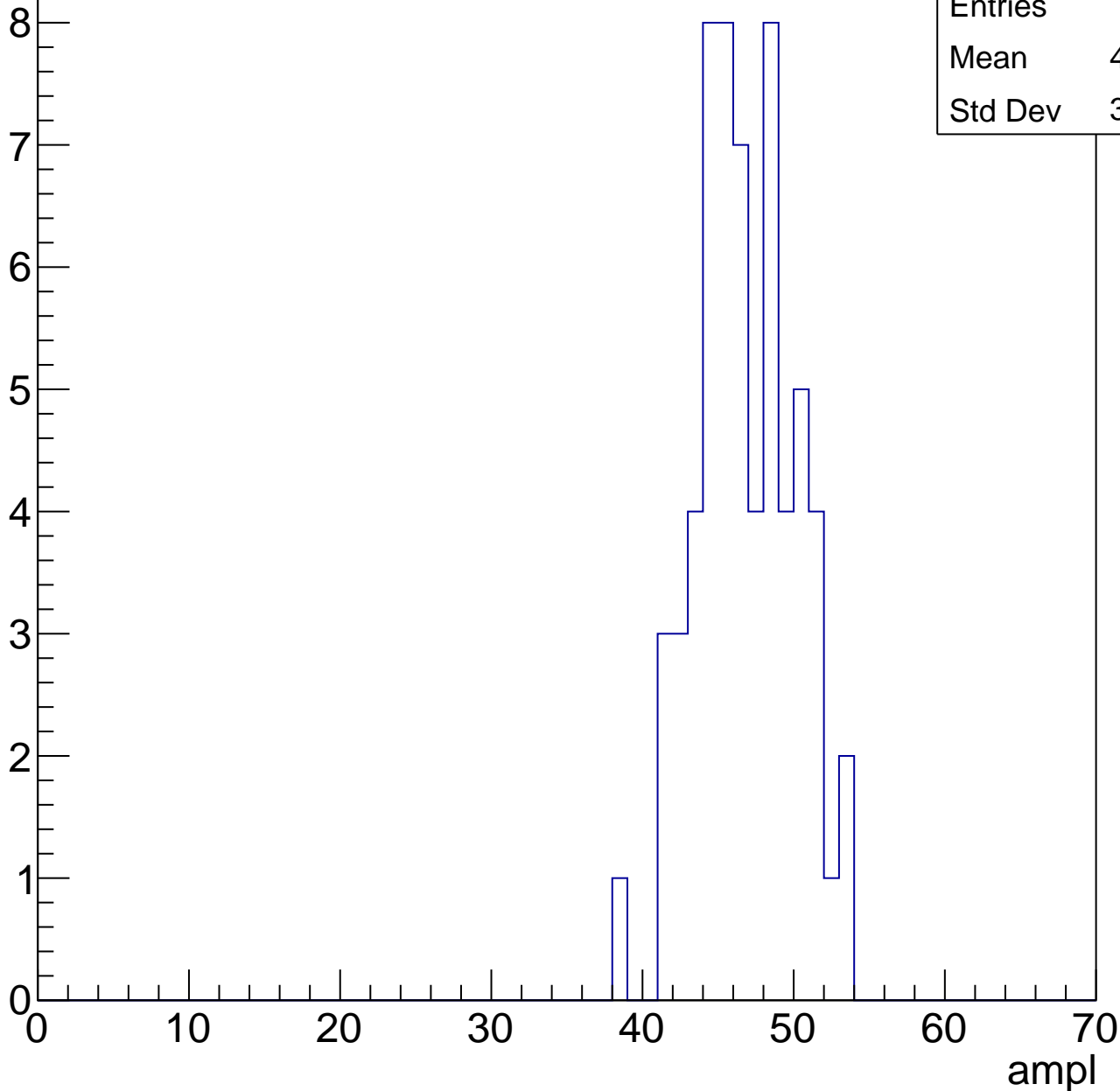


# B1L002S, U18-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.34
Std Dev	3.218

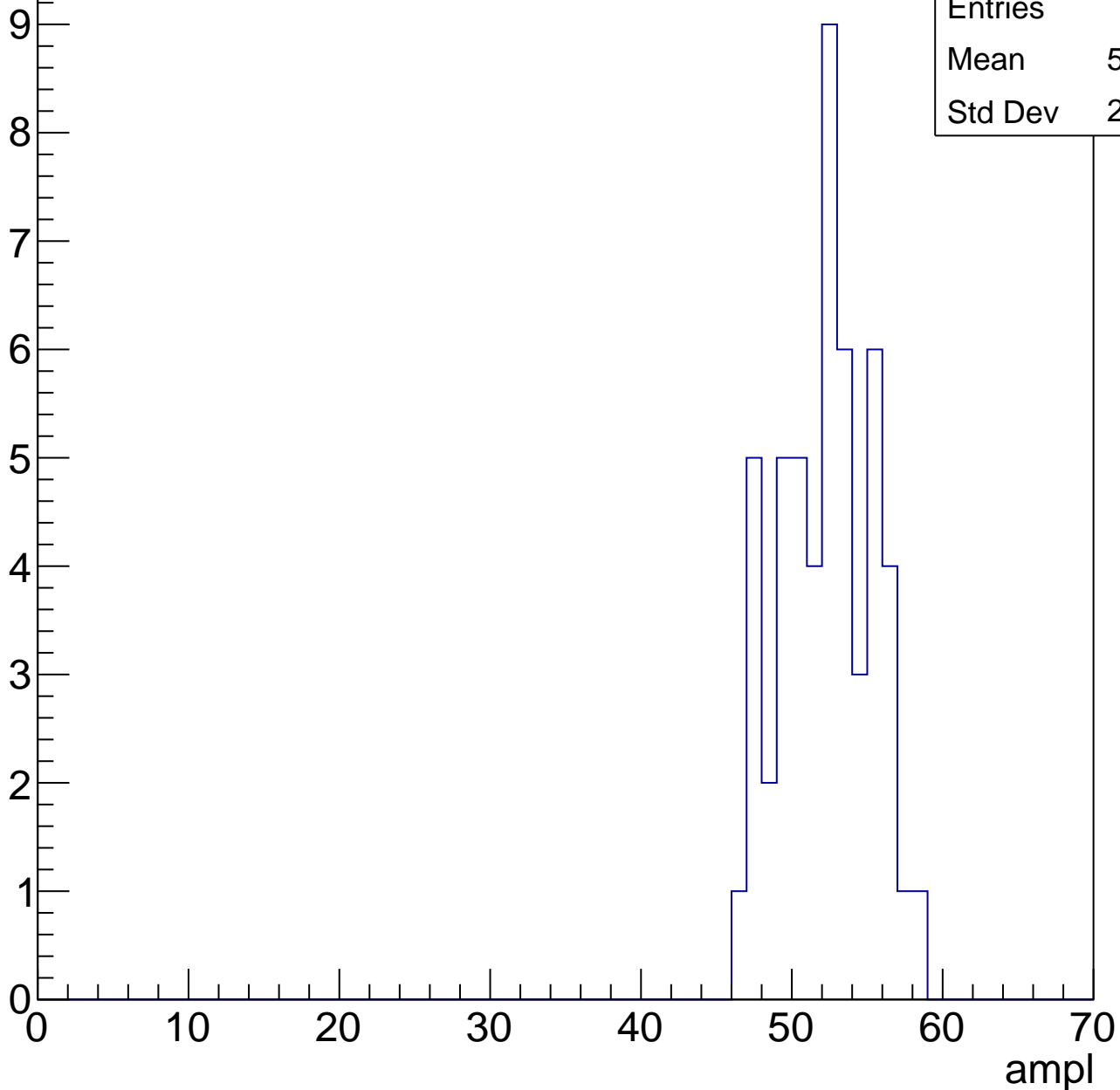


# B1L002S, U18-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

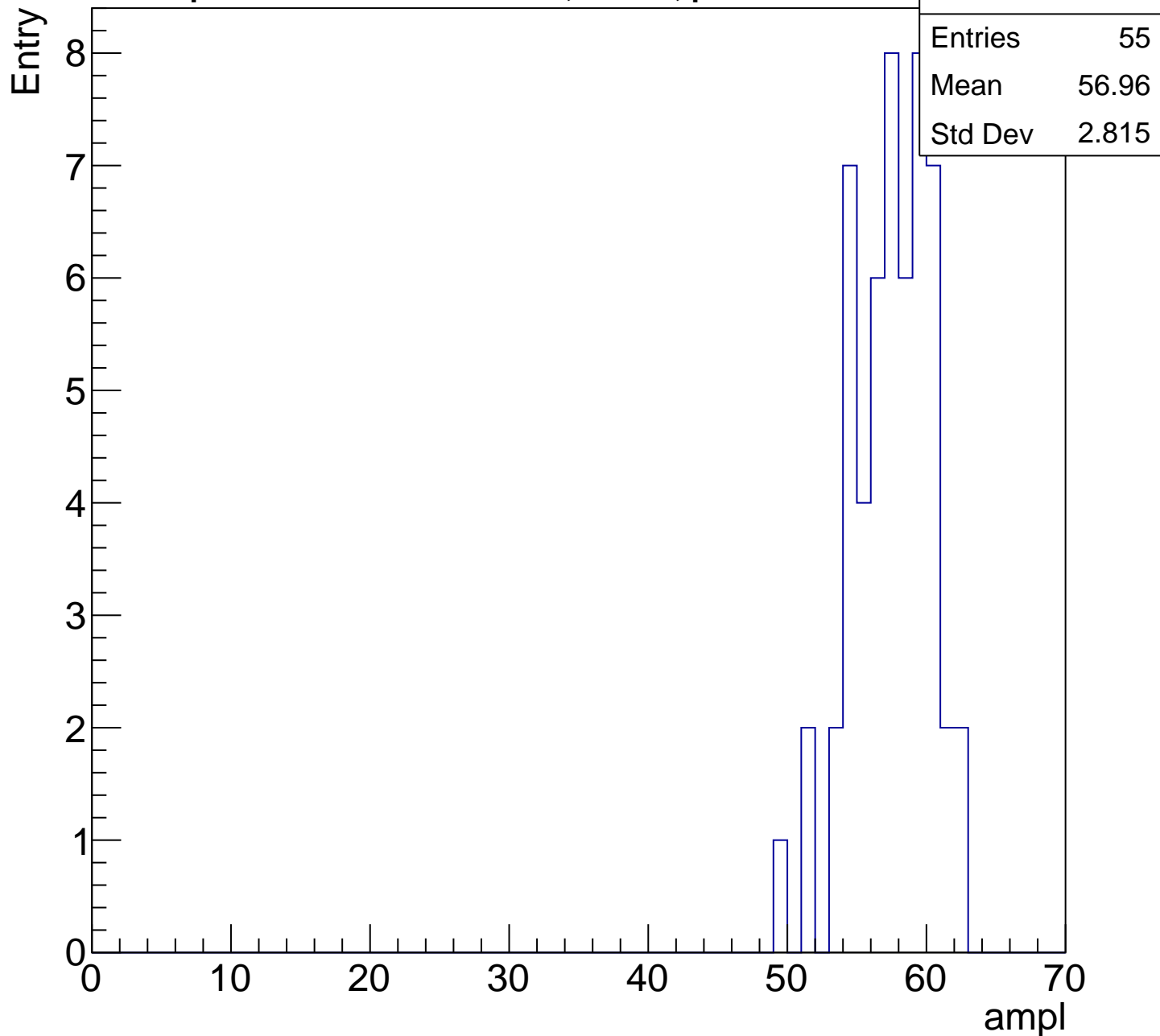
Entry

Entries	52
Mean	51.79
Std Dev	2.963



# B1L002S, U18-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

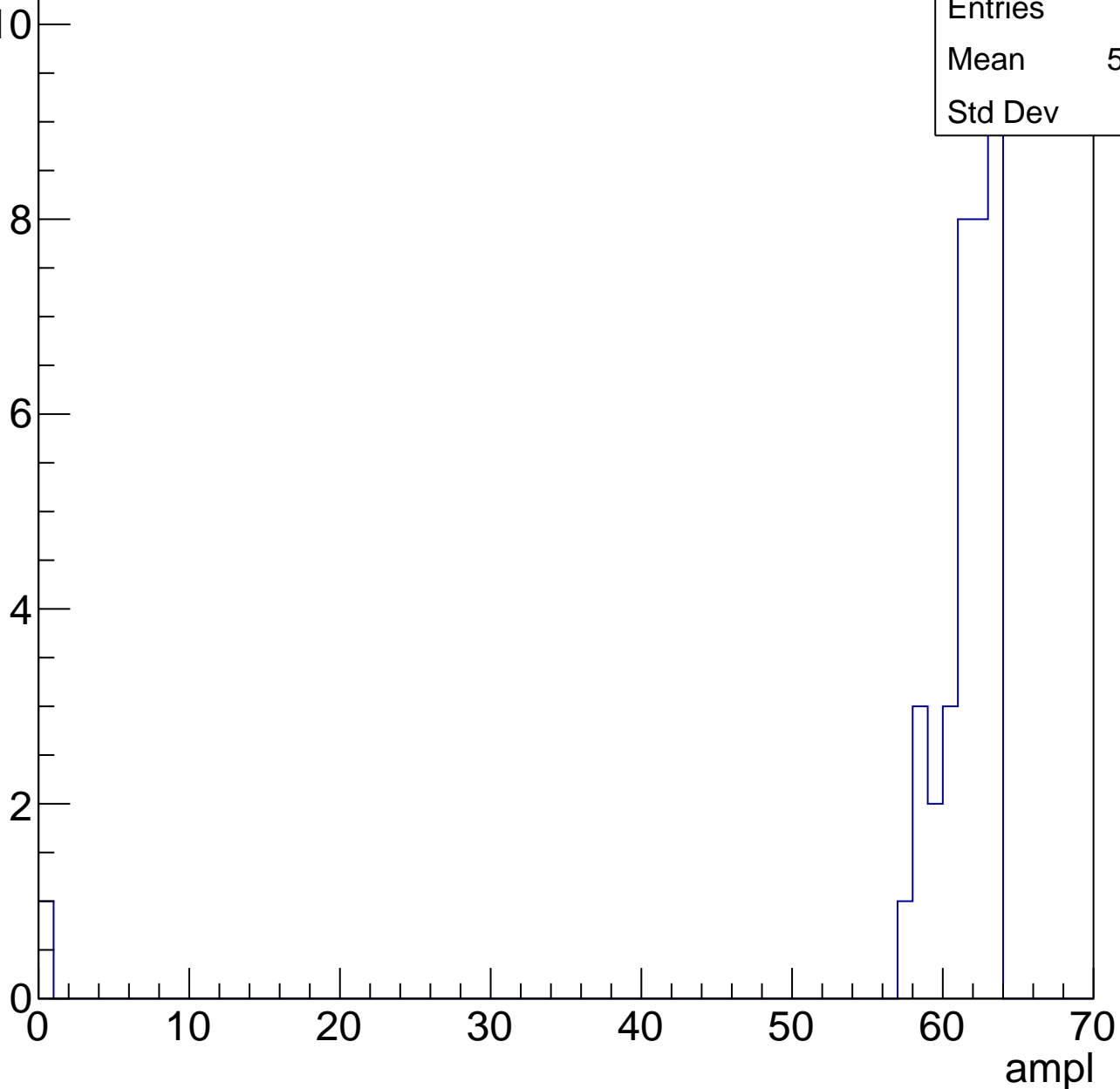


# B1L002S, U18-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	59.53
Std Dev	10.2





# B1L002S, U18-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U18-ch22, adc0

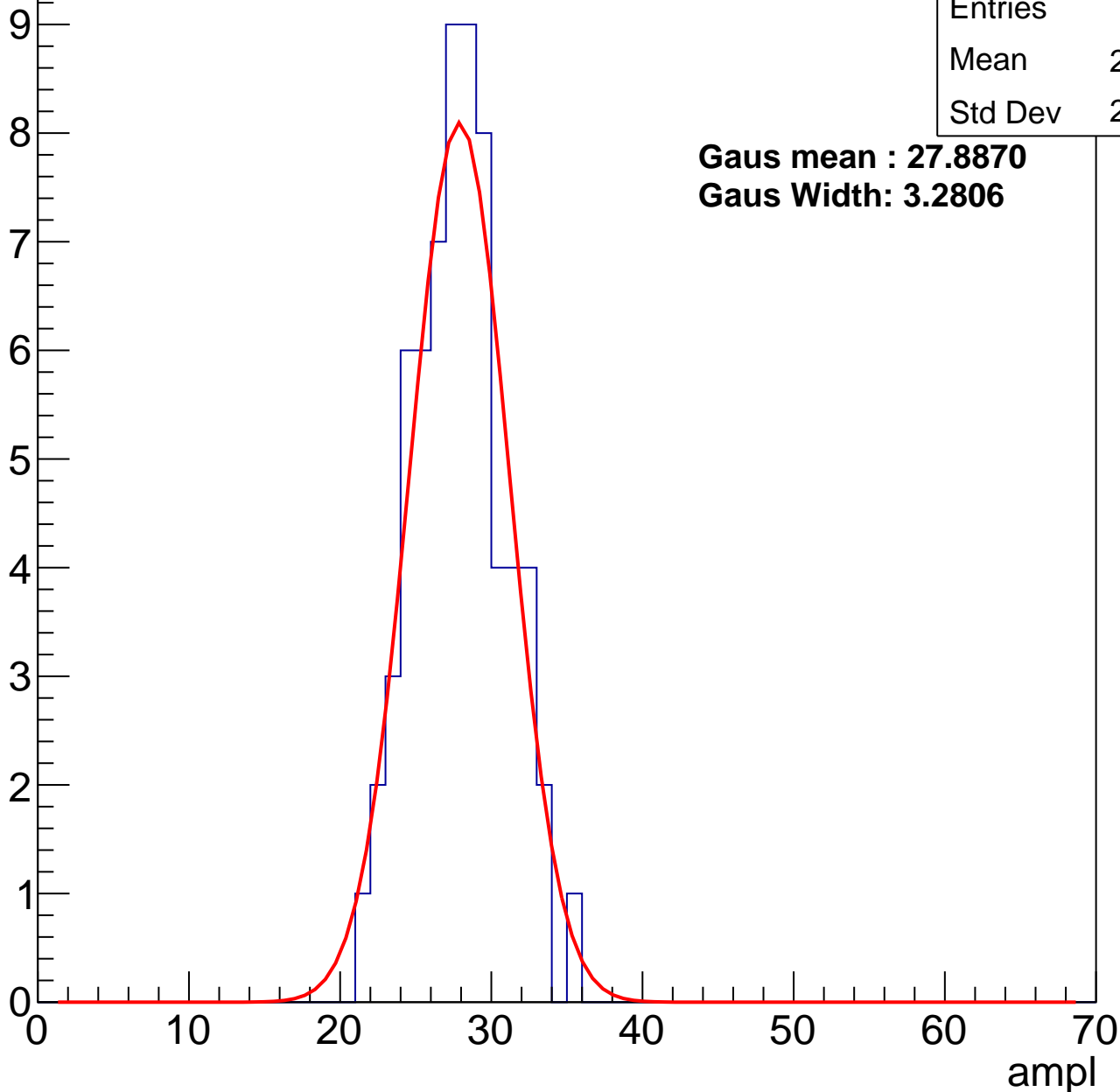
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.42
Std Dev	2.975

**Gaus mean : 27.8870**

**Gaus Width: 3.2806**



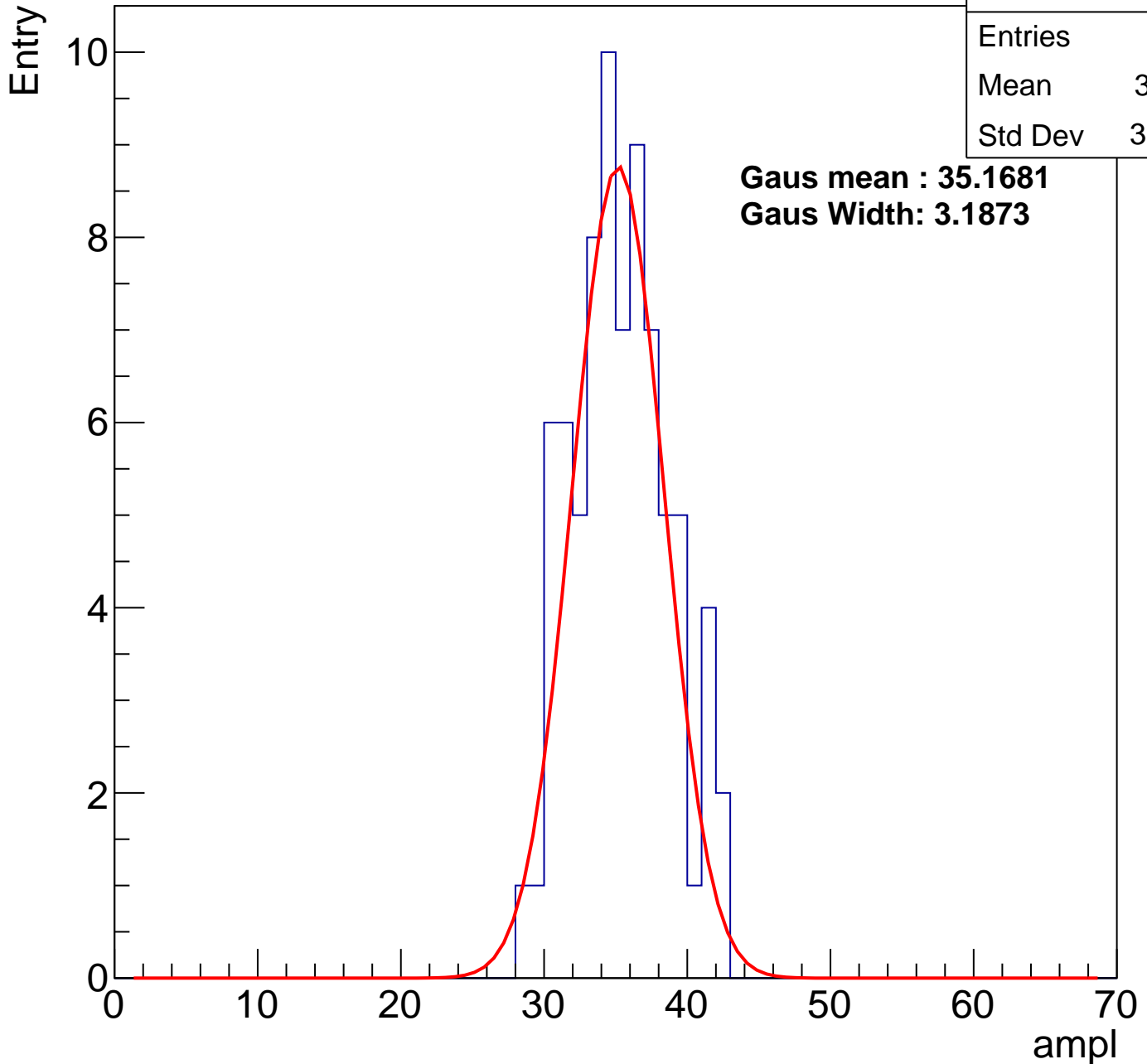
# B1L002S, U18-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	34.91
Std Dev	3.315

**Gaus mean : 35.1681**

**Gaus Width: 3.1873**



# B1L002S, U18-ch22, adc2

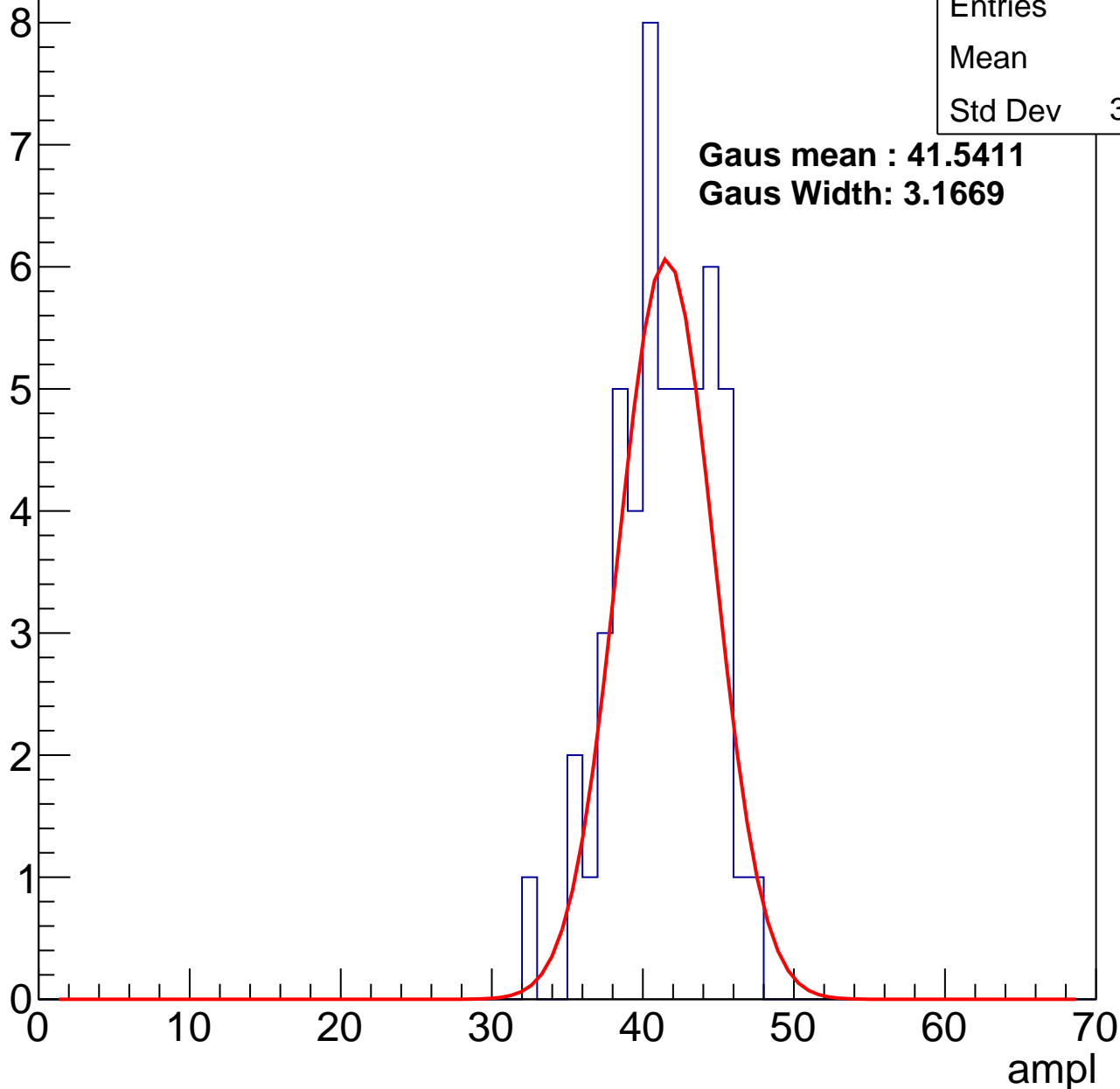
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.9
Std Dev	3.146

**Gaus mean : 41.5411**

**Gaus Width: 3.1669**

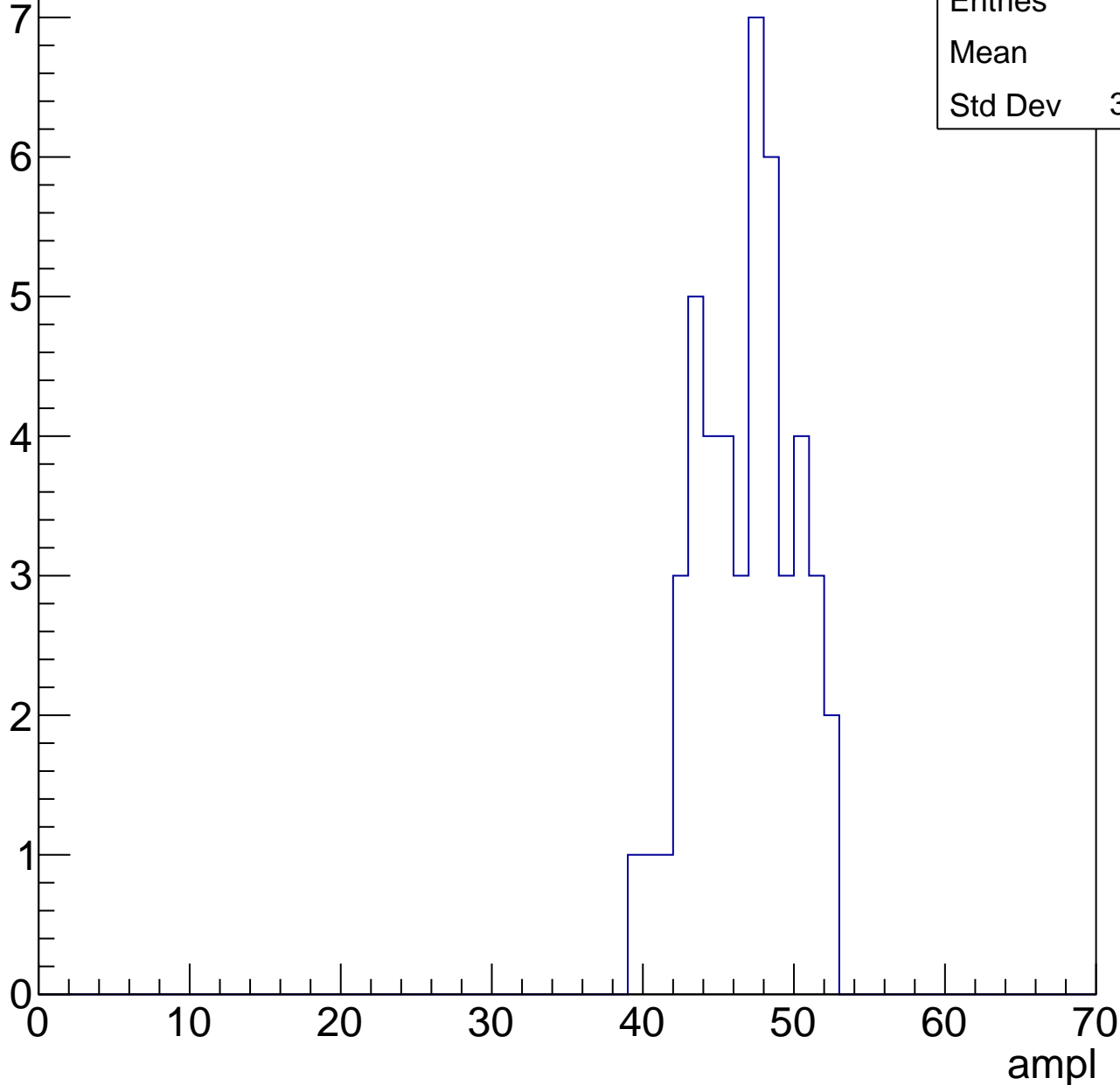


# B1L002S, U18-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	46.3
Std Dev	3.222

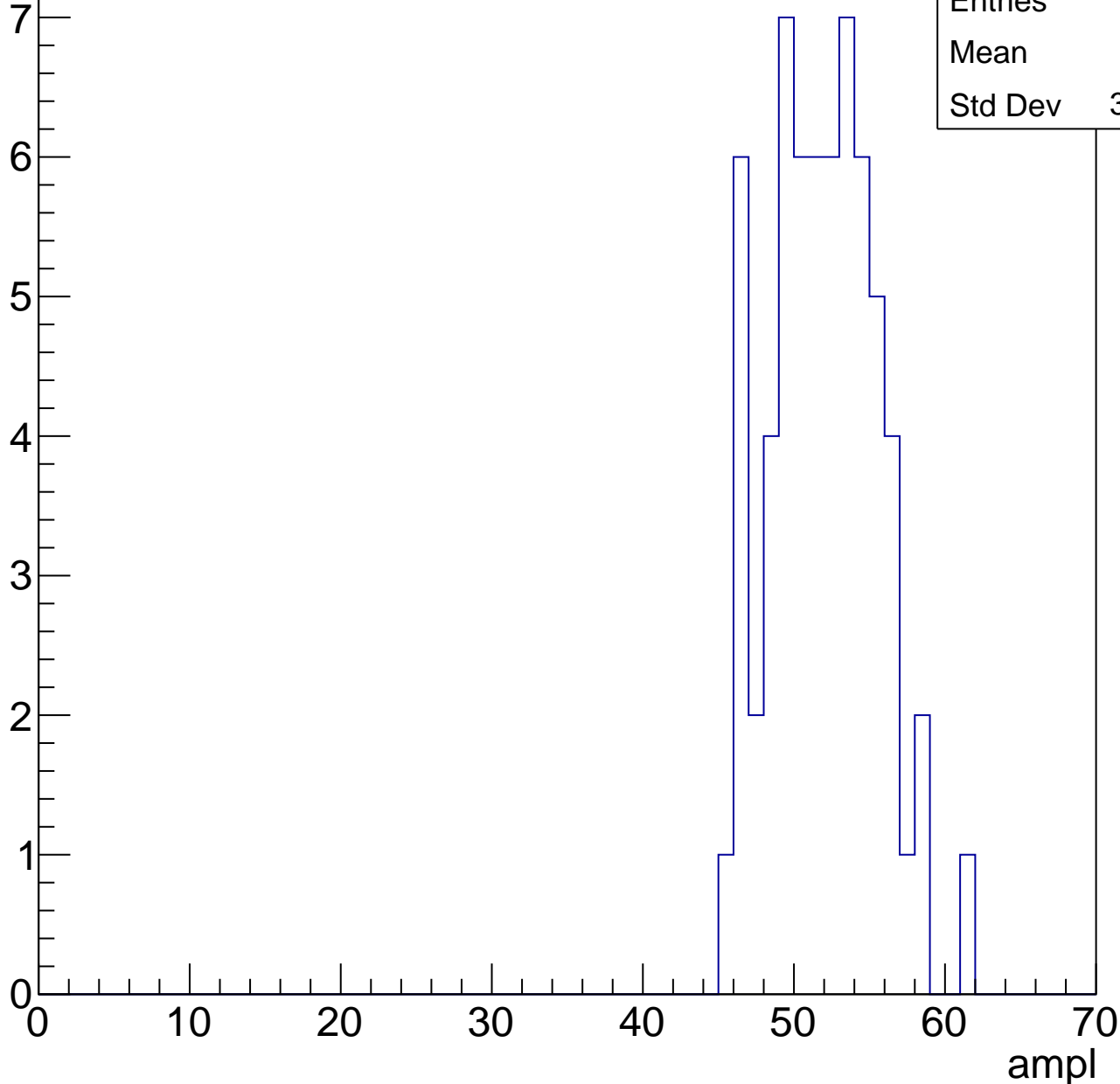


# B1L002S, U18-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	51.5
Std Dev	3.478

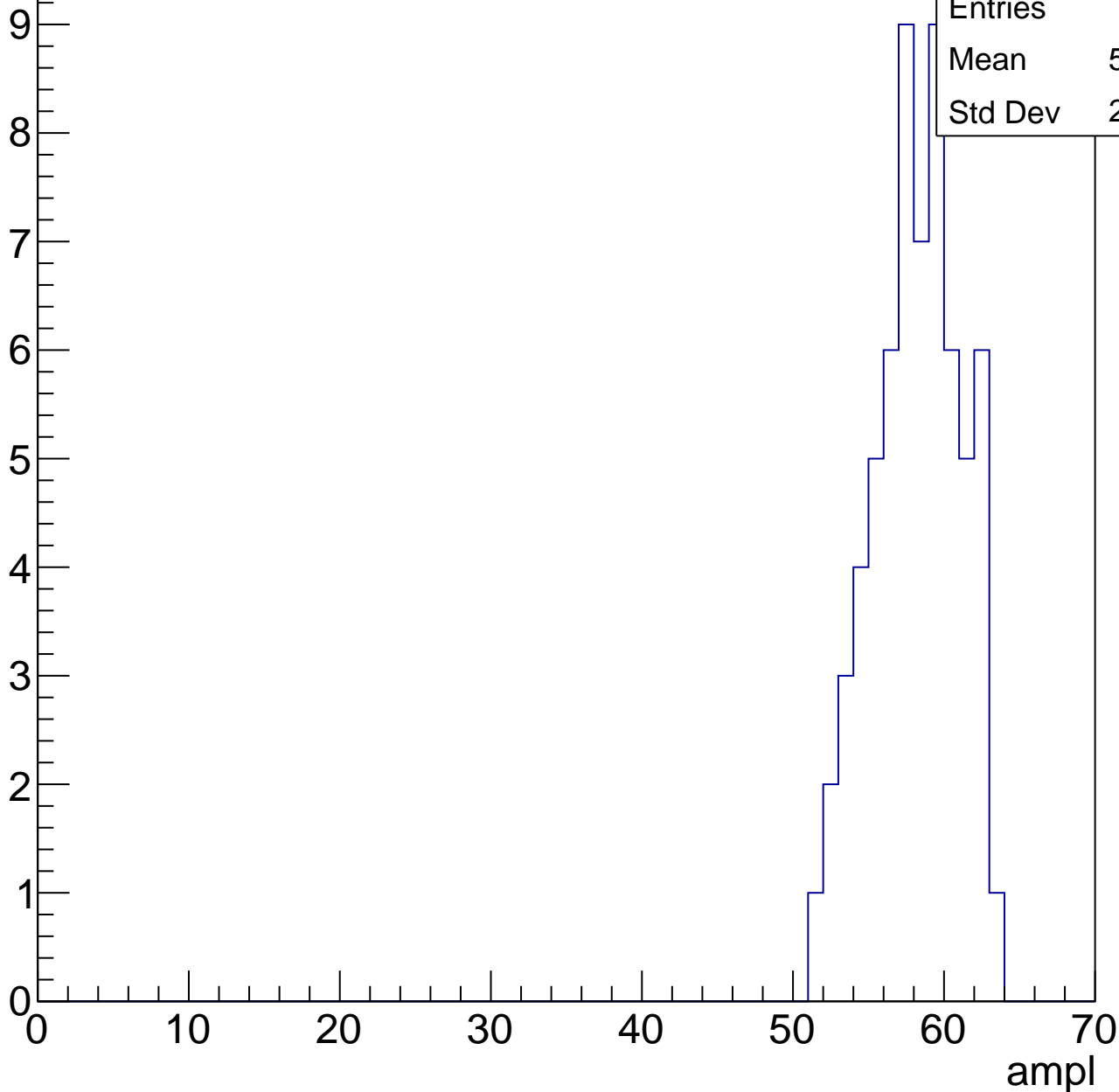


# B1L002S, U18-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.67
Std Dev	2.878

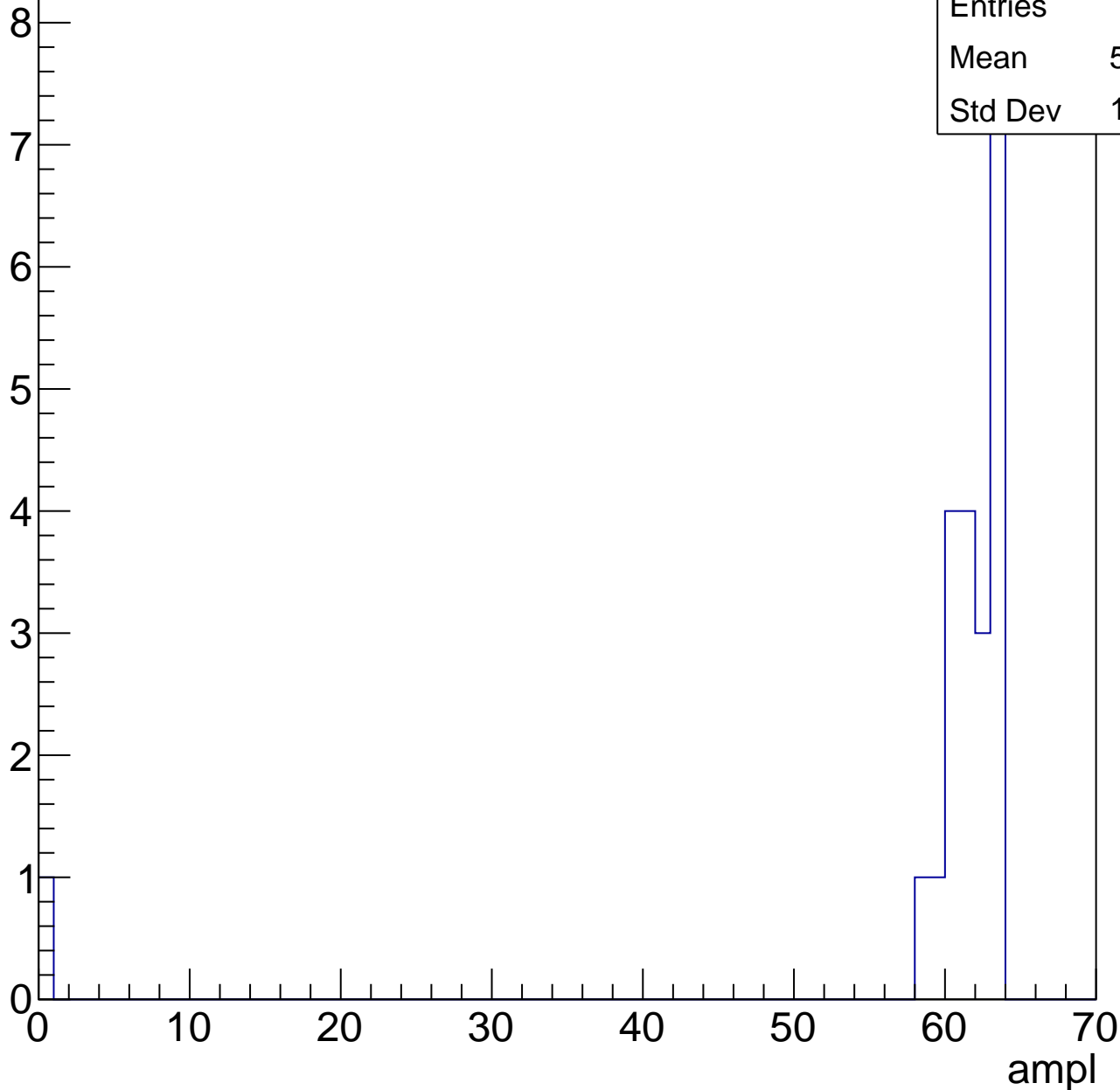


# B1L002S, U18-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.68
Std Dev	12.89

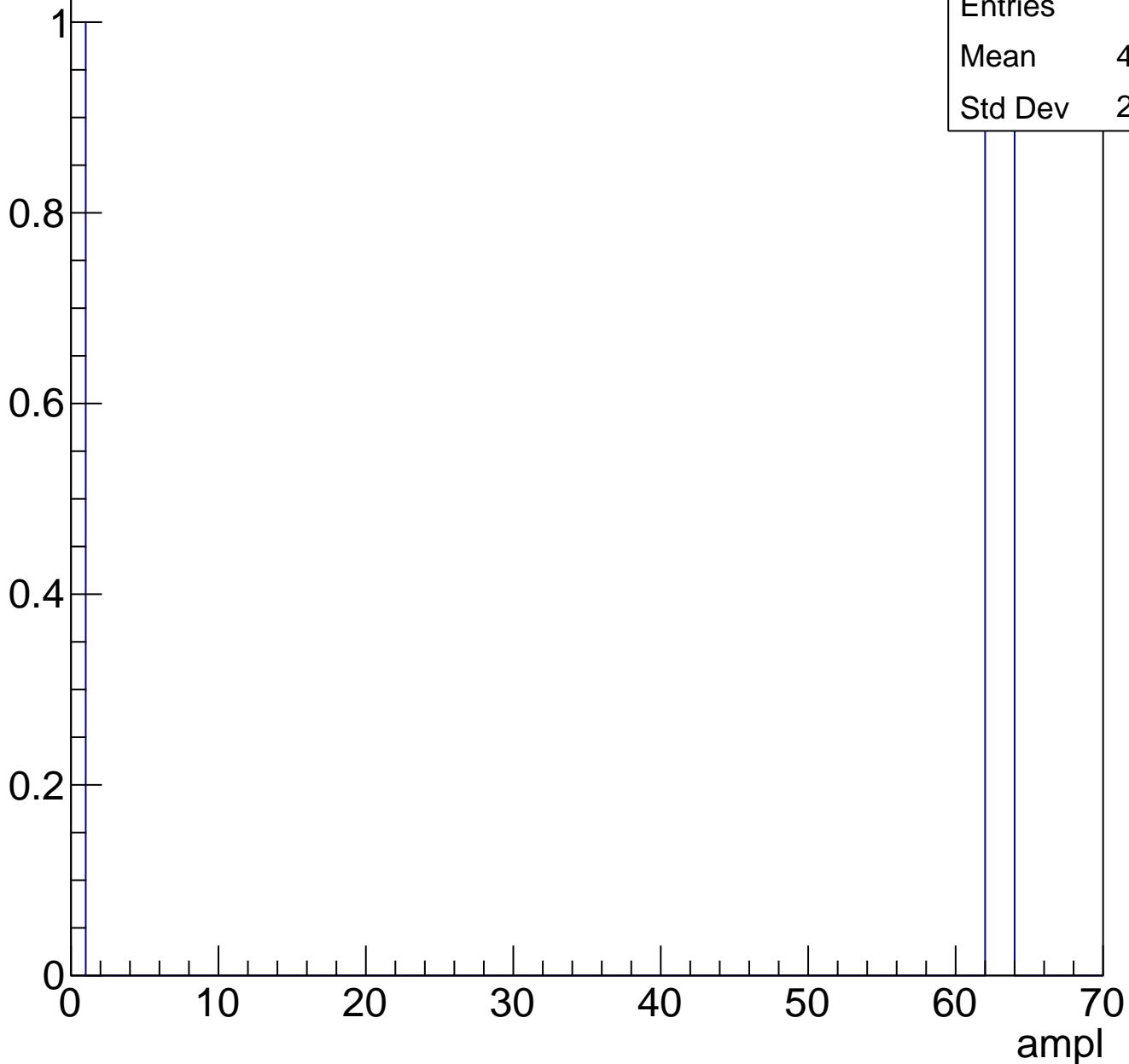




# B1L002S, U18-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch23, adc0

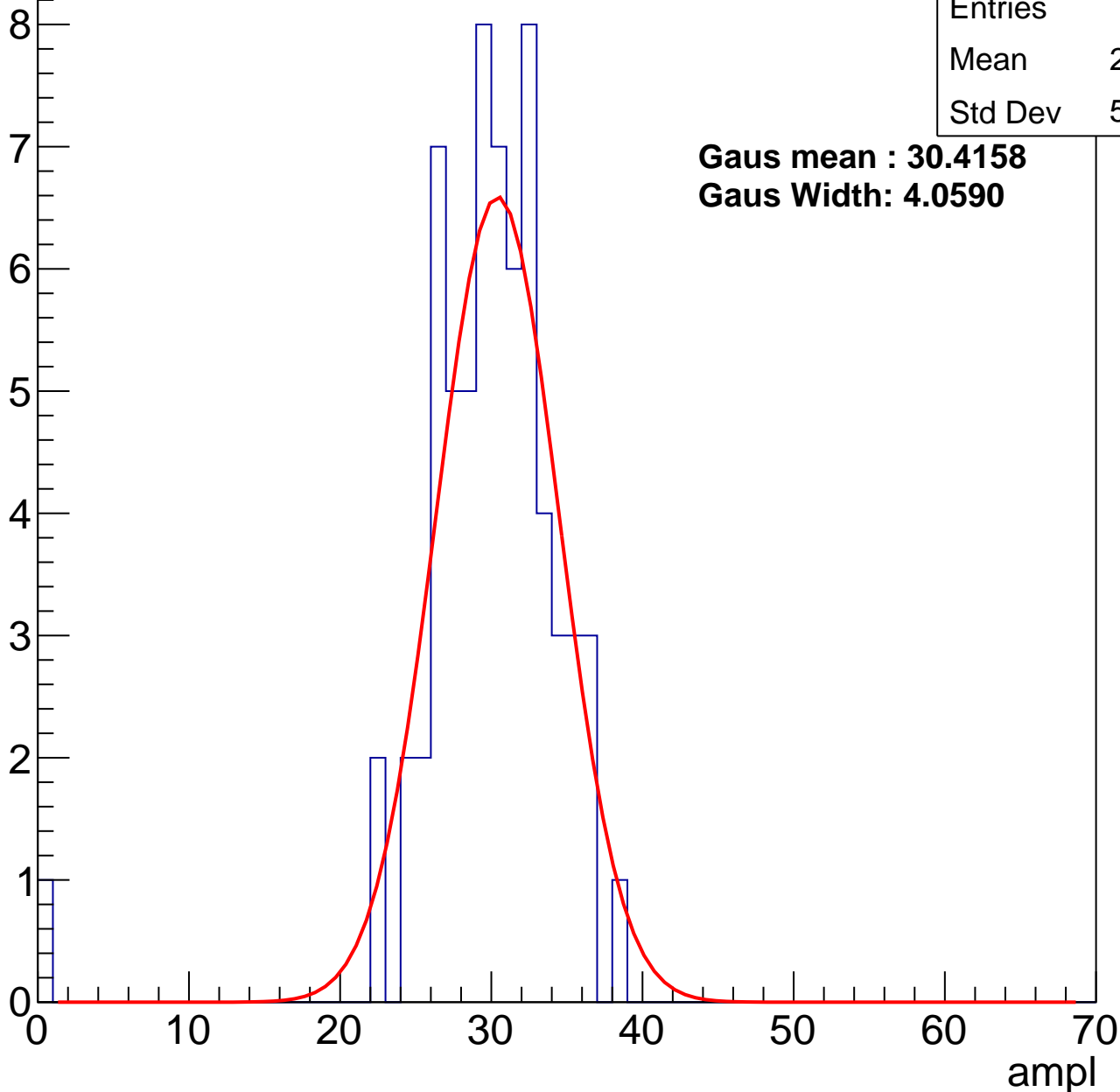
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.37
Std Dev	5.004

**Gaus mean : 30.4158**

**Gaus Width: 4.0590**



# B1L002S, U18-ch23, adc1

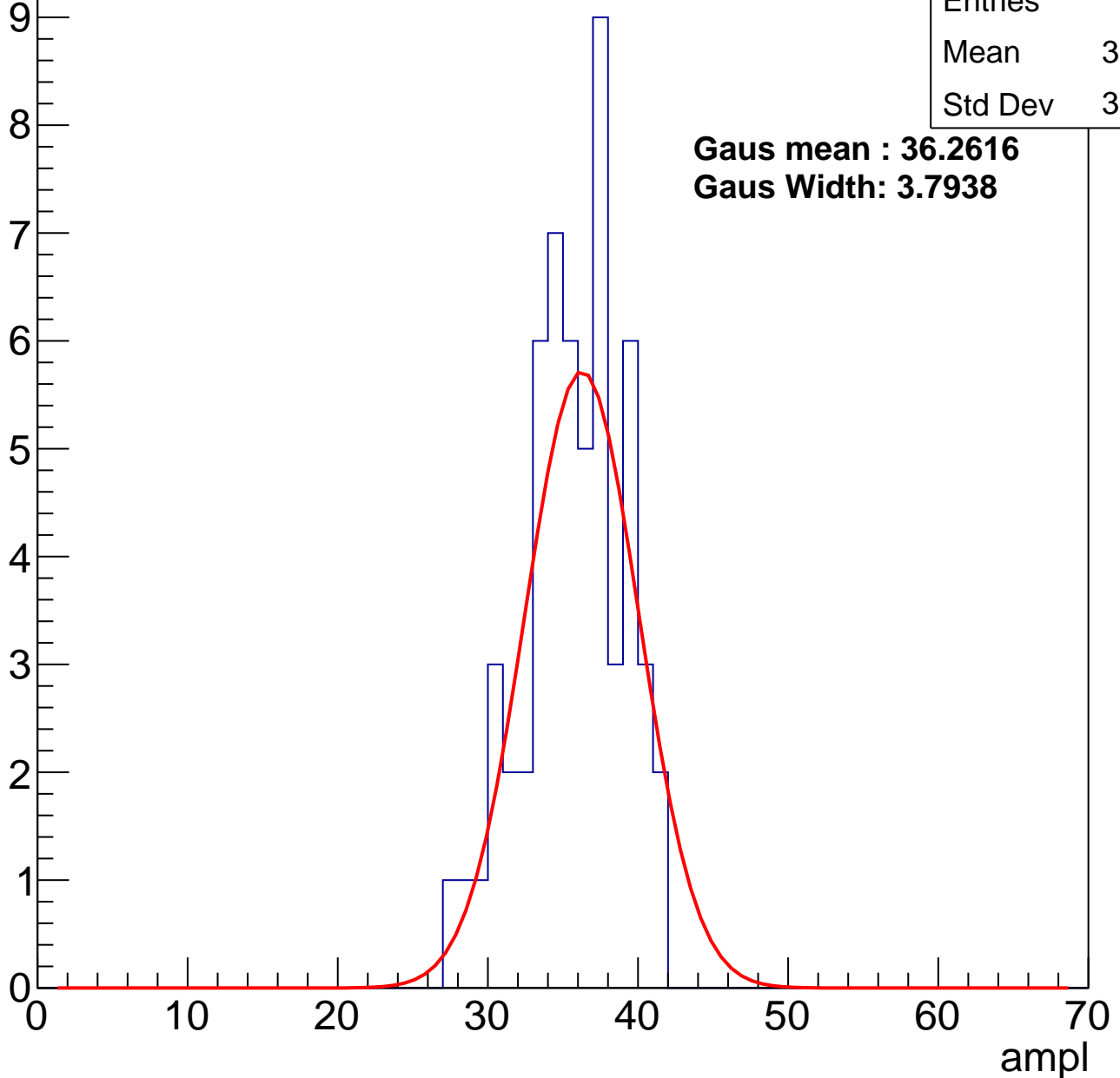
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	35.25
Std Dev	3.278

**Gaus mean : 36.2616**

**Gaus Width: 3.7938**



# B1L002S, U18-ch23, adc2

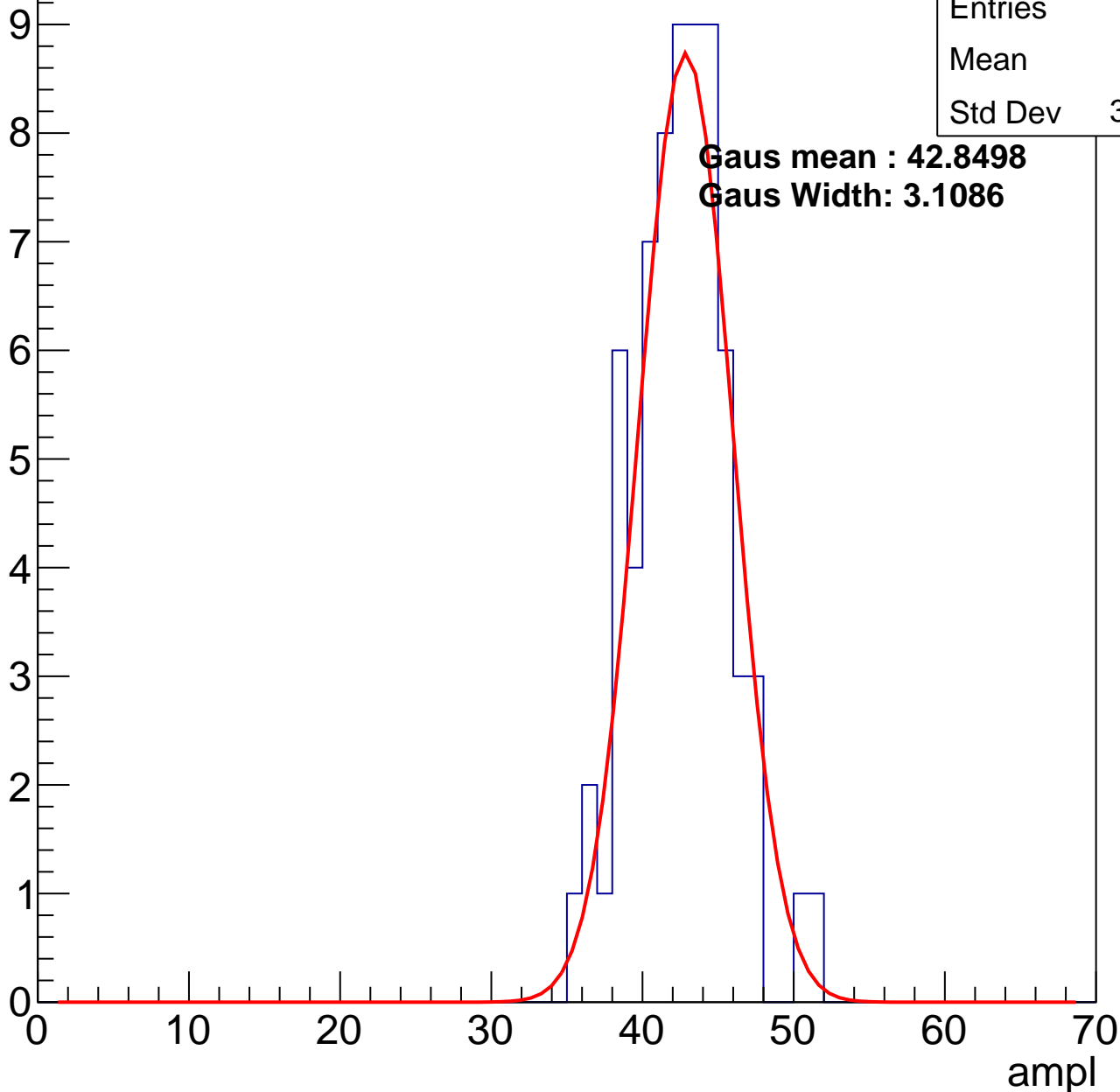
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	42.1
Std Dev	3.117

**Gaus mean : 42.8498**

**Gaus Width: 3.1086**

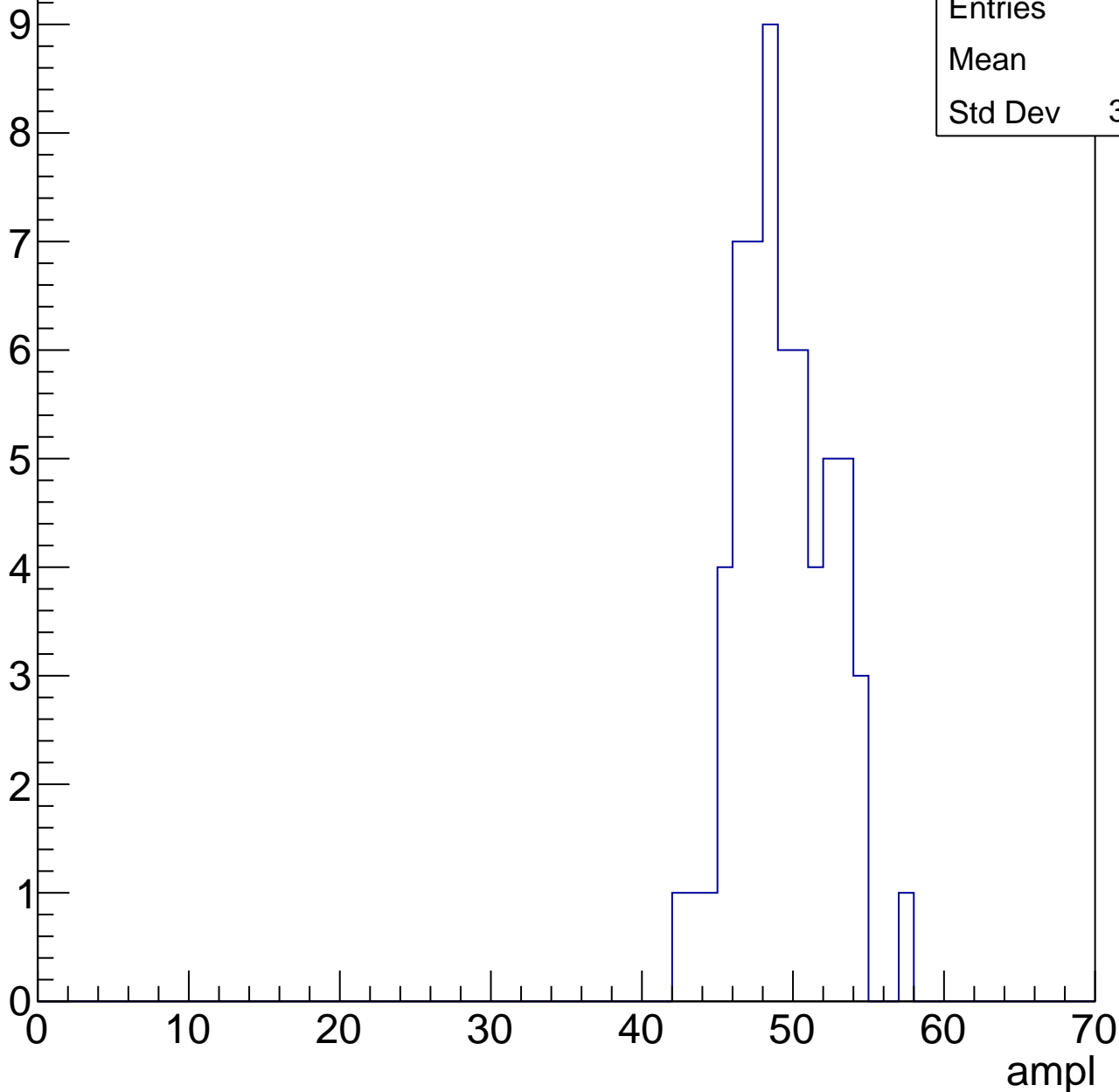


# B1L002S, U18-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

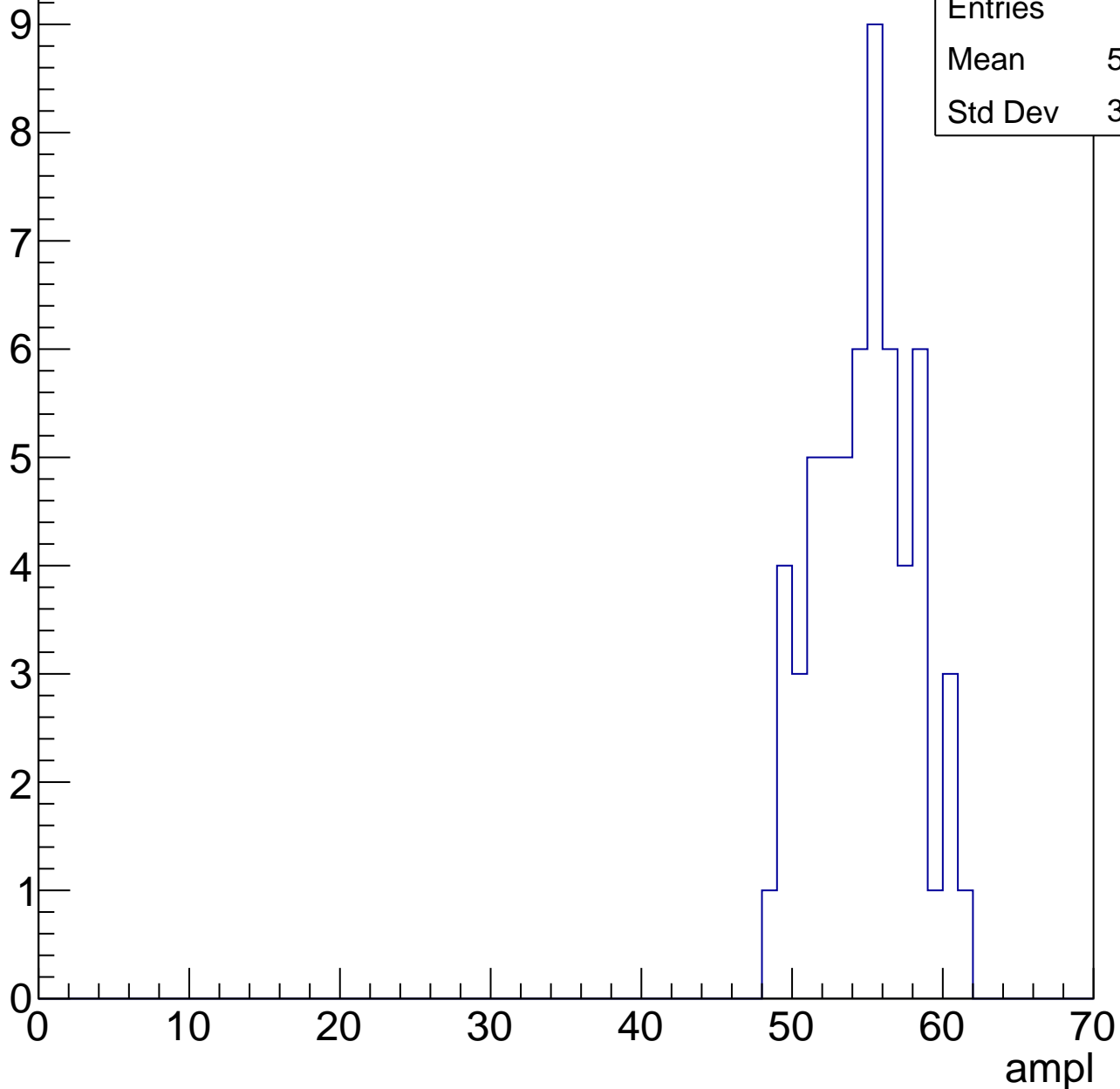
Entries	60
Mean	48.9
Std Dev	3.053



# B1L002S, U18-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

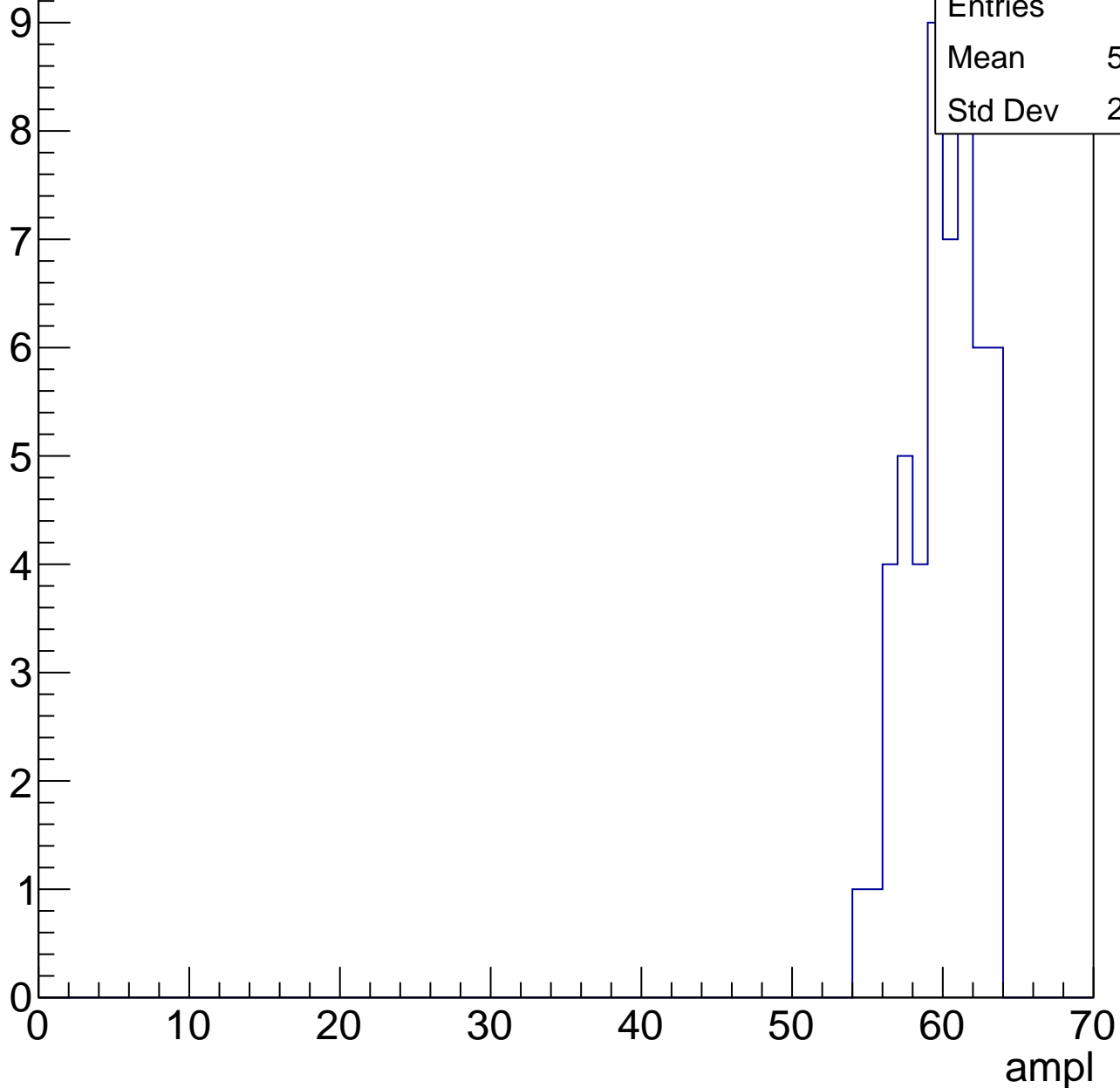
Entry



# B1L002S, U18-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

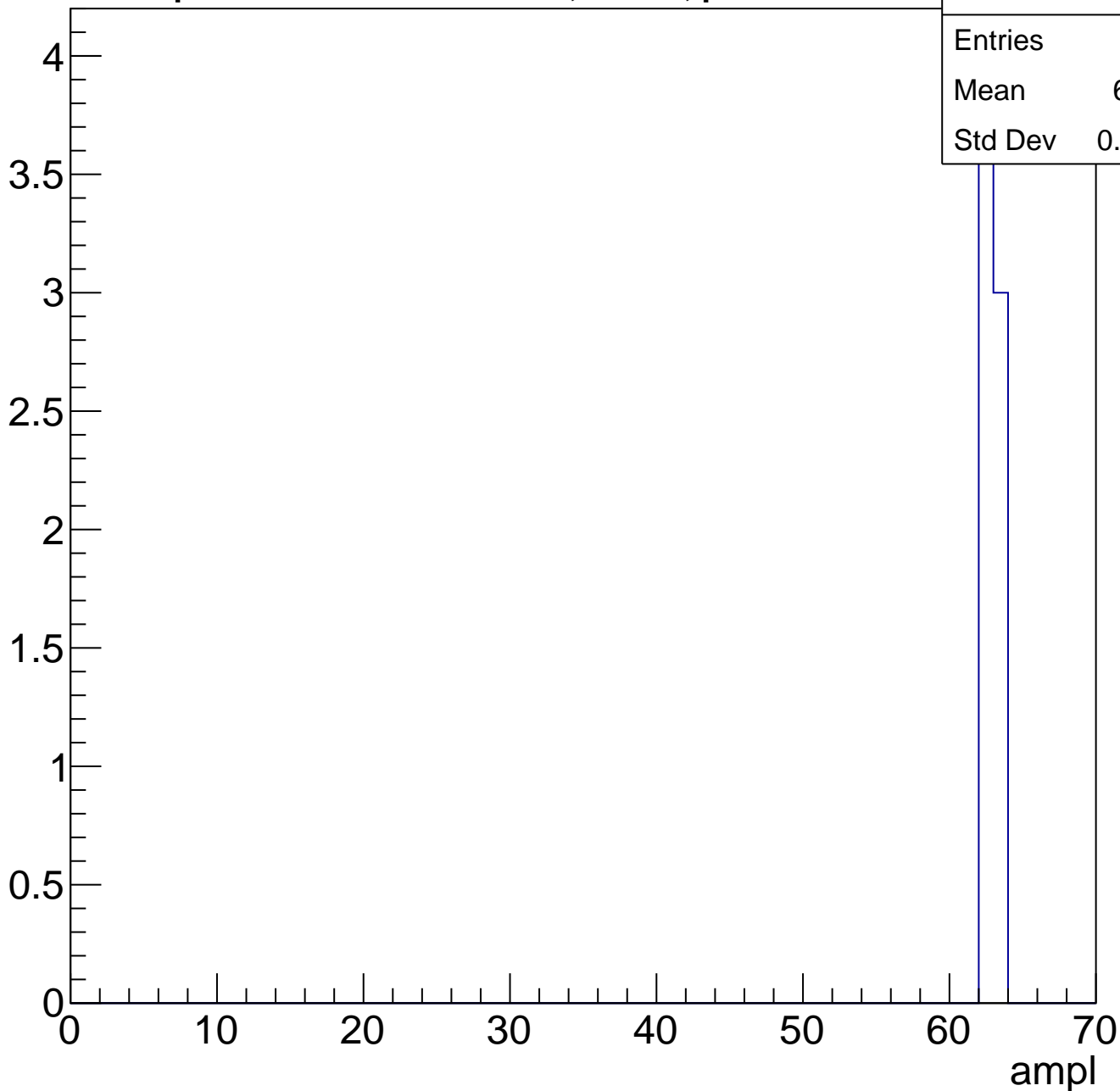


Entries	52
Mean	59.62
Std Dev	2.297

# B1L002S, U18-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

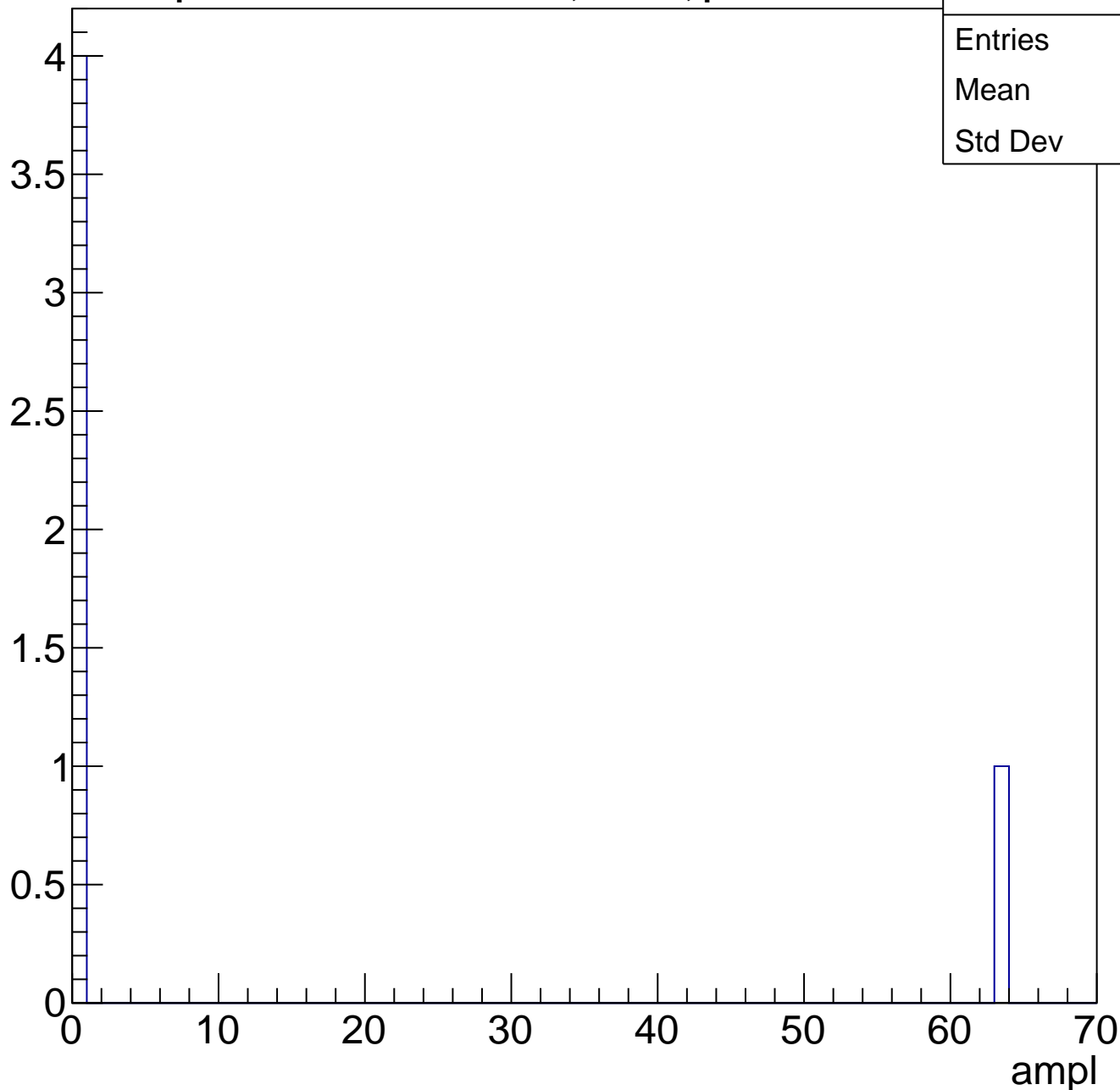




# B1L002S, U18-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch24, adc0

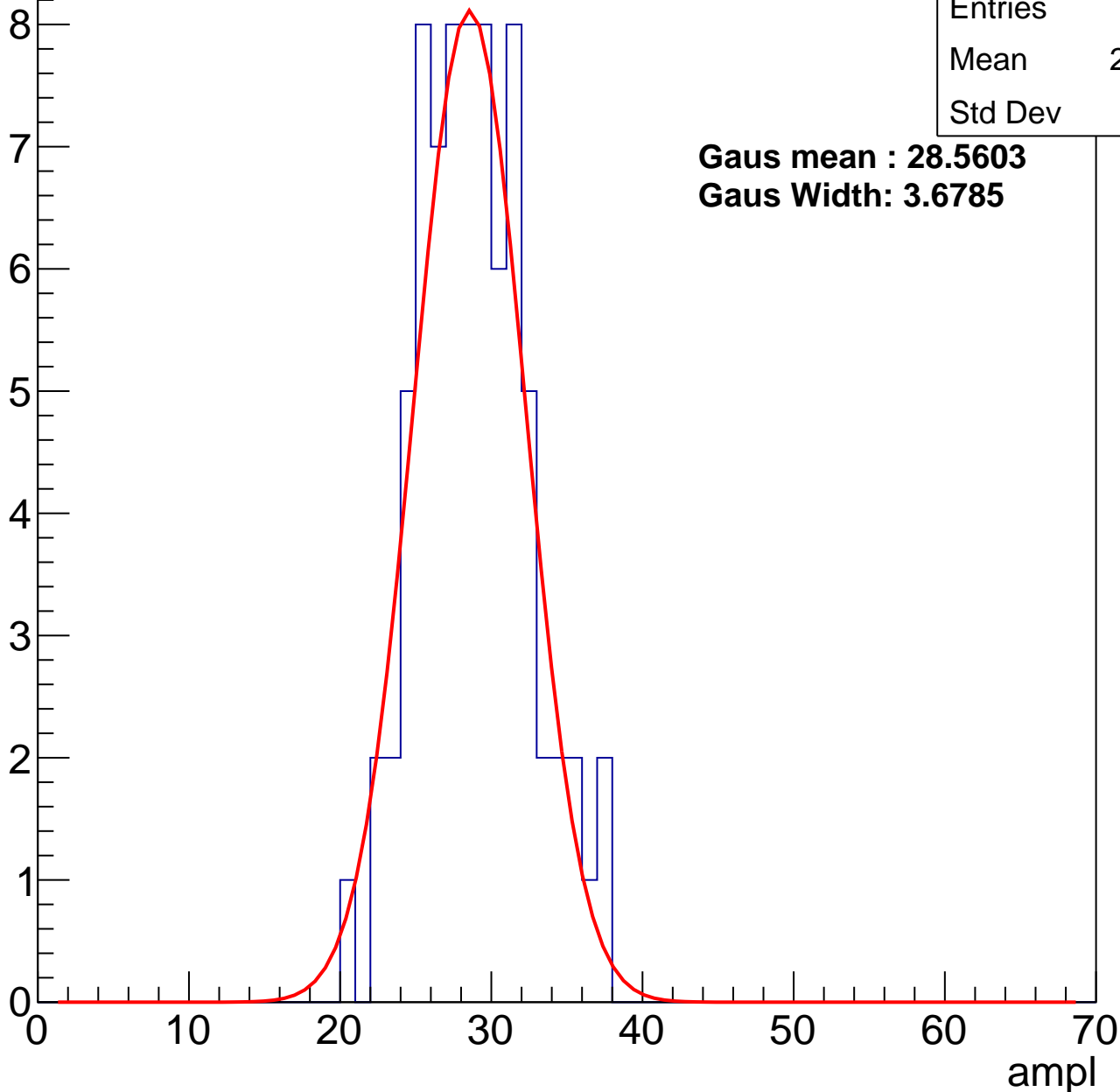
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	28.39
Std Dev	3.59

**Gaus mean : 28.5603**

**Gaus Width: 3.6785**



# B1L002S, U18-ch24, adc1

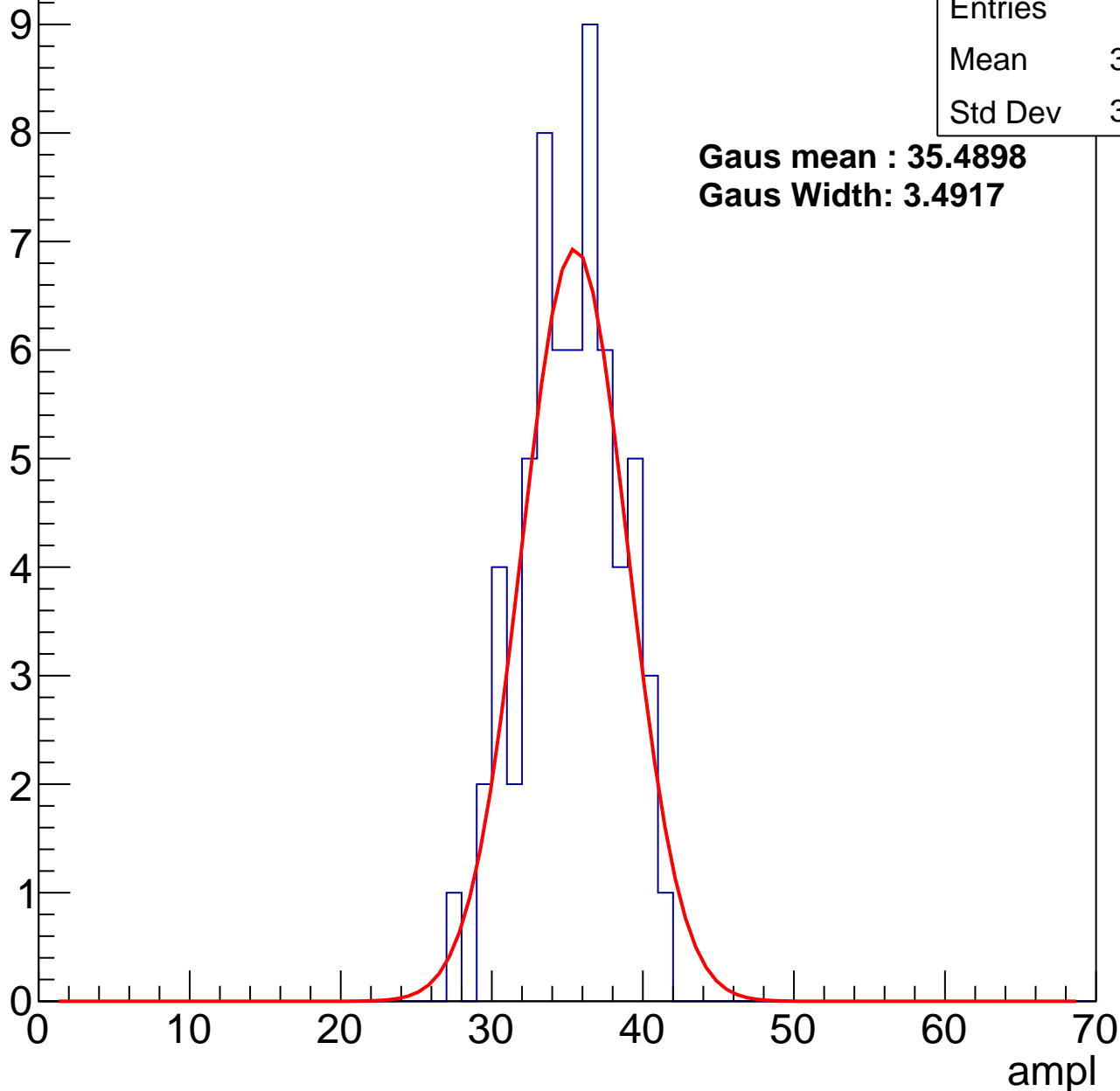
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.82
Std Dev	3.134

**Gaus mean : 35.4898**

**Gaus Width: 3.4917**



# B1L002S, U18-ch24, adc2

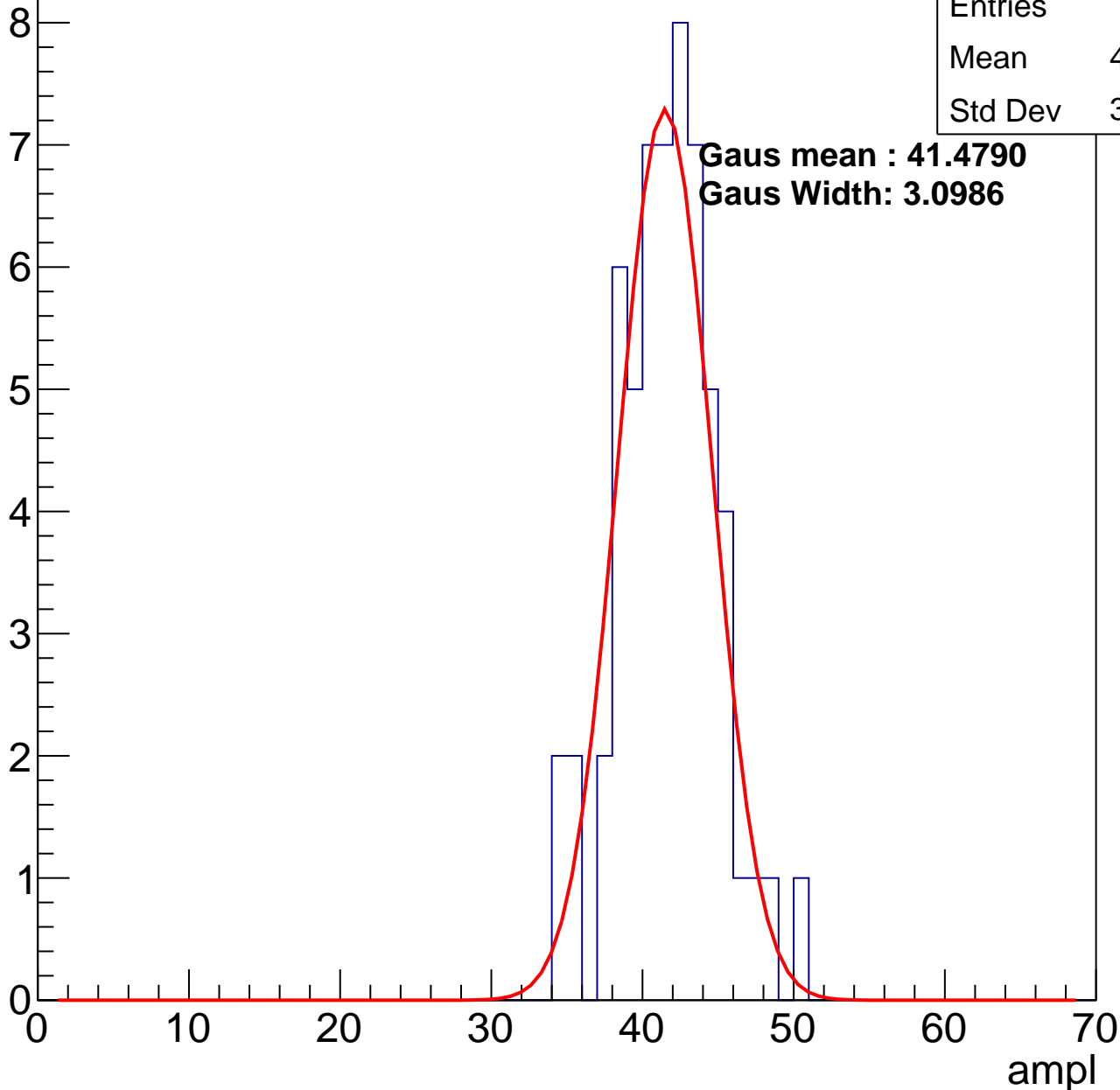
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.19
Std Dev	3.234

**Gaus mean : 41.4790**

**Gaus Width: 3.0986**

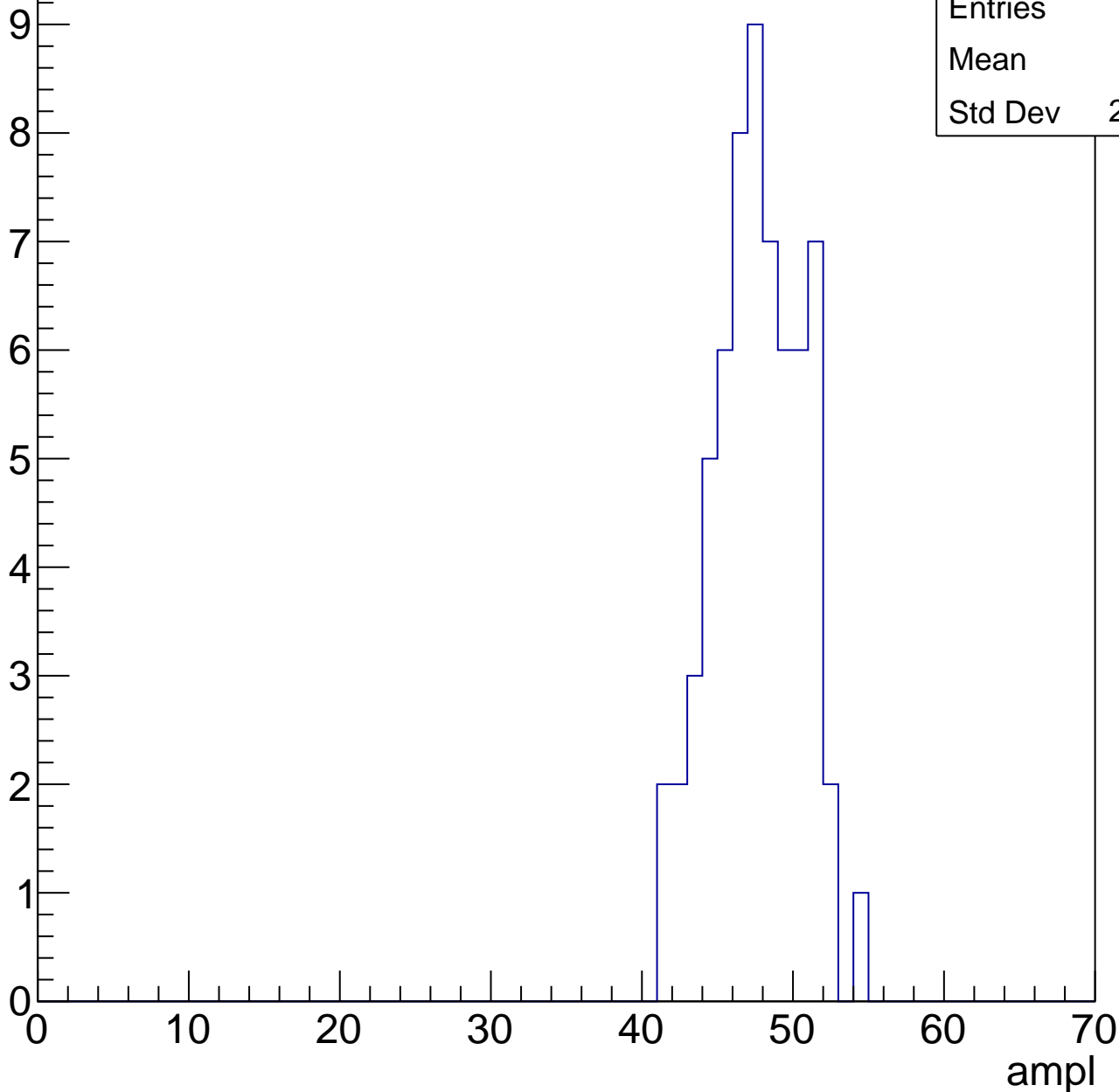


# B1L002S, U18-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	47.2
Std Dev	2.906

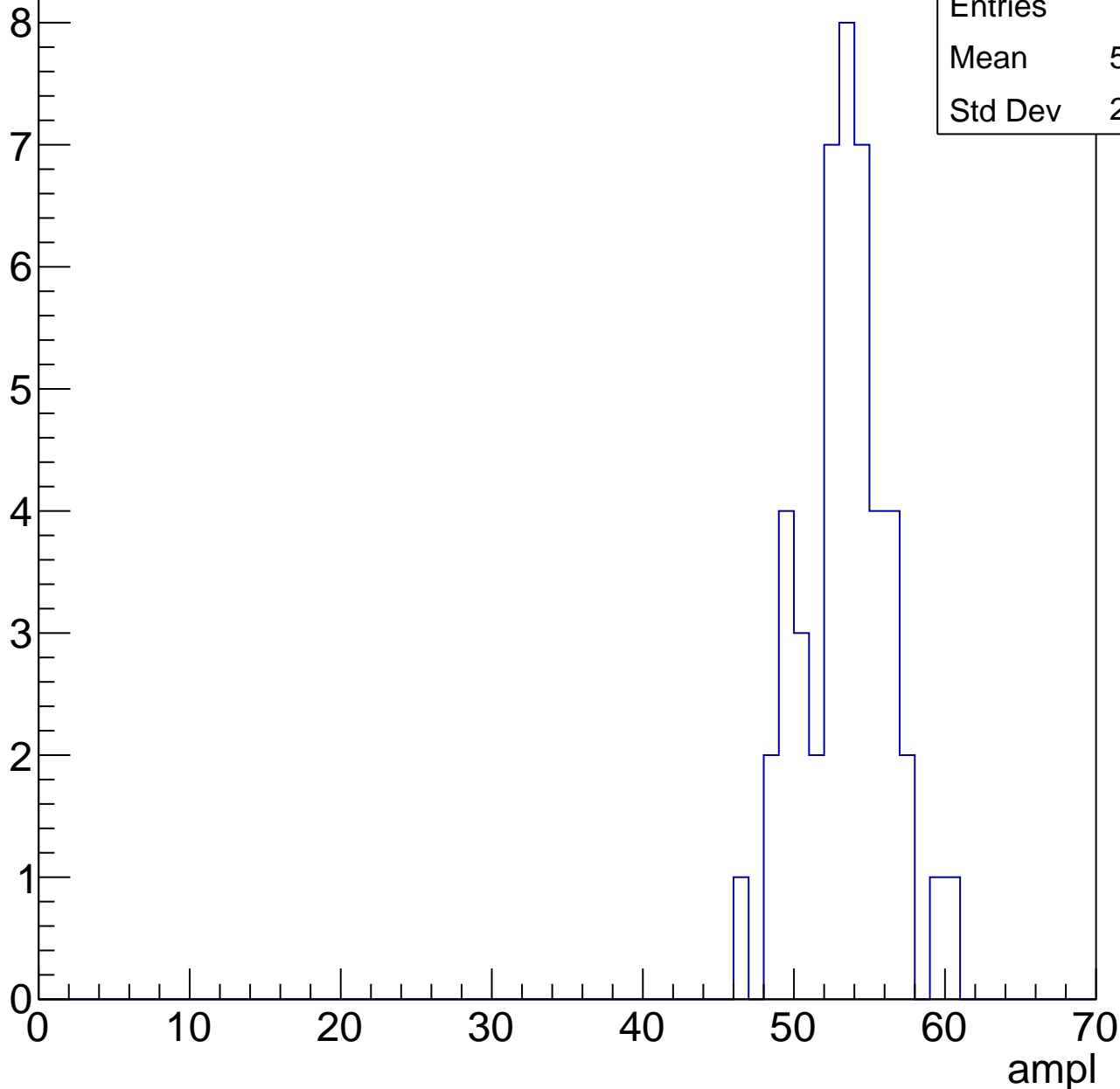


# B1L002S, U18-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

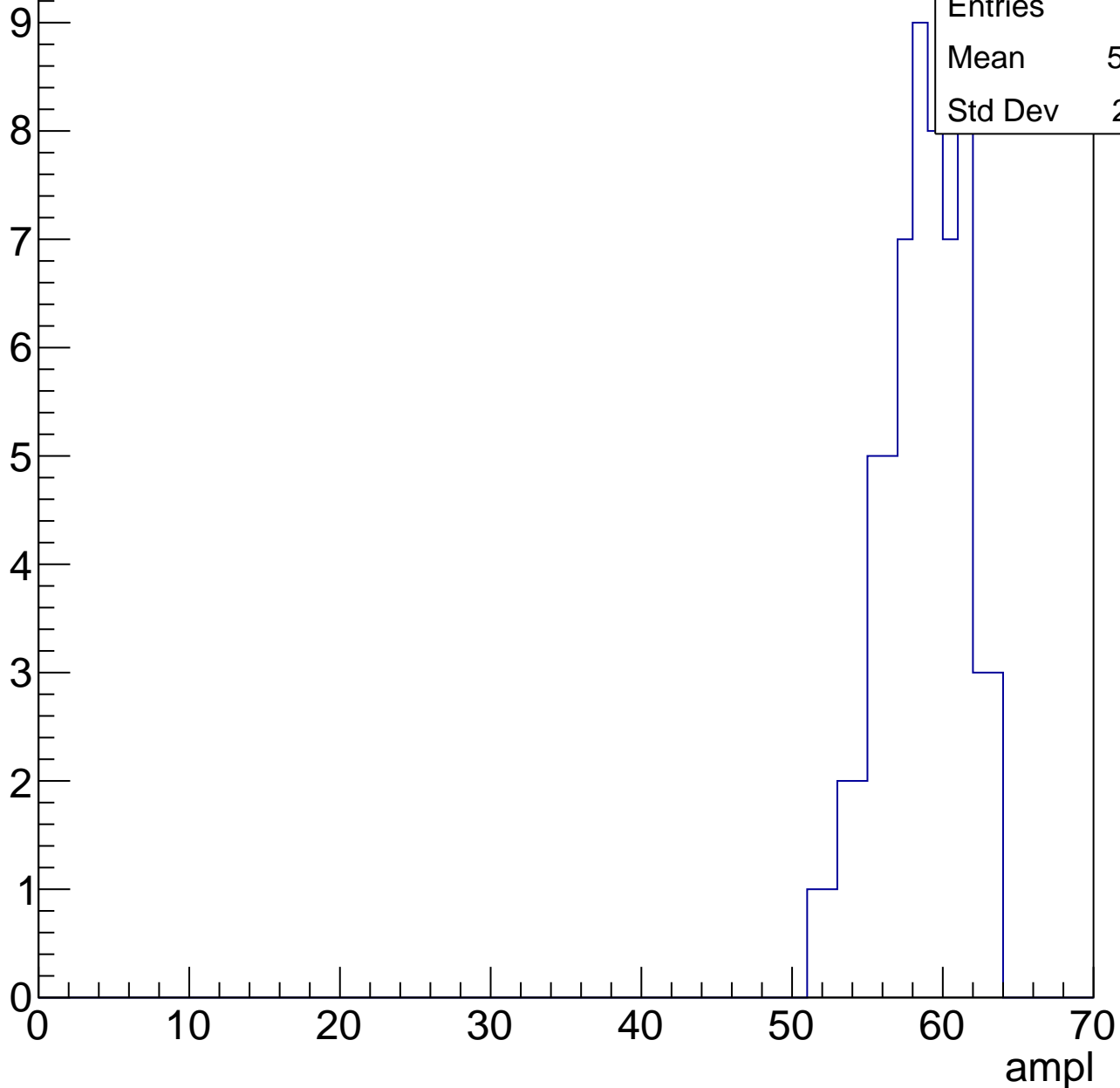
Entries	46
Mean	52.89
Std Dev	2.876



# B1L002S, U18-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



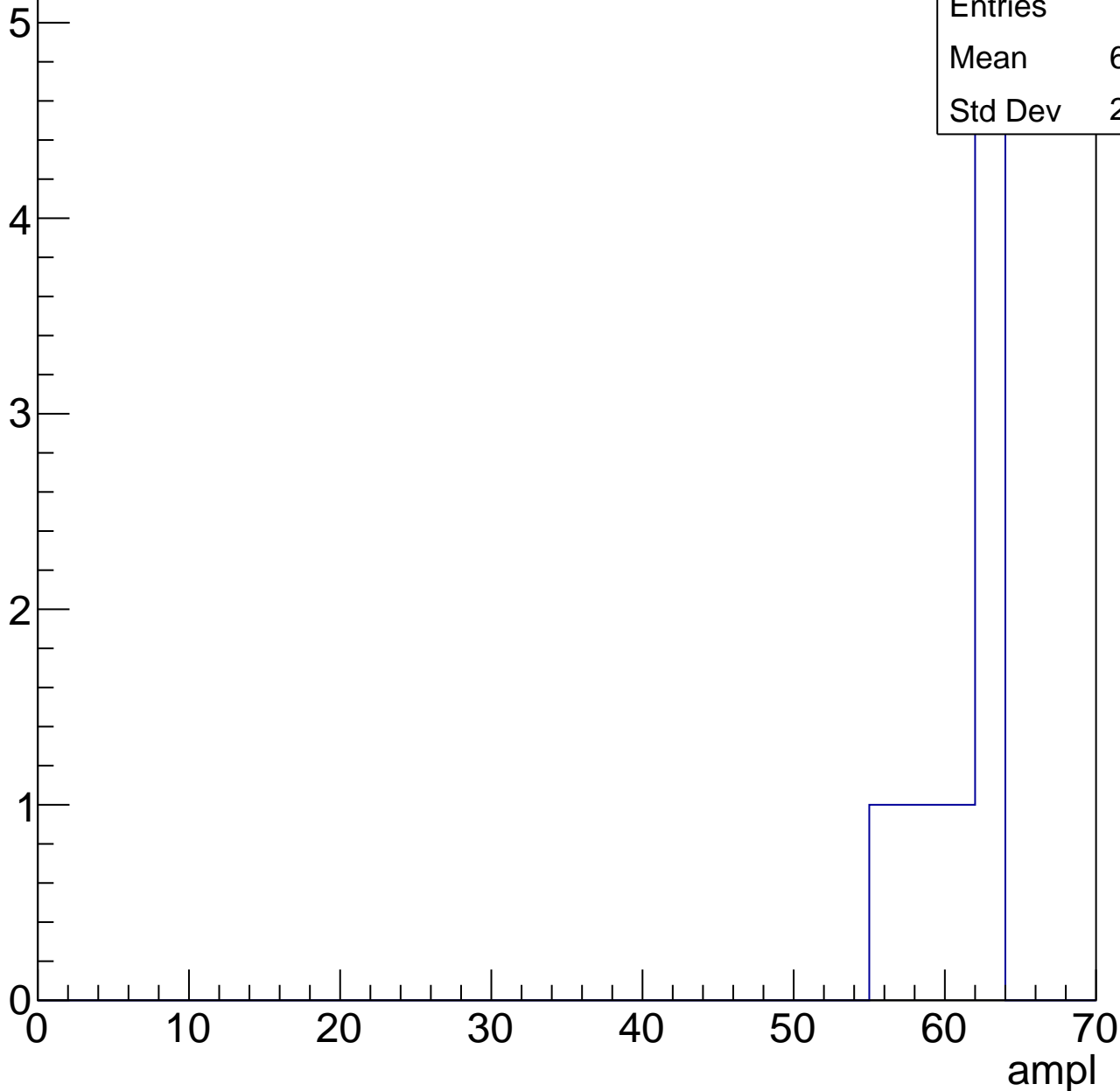
Entries	61
Mean	58.16
Std Dev	2.771

# B1L002S, U18-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	60.65
Std Dev	2.588

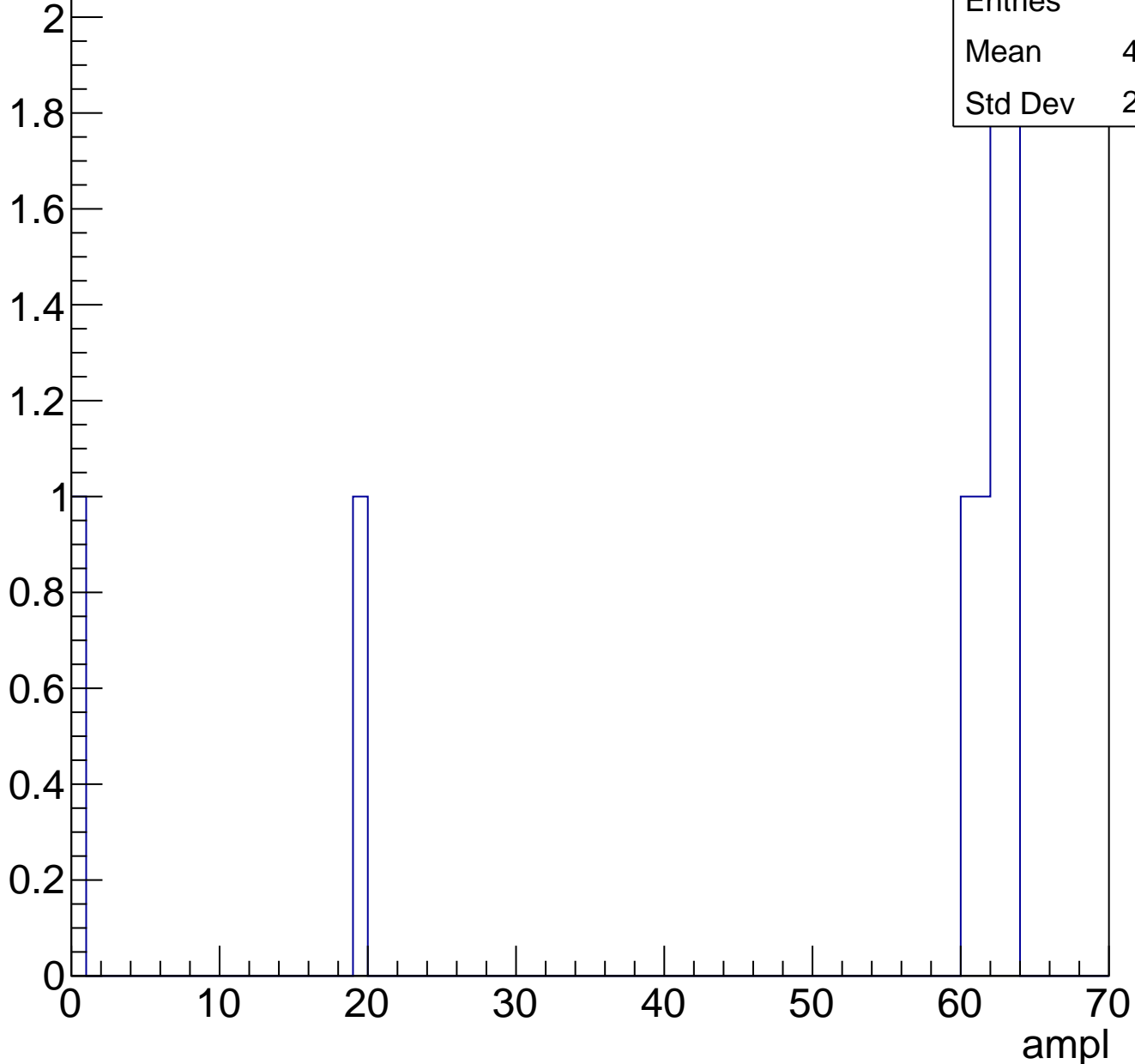




# B1L002S, U18-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	8
Mean	48.75
Std Dev	23.17

# B1L002S, U18-ch25, adc0

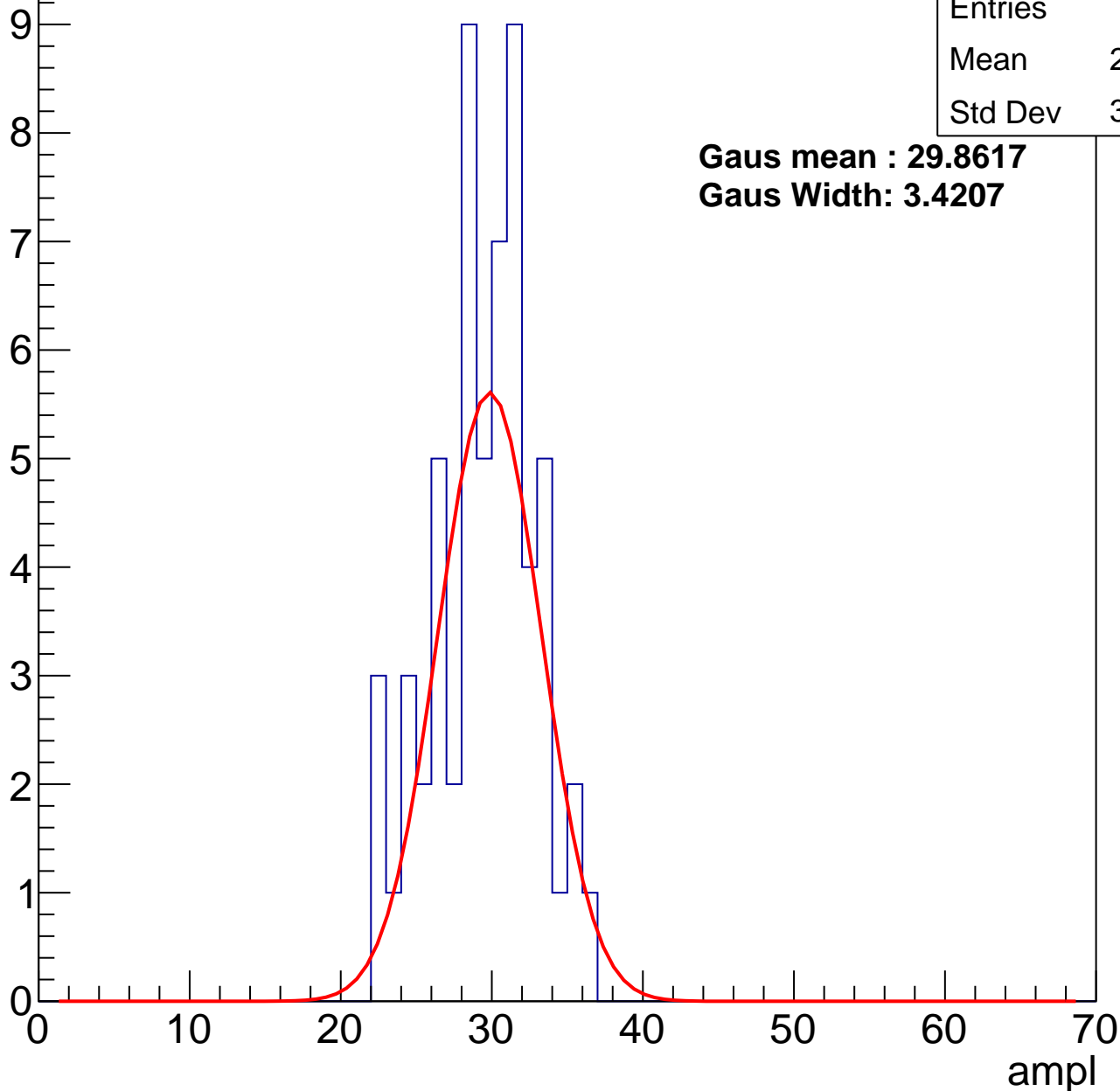
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	29.05
Std Dev	3.337

**Gaus mean : 29.8617**

**Gaus Width: 3.4207**



# B1L002S, U18-ch25, adc1

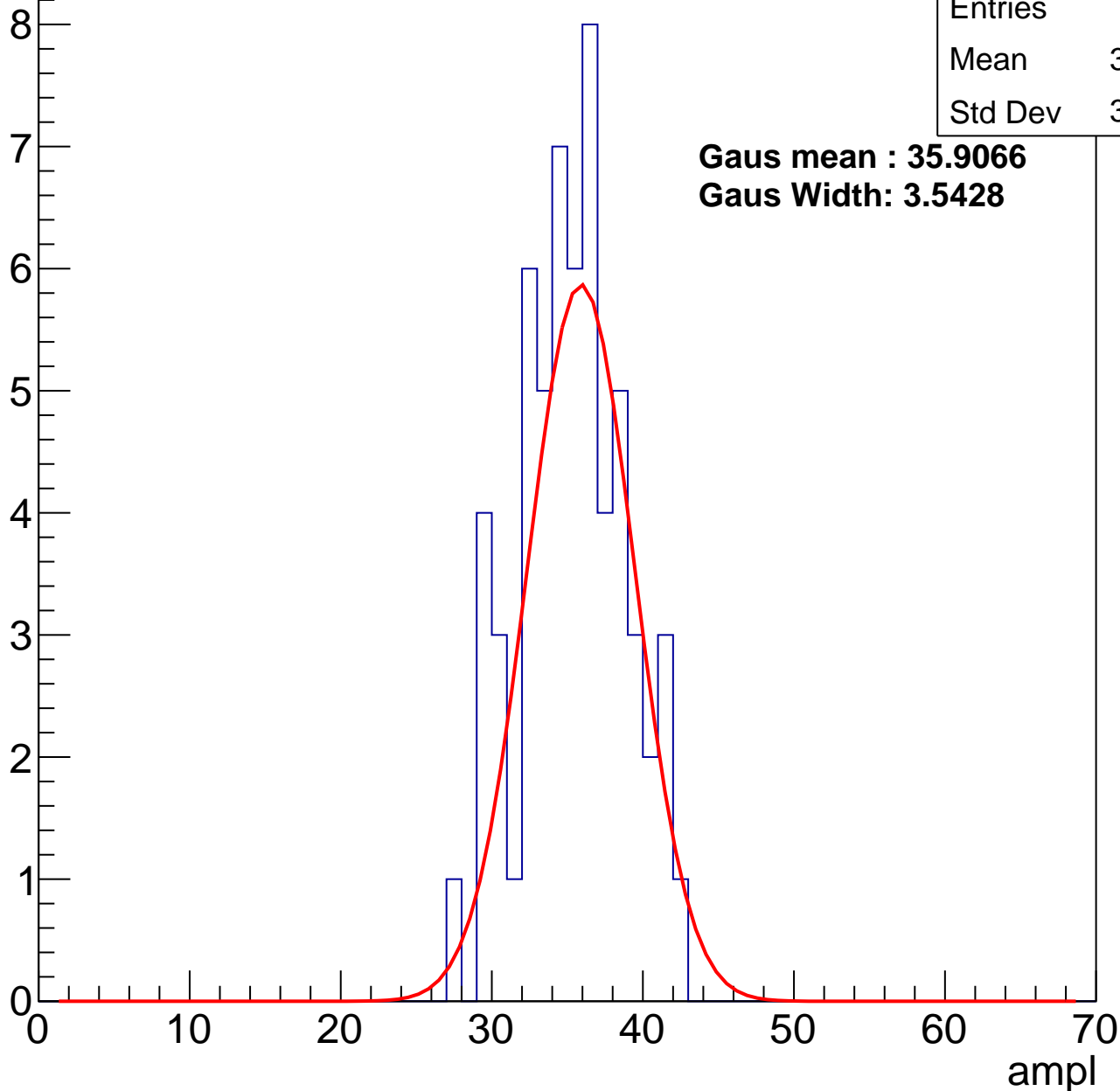
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.86
Std Dev	3.452

**Gaus mean : 35.9066**

**Gaus Width: 3.5428**



# B1L002S, U18-ch25, adc2

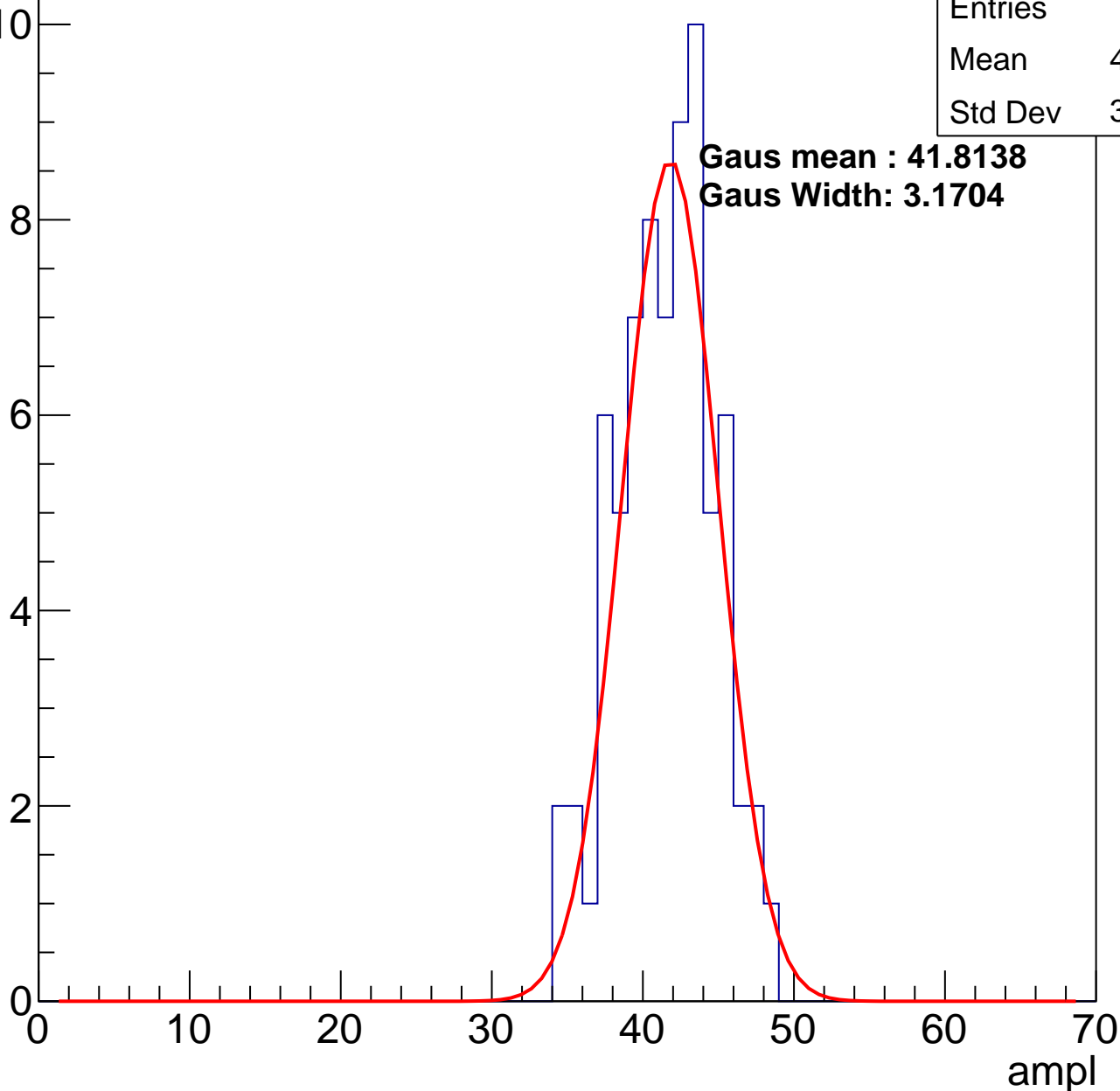
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	41.07
Std Dev	3.168

**Gaus mean : 41.8138**

**Gaus Width: 3.1704**



# B1L002S, U18-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

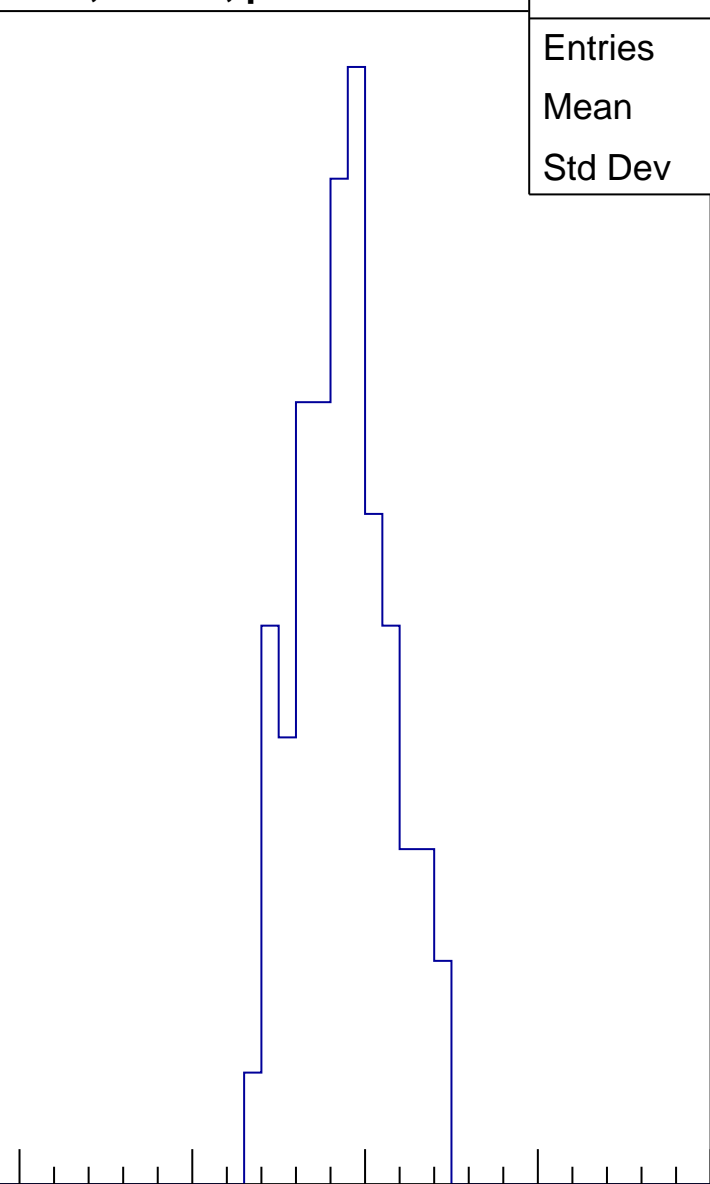
Entries	62
Mean	48.29
Std Dev	2.678

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

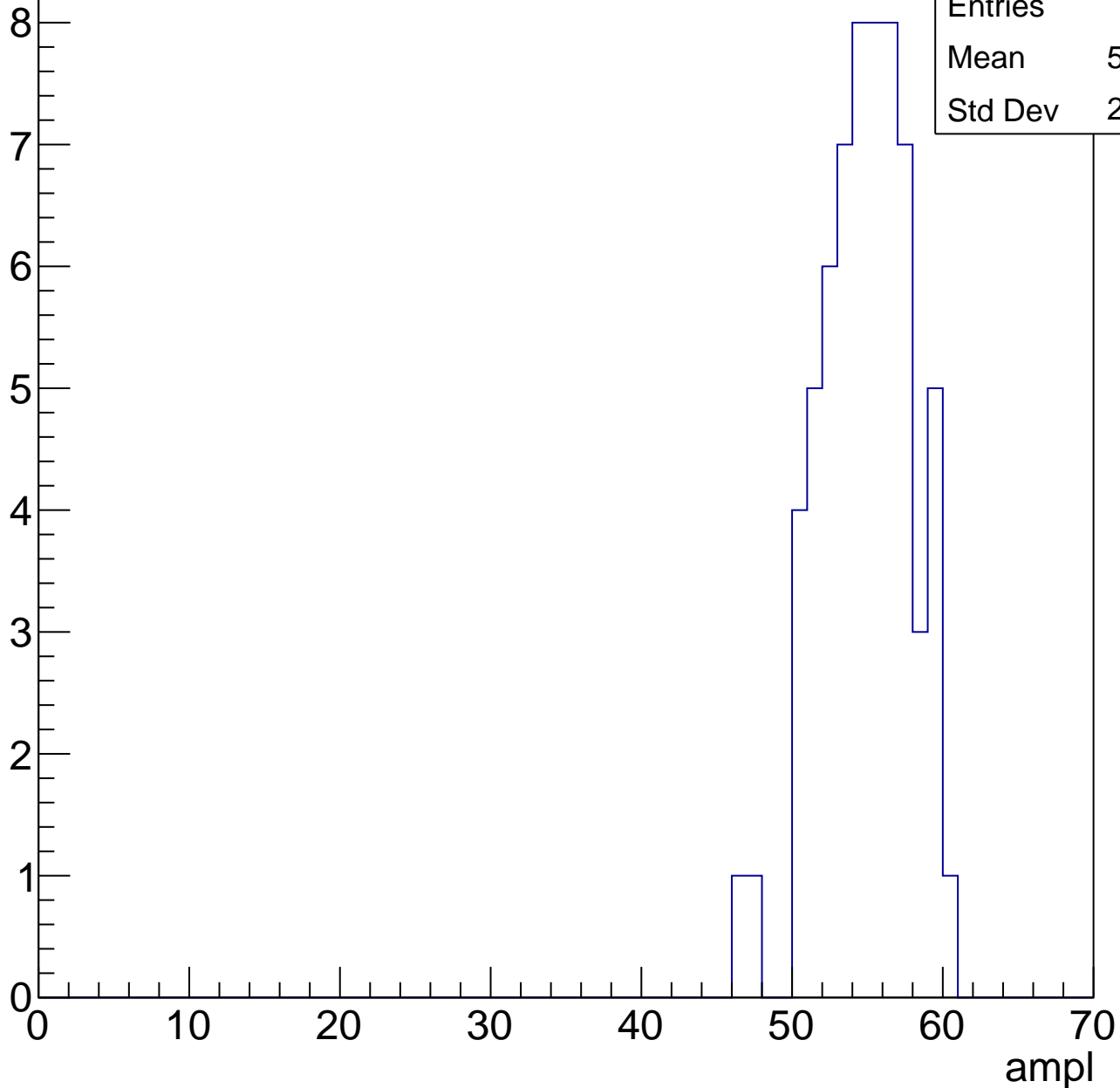


# B1L002S, U18-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	54.36
Std Dev	2.949

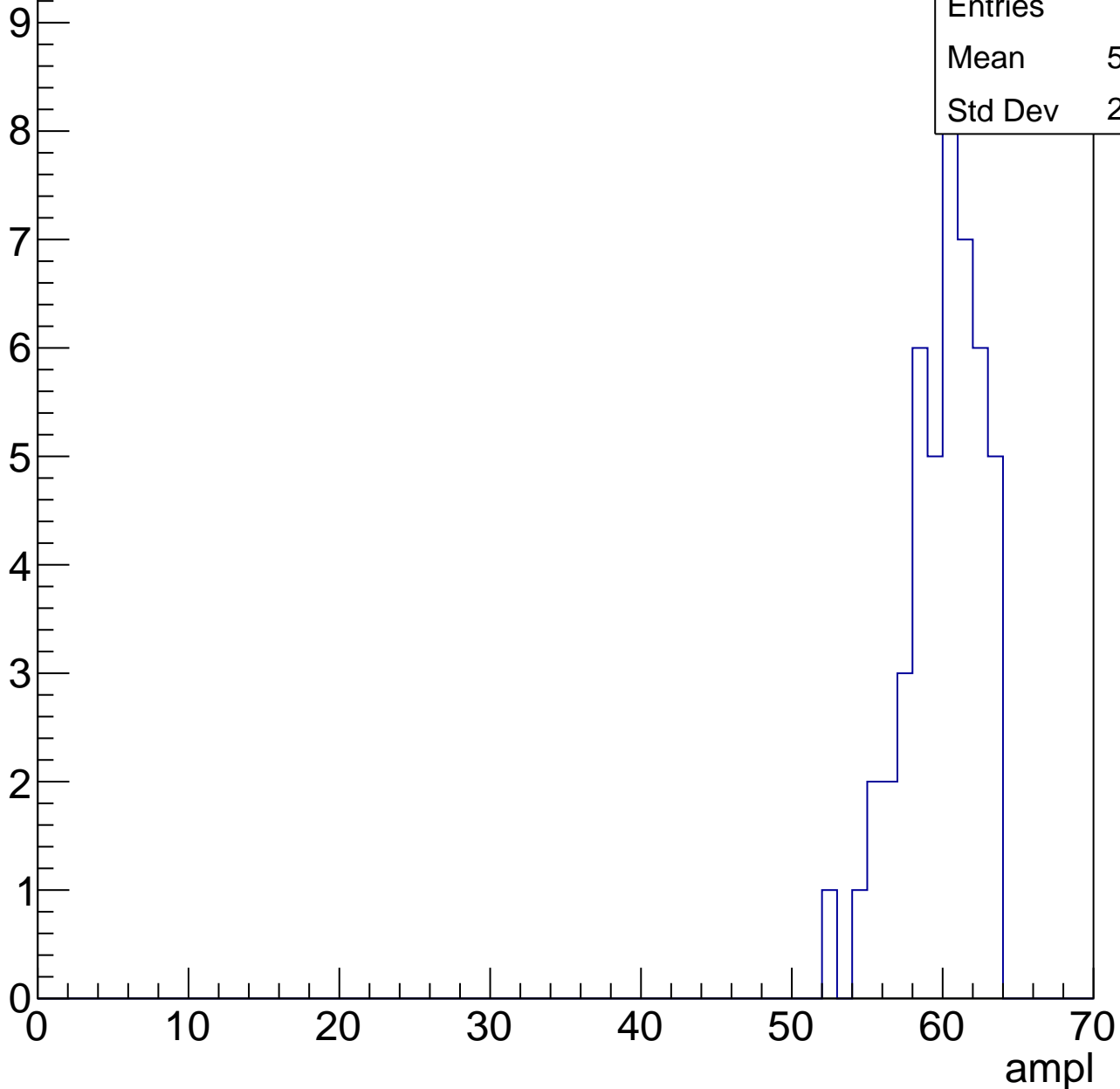


# B1L002S, U18-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	59.49
Std Dev	2.534

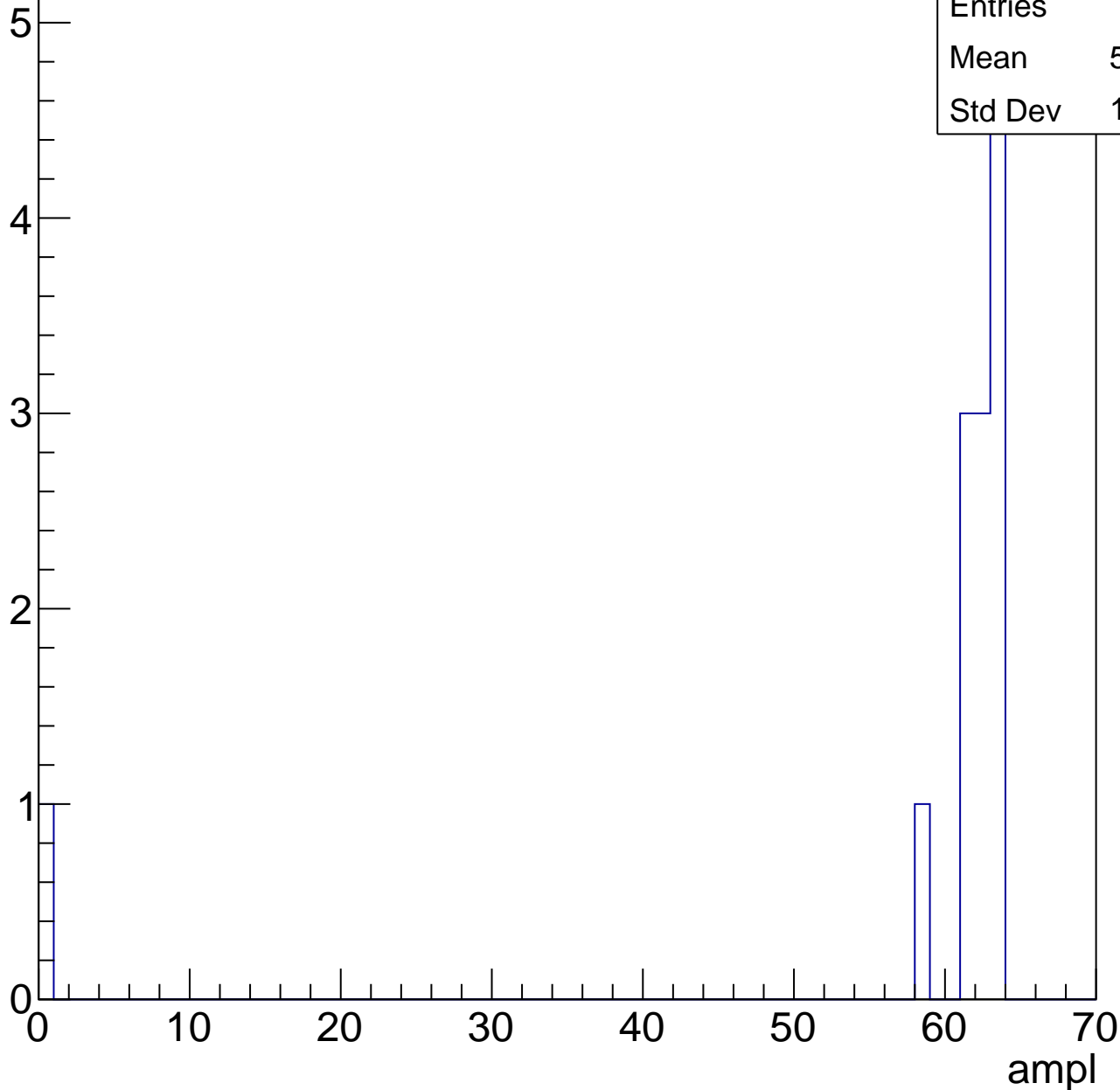


# B1L002S, U18-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	57.08
Std Dev	16.53

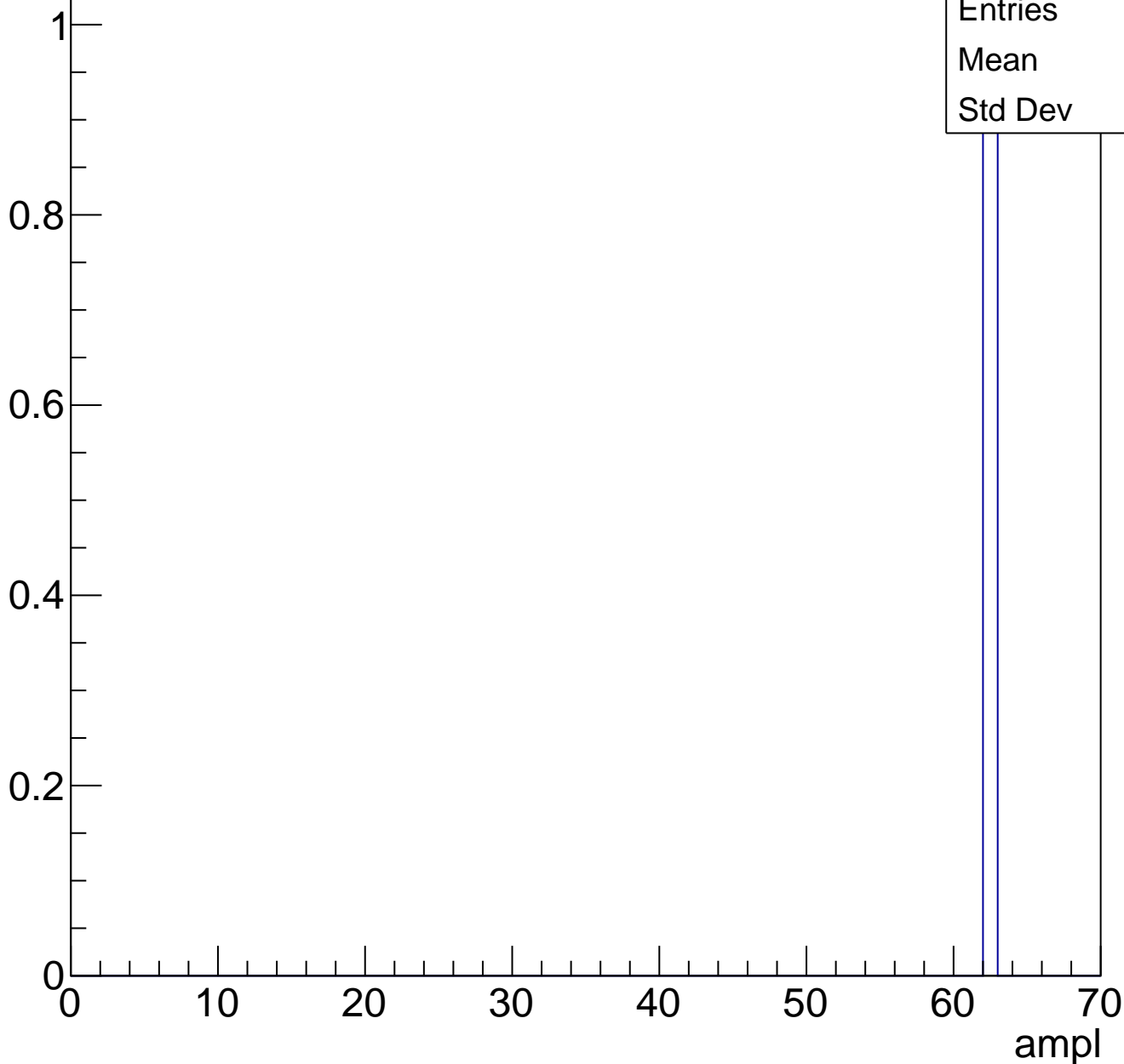




# B1L002S, U18-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch26, adc0

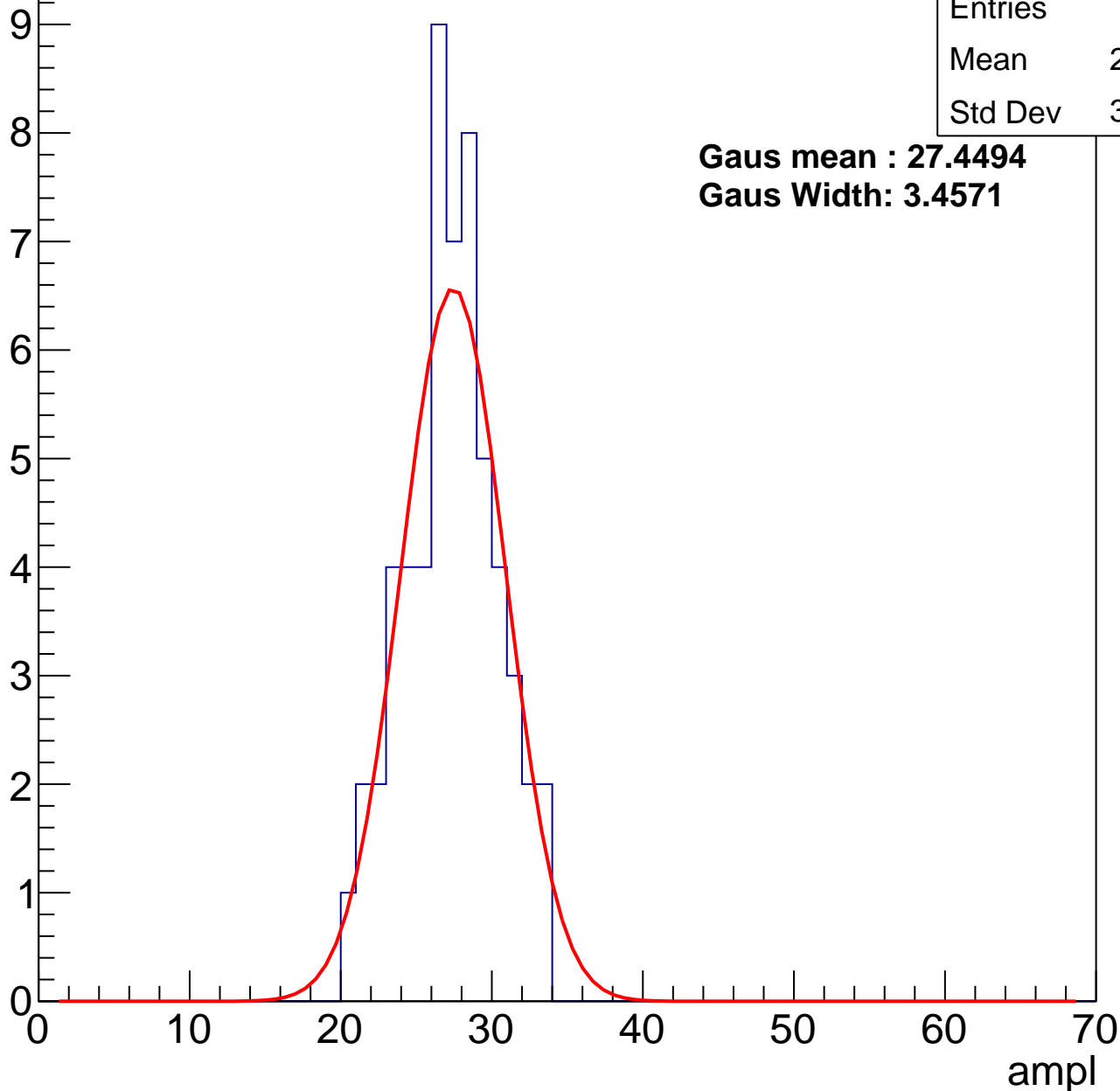
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	26.82
Std Dev	3.044

**Gaus mean : 27.4494**

**Gaus Width: 3.4571**



# B1L002S, U18-ch26, adc1

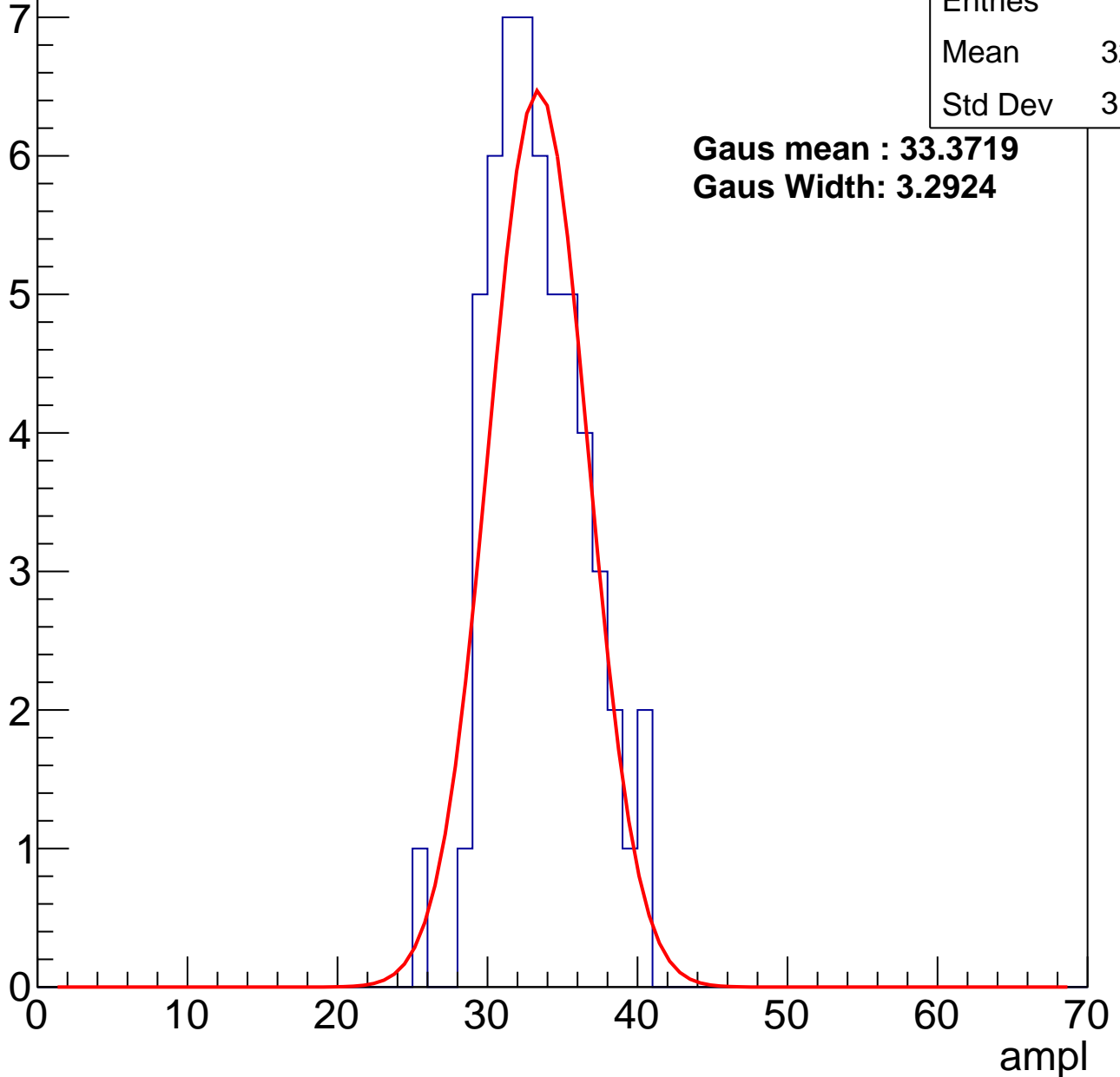
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	32.95
Std Dev	3.165

**Gaus mean : 33.3719**

**Gaus Width: 3.2924**



# B1L002S, U18-ch26, adc2

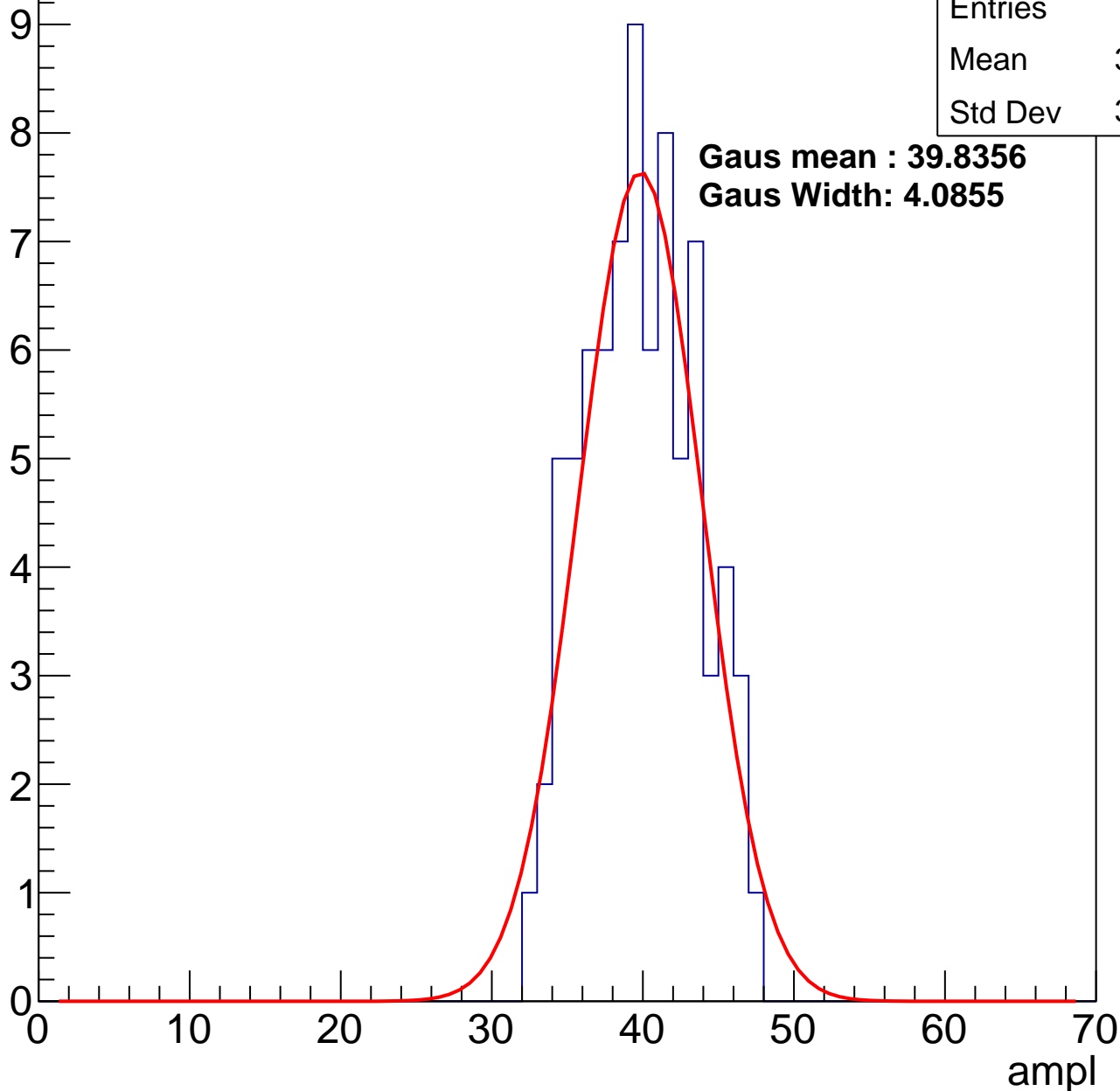
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	39.41
Std Dev	3.621

**Gaus mean : 39.8356**

**Gaus Width: 4.0855**

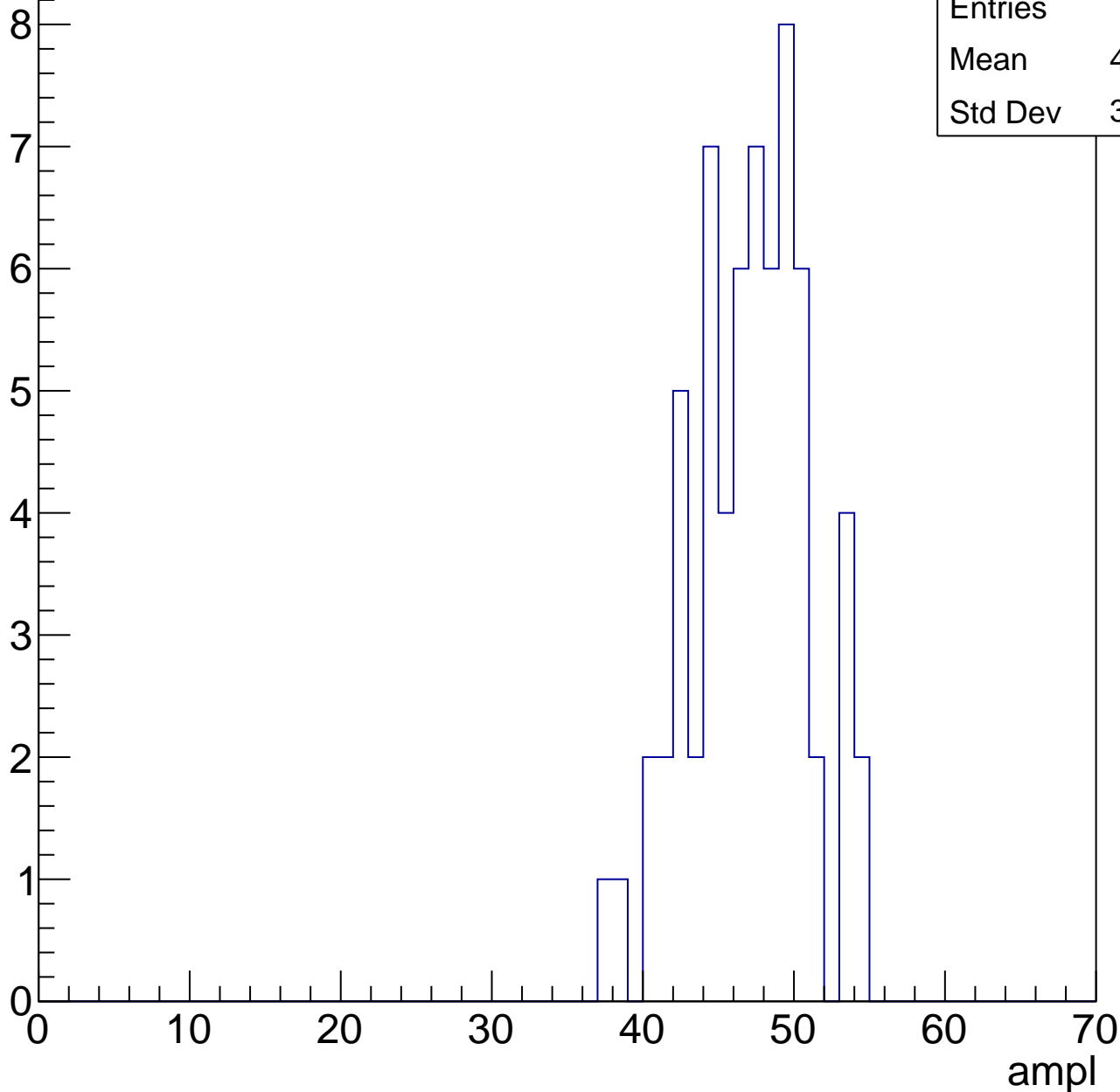


# B1L002S, U18-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.58
Std Dev	3.819

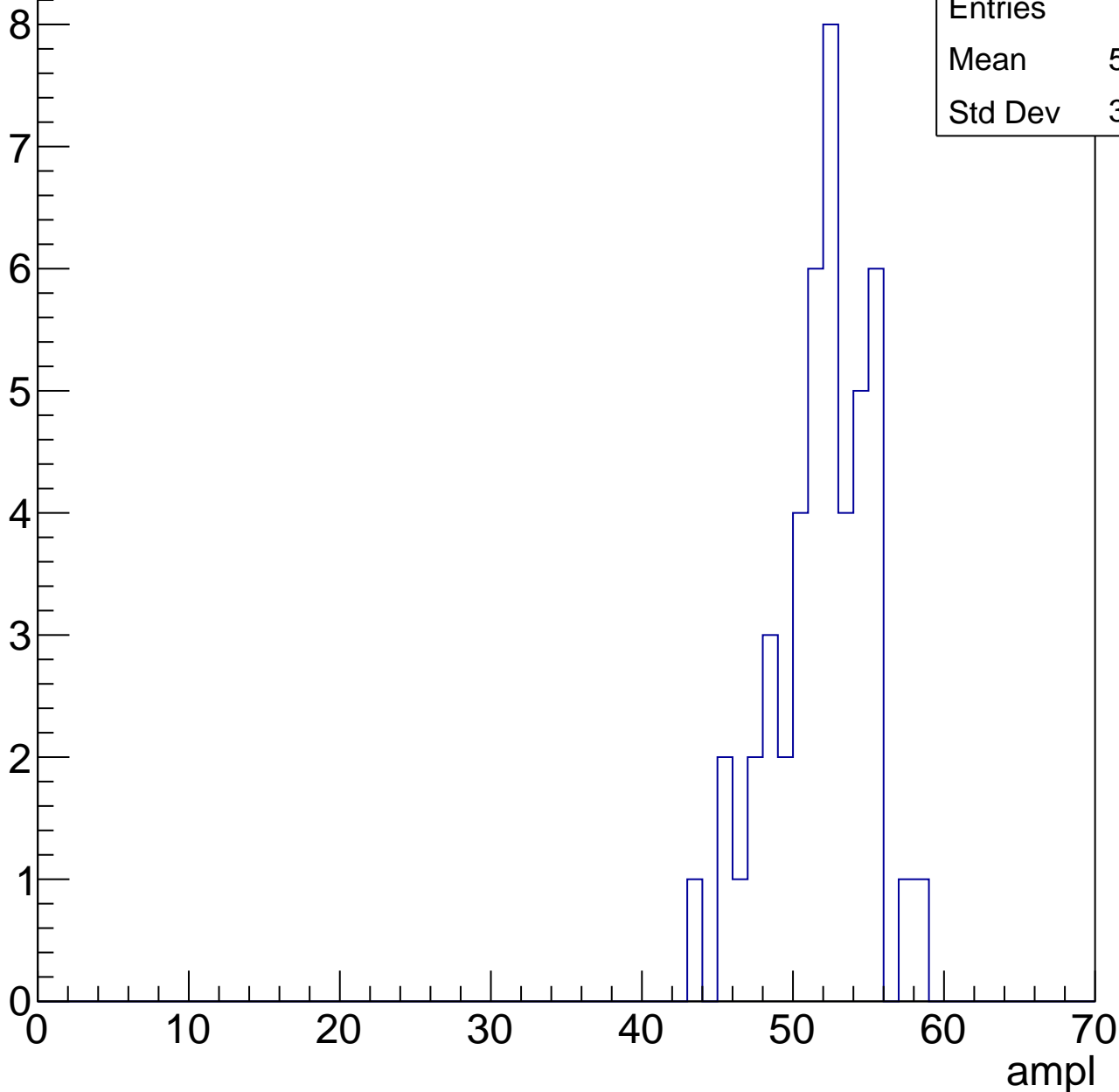


# B1L002S, U18-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	51.39
Std Dev	3.213



# B1L002S, U18-ch26, adc5

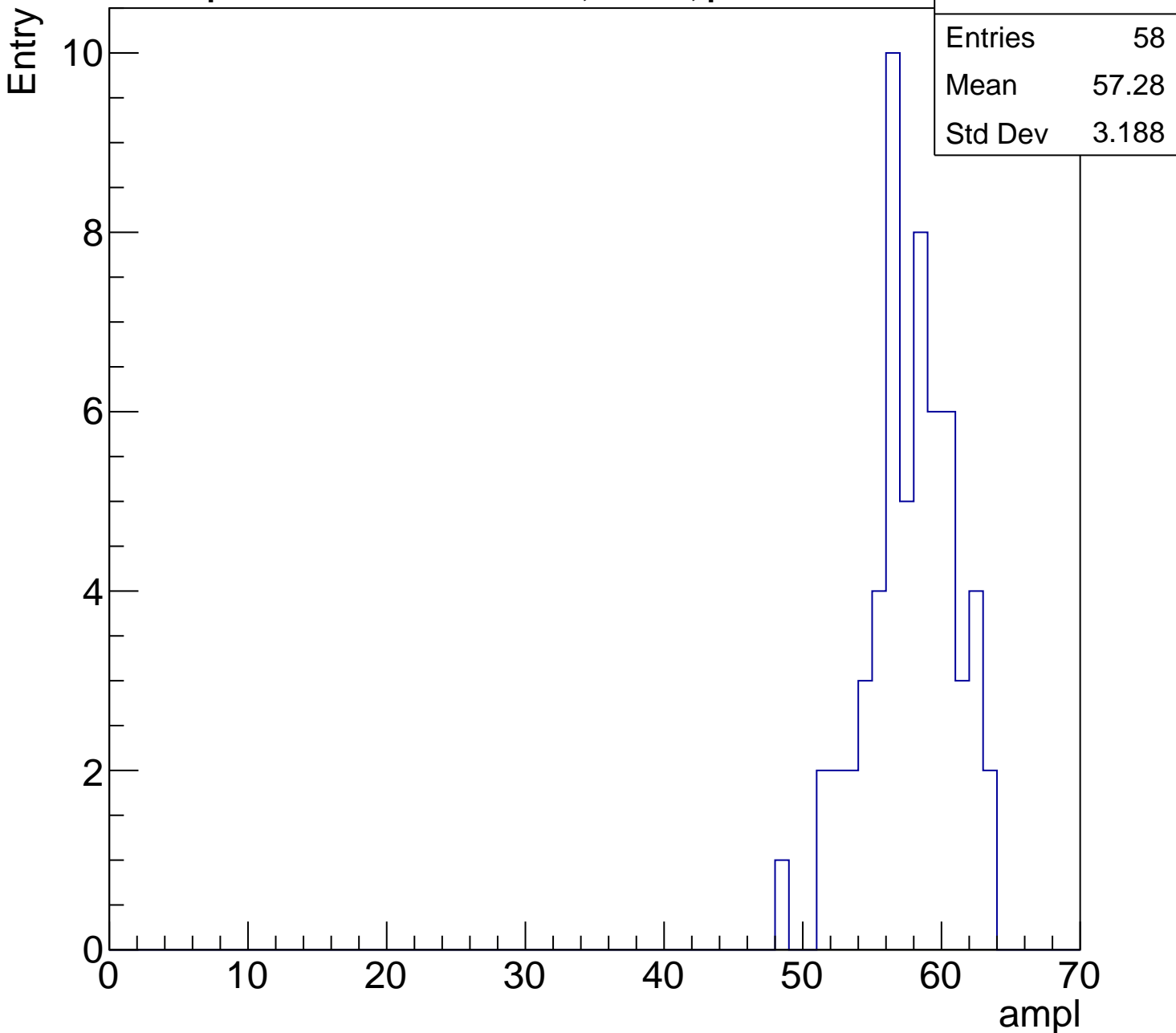
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	57.28
Std Dev	3.188

Entry

10  
8  
6  
4  
2  
0

ampl

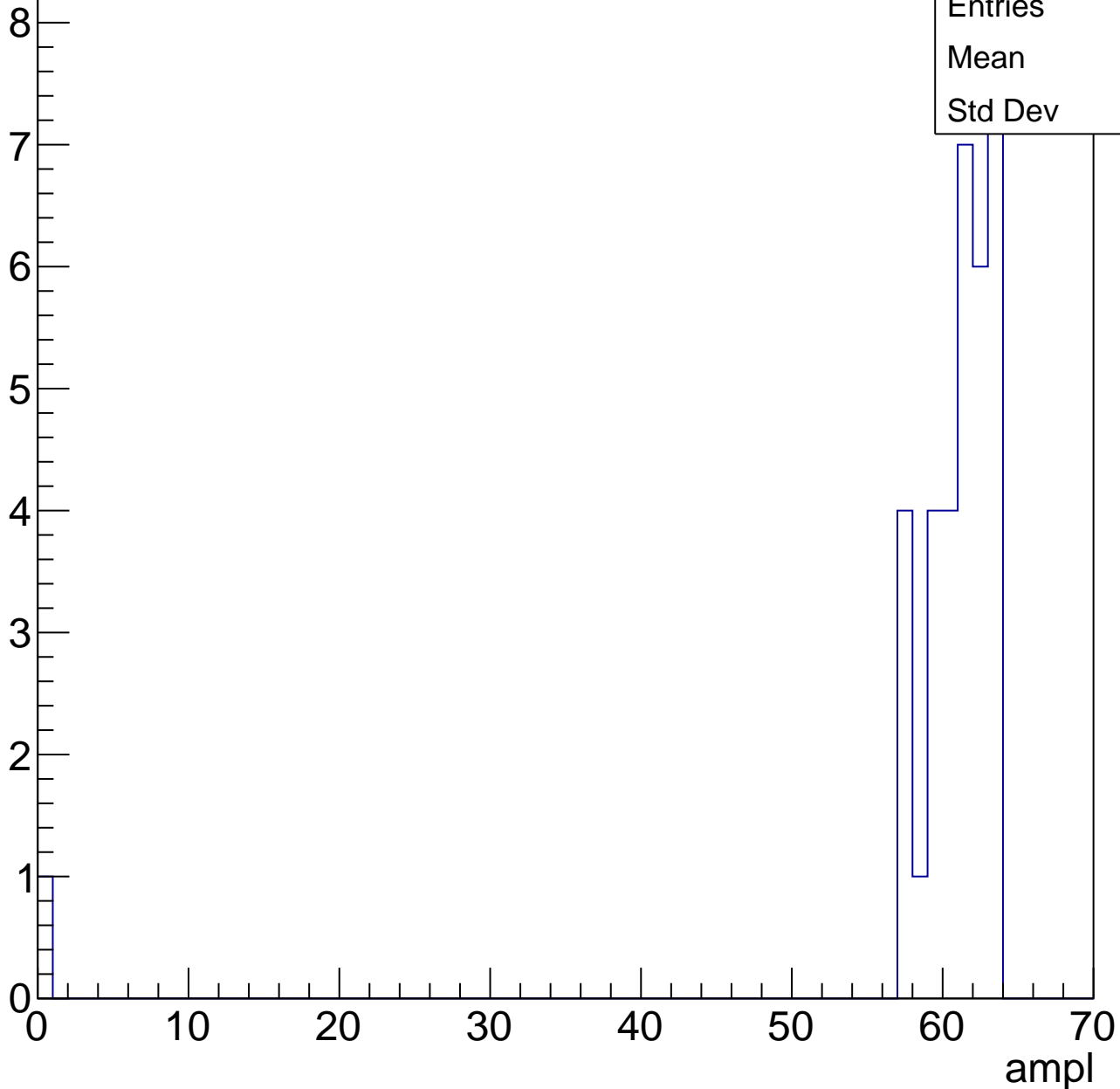


# B1L002S, U18-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	59
Std Dev	10.3





# B1L002S, U18-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	28.07
Std Dev	6.437

**Gaus mean : 30.0117**

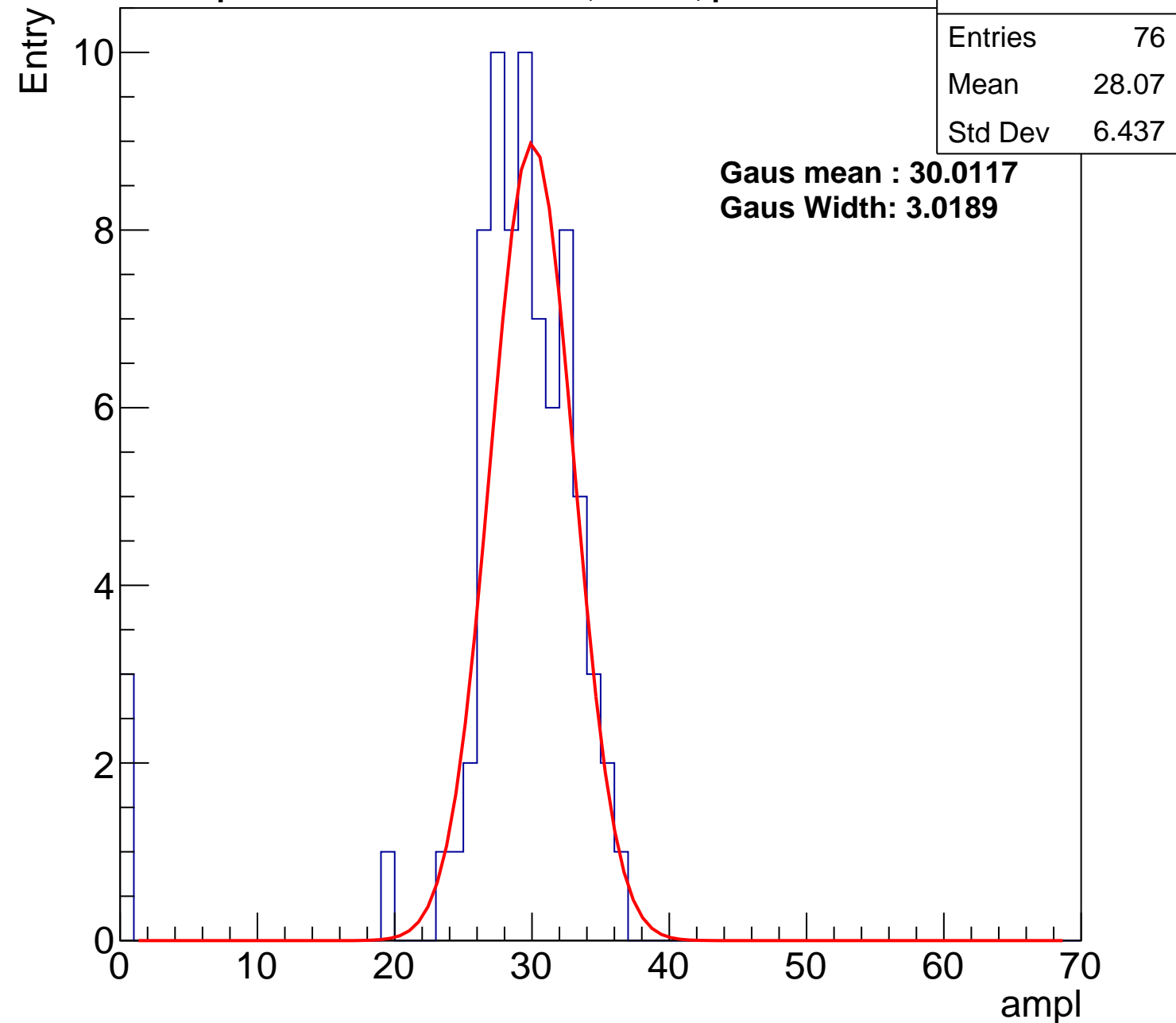
**Gaus Width: 3.0189**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch27, adc1

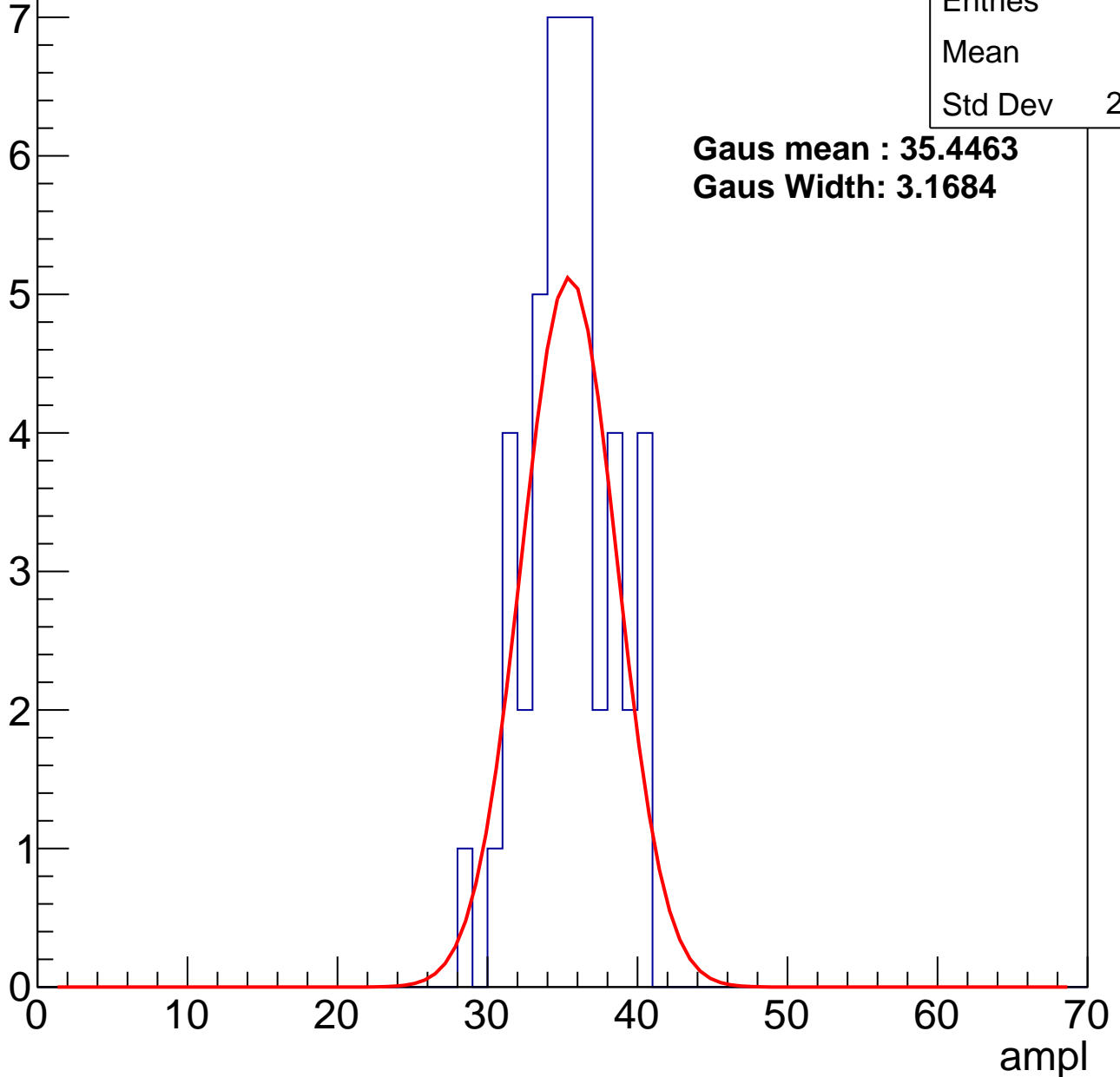
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	35
Std Dev	2.821

**Gaus mean : 35.4463**

**Gaus Width: 3.1684**



# B1L002S, U18-ch27, adc2

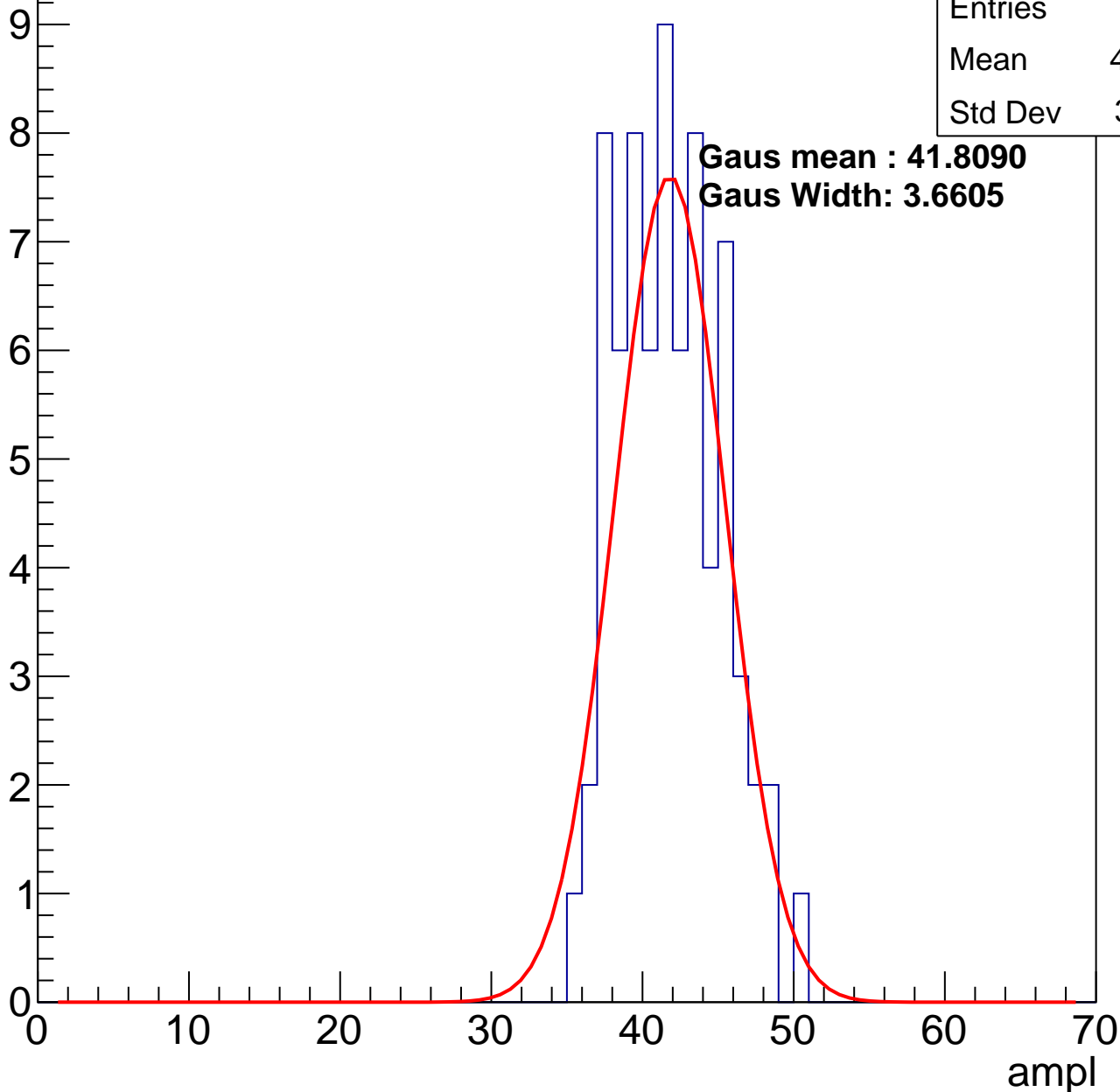
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	41.33
Std Dev	3.331

**Gaus mean : 41.8090**

**Gaus Width: 3.6605**

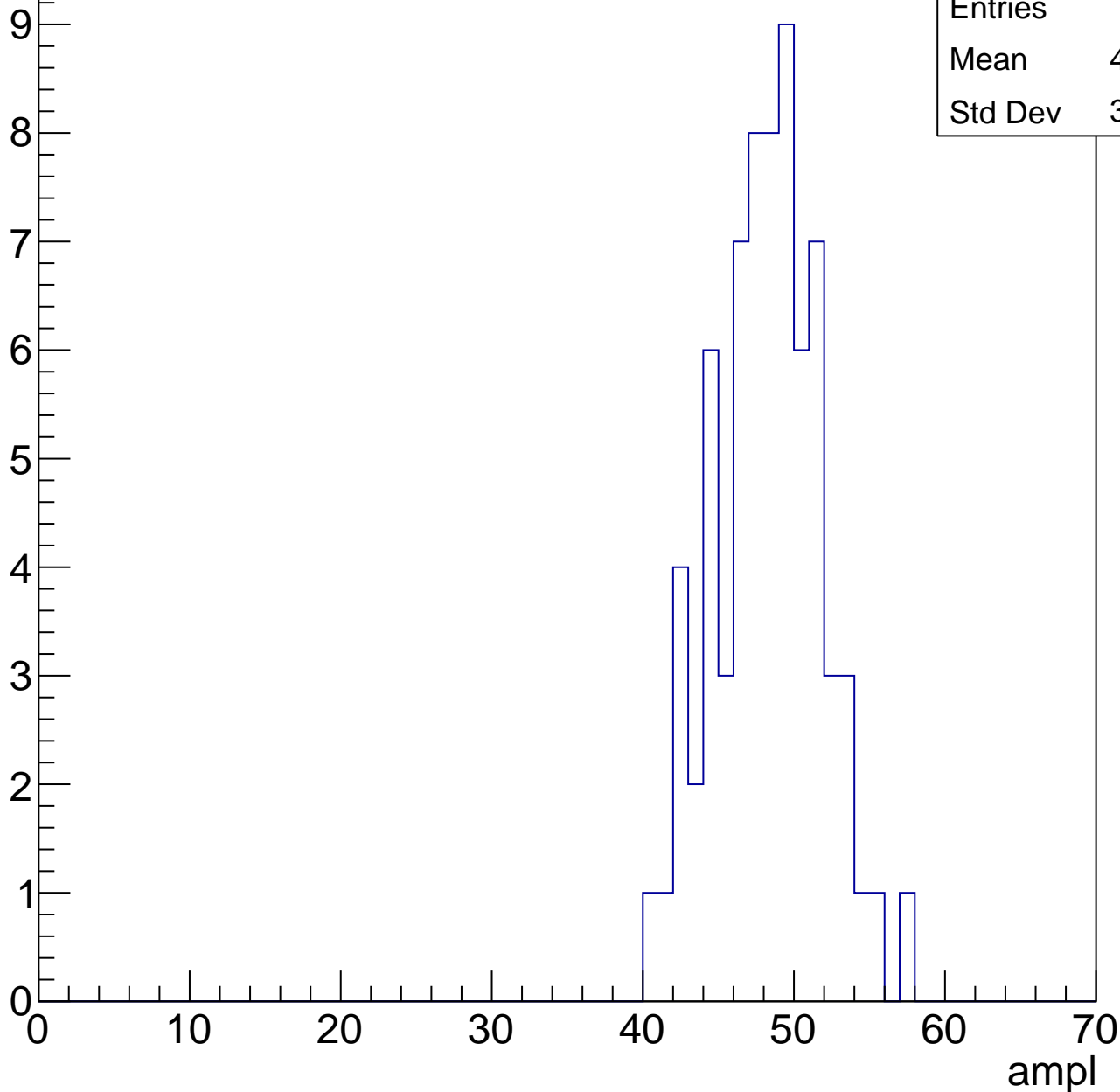


# B1L002S, U18-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

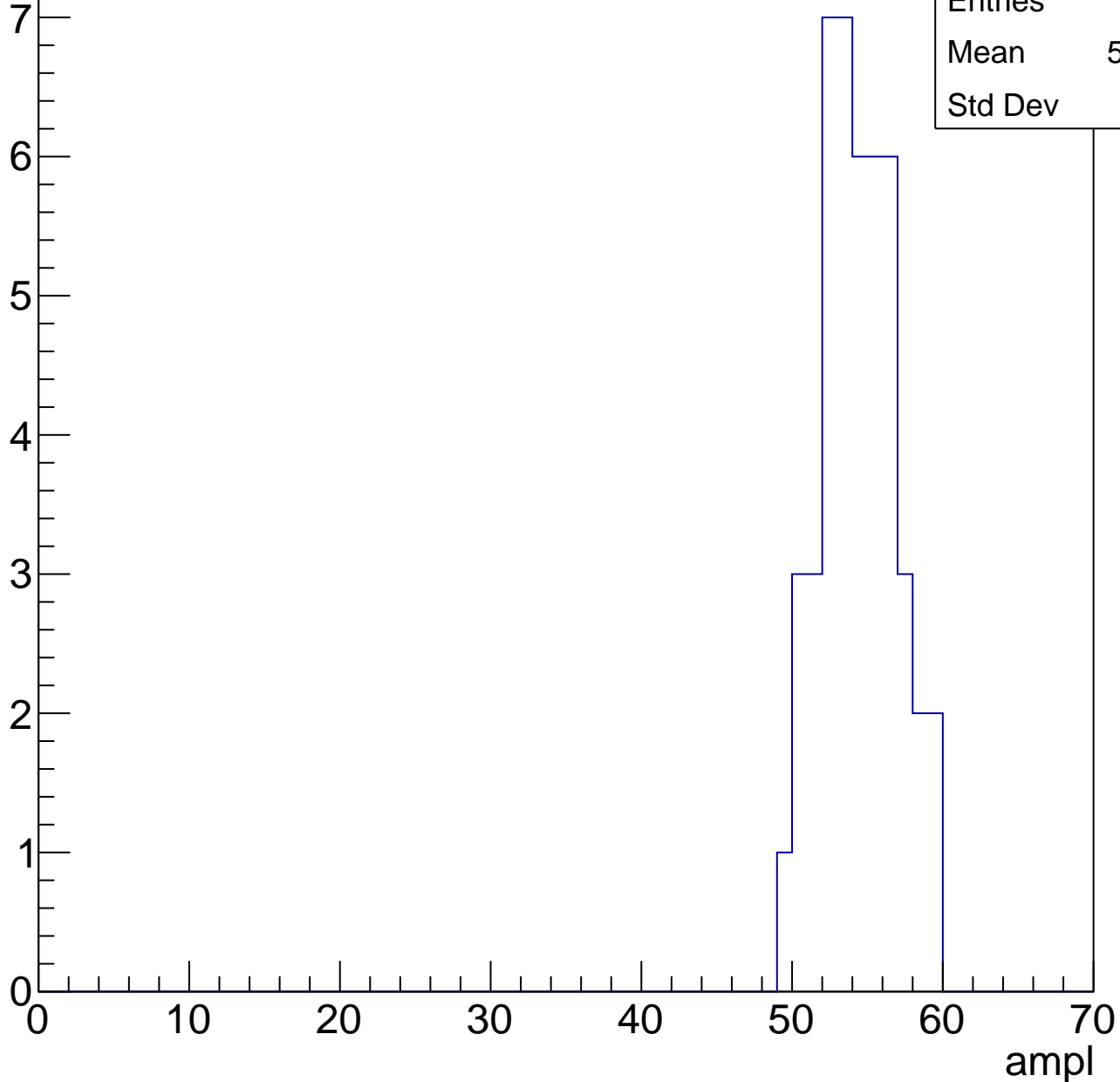
Entries	71
Mean	47.82
Std Dev	3.457



# B1L002S, U18-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



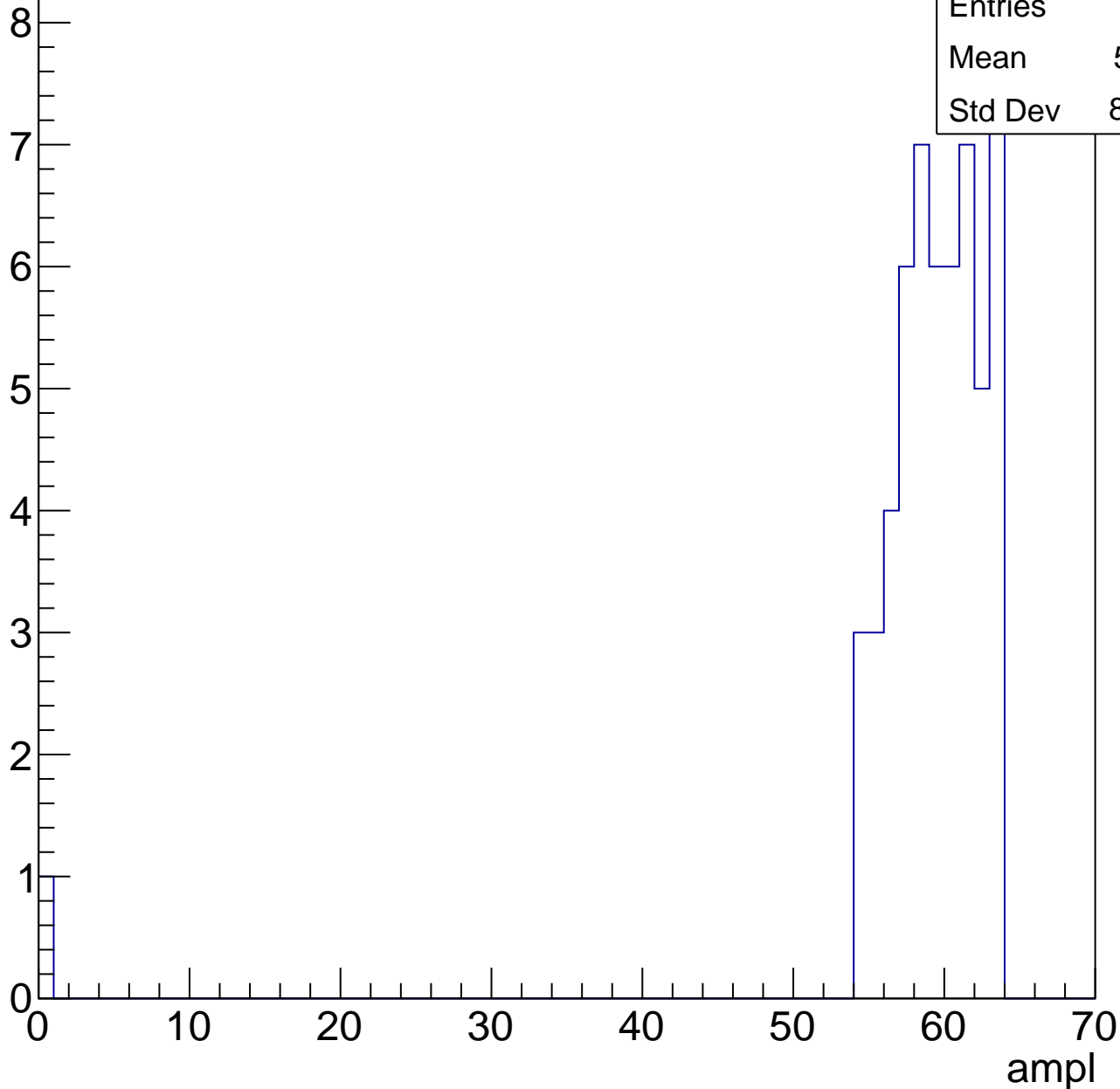
Entries	46
Mean	53.96
Std Dev	2.44

# B1L002S, U18-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	58.11
Std Dev	8.274

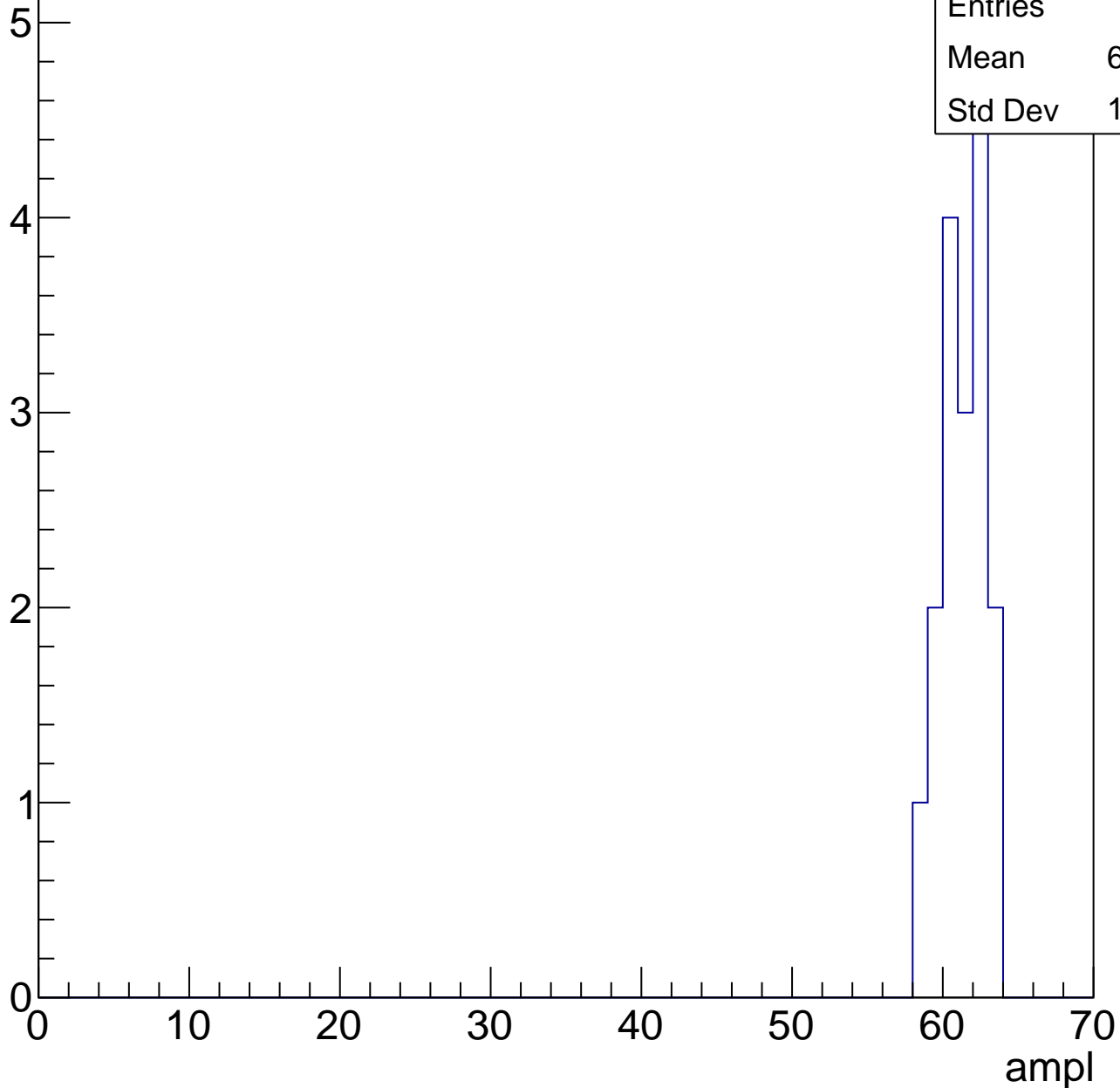


# B1L002S, U18-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	60.88
Std Dev	1.409





# B1L002S, U18-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U18-ch28, adc0

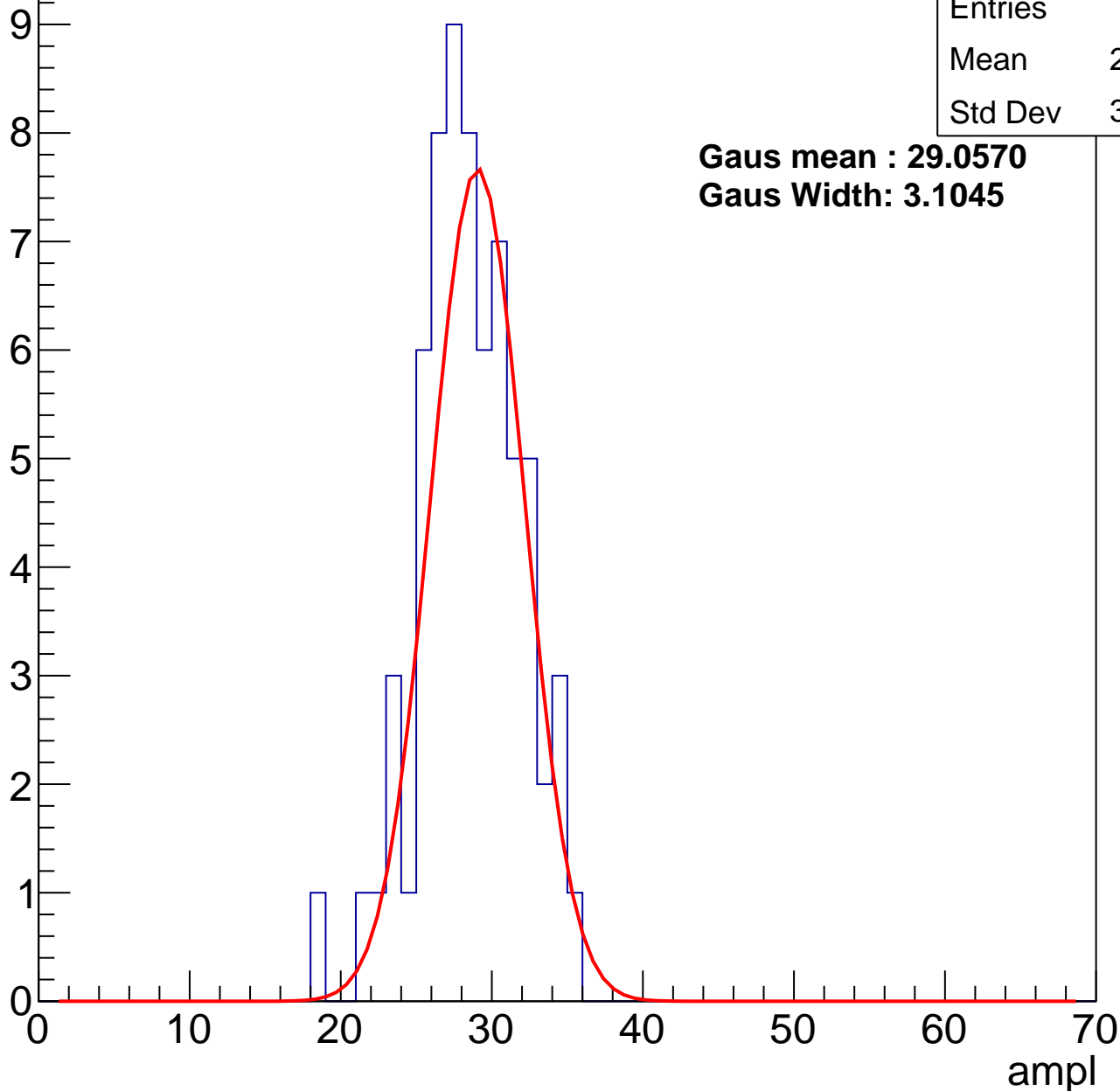
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.07
Std Dev	3.316

**Gaus mean : 29.0570**

**Gaus Width: 3.1045**



# B1L002S, U18-ch28, adc1

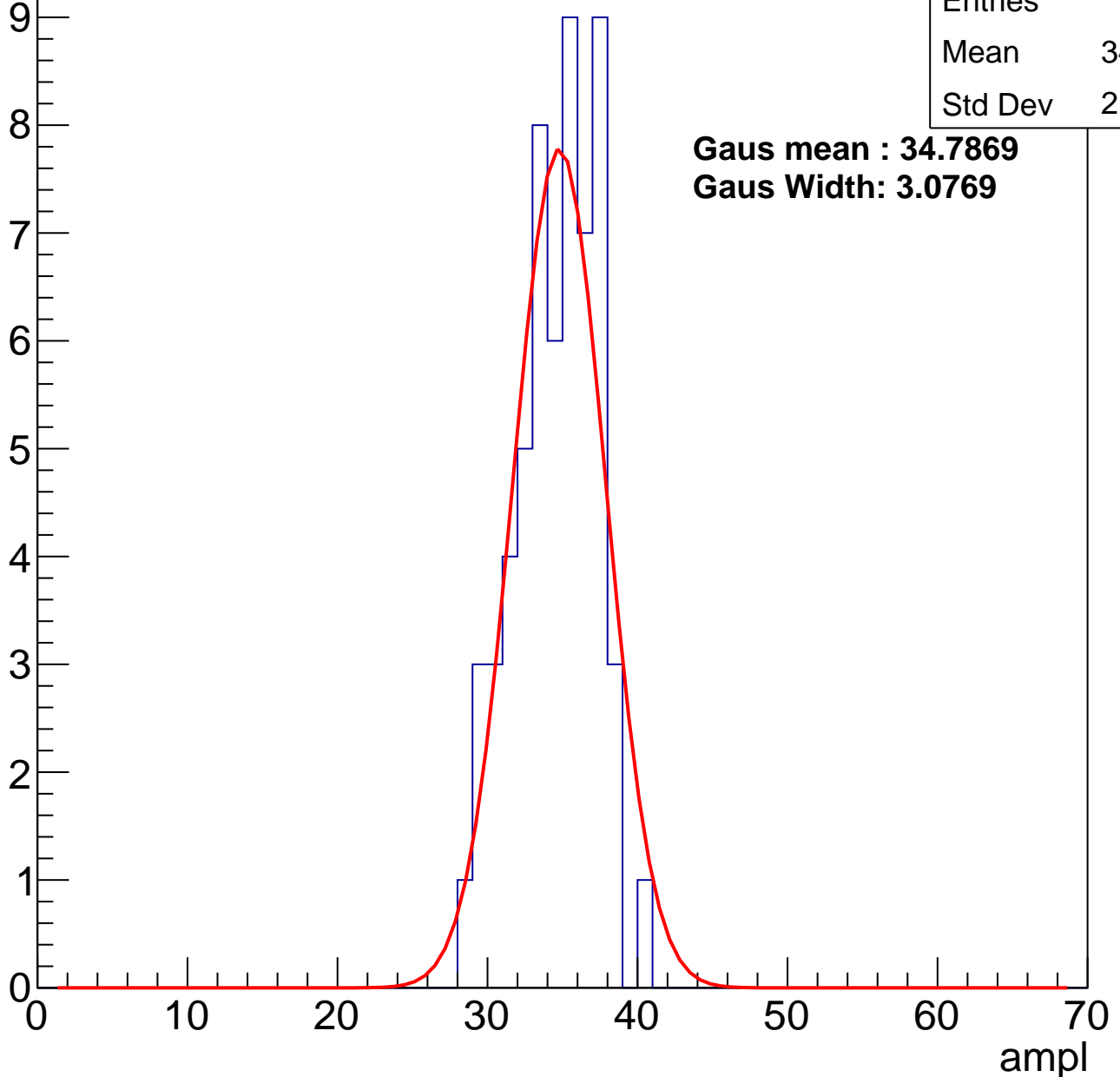
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.08
Std Dev	2.683

**Gaus mean : 34.7869**

**Gaus Width: 3.0769**



# B1L002S, U18-ch28, adc2

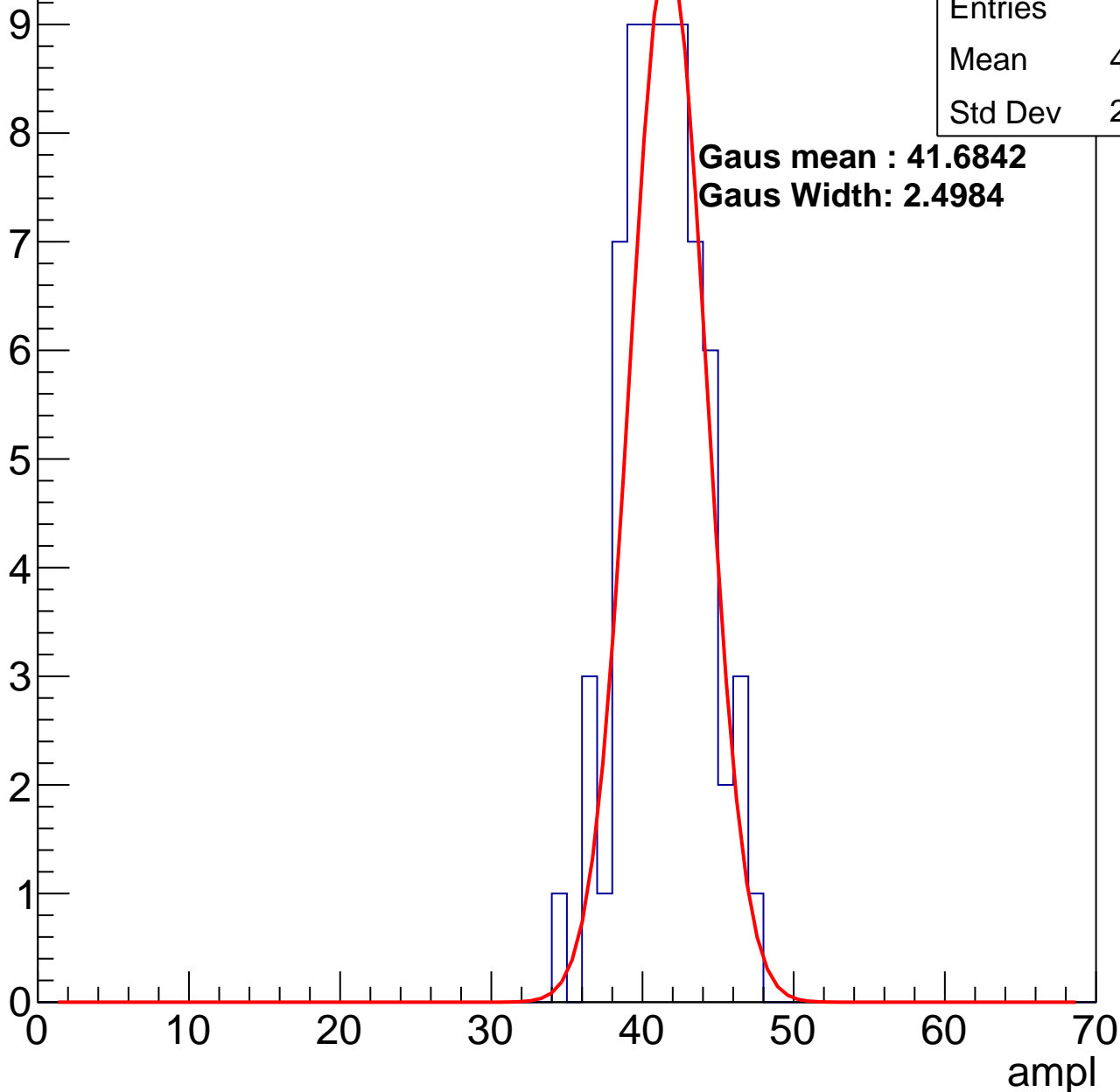
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.94
Std Dev	2.682

**Gaus mean : 41.6842**

**Gaus Width: 2.4984**

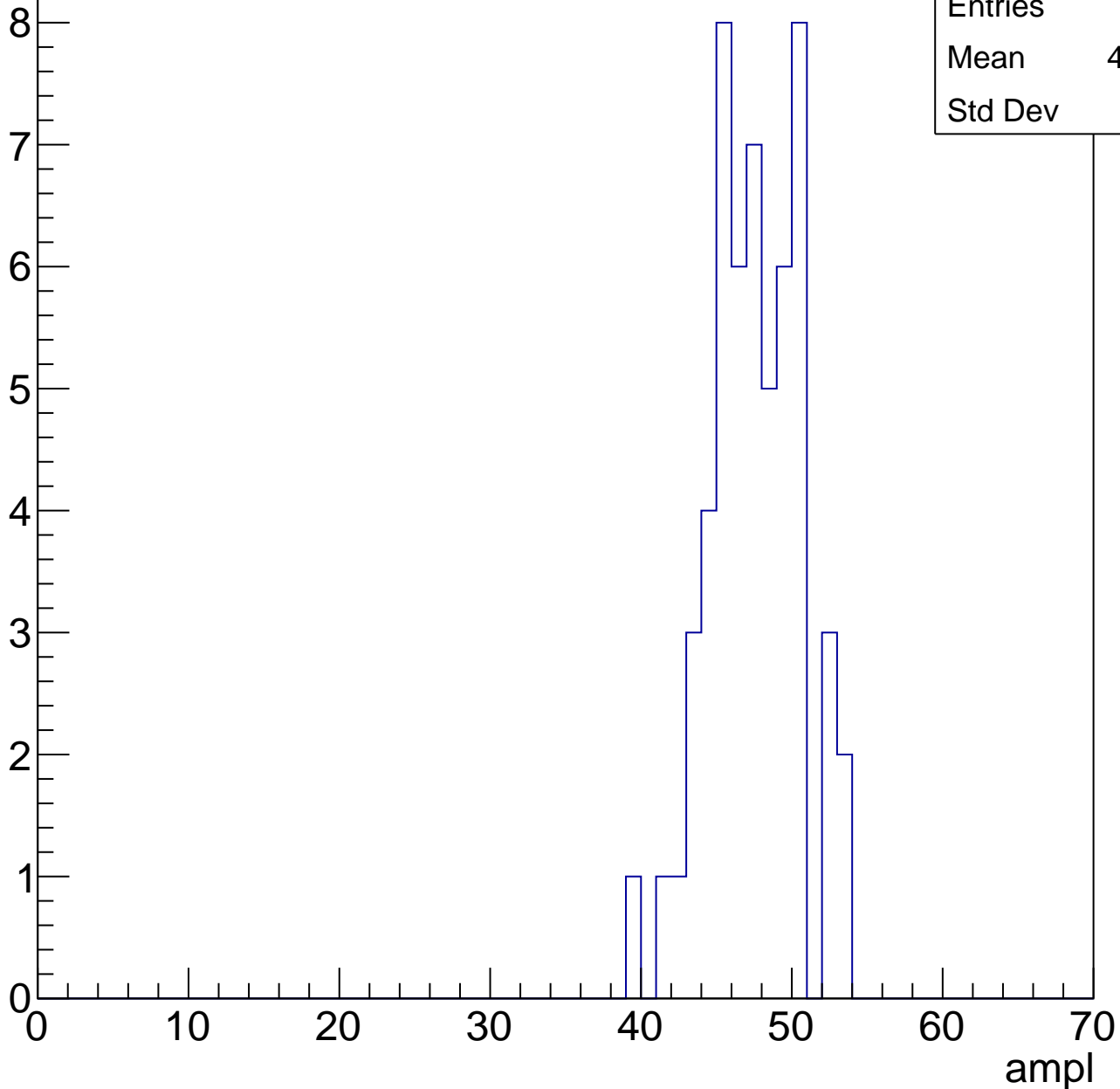


# B1L002S, U18-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	47.05
Std Dev	3

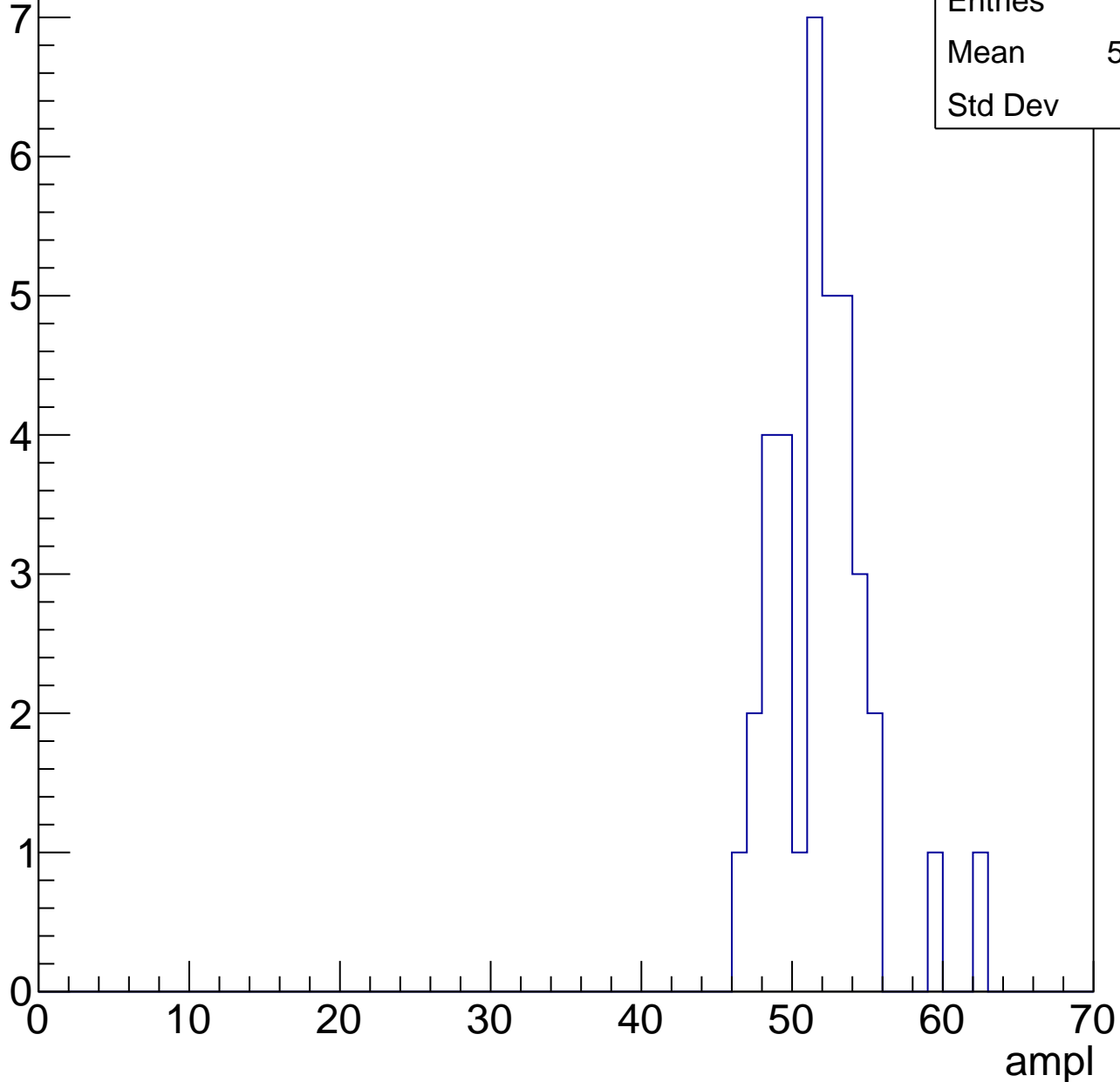


# B1L002S, U18-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	51.47
Std Dev	3.21



# B1L002S, U18-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

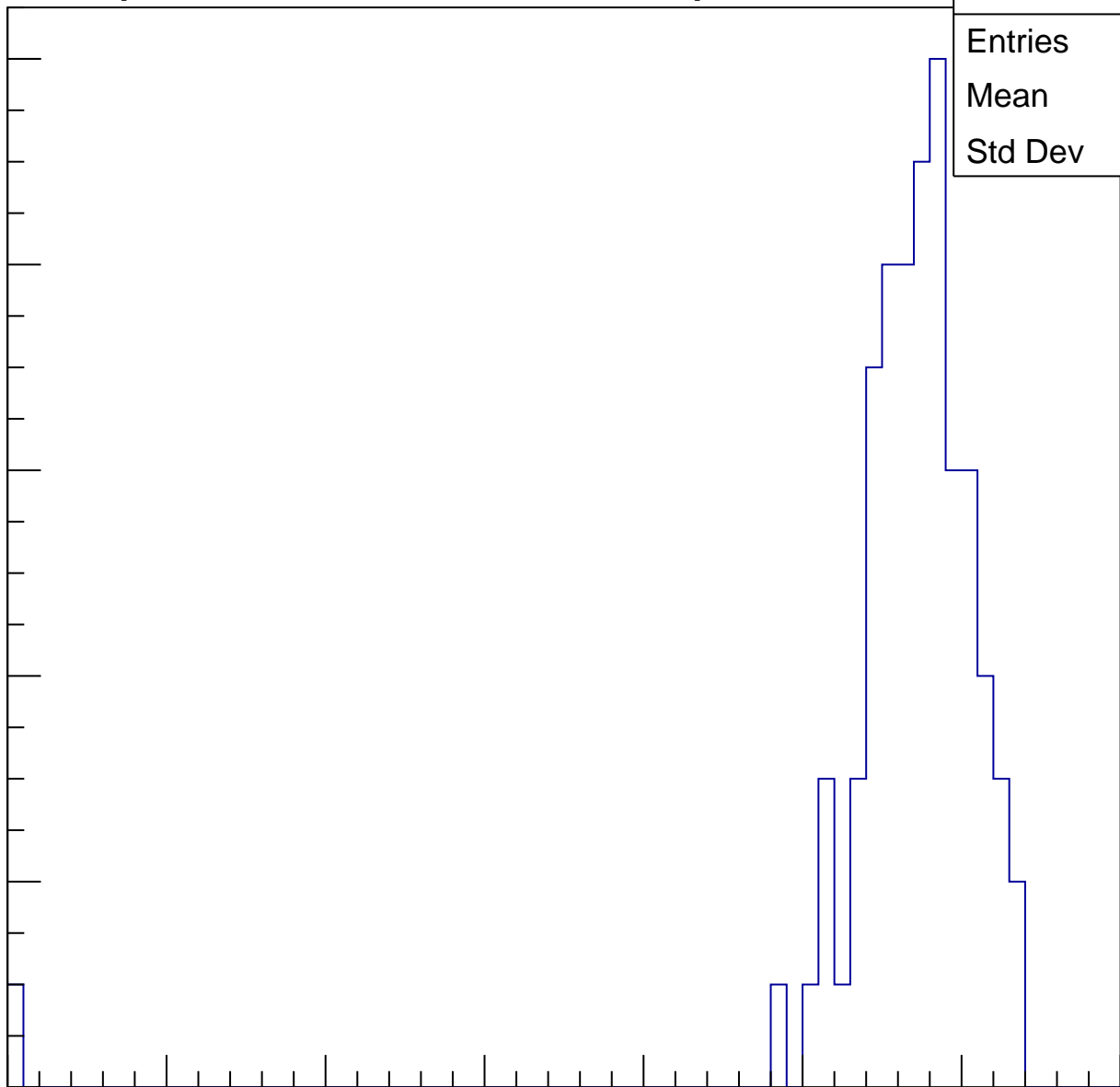
Entries	73
Mean	56.04
Std Dev	7.304

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

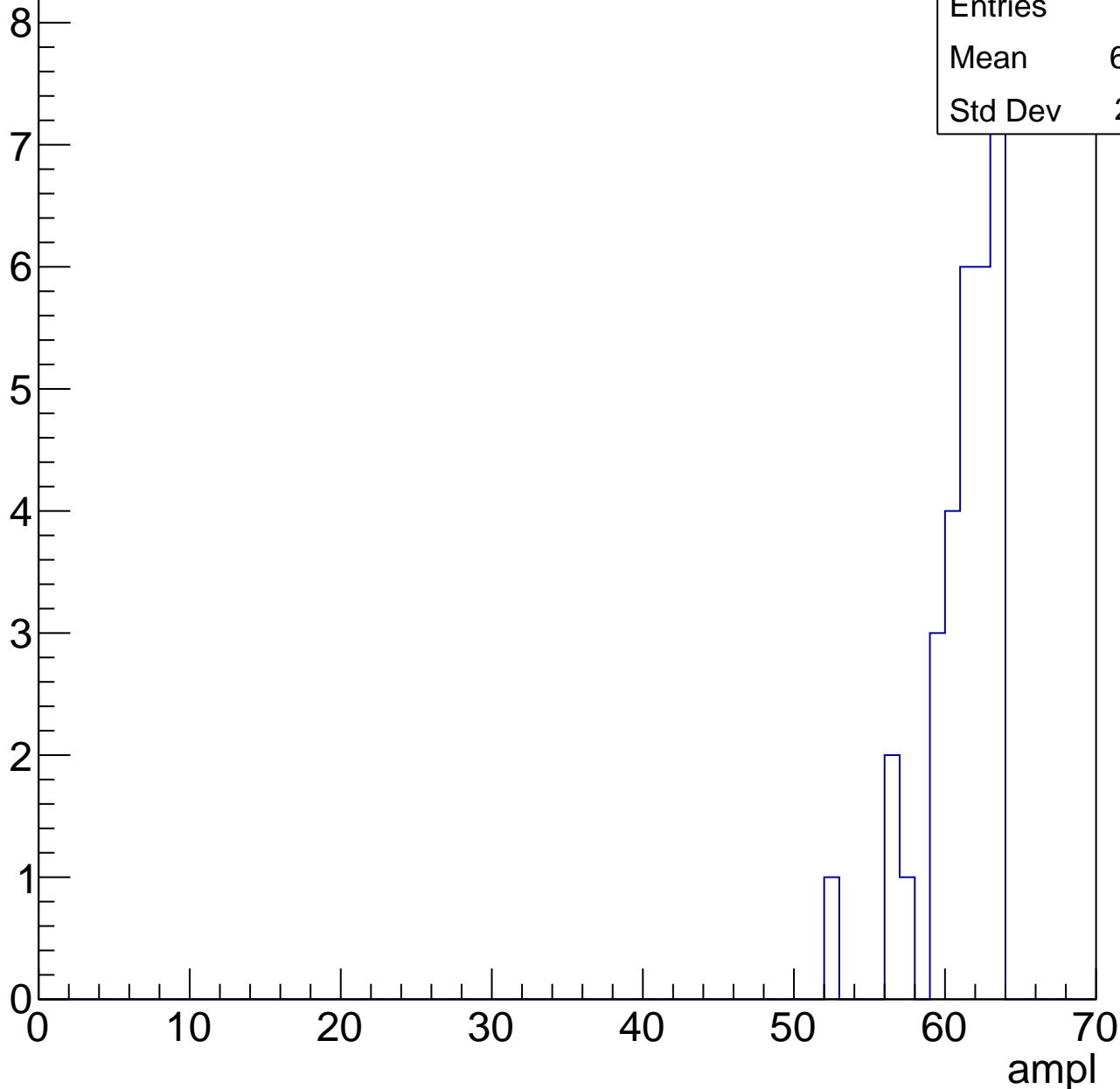


# B1L002S, U18-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	60.65
Std Dev	2.521





# B1L002S, U18-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L002S, U18-ch29, adc0

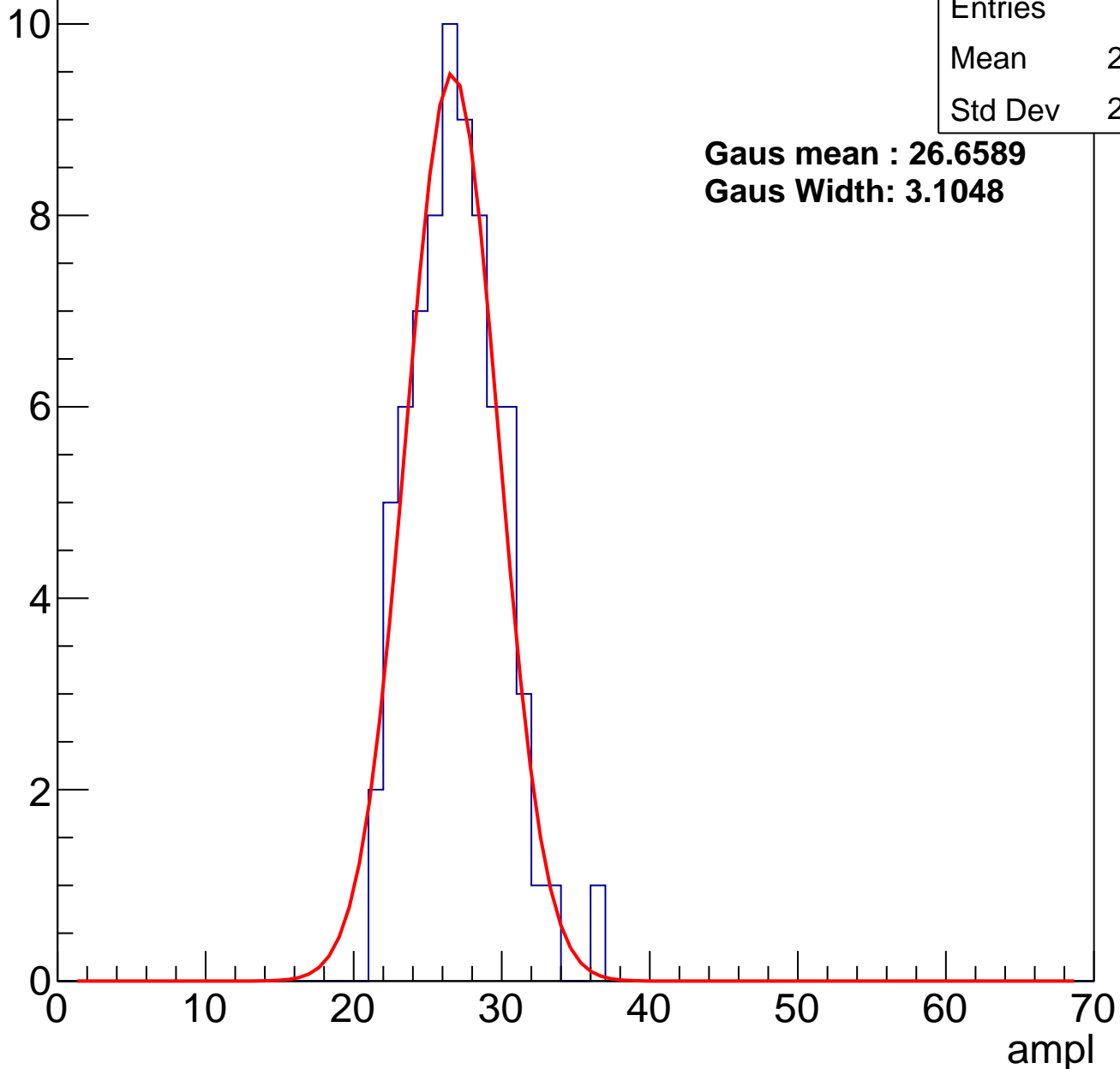
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	26.48
Std Dev	2.994

**Gaus mean : 26.6589**

**Gaus Width: 3.1048**

Entry



# B1L002S, U18-ch29, adc1

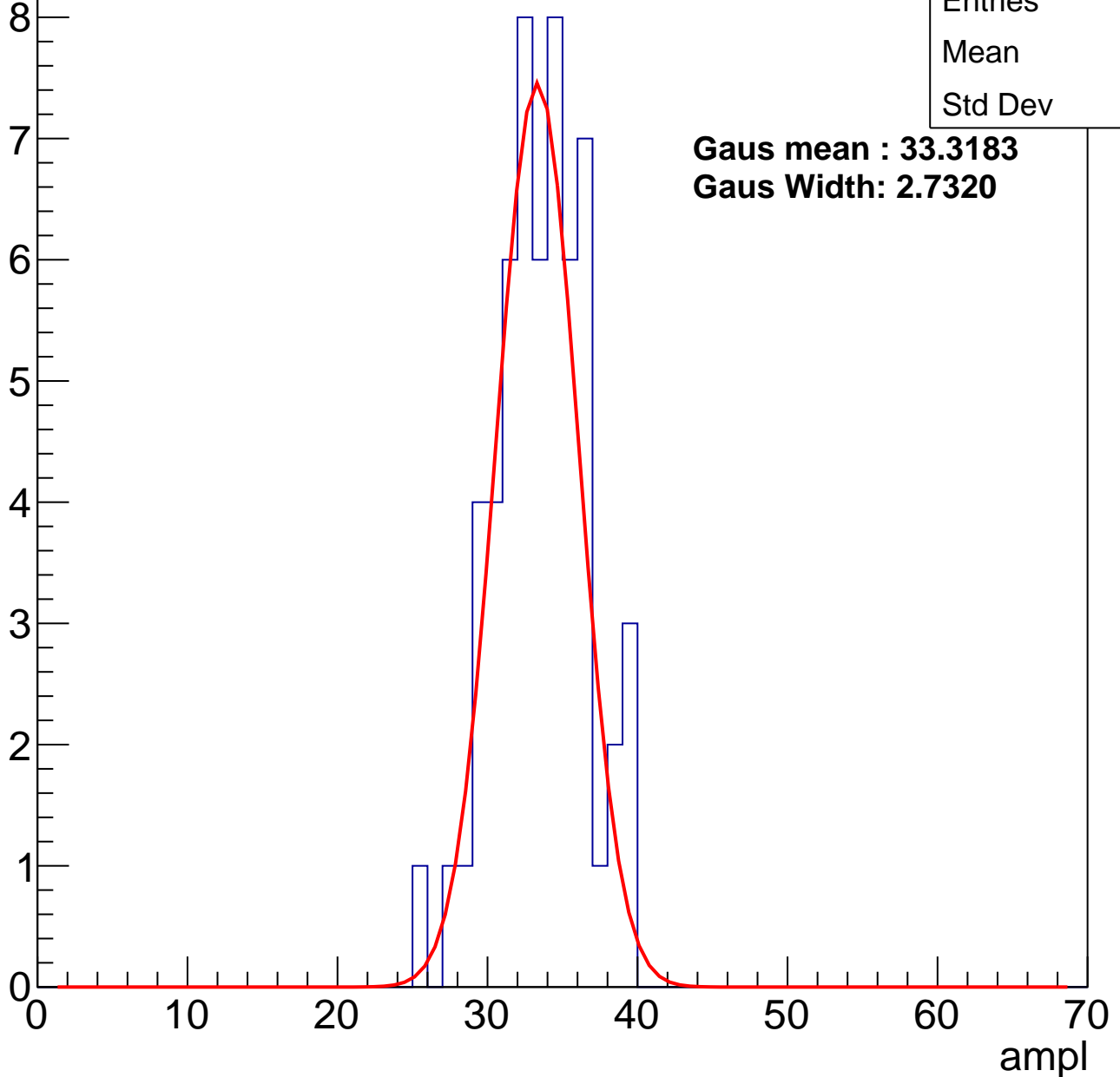
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	33.1
Std Dev	3.01

**Gaus mean : 33.3183**

**Gaus Width: 2.7320**



# B1L002S, U18-ch29, adc2

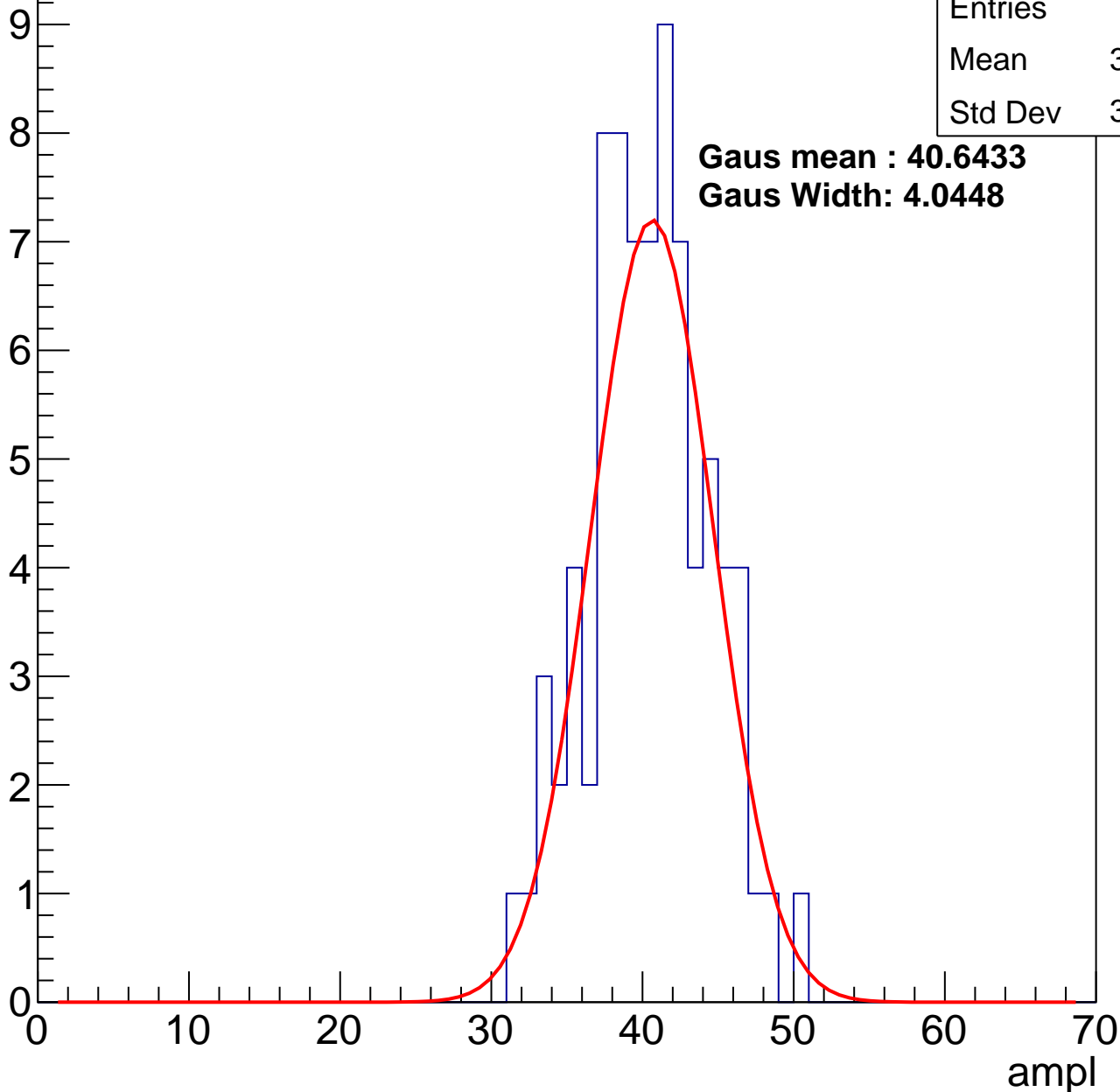
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	39.99
Std Dev	3.925

**Gaus mean : 40.6433**

**Gaus Width: 4.0448**

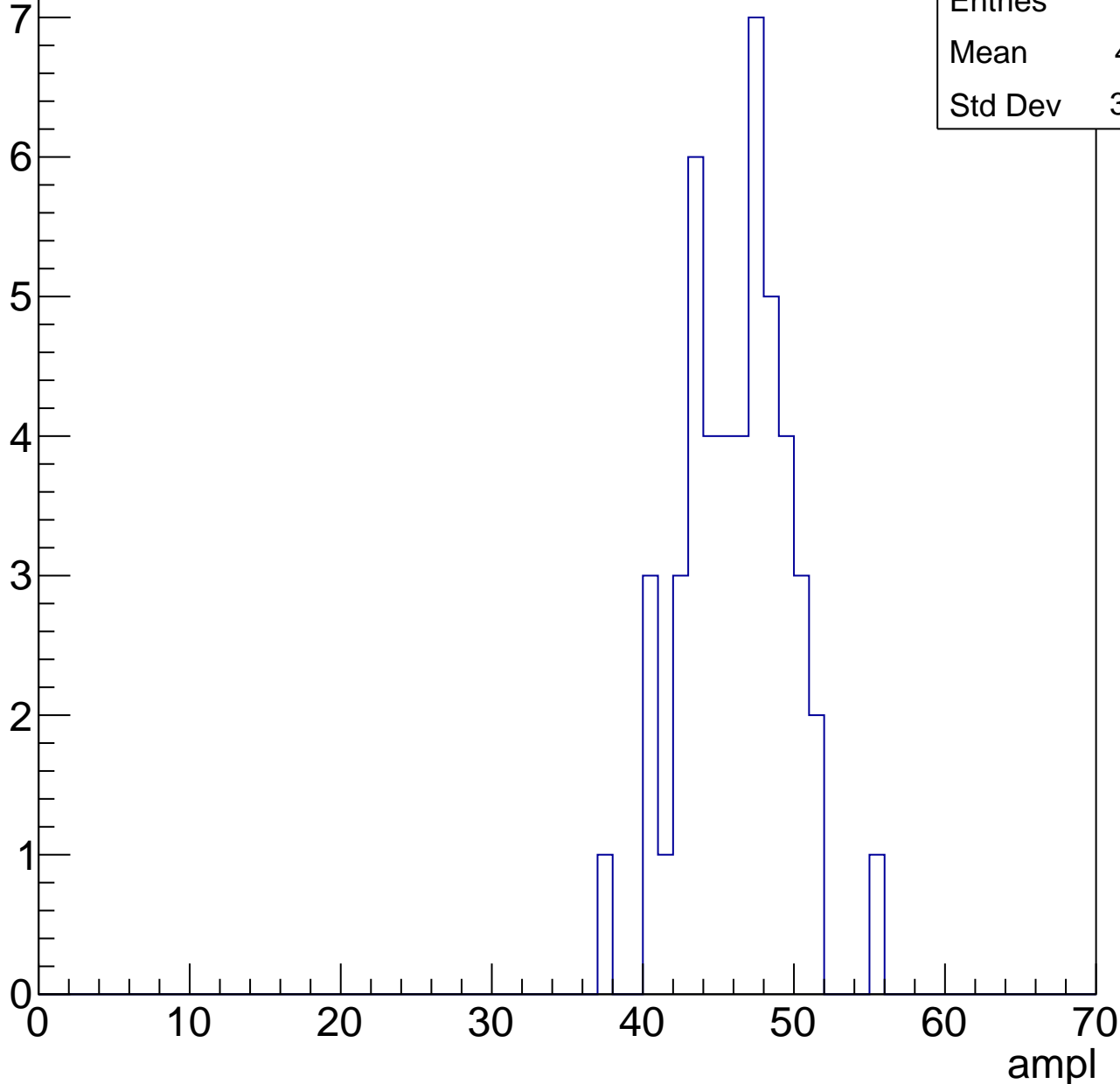


# B1L002S, U18-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	45.71
Std Dev	3.458

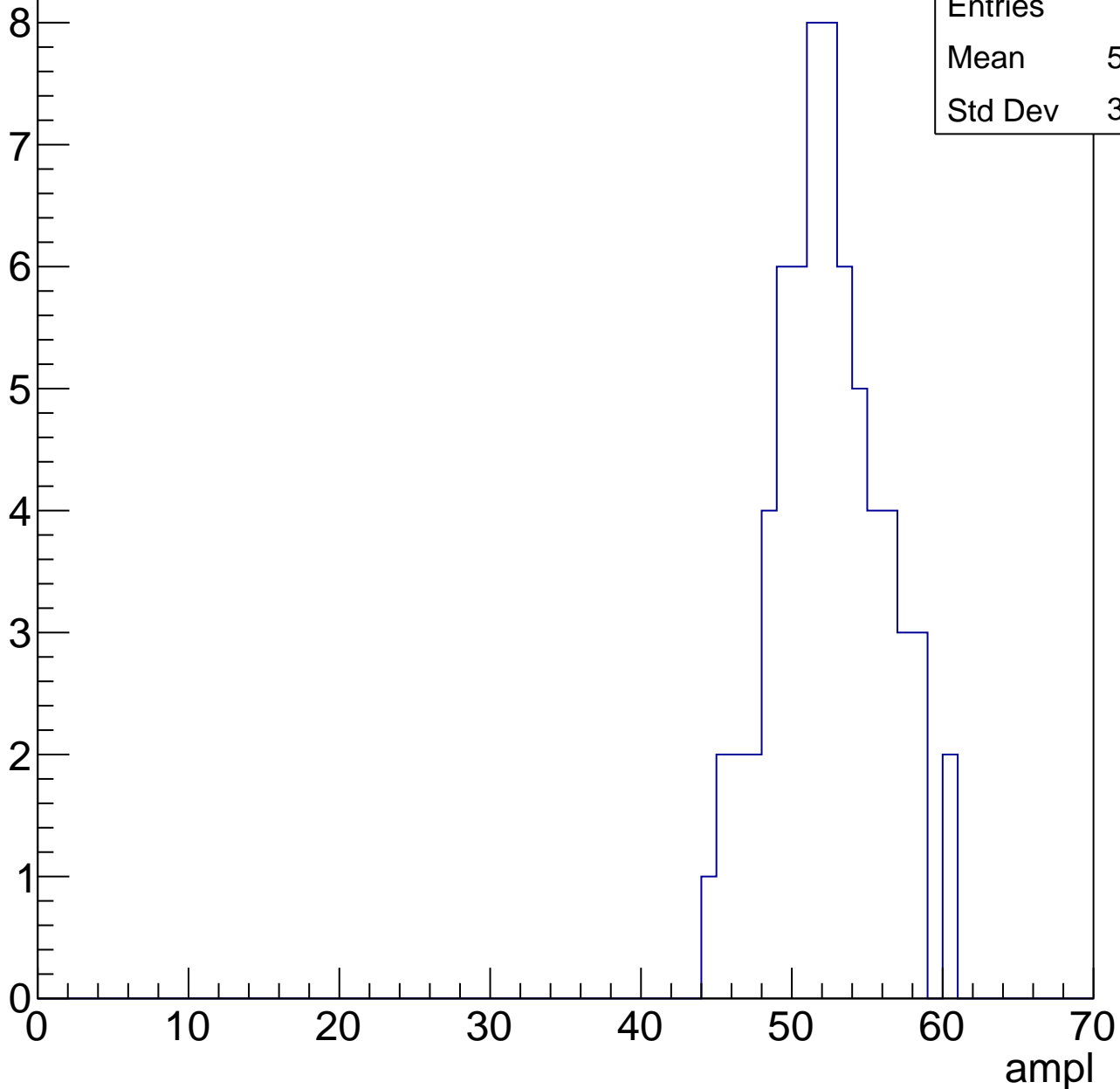


# B1L002S, U18-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	51.92
Std Dev	3.632

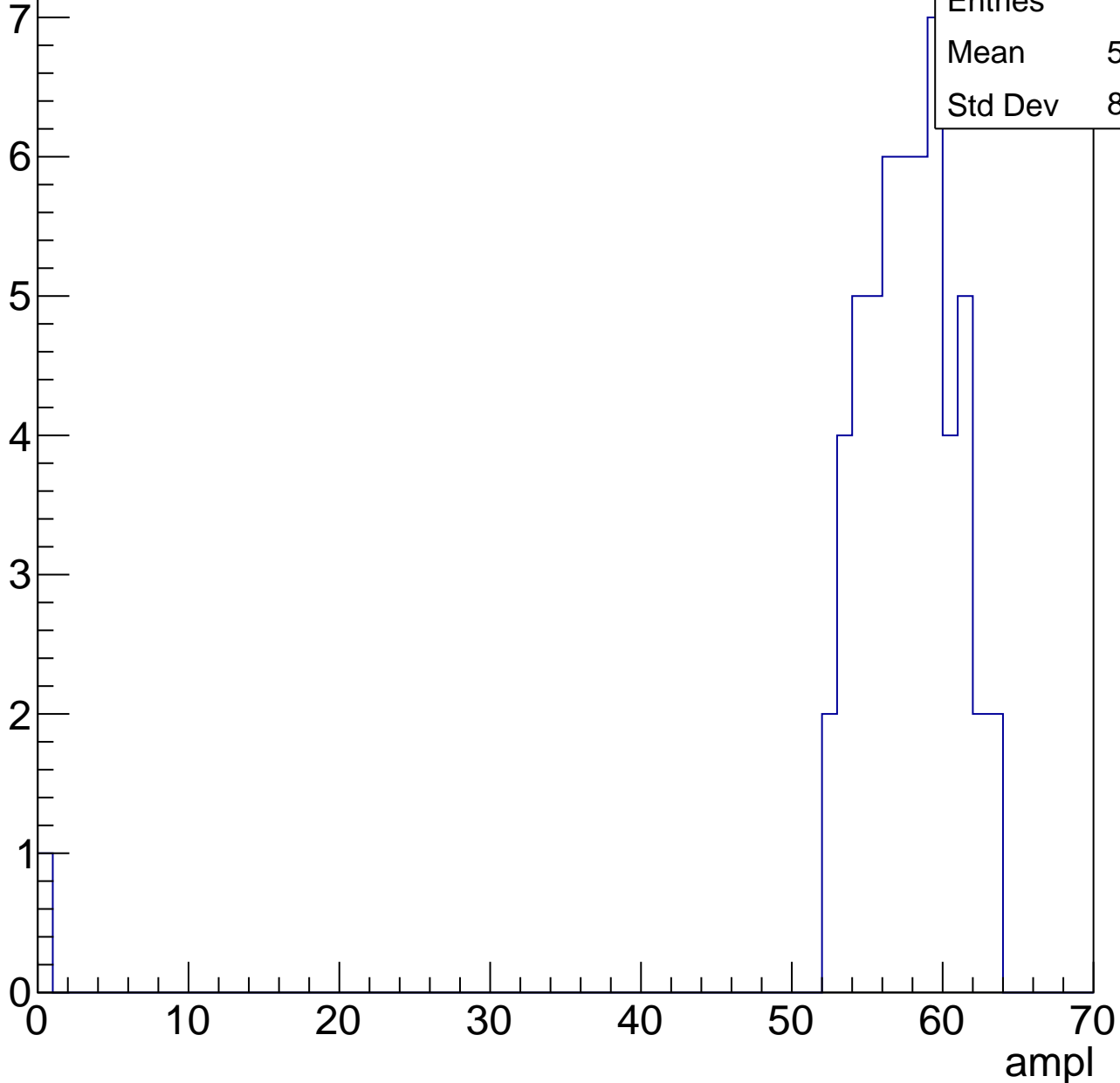


# B1L002S, U18-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	56.27
Std Dev	8.176

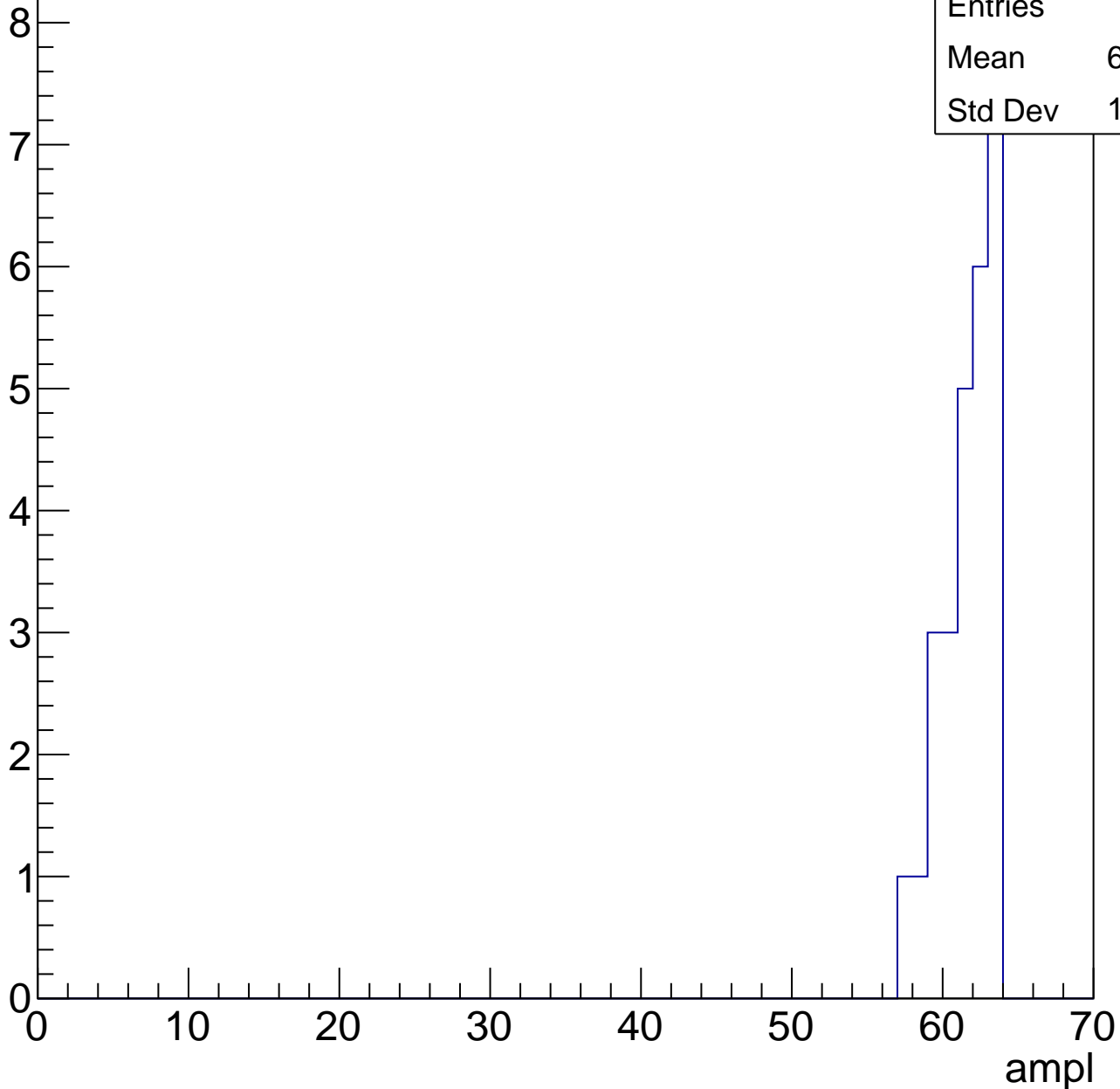


# B1L002S, U18-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	61.22
Std Dev	1.685

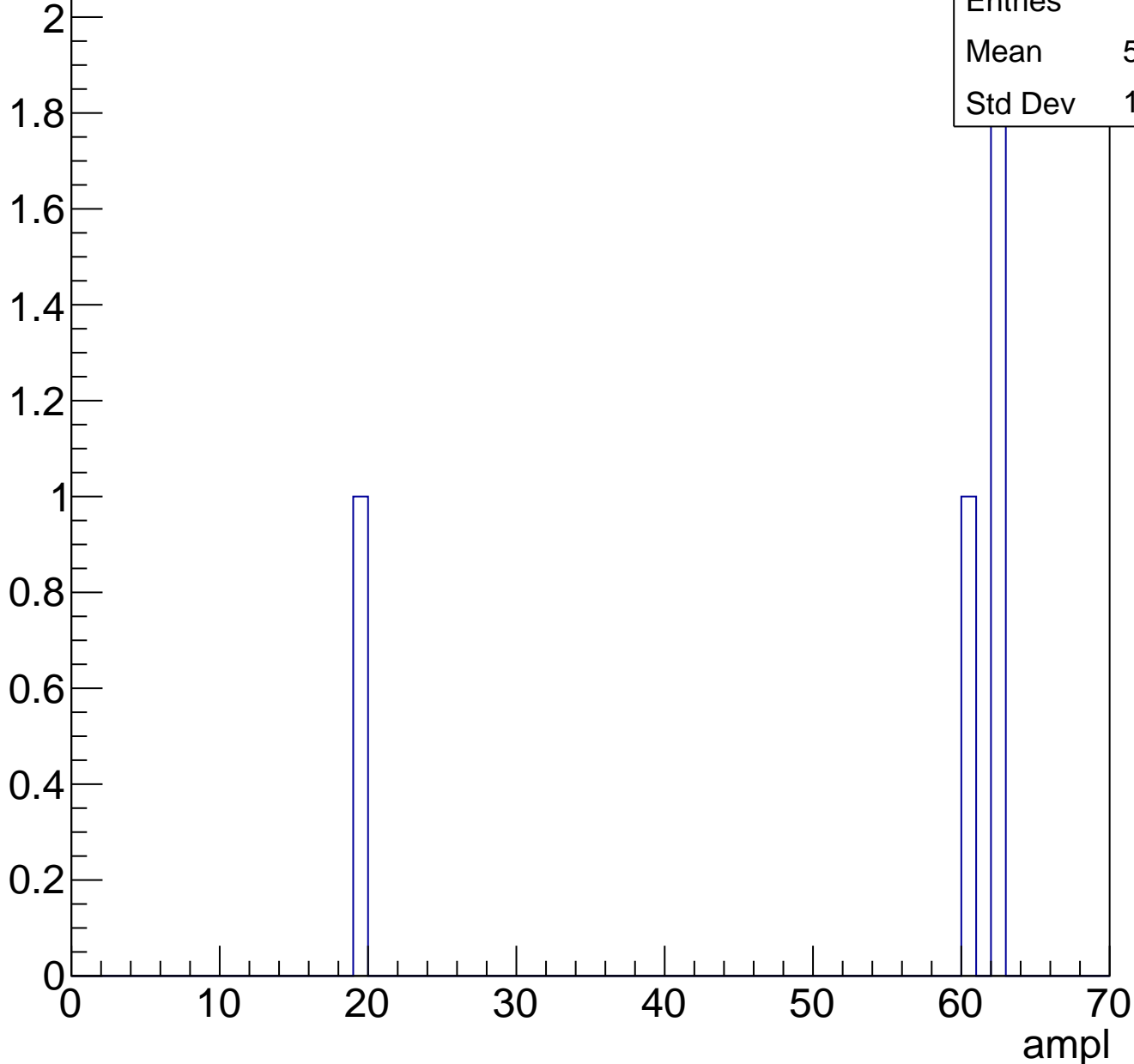




# B1L002S, U18-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	50.75
Std Dev	18.35

# B1L002S, U18-ch30, adc0

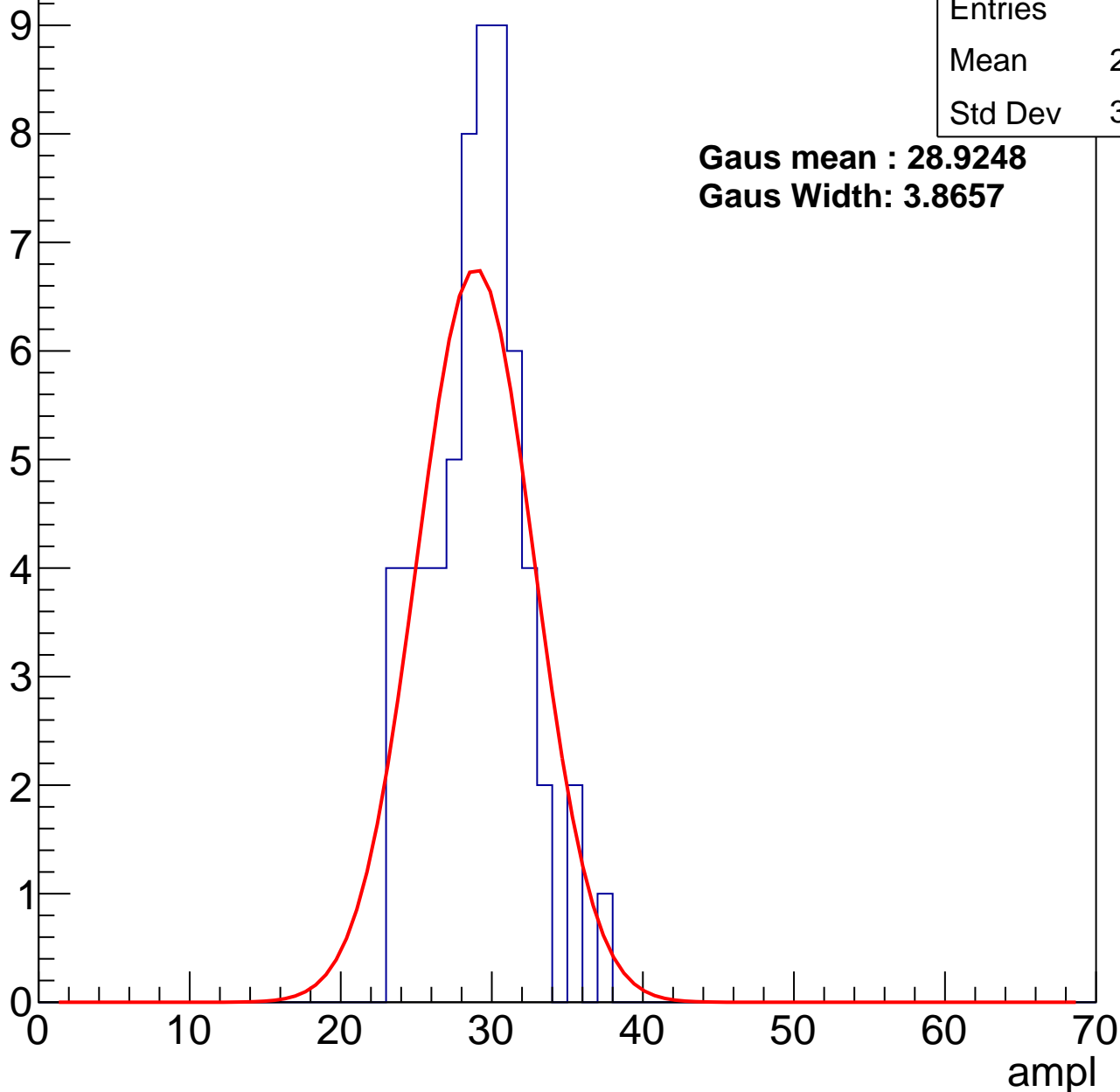
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.53
Std Dev	3.099

**Gaus mean : 28.9248**

**Gaus Width: 3.8657**



# B1L002S, U18-ch30, adc1

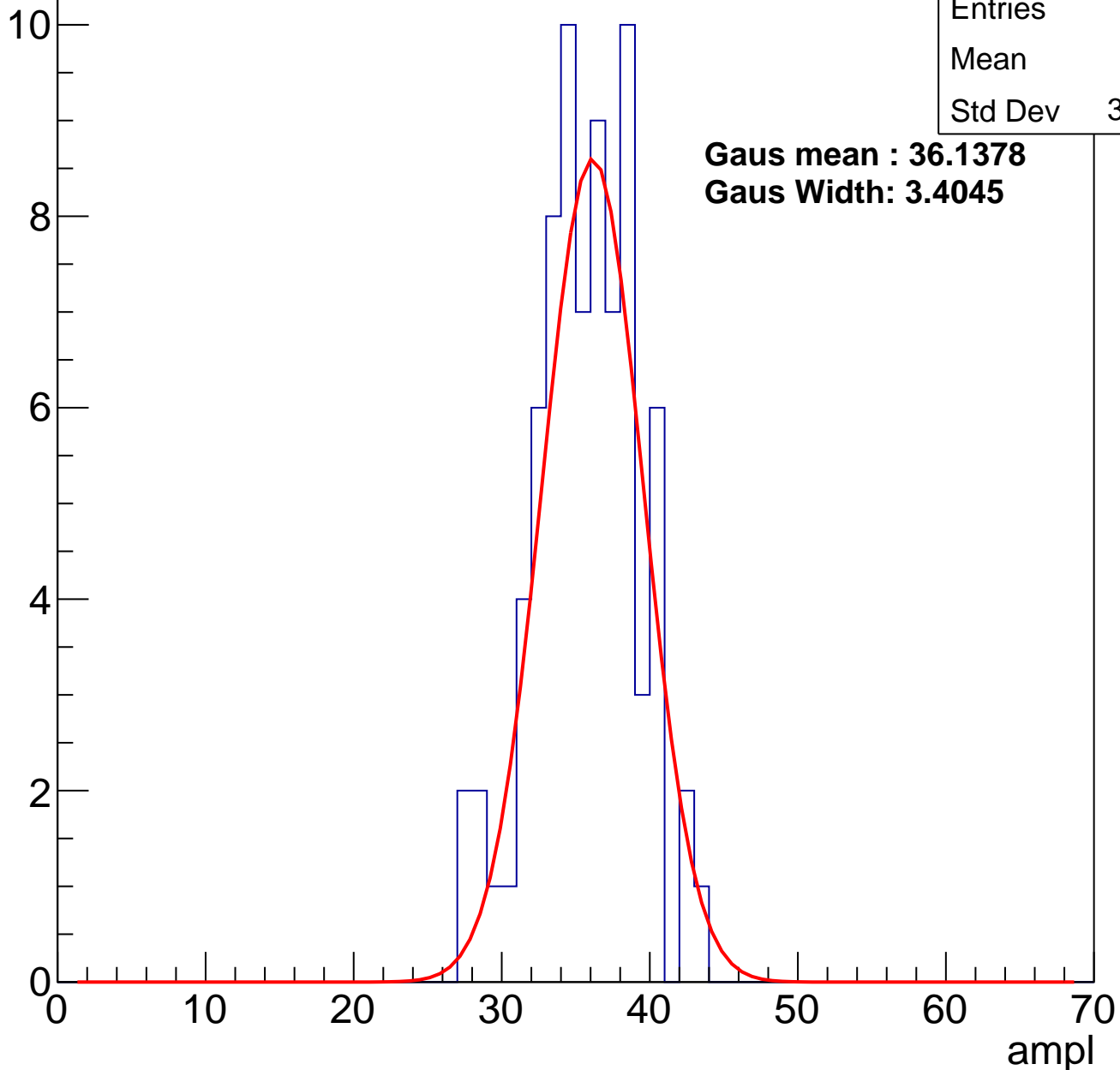
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	35.2
Std Dev	3.432

**Gaus mean : 36.1378**

**Gaus Width: 3.4045**

Entry



# B1L002S, U18-ch30, adc2

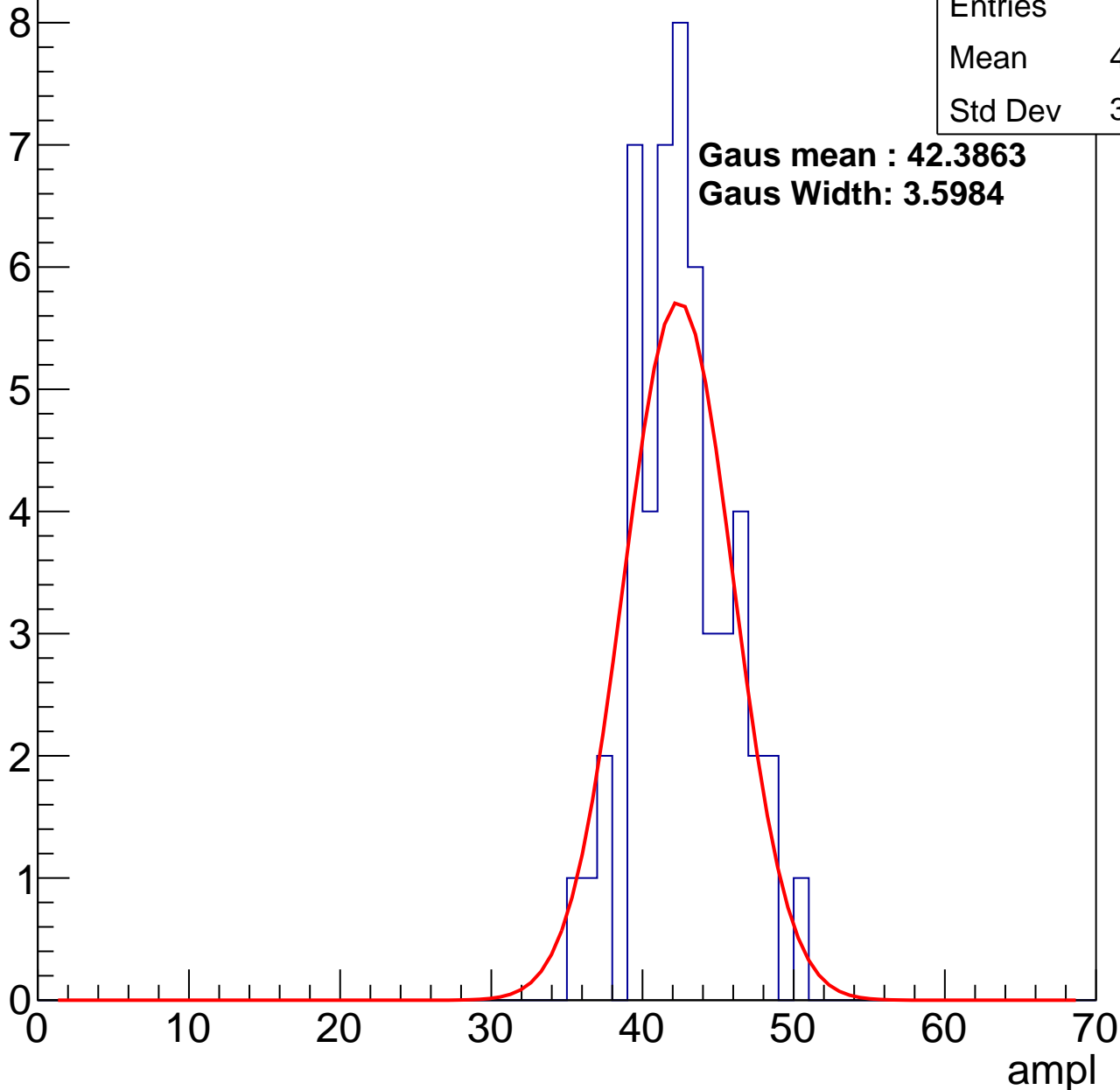
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	42.16
Std Dev	3.177

**Gaus mean : 42.3863**

**Gaus Width: 3.5984**

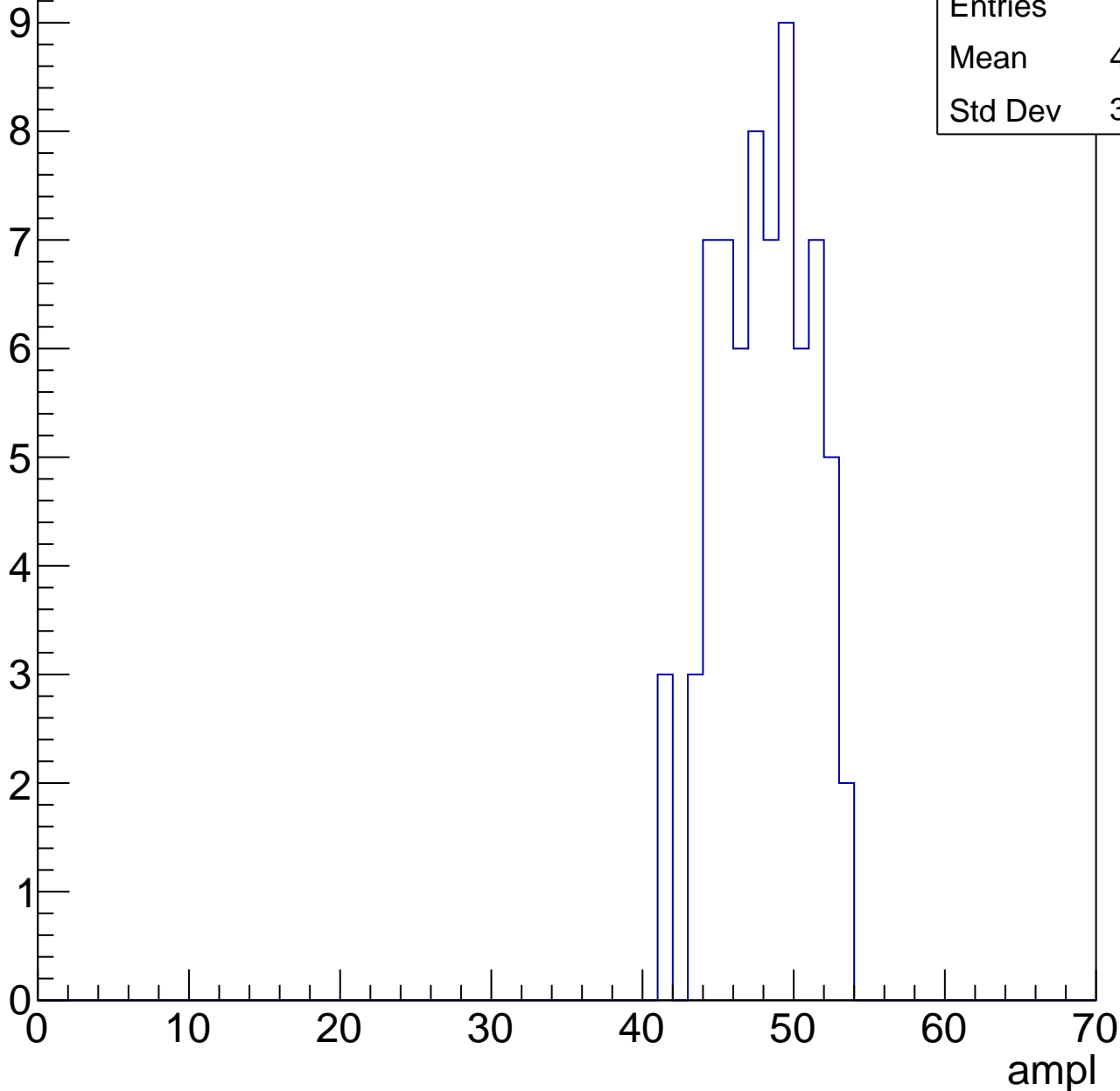


# B1L002S, U18-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

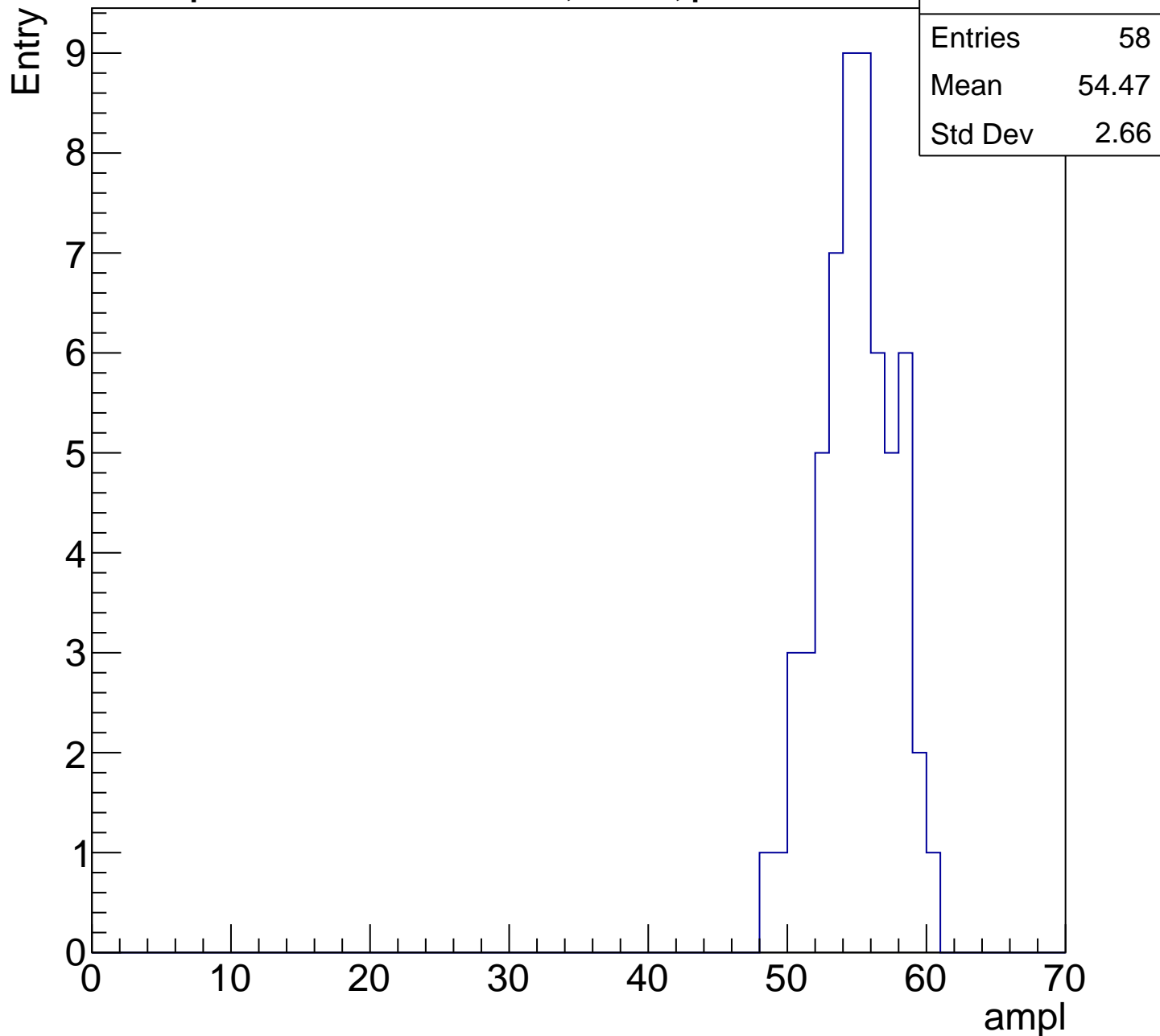
Entry

Entries	70
Mean	47.53
Std Dev	3.022



# B1L002S, U18-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

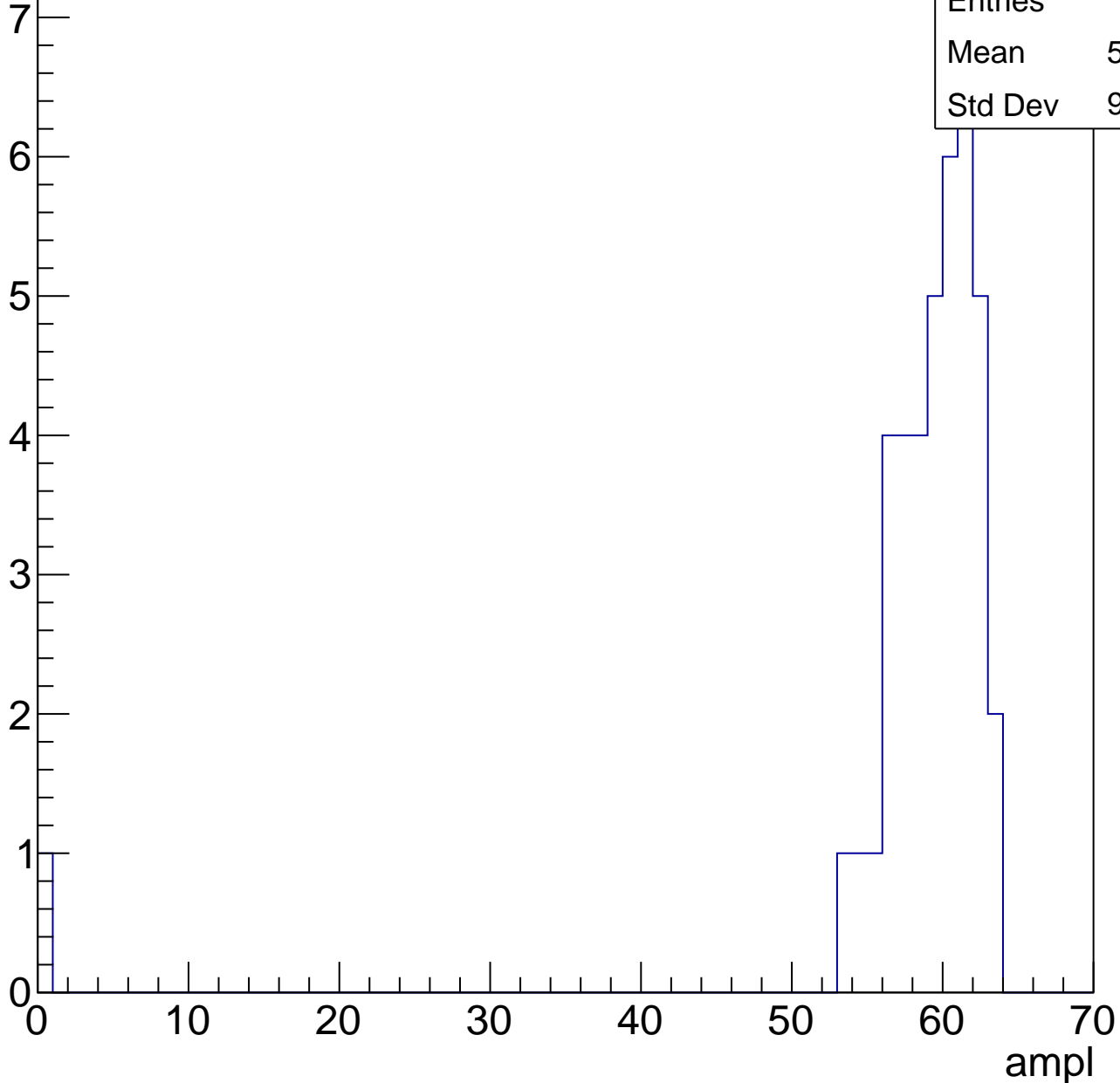


# B1L002S, U18-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	57.66
Std Dev	9.437

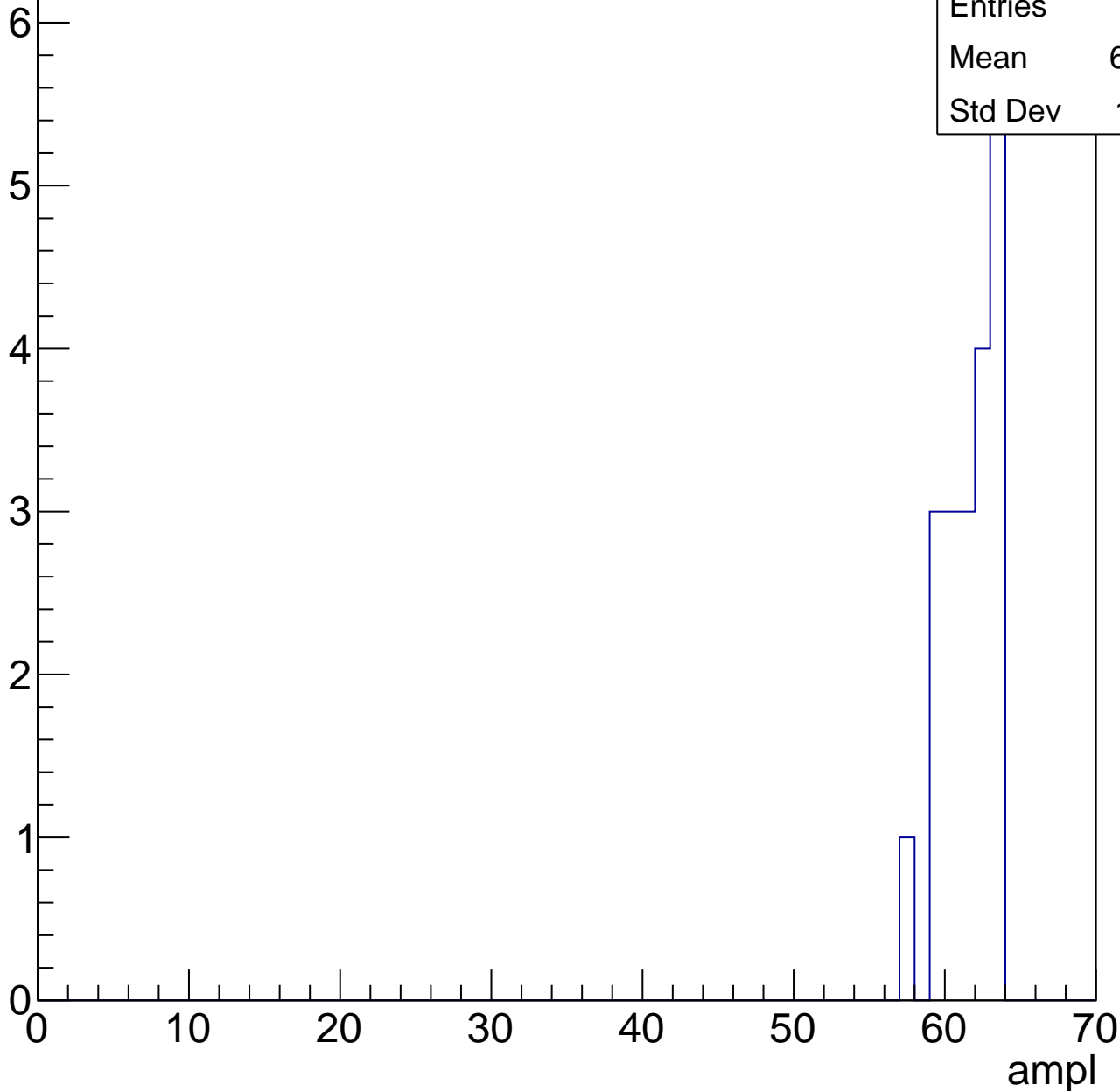


# B1L002S, U18-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.15
Std Dev	1.711

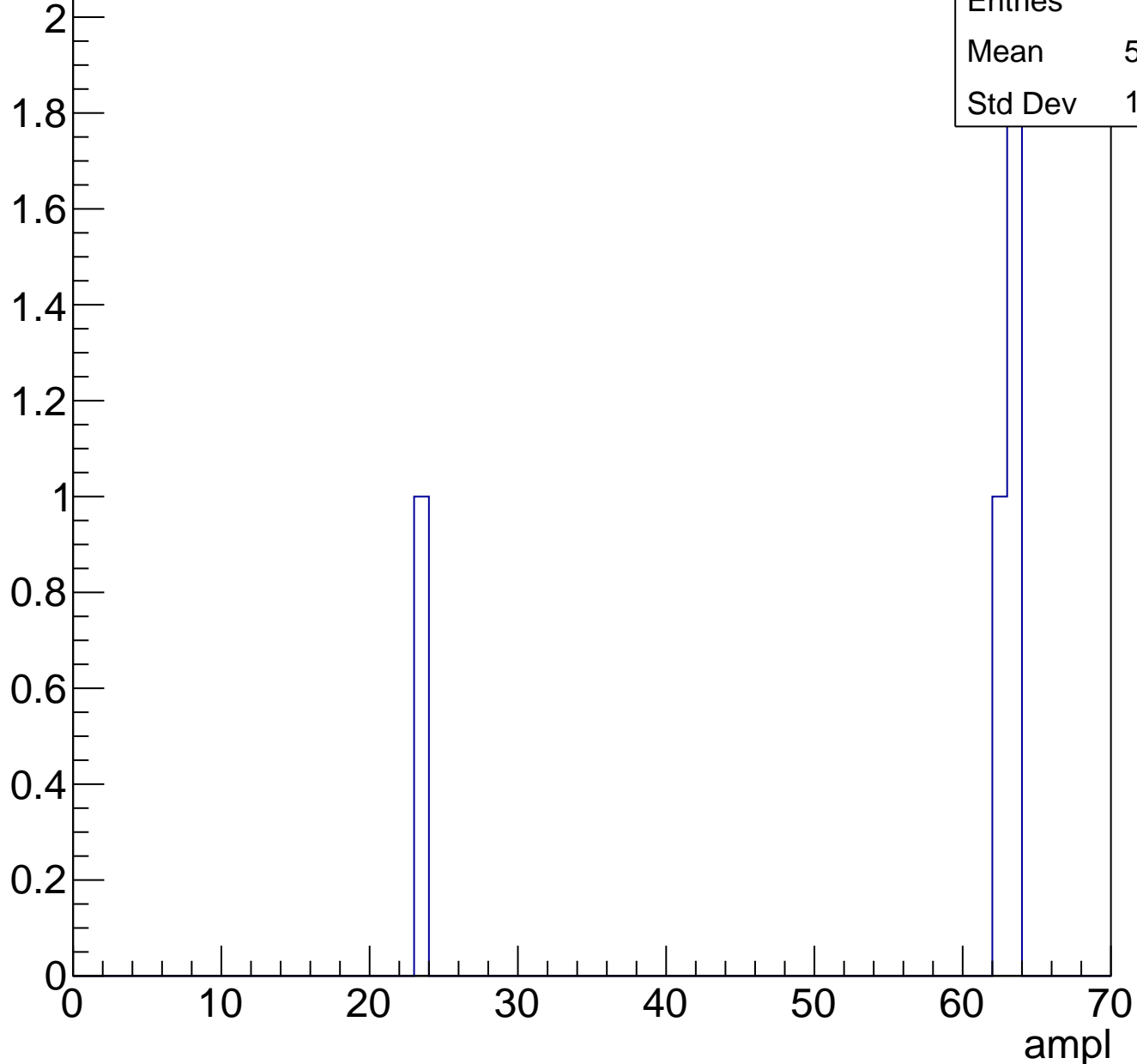




# B1L002S, U18-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch31, adc0

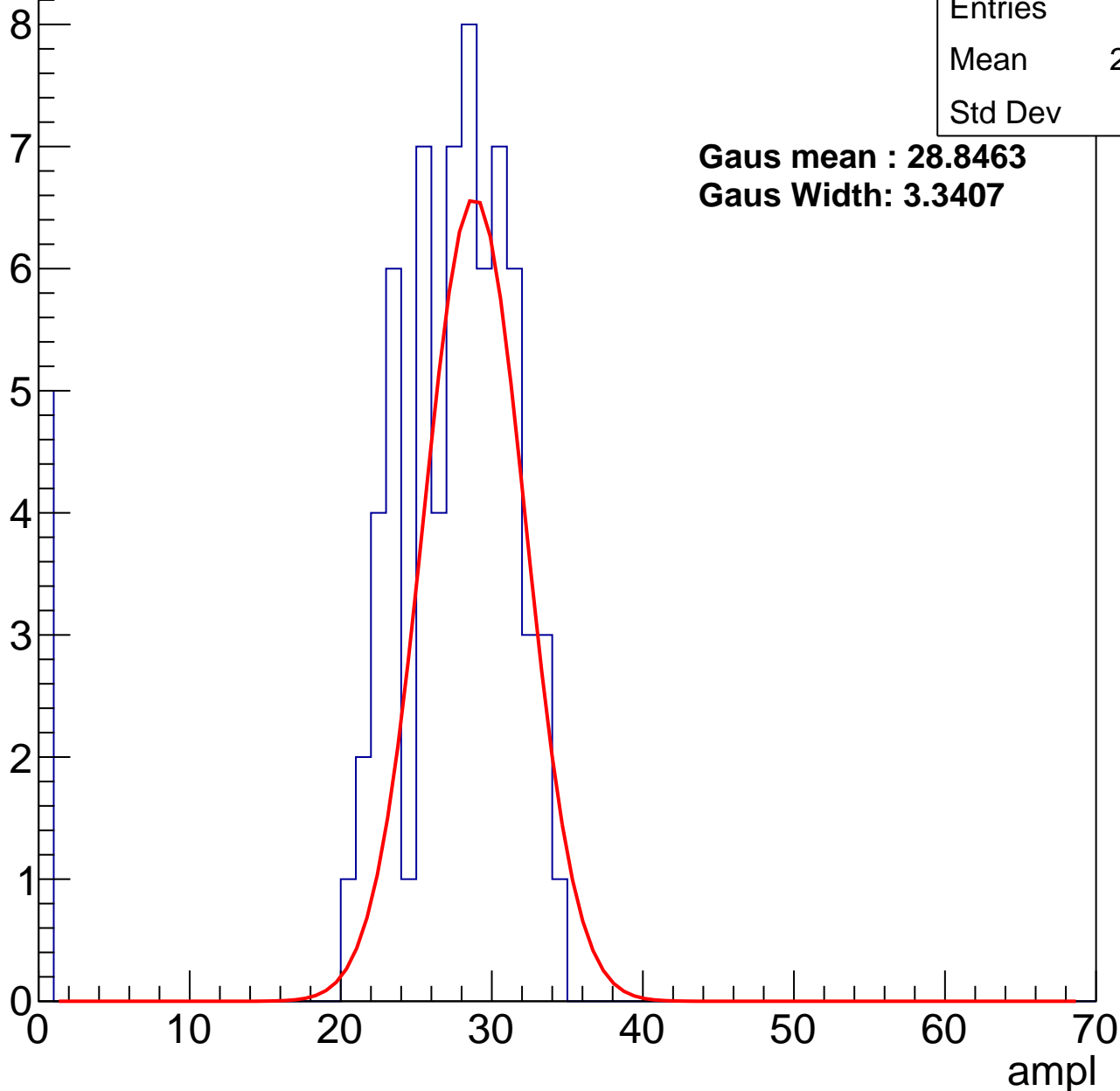
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	25.39
Std Dev	7.73

**Gaus mean : 28.8463**

**Gaus Width: 3.3407**



# B1L002S, U18-ch31, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	34.02
Std Dev	3.295

**Gaus mean : 34.6185**

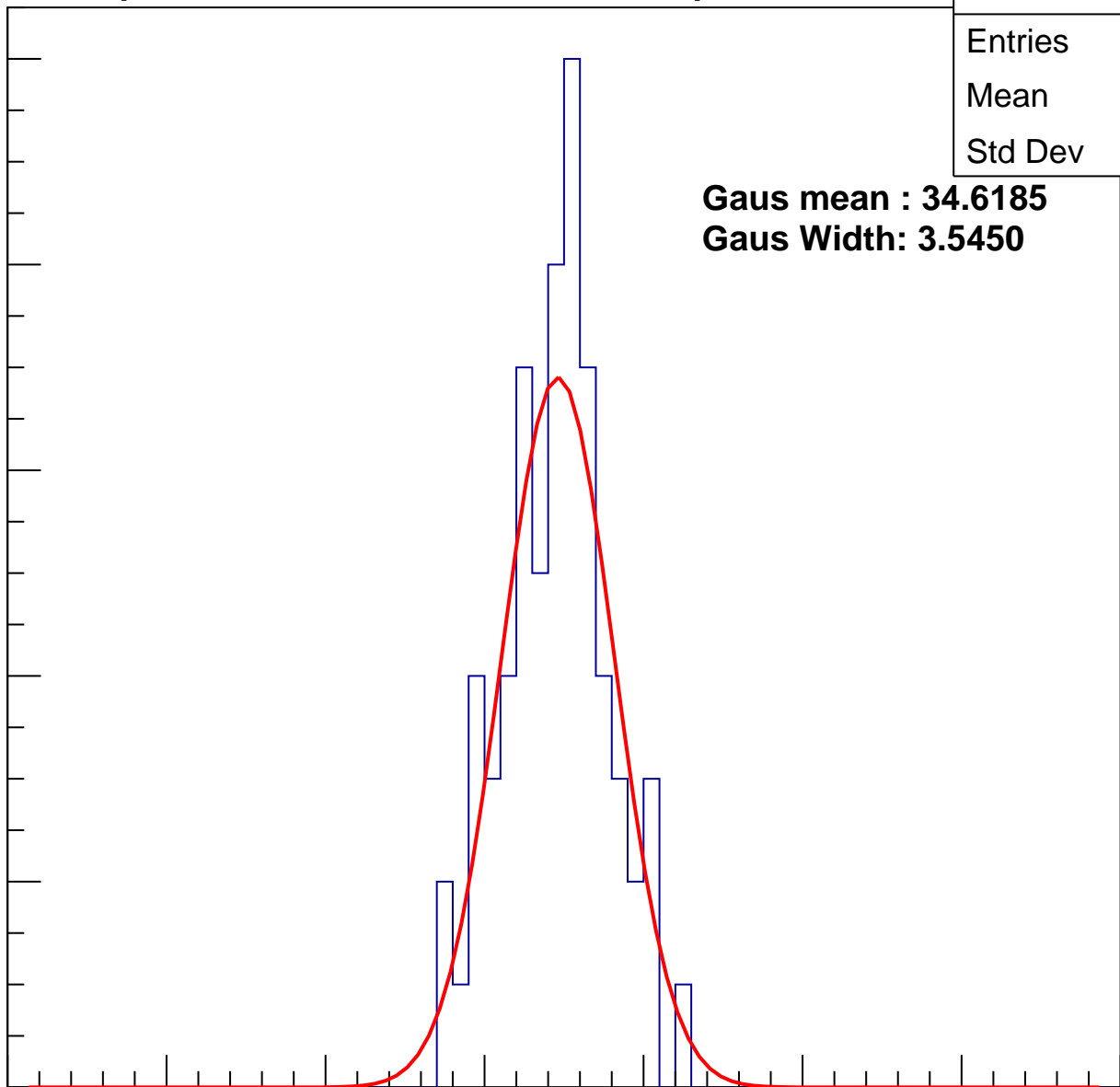
**Gaus Width: 3.5450**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



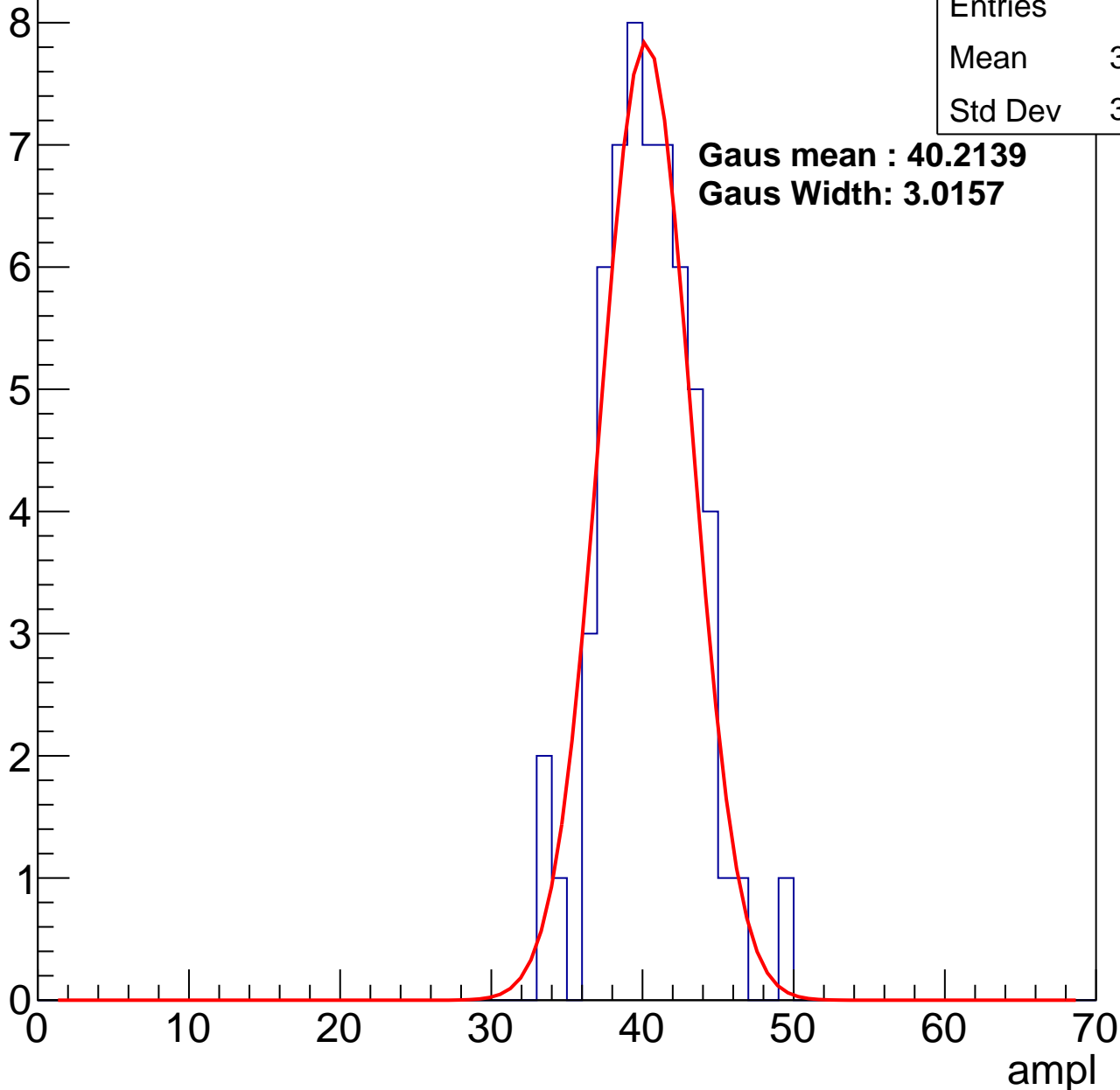
# B1L002S, U18-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	39.97
Std Dev	3.064

**Gaus mean : 40.2139**  
**Gaus Width: 3.0157**



# B1L002S, U18-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	46.52
Std Dev	2.969

Entry

10

8

6

4

2

0

0

10

20

30

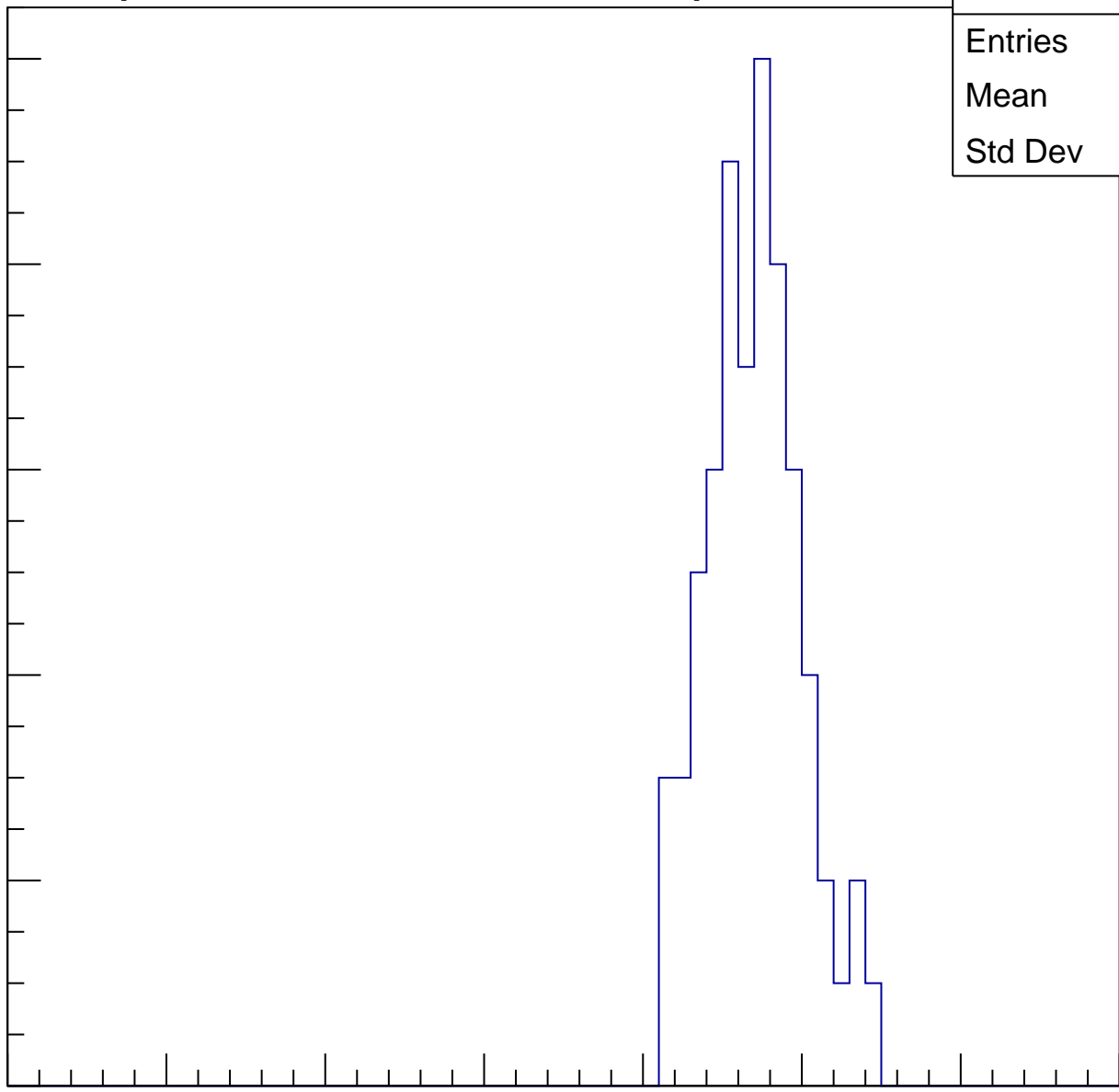
40

50

60

70

ampl

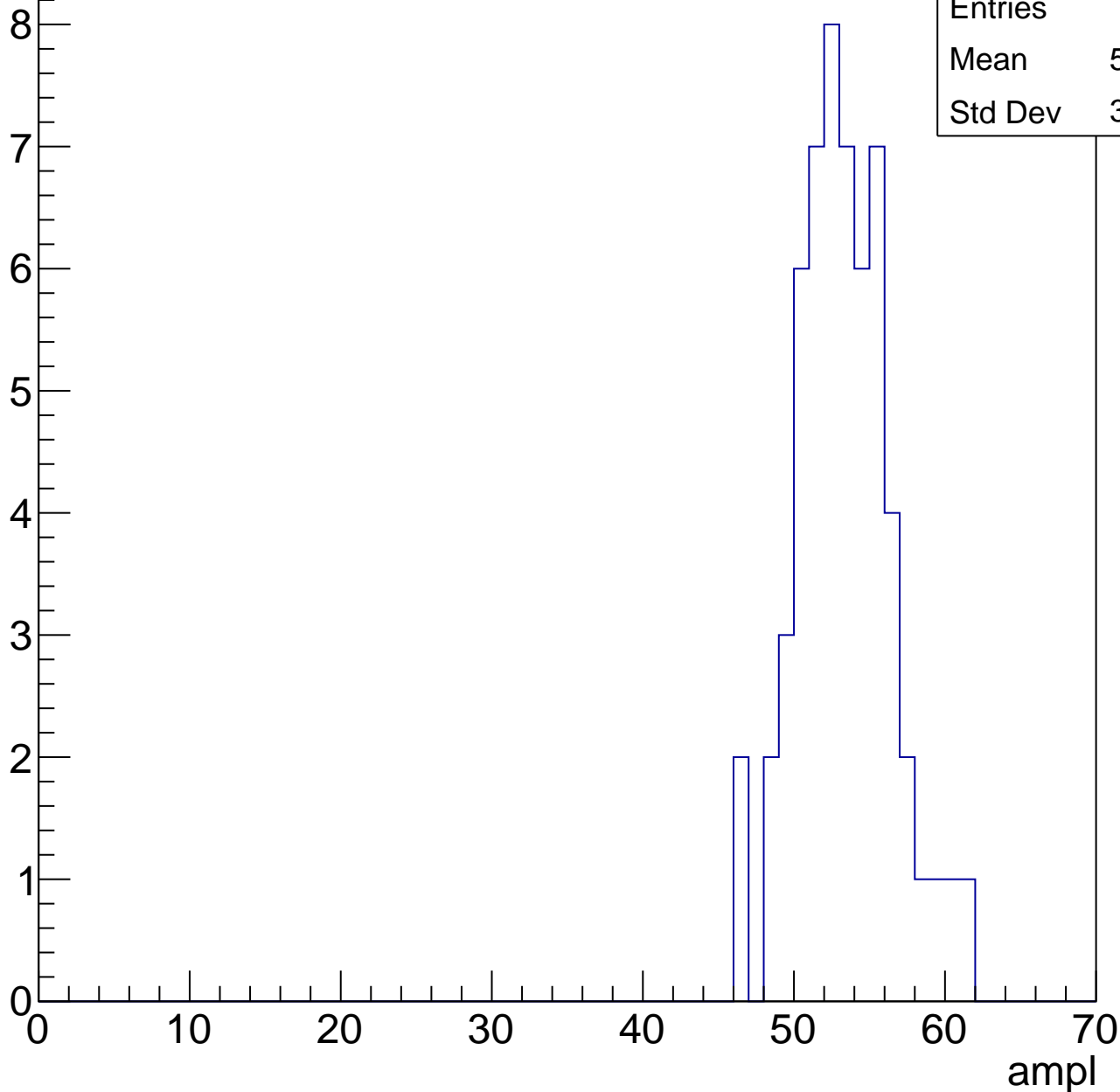


# B1L002S, U18-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.83
Std Dev	3.108

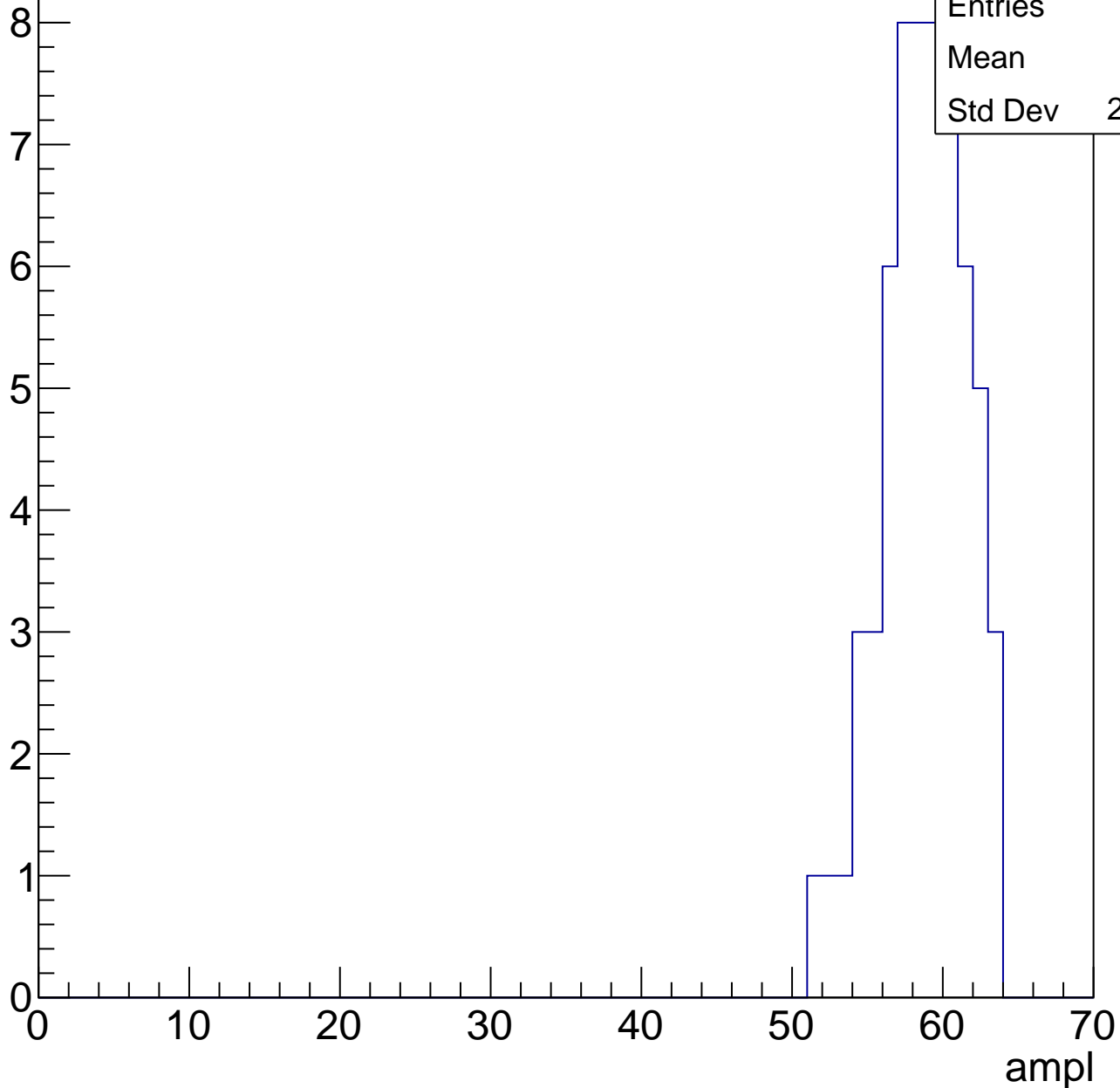


# B1L002S, U18-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	58.3
Std Dev	2.748

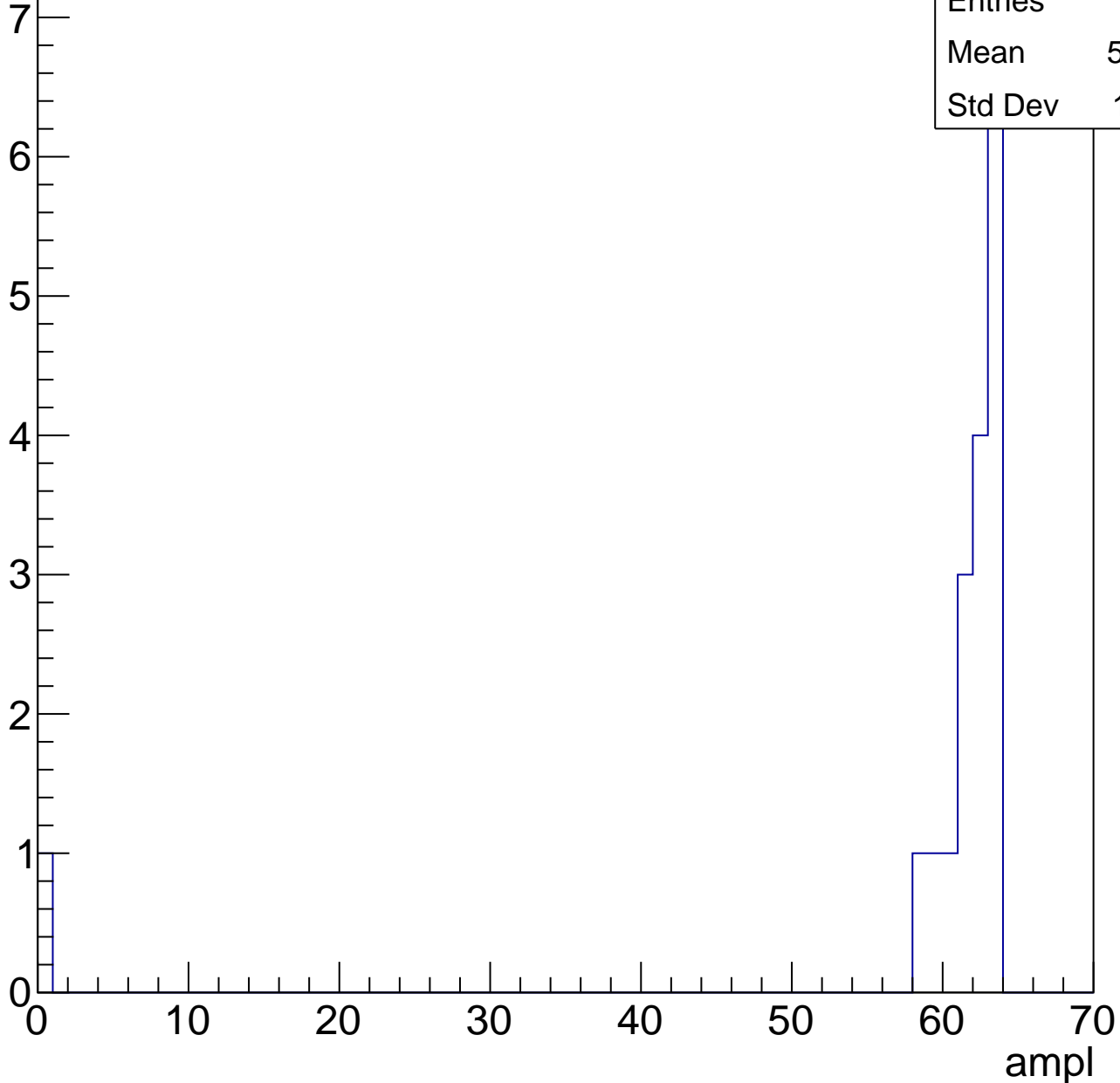


# B1L002S, U18-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.28
Std Dev	14.21





# B1L002S, U18-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch32, adc0

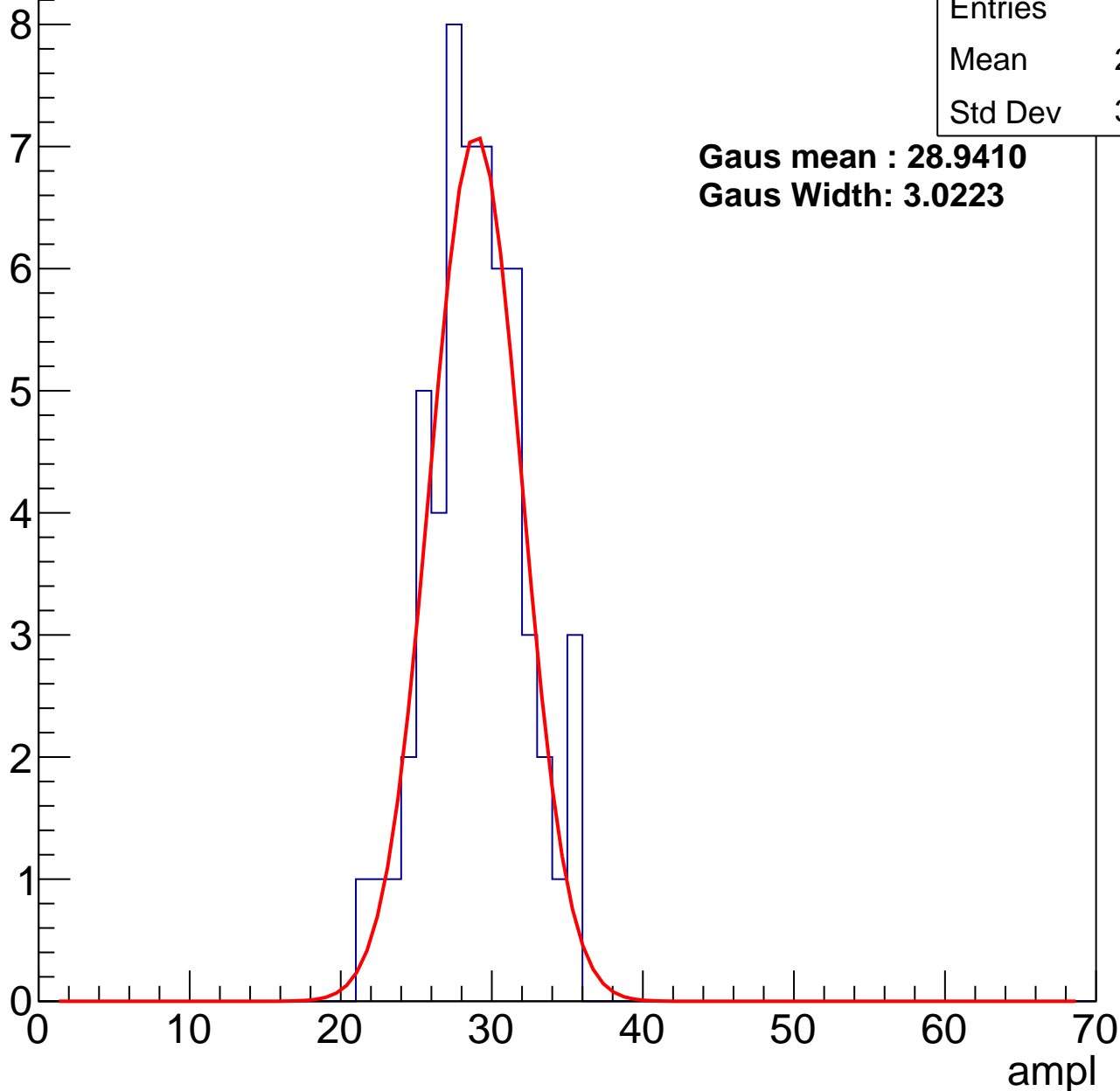
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.51
Std Dev	3.141

**Gaus mean : 28.9410**

**Gaus Width: 3.0223**



# B1L002S, U18-ch32, adc1

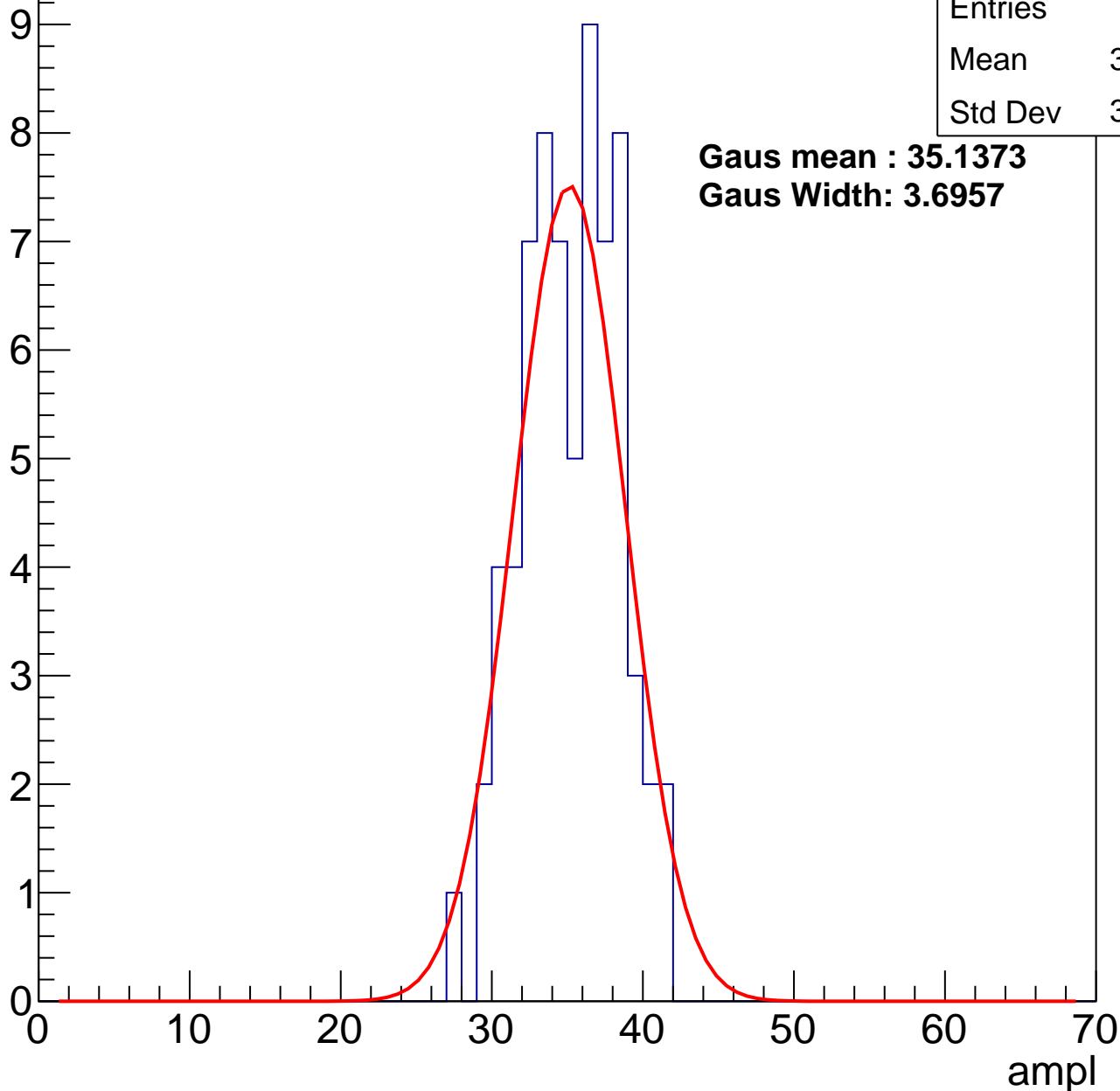
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.72
Std Dev	3.129

**Gaus mean : 35.1373**

**Gaus Width: 3.6957**



# B1L002S, U18-ch32, adc2

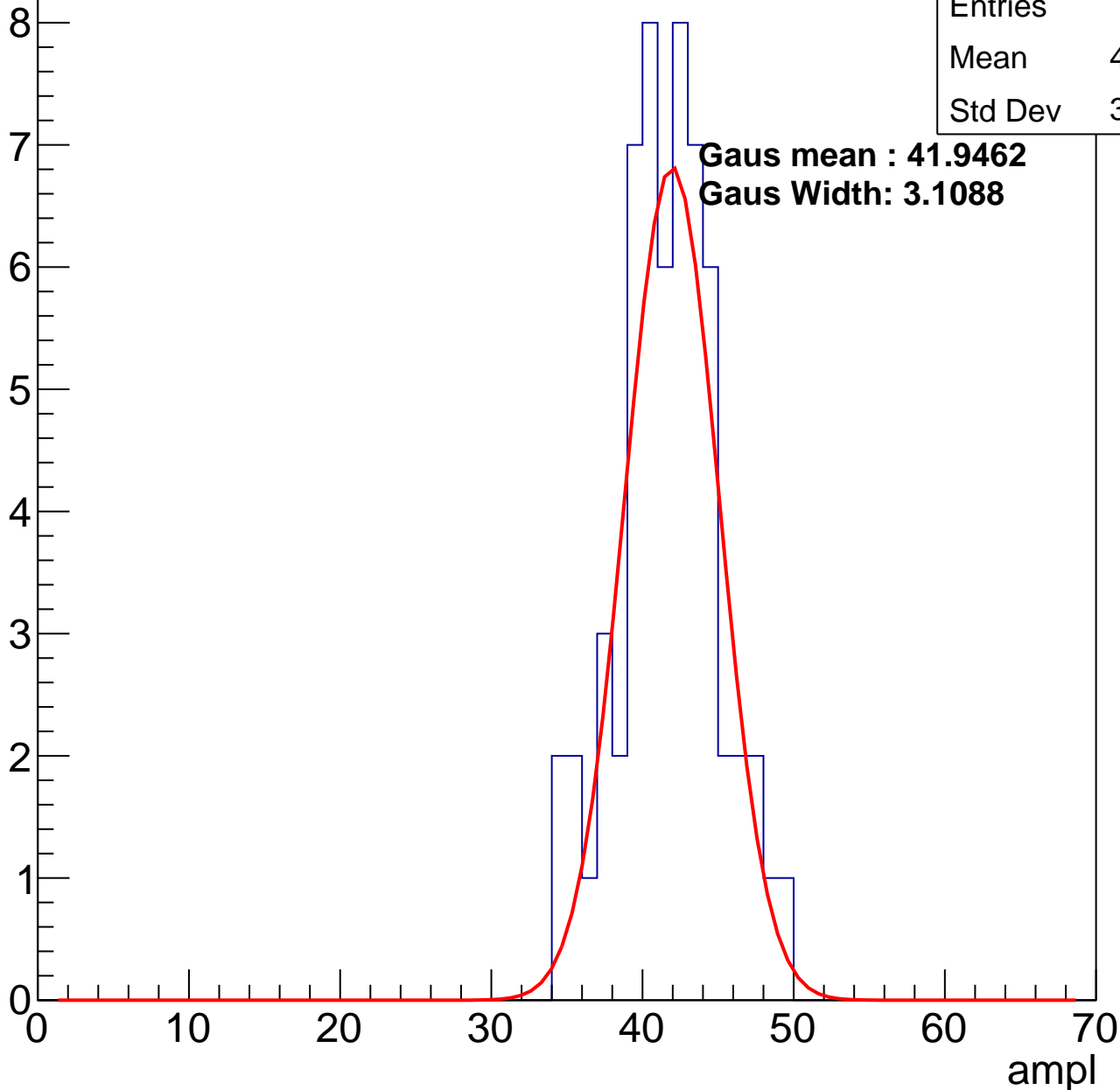
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.23
Std Dev	3.293

**Gaus mean : 41.9462**

**Gaus Width: 3.1088**

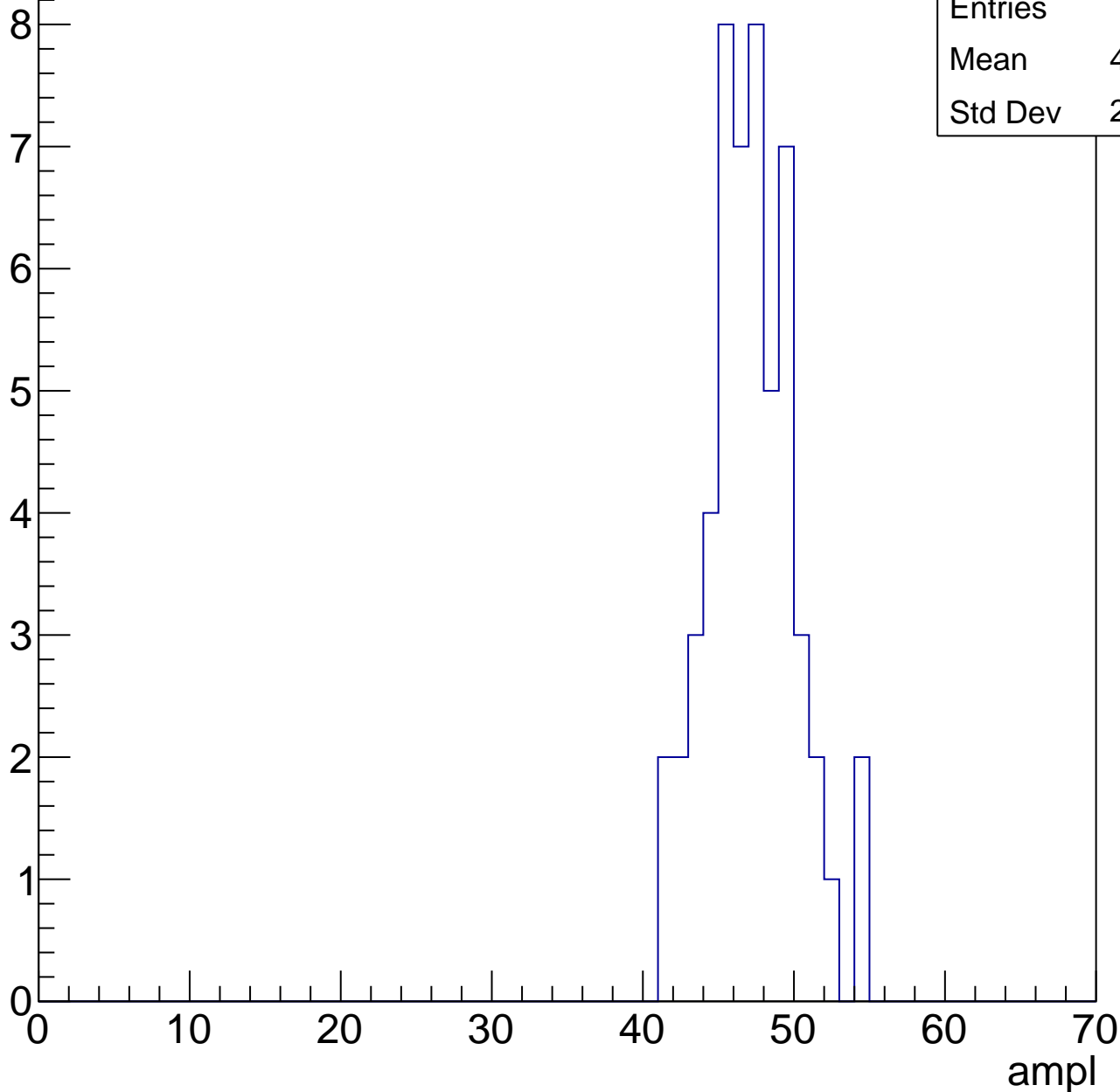


# B1L002S, U18-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	46.74
Std Dev	2.907

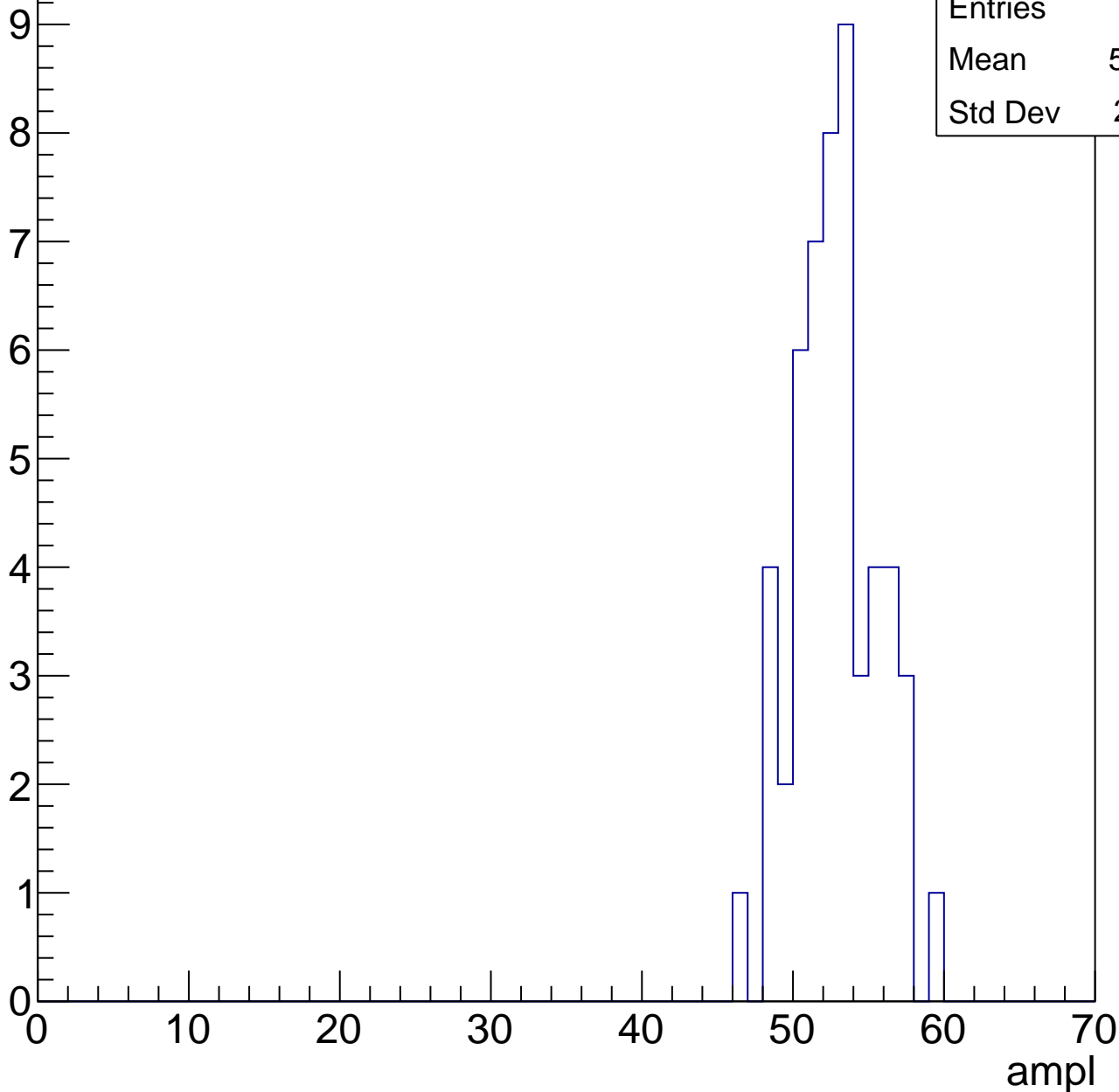


# B1L002S, U18-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.35
Std Dev	2.731



# B1L002S, U18-ch32, adc5

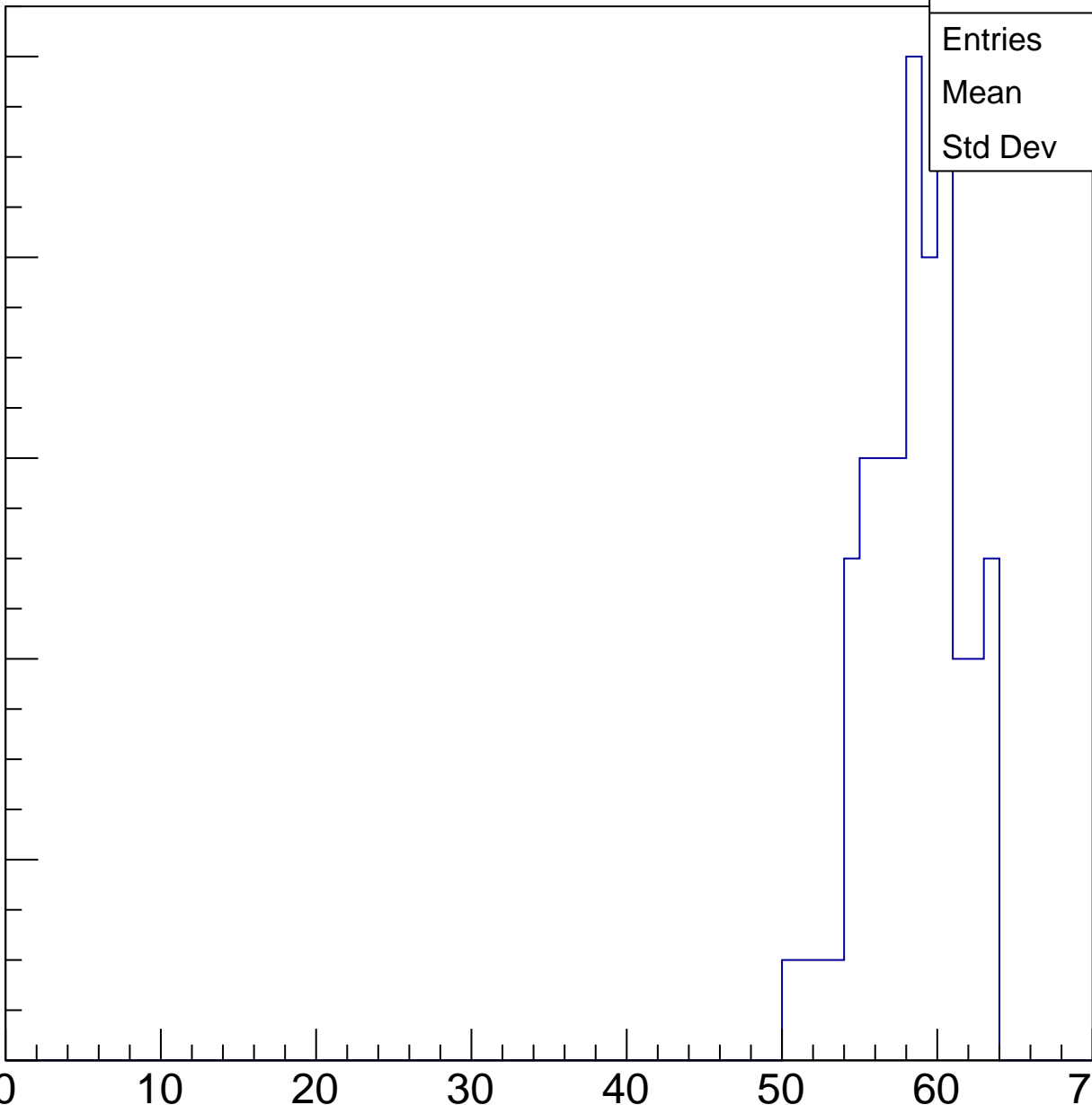
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	67
Mean	57.96
Std Dev	3.01

ampl



# B1L002S, U18-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

6

5

4

3

2

1

0

Entries	20
Mean	58.4
Std Dev	13.48

ampl

0

10

20

30

40

50

60

70



# B1L002S, U18-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	87
Mean	27.79
Std Dev	4.506

**Gaus mean : 28.7836**

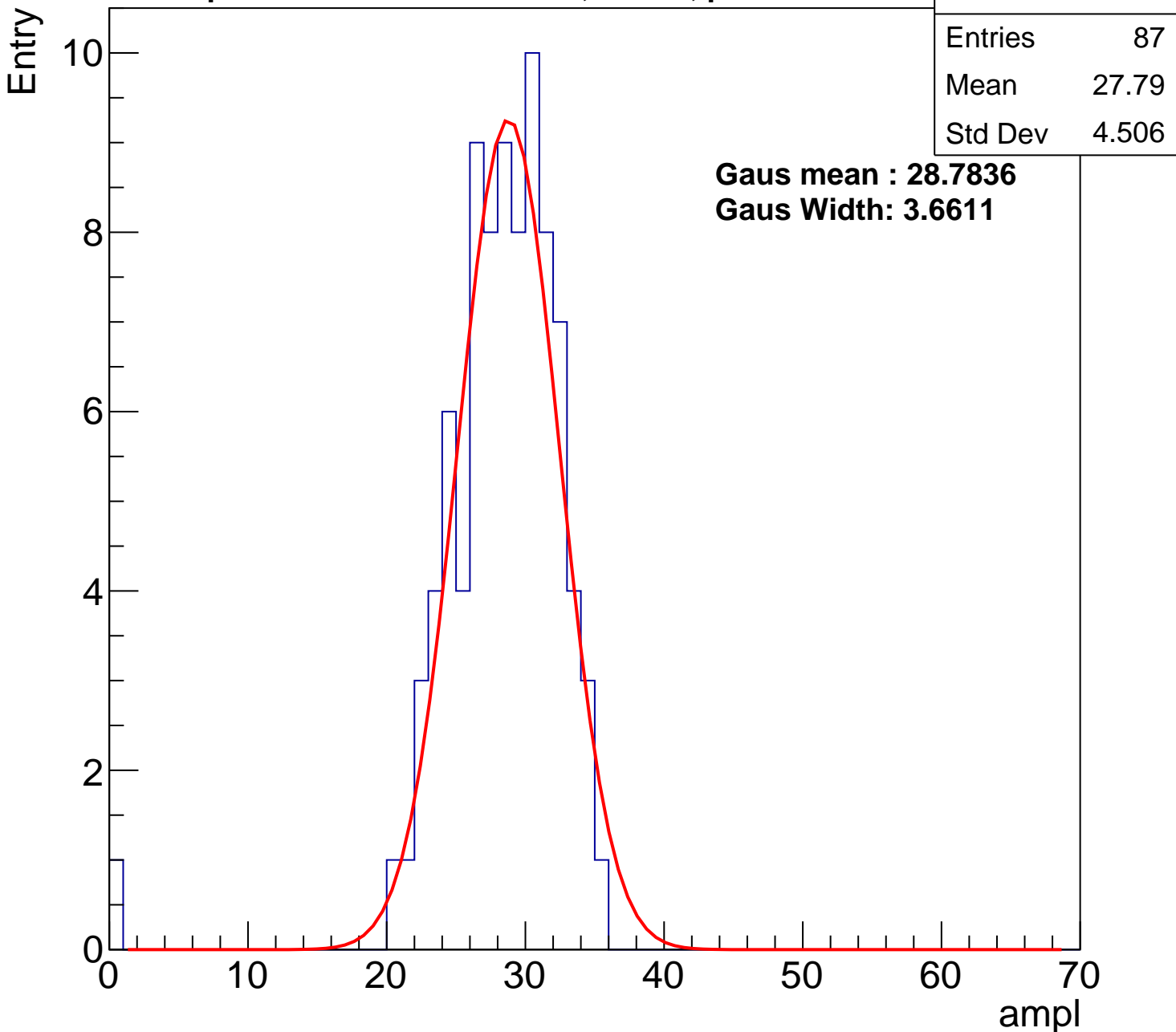
**Gaus Width: 3.6611**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



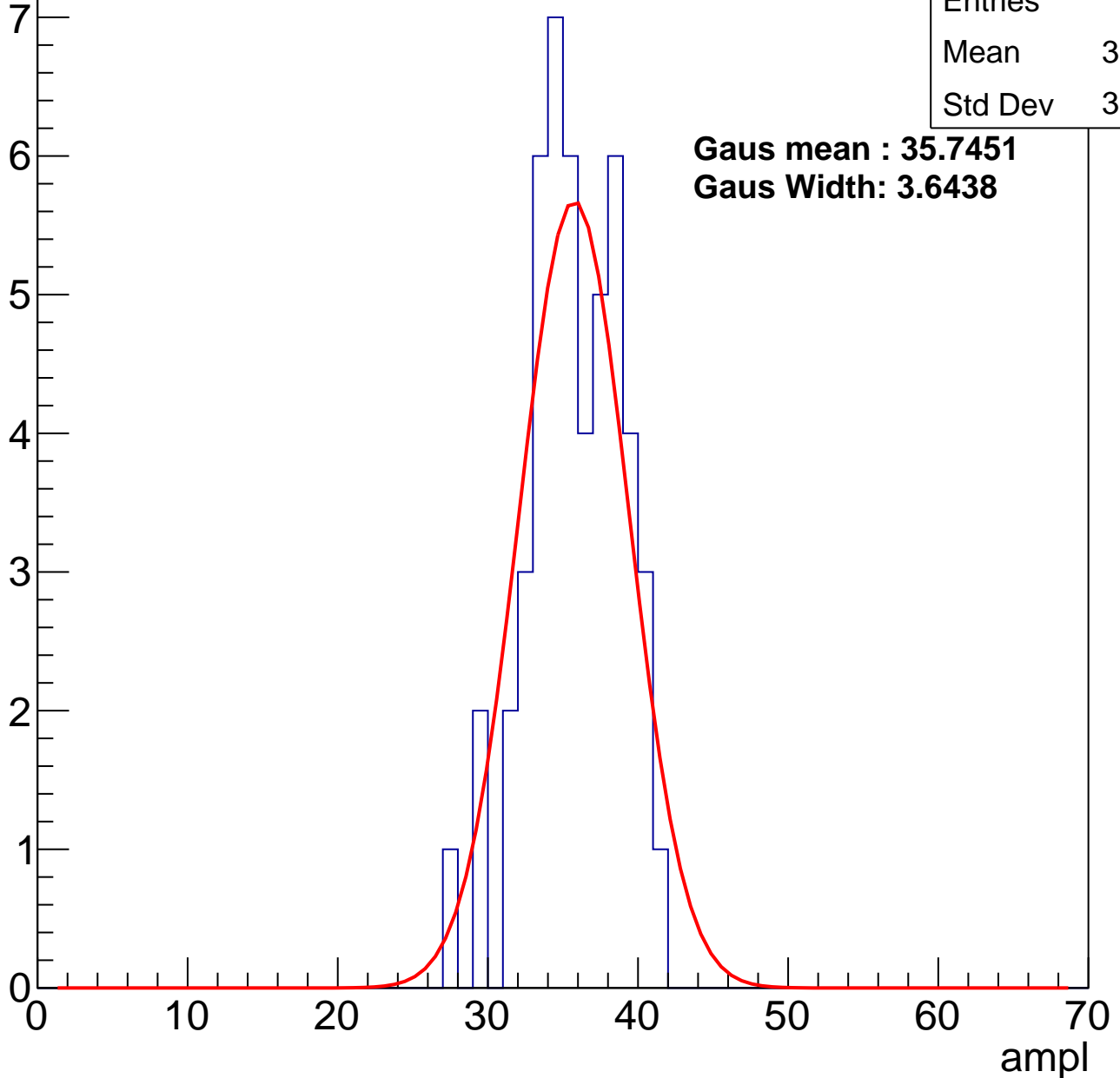
# B1L002S, U18-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	35.26
Std Dev	3.084

**Gaus mean : 35.7451**  
**Gaus Width: 3.6438**



# B1L002S, U18-ch33, adc2

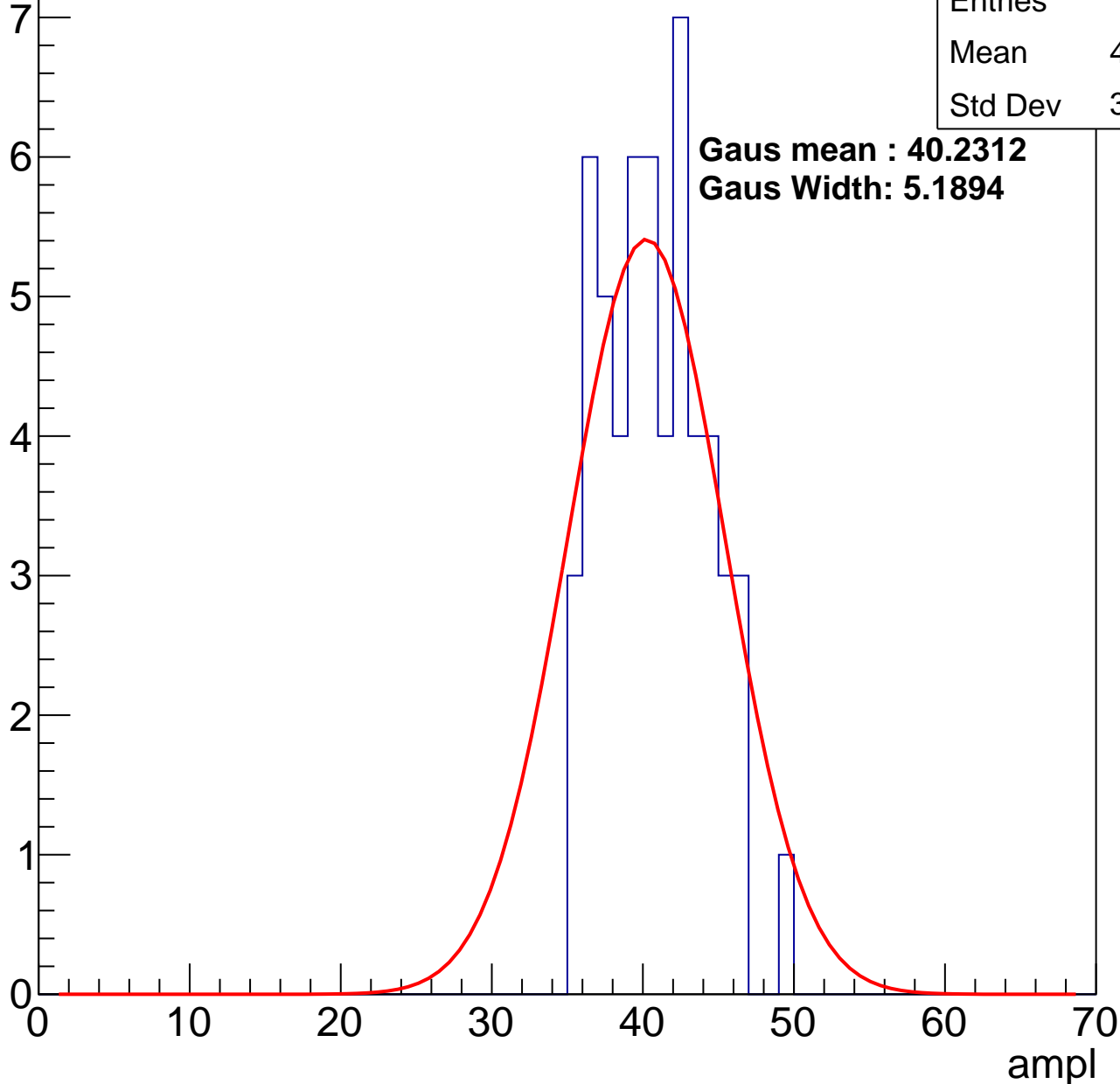
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.36
Std Dev	3.346

**Gaus mean : 40.2312**

**Gaus Width: 5.1894**

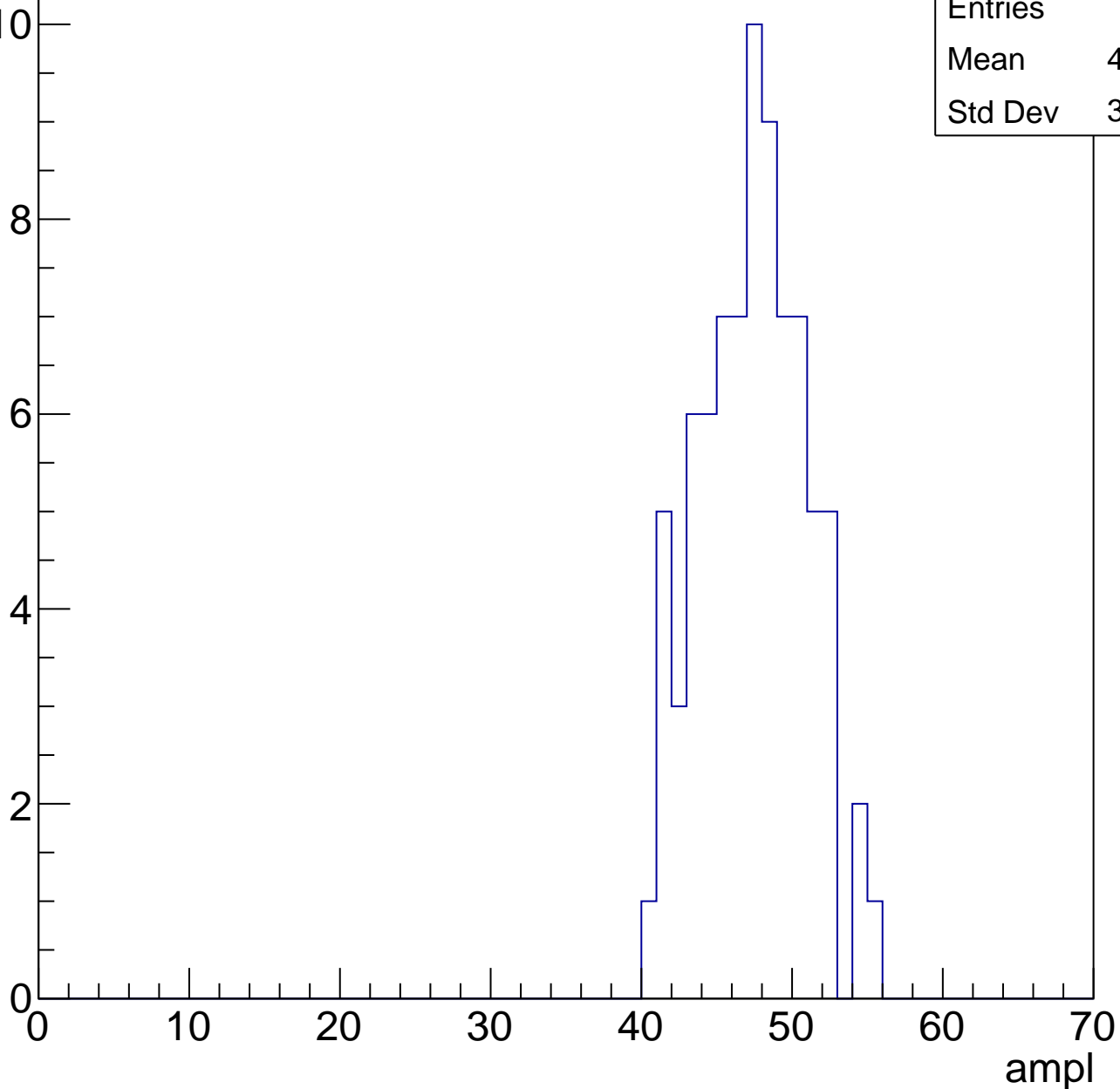


# B1L002S, U18-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	46.95
Std Dev	3.435

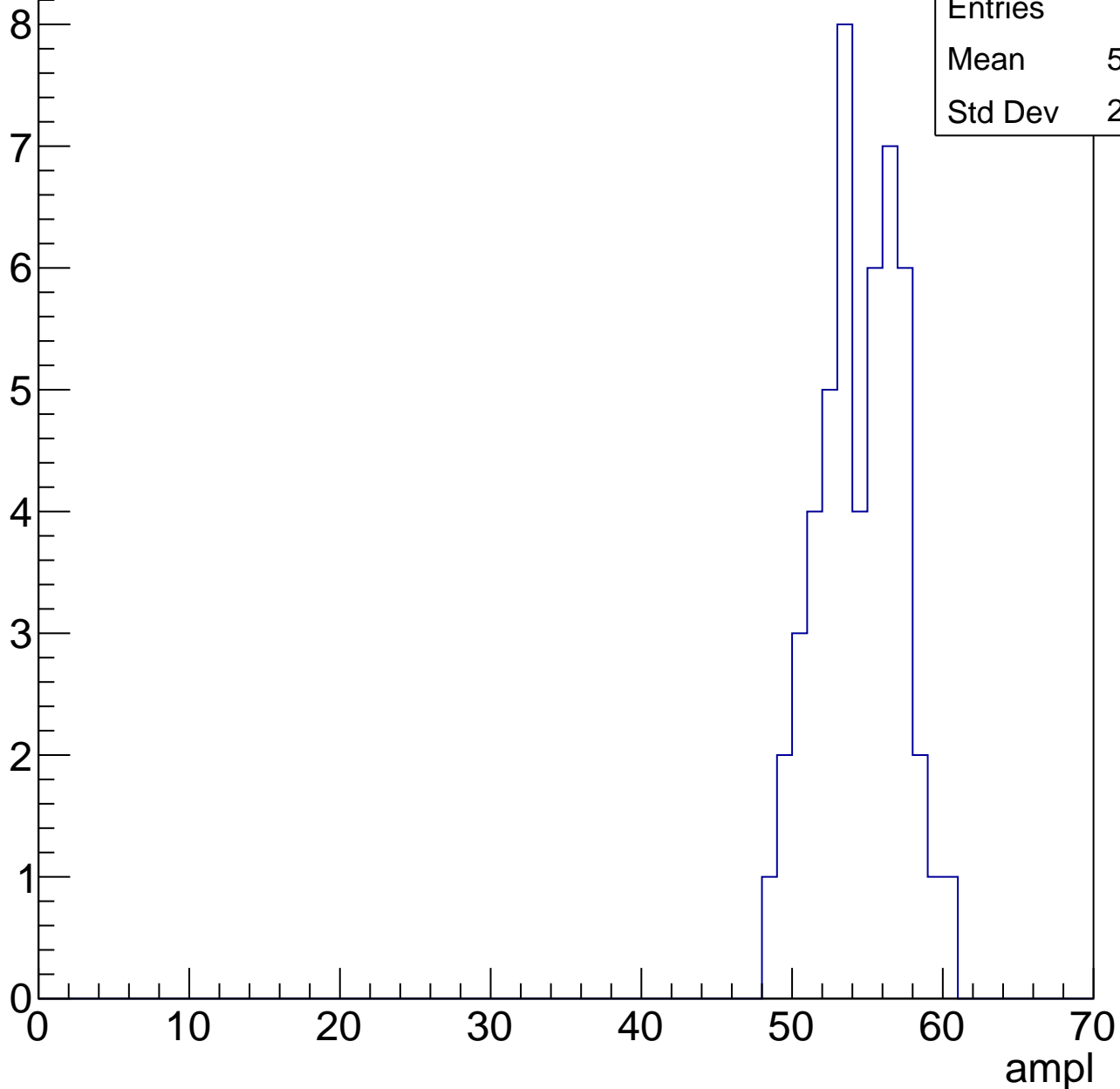


# B1L002S, U18-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	53.98
Std Dev	2.753

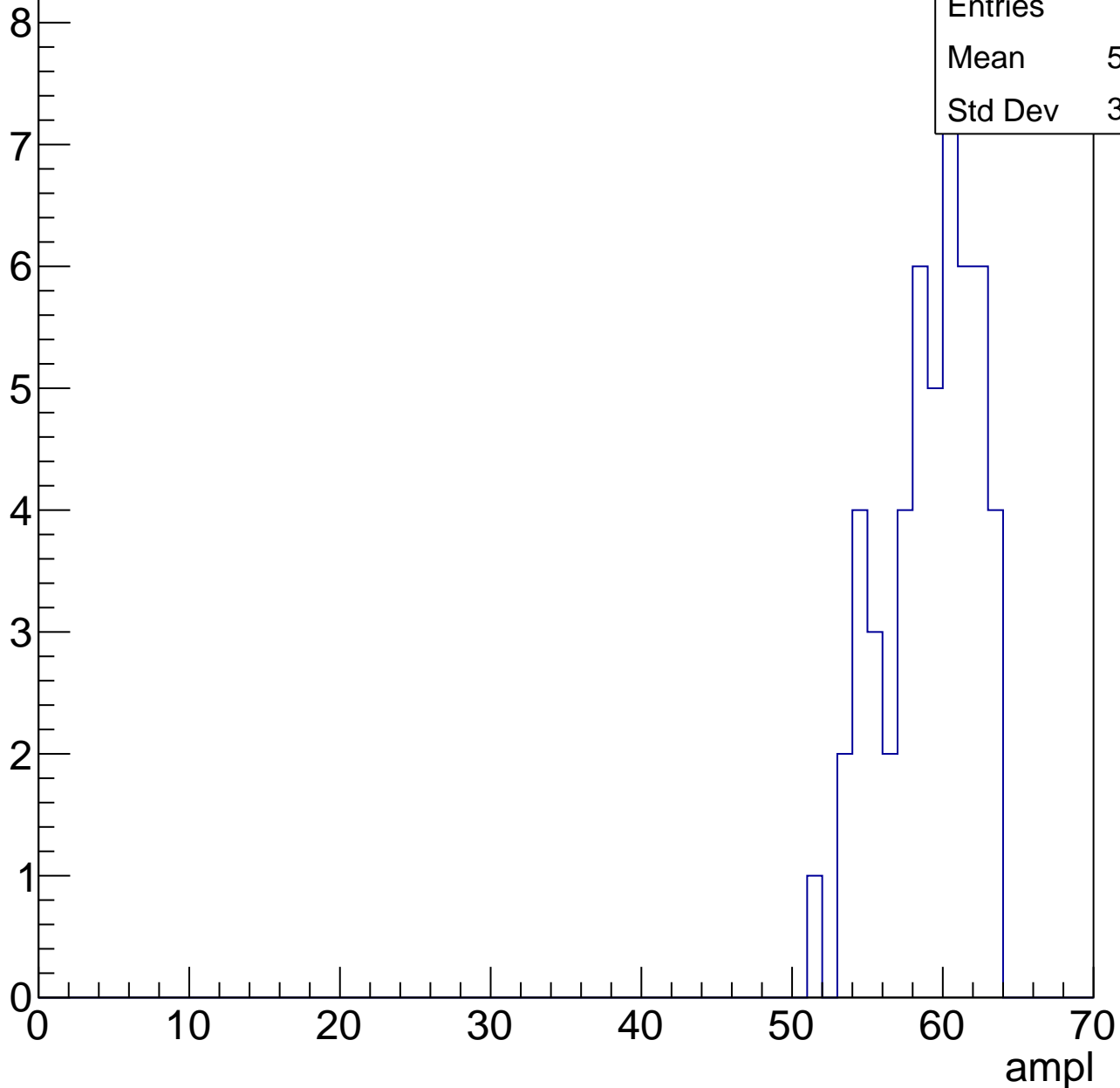


# B1L002S, U18-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.65
Std Dev	3.028

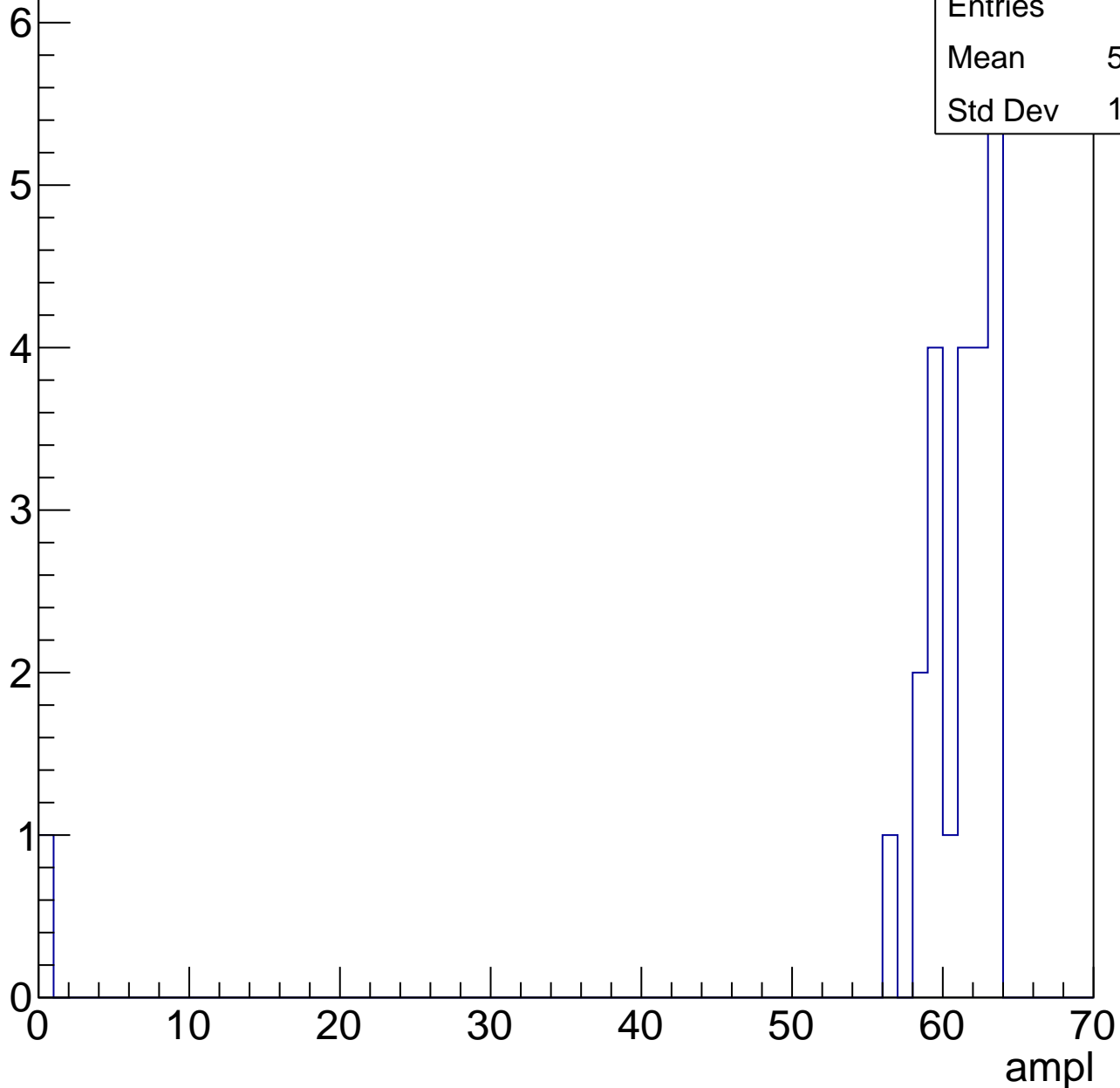


# B1L002S, U18-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.17
Std Dev	12.55





# B1L002S, U18-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U18-ch34, adc0

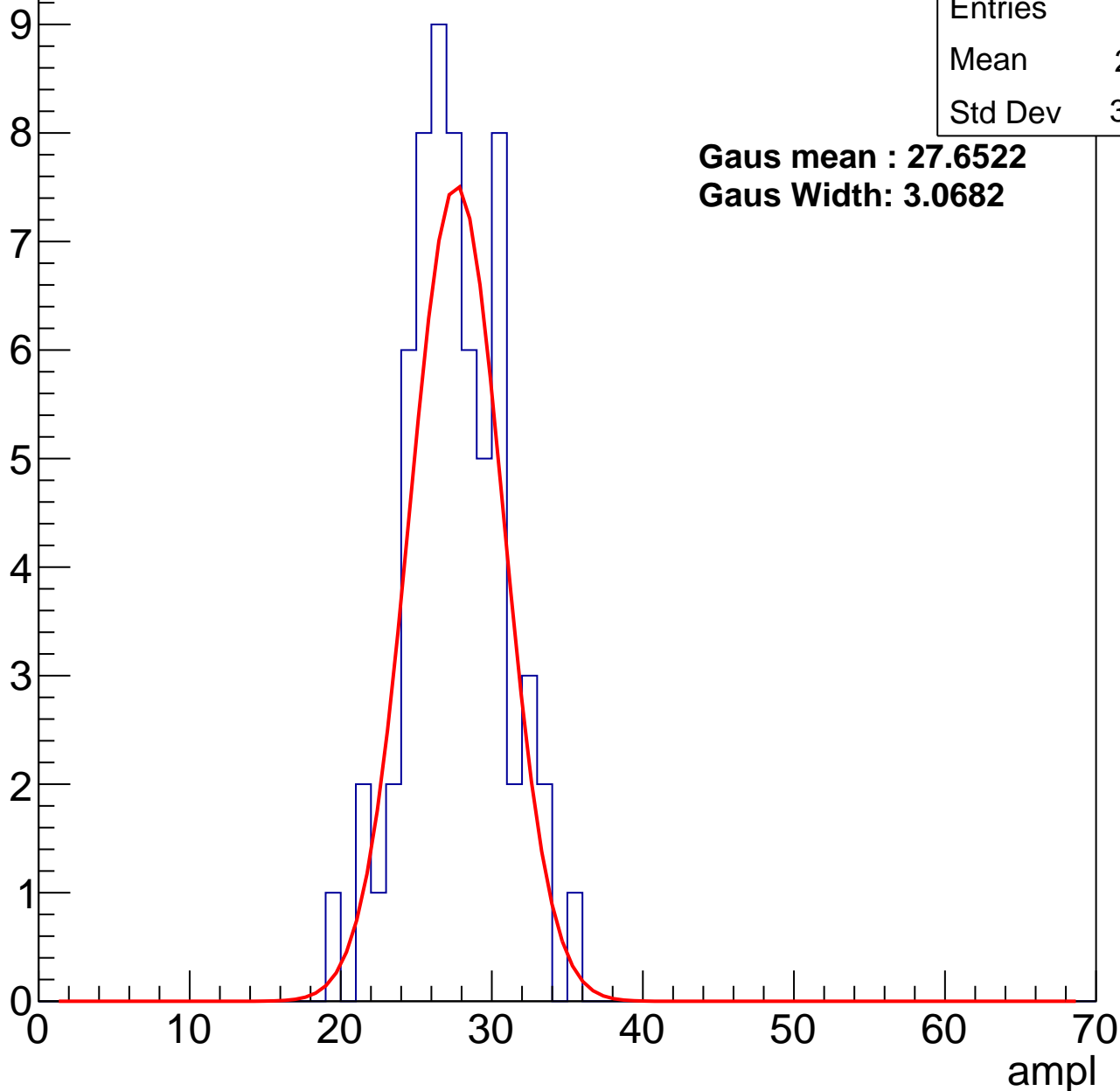
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.11
Std Dev	3.133

**Gaus mean : 27.6522**

**Gaus Width: 3.0682**



# B1L002S, U18-ch34, adc1

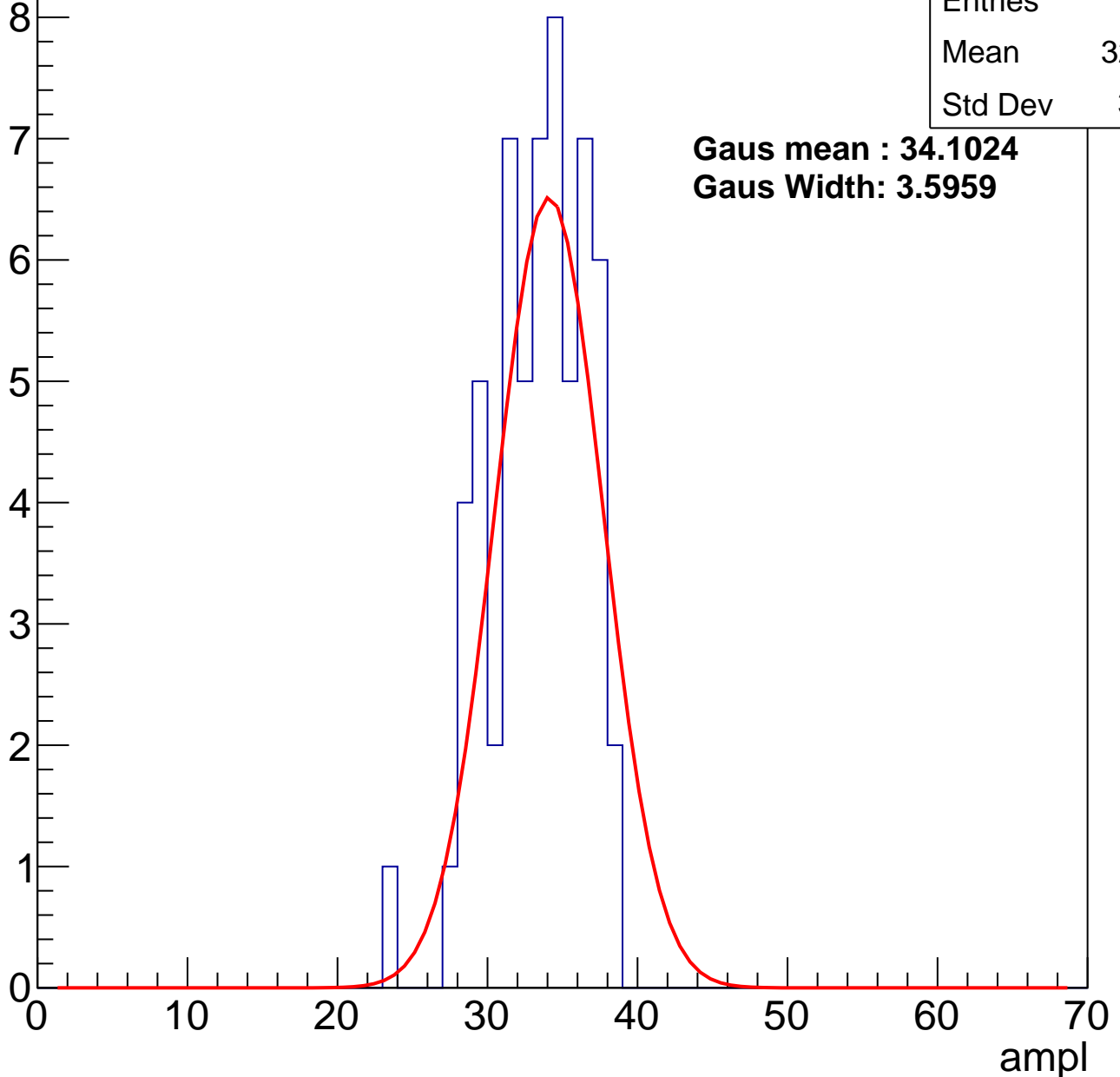
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	32.87
Std Dev	3.17

**Gaus mean : 34.1024**

**Gaus Width: 3.5959**



# B1L002S, U18-ch34, adc2

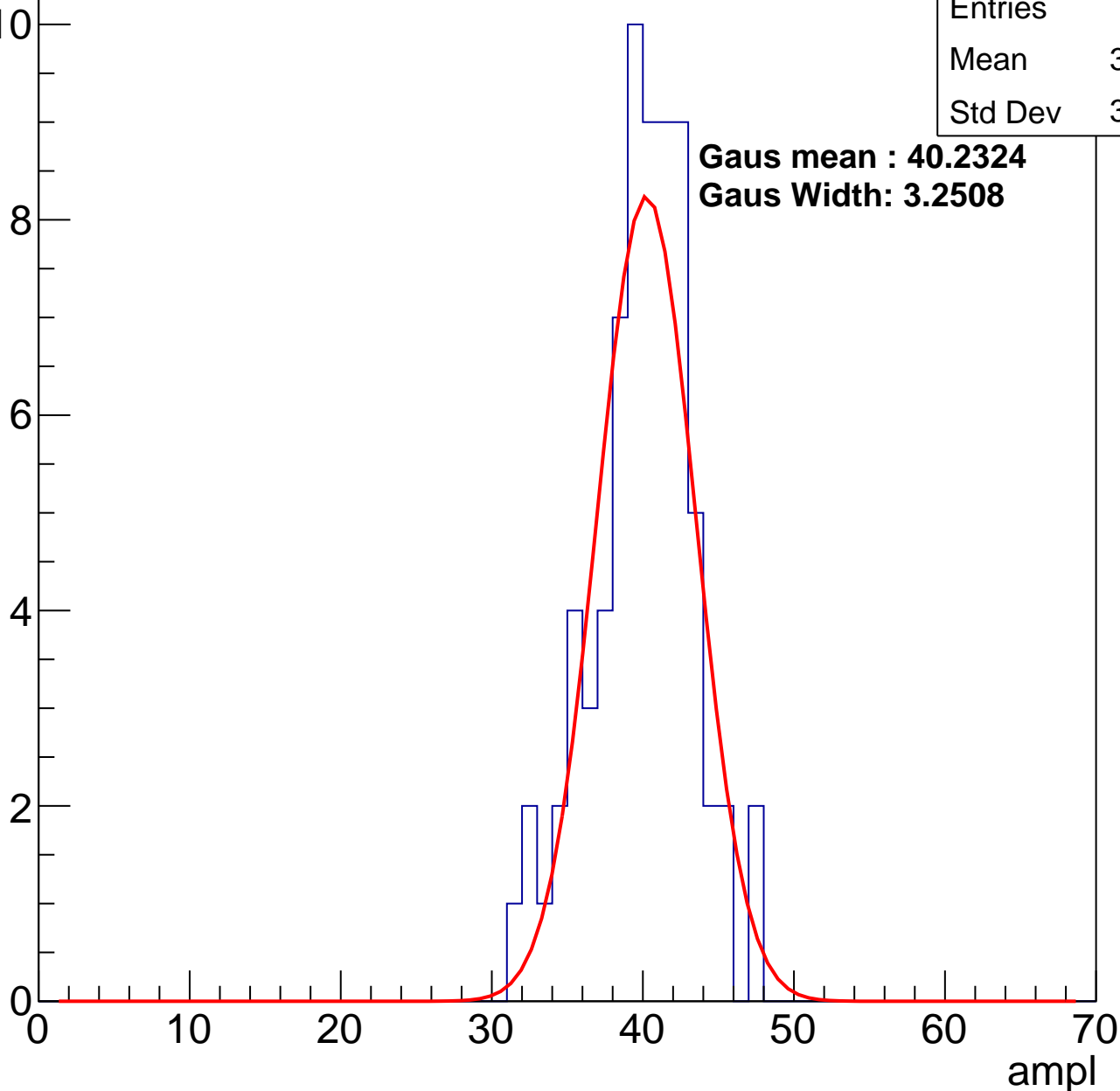
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	39.47
Std Dev	3.337

**Gaus mean : 40.2324**

**Gaus Width: 3.2508**



# B1L002S, U18-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	47.04
Std Dev	3.235

Entry

10

8

6

4

2

0

0

10

20

30

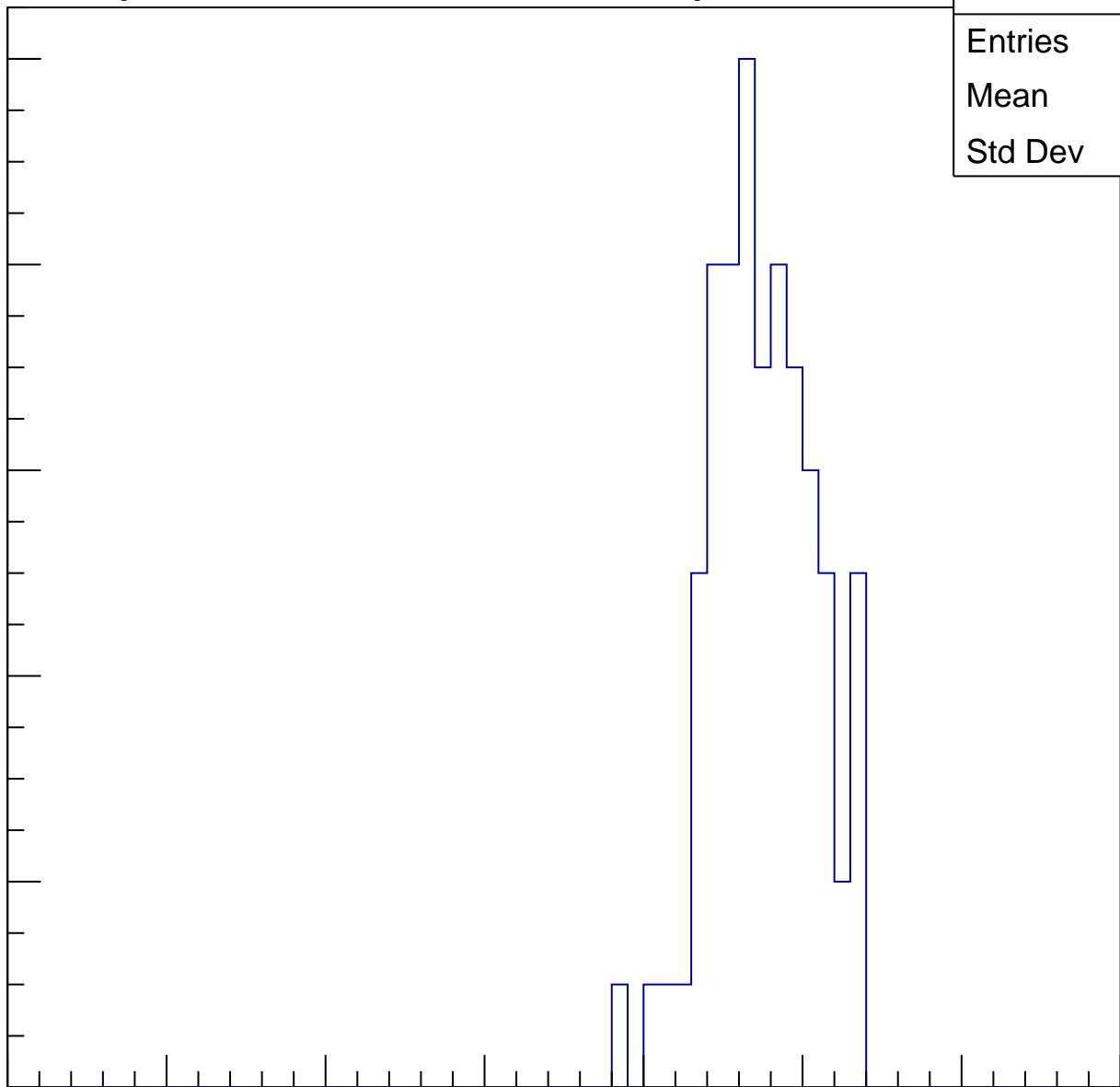
40

50

60

70

ampl

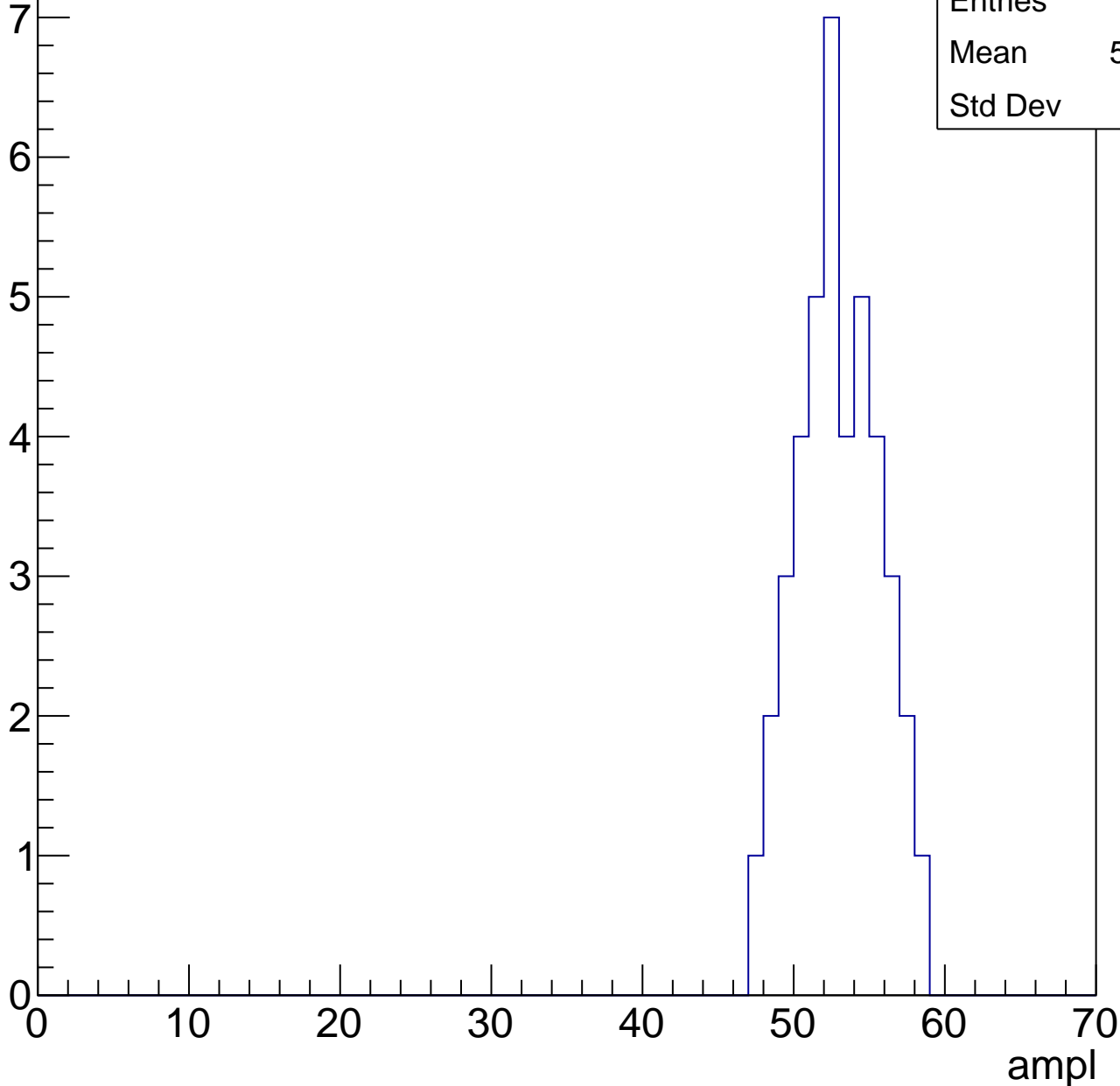


# B1L002S, U18-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	52.46
Std Dev	2.66

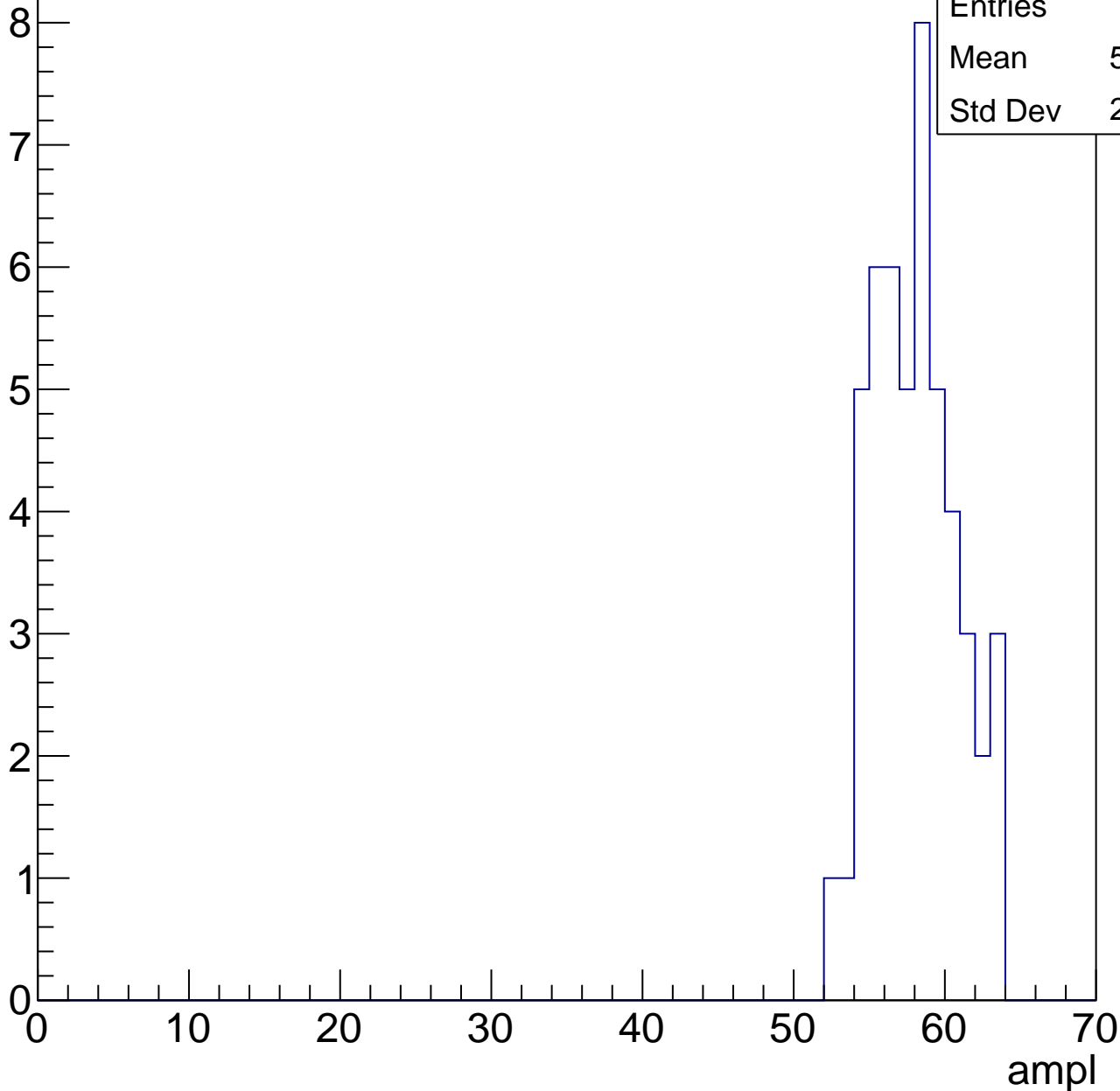


# B1L002S, U18-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	57.57
Std Dev	2.748

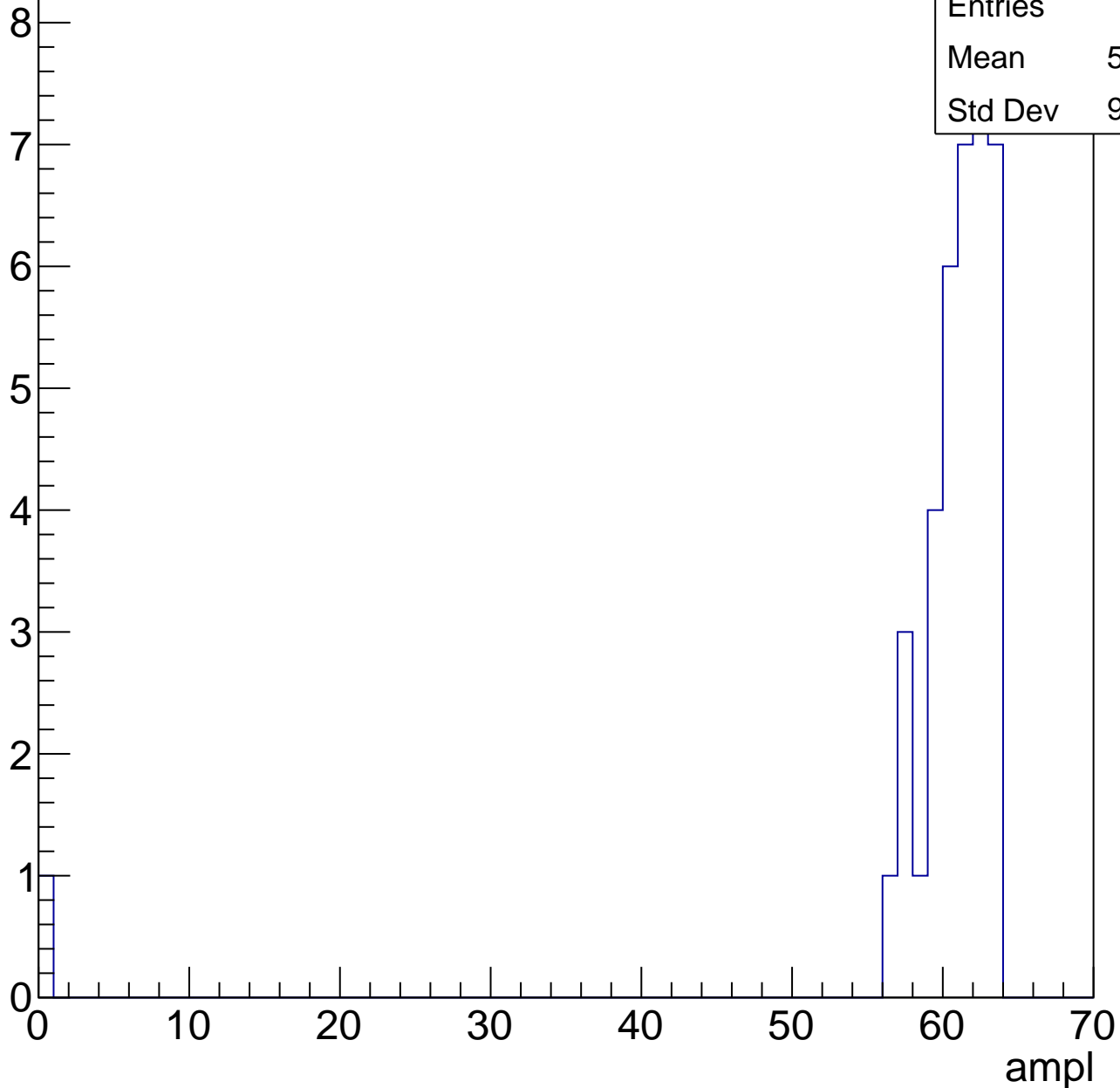


# B1L002S, U18-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	59.08
Std Dev	9.895

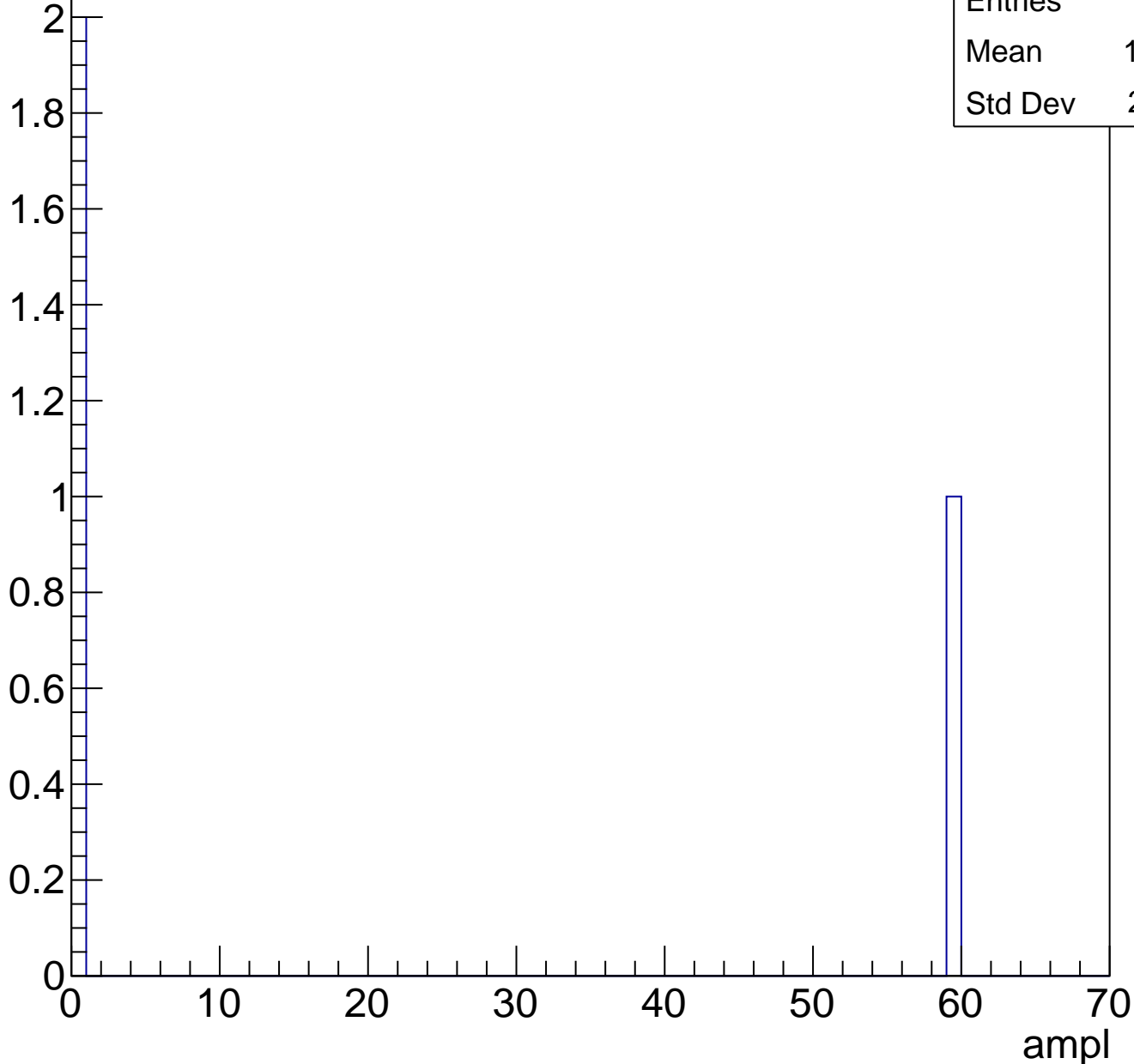




# B1L002S, U18-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch35, adc0

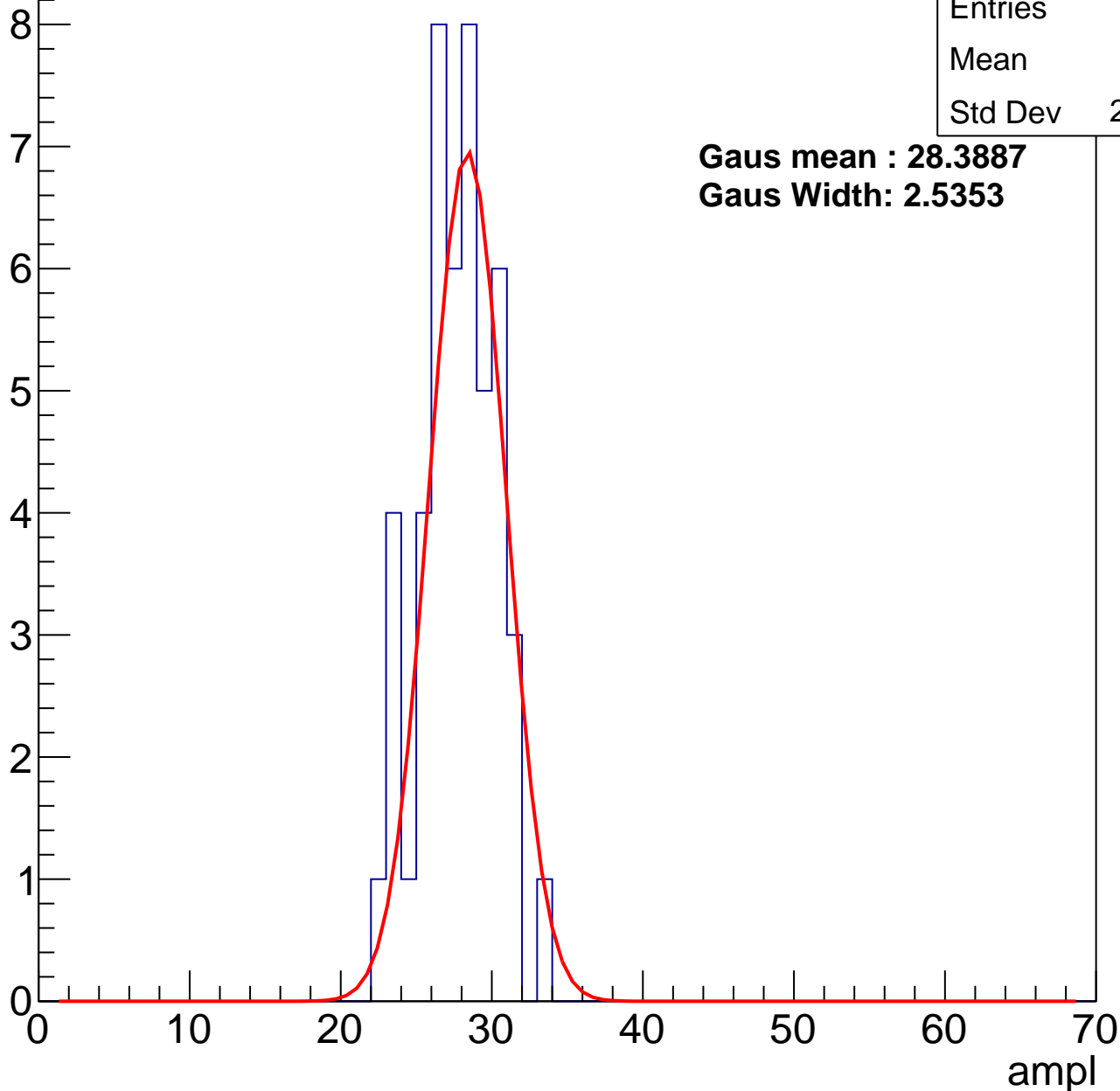
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	27.3
Std Dev	2.457

**Gaus mean : 28.3887**

**Gaus Width: 2.5353**



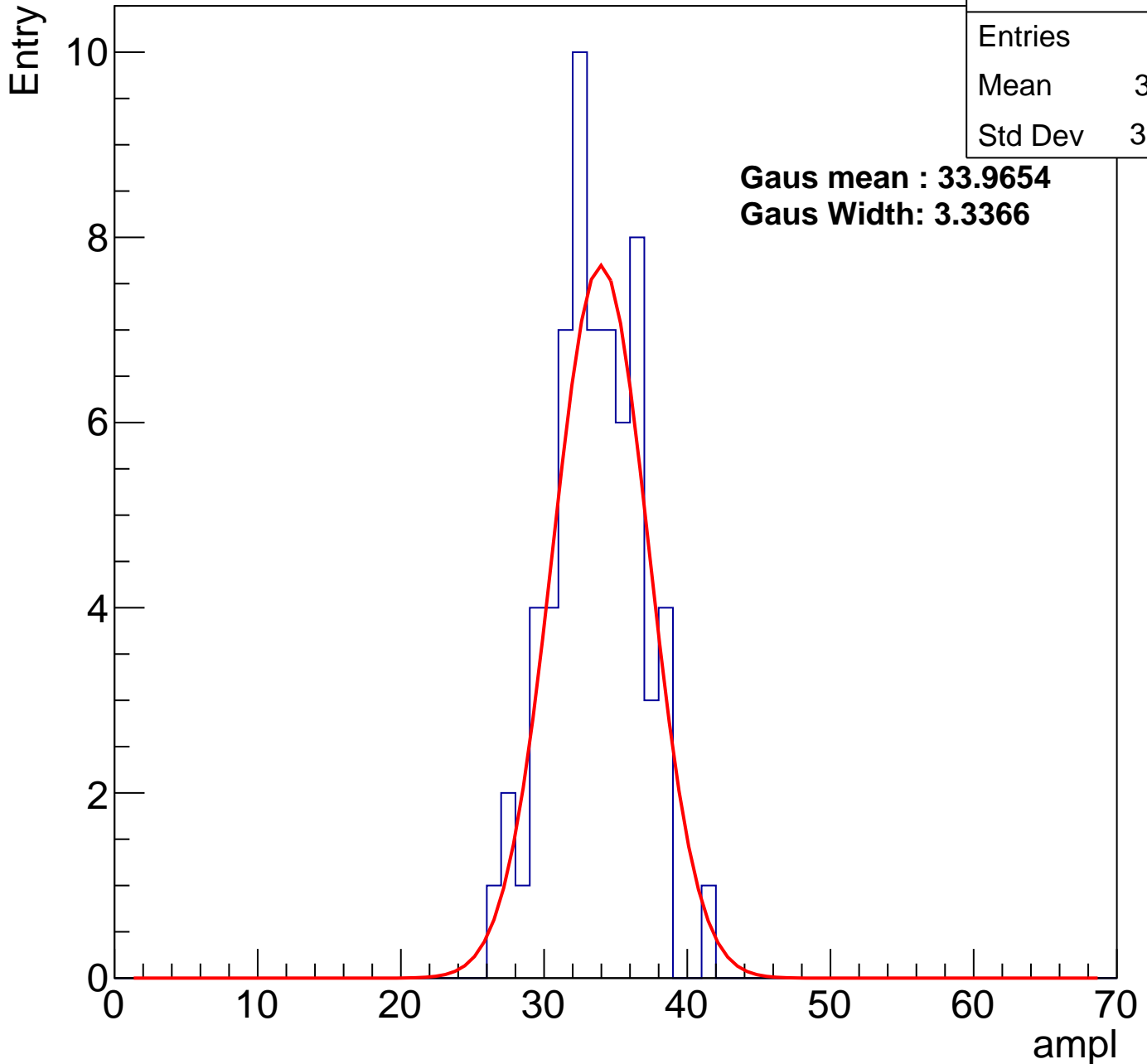
# B1L002S, U18-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	33.11
Std Dev	3.034

**Gaus mean : 33.9654**

**Gaus Width: 3.3366**



# B1L002S, U18-ch35, adc2

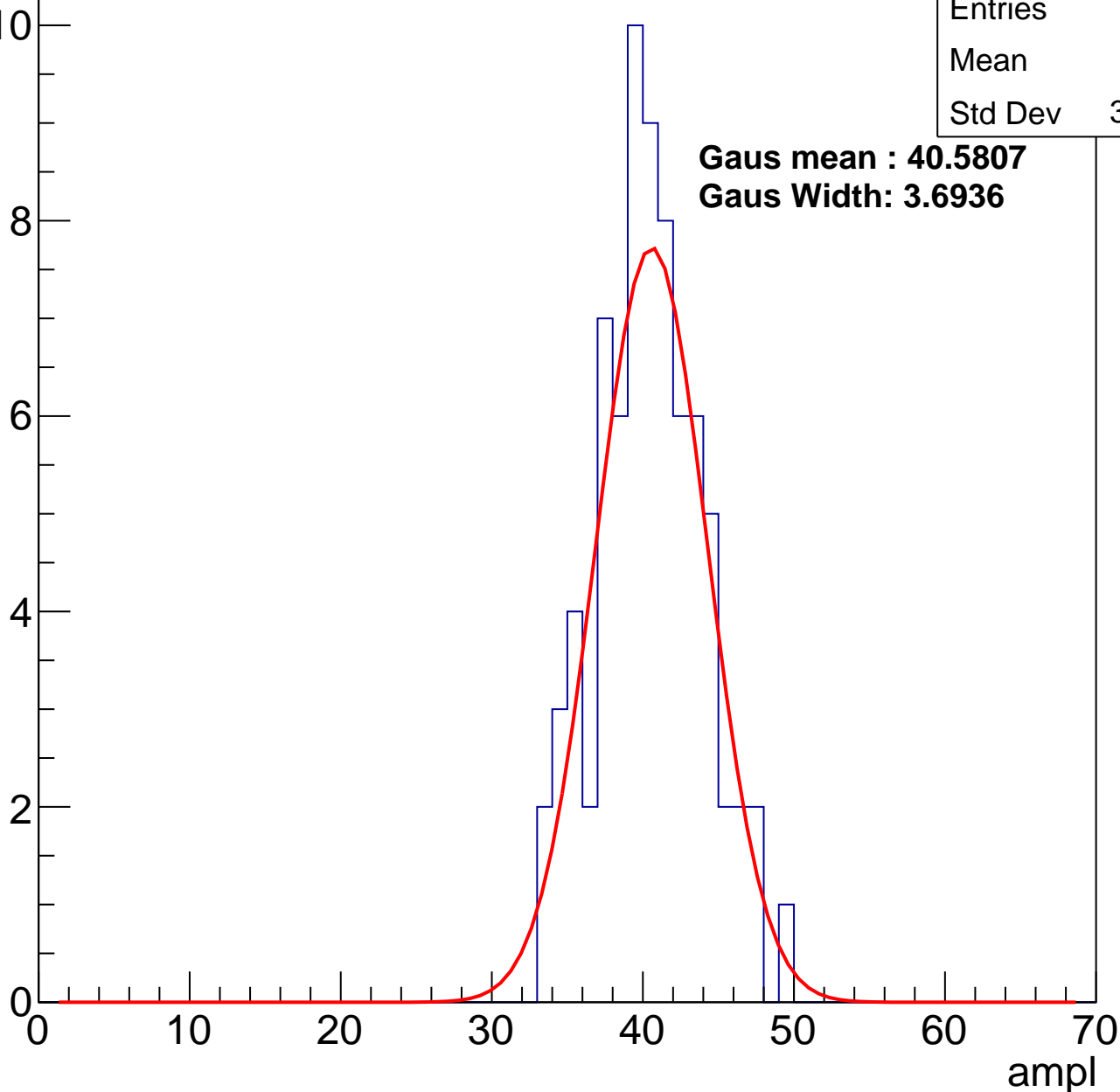
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	40
Std Dev	3.468

**Gaus mean : 40.5807**

**Gaus Width: 3.6936**

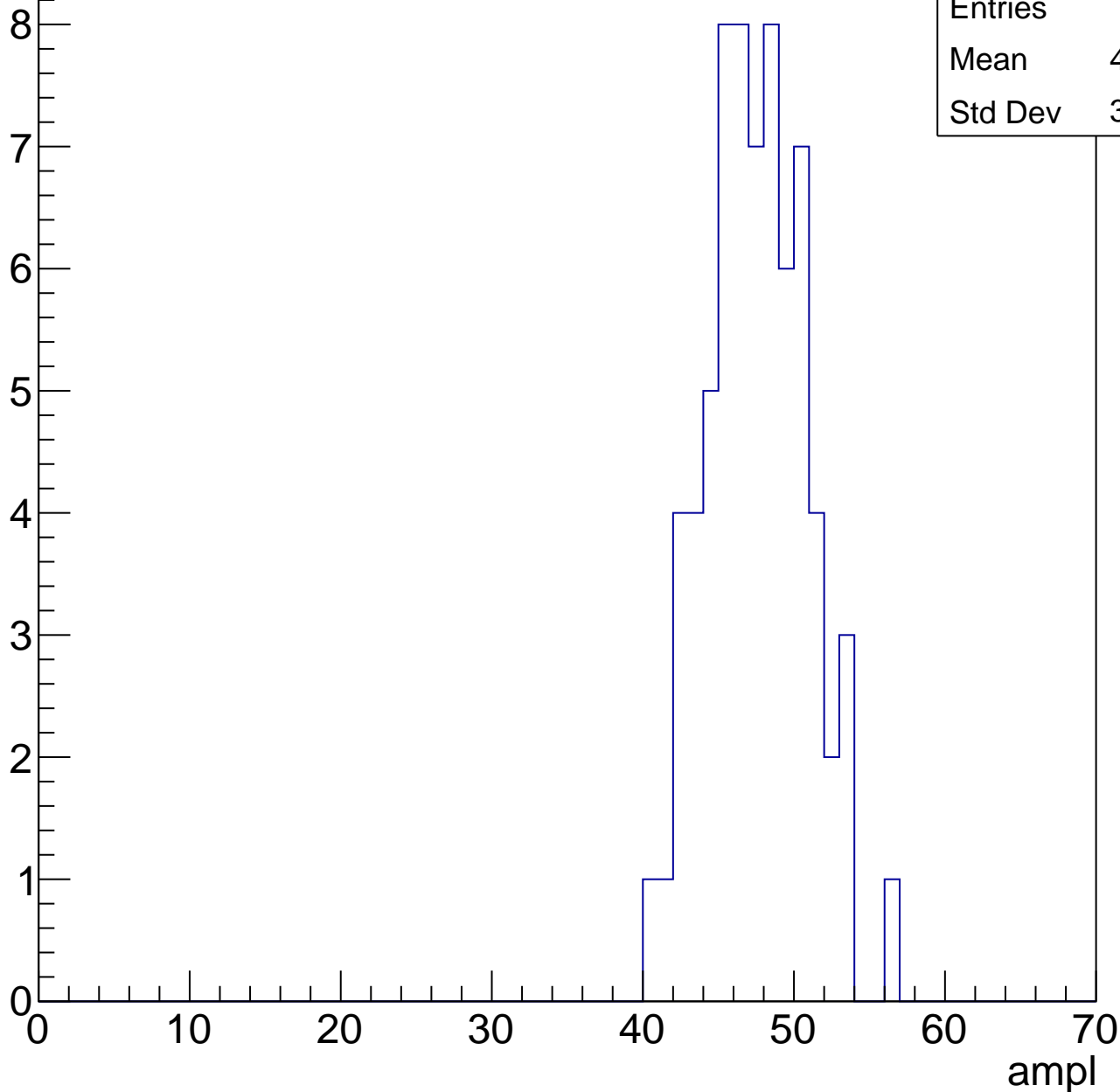


# B1L002S, U18-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	47.09
Std Dev	3.256

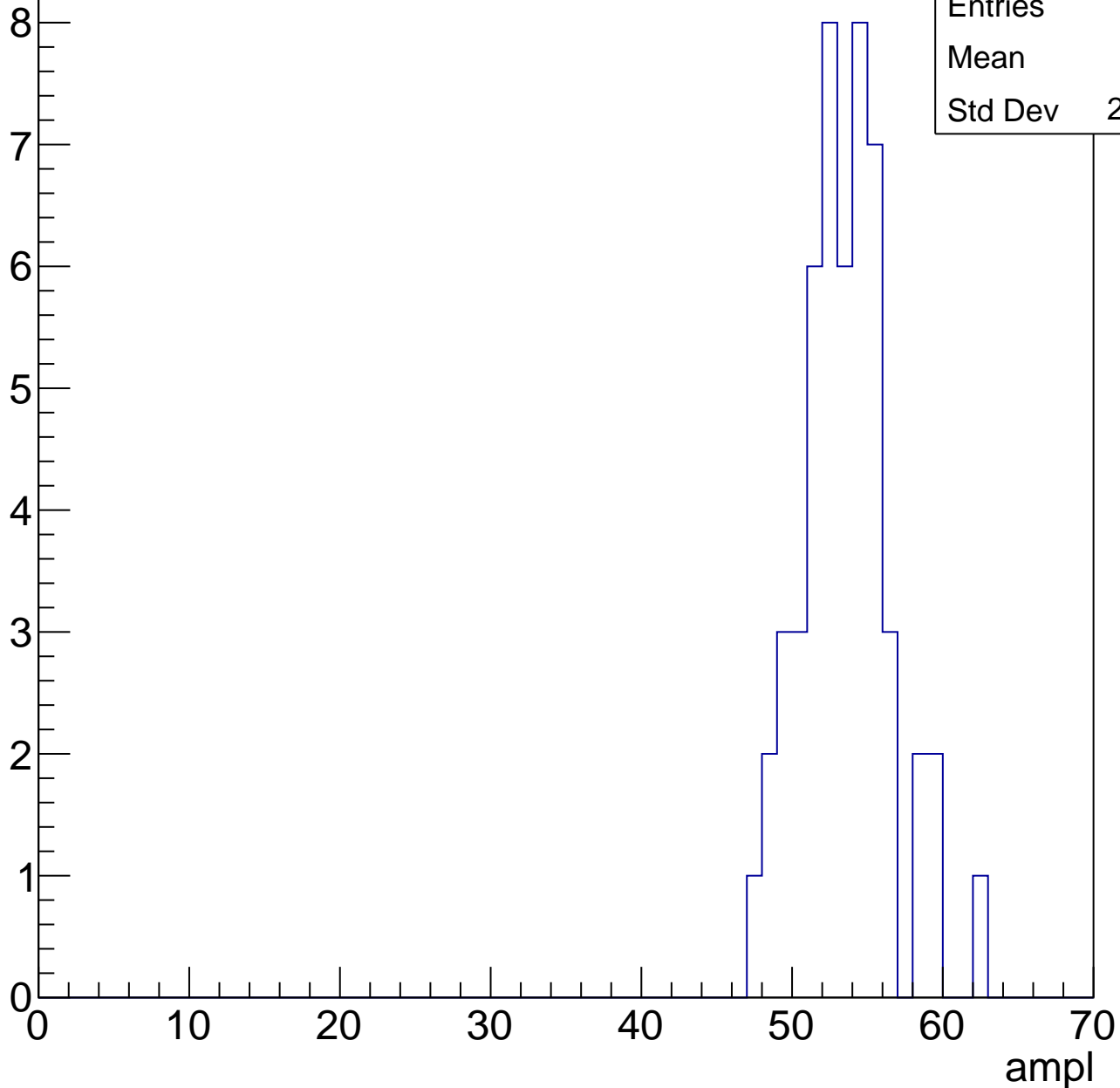


# B1L002S, U18-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.1
Std Dev	2.969



# B1L002S, U18-ch35, adc5

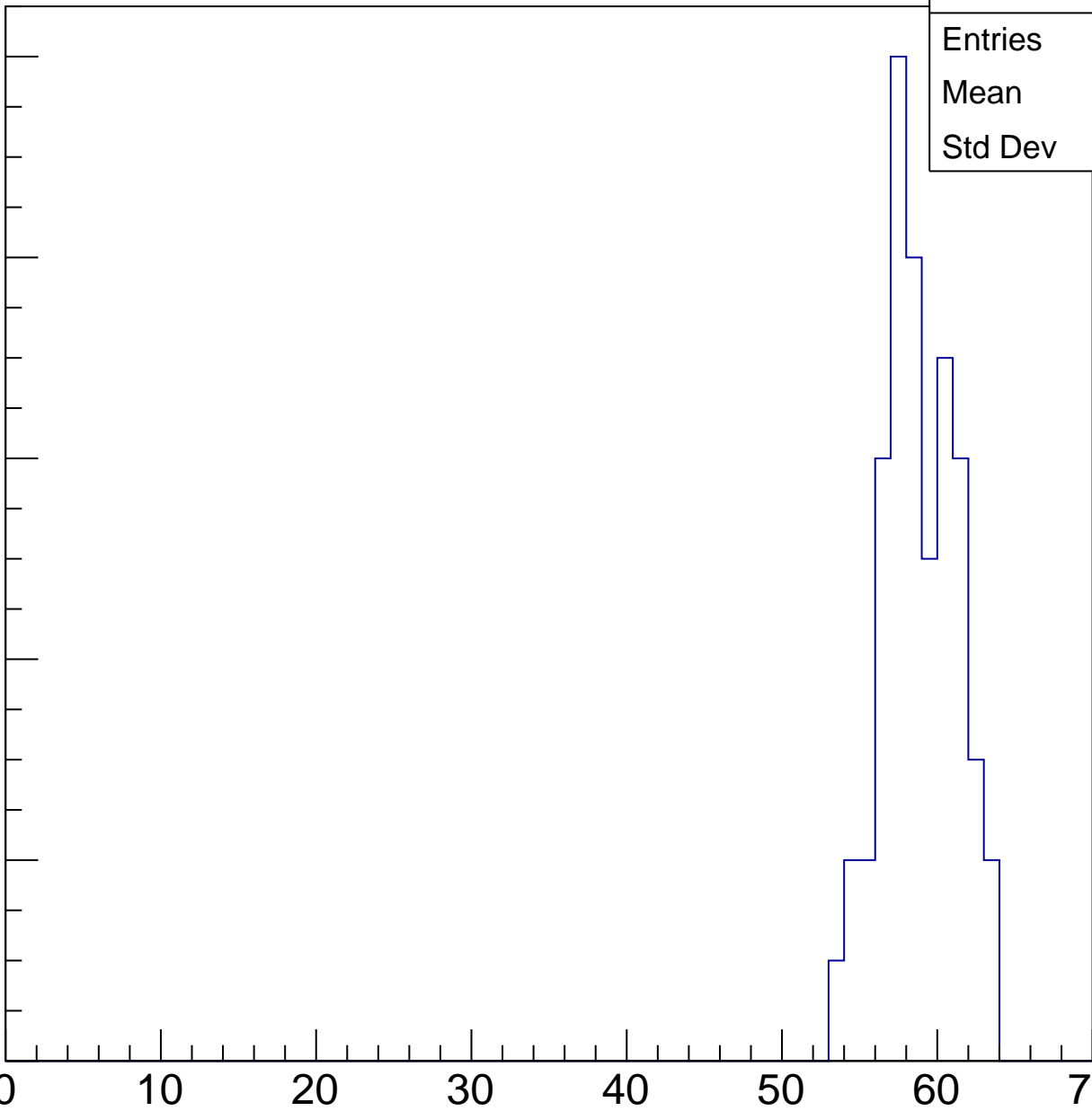
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	52
Mean	58.35
Std Dev	2.352

ampl

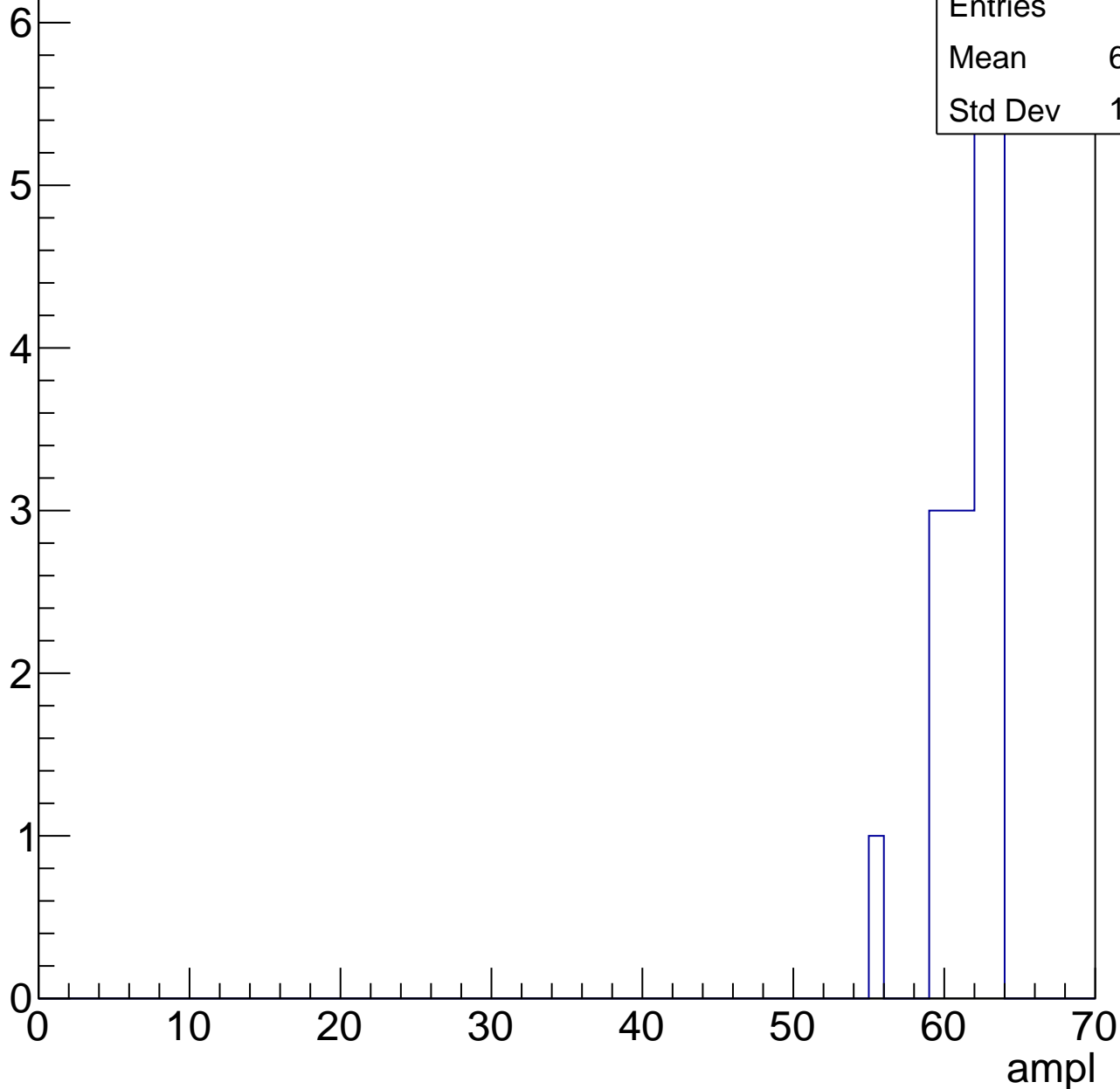


# B1L002S, U18-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	61.14
Std Dev	1.914

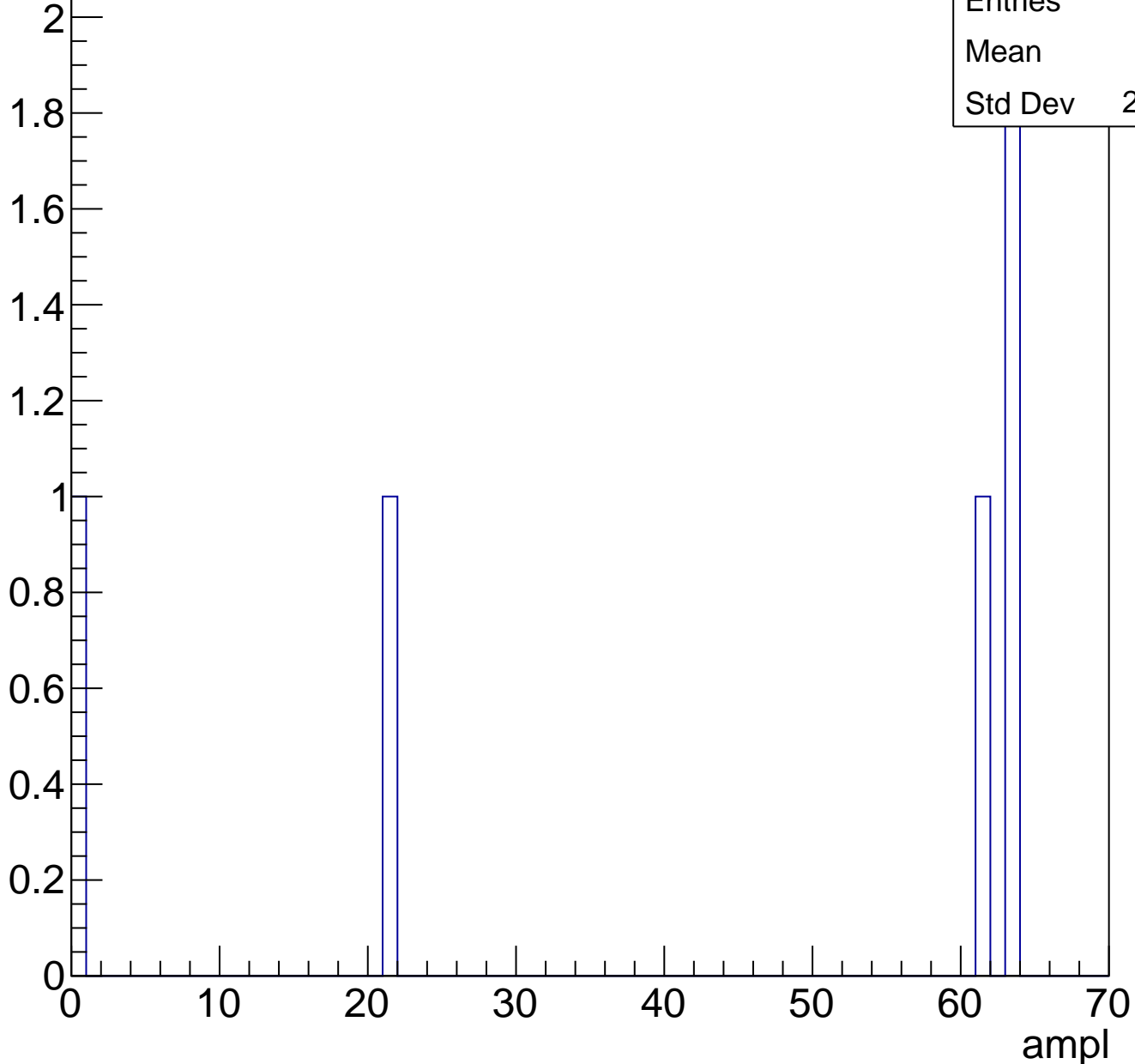




# B1L002S, U18-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	41.6
Std Dev	26.26

# B1L002S, U18-ch36, adc0

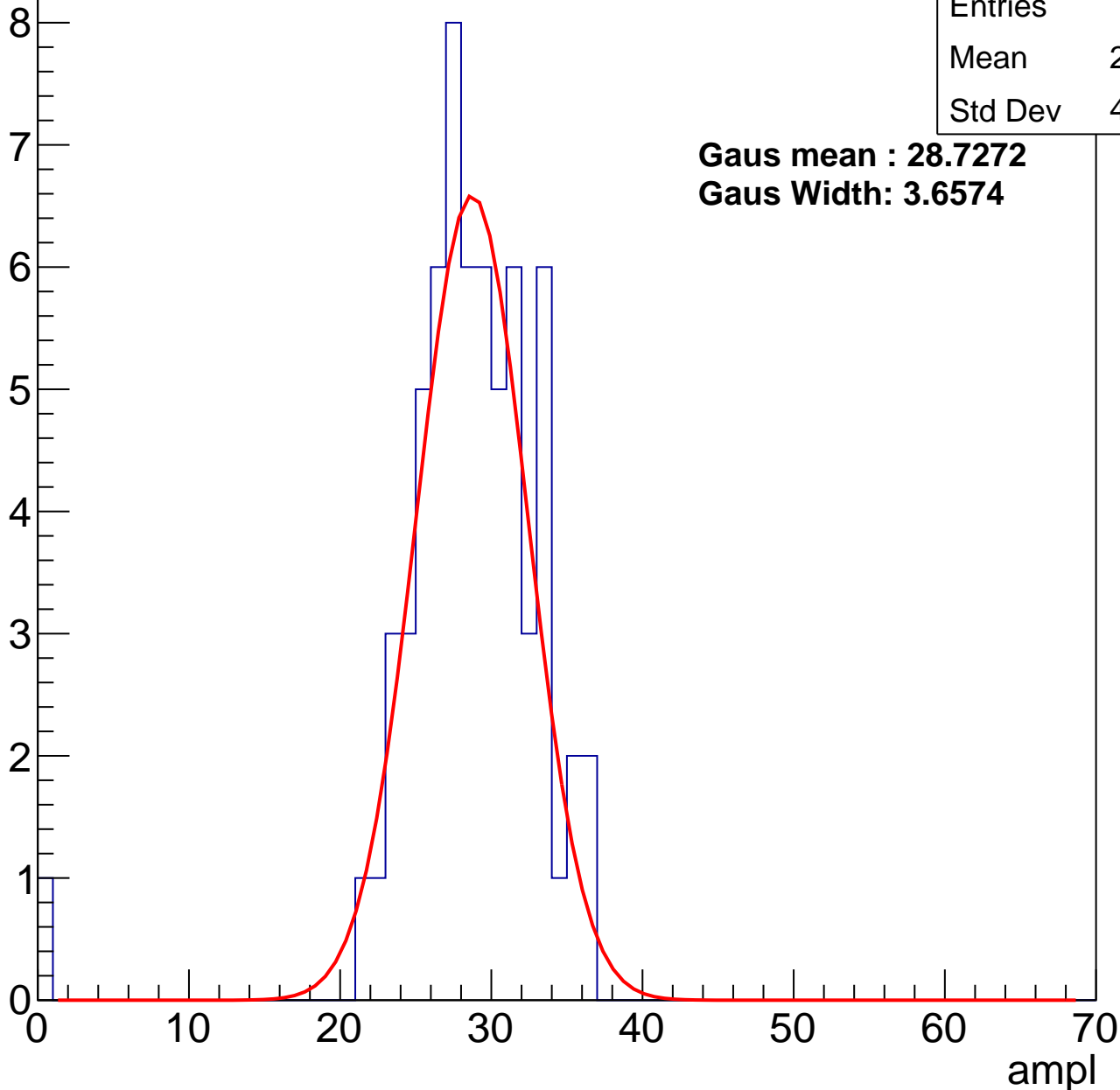
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.14
Std Dev	4.973

**Gaus mean : 28.7272**

**Gaus Width: 3.6574**



# B1L002S, U18-ch36, adc1

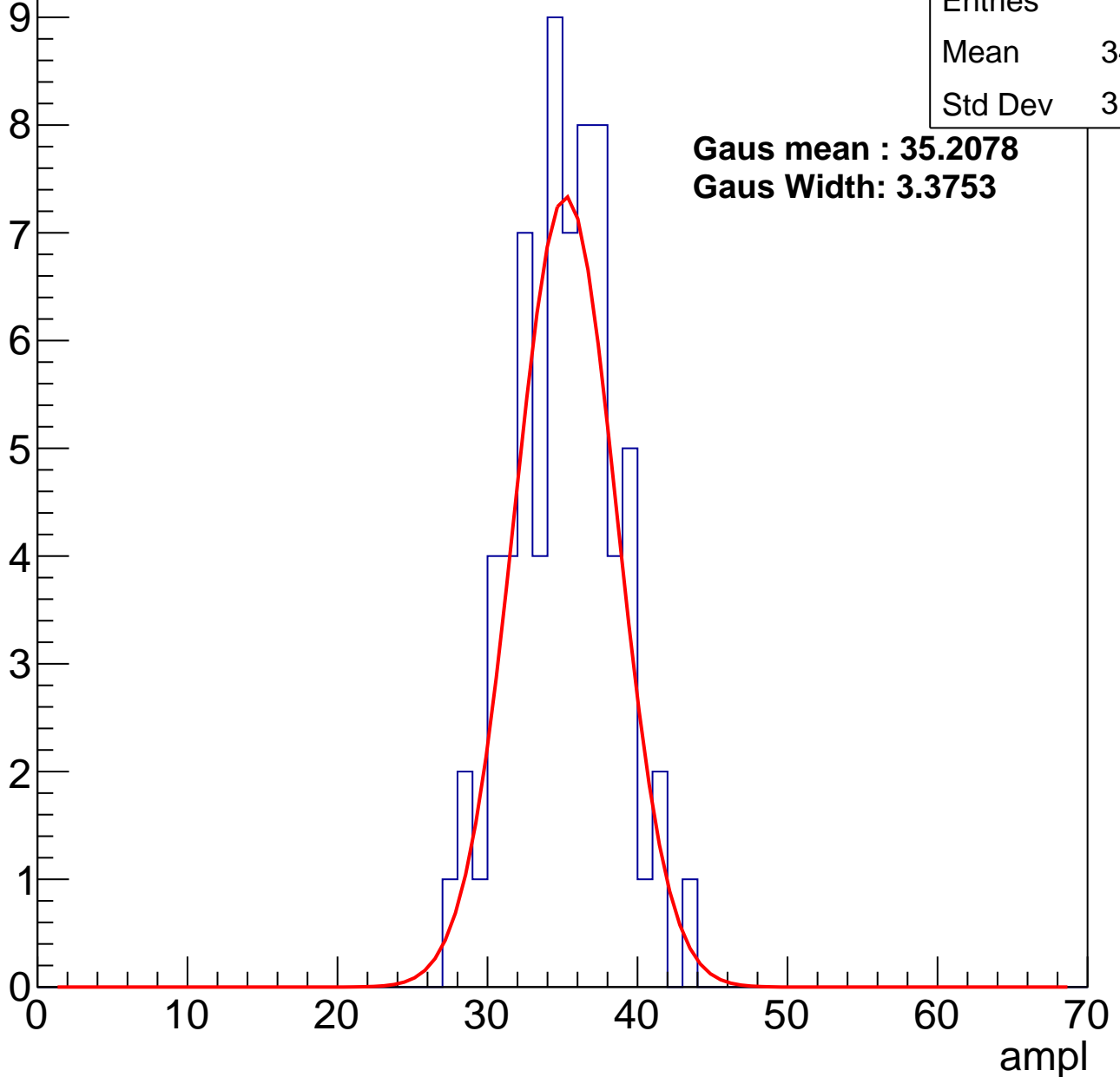
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.69
Std Dev	3.344

**Gaus mean : 35.2078**

**Gaus Width: 3.3753**



# B1L002S, U18-ch36, adc2

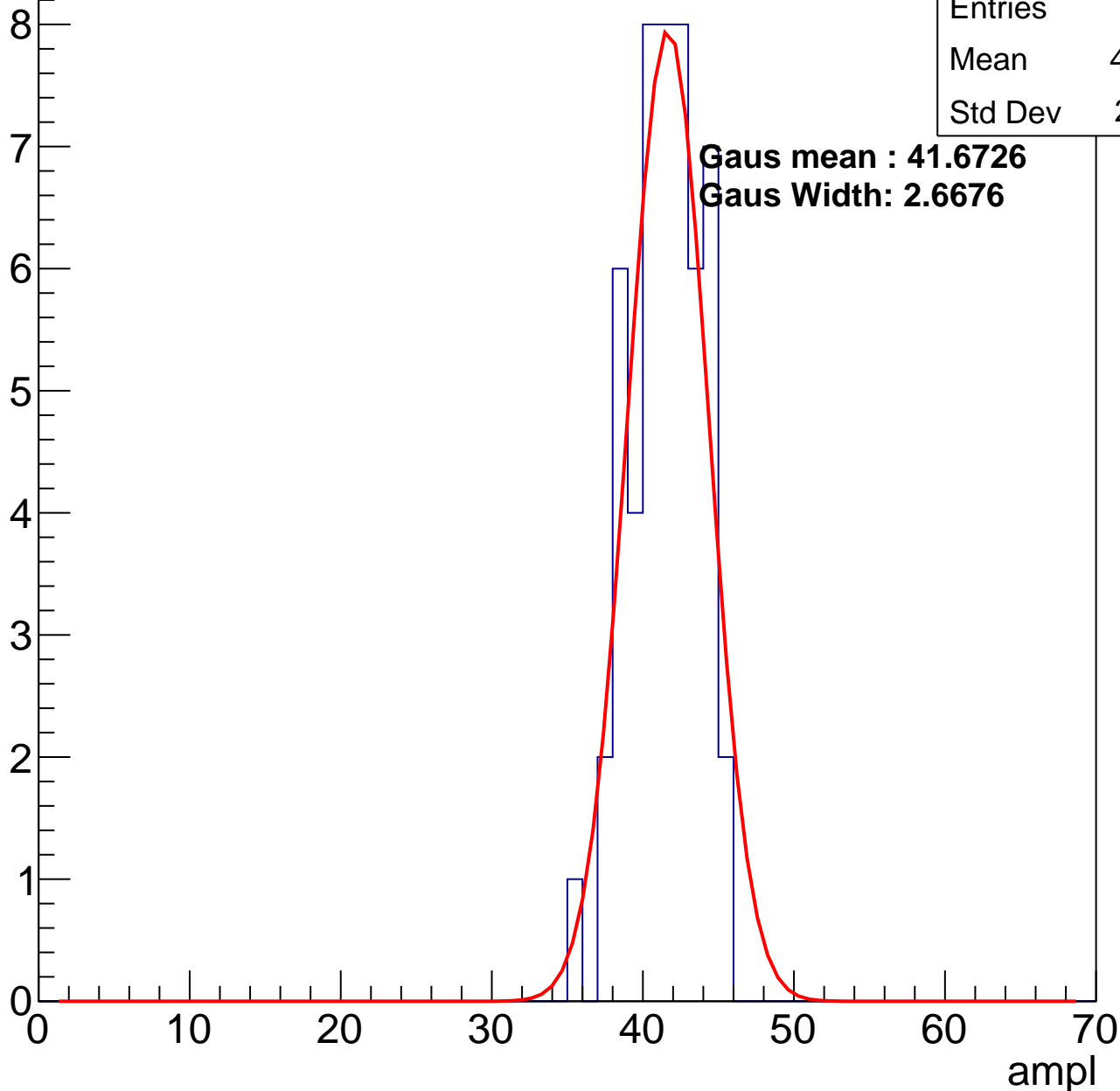
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	41.02
Std Dev	2.291

**Gaus mean : 41.6726**

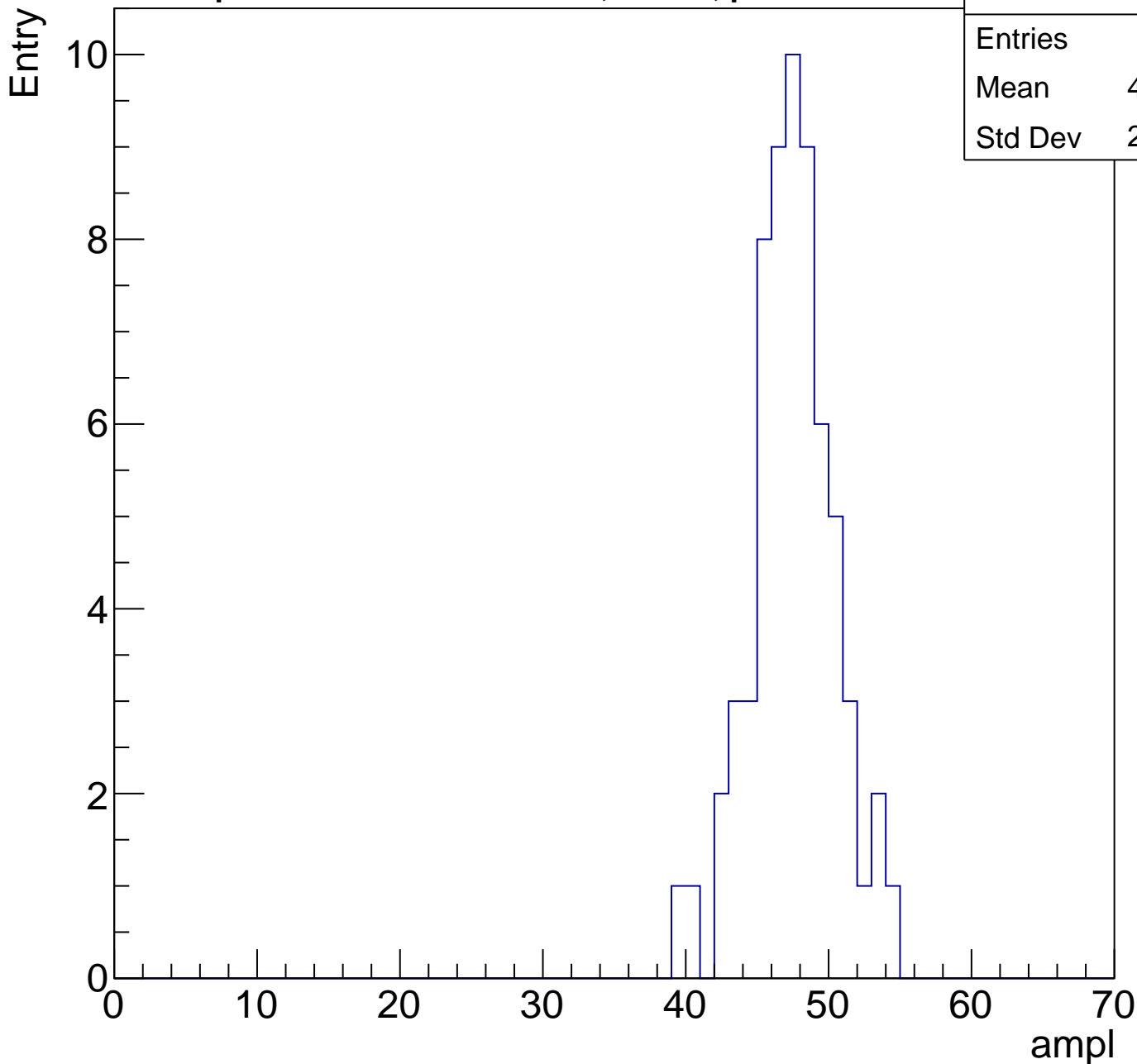
**Gaus Width: 2.6676**



# B1L002S, U18-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	47.02
Std Dev	2.934

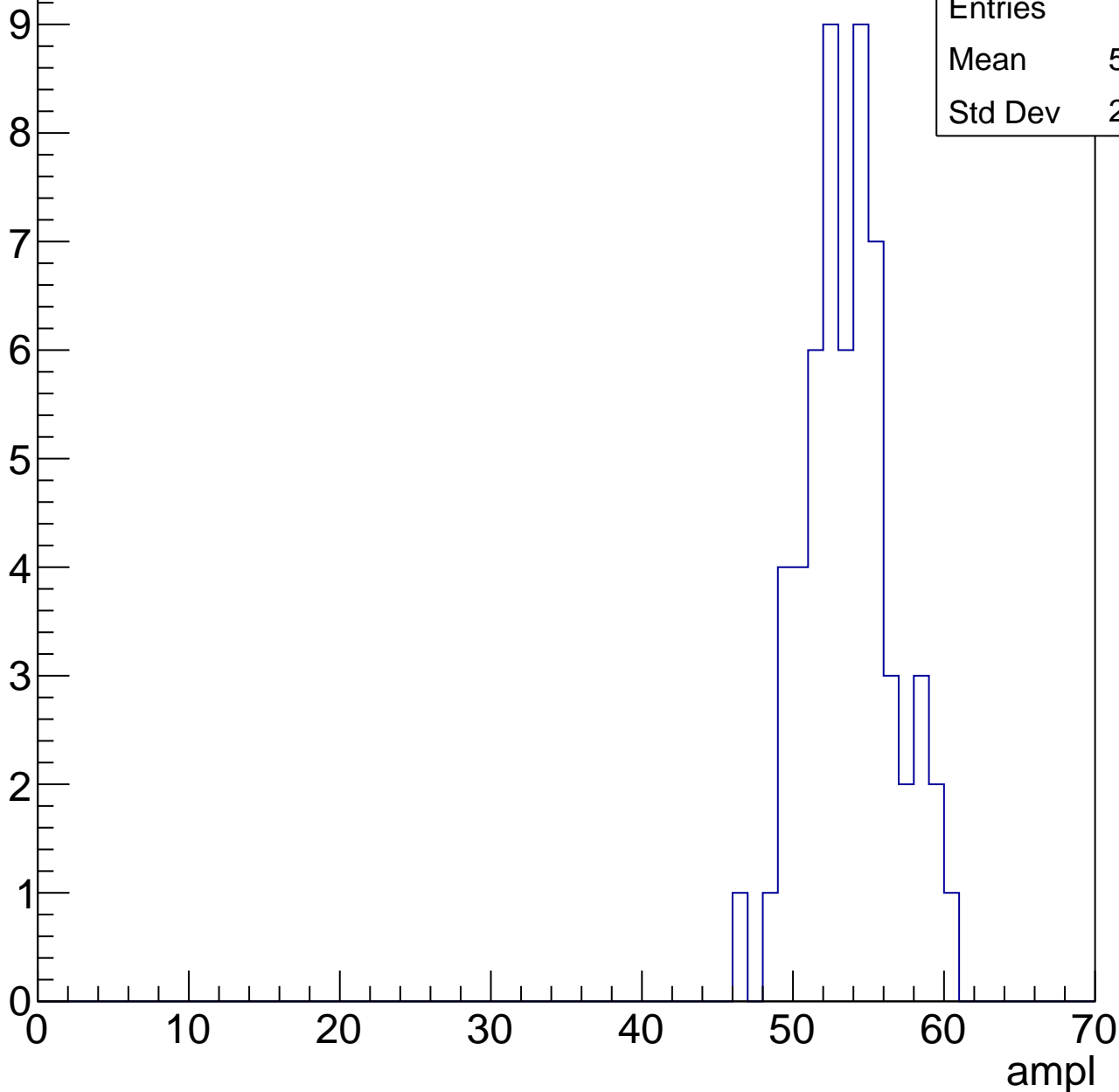


# B1L002S, U18-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.22
Std Dev	2.925

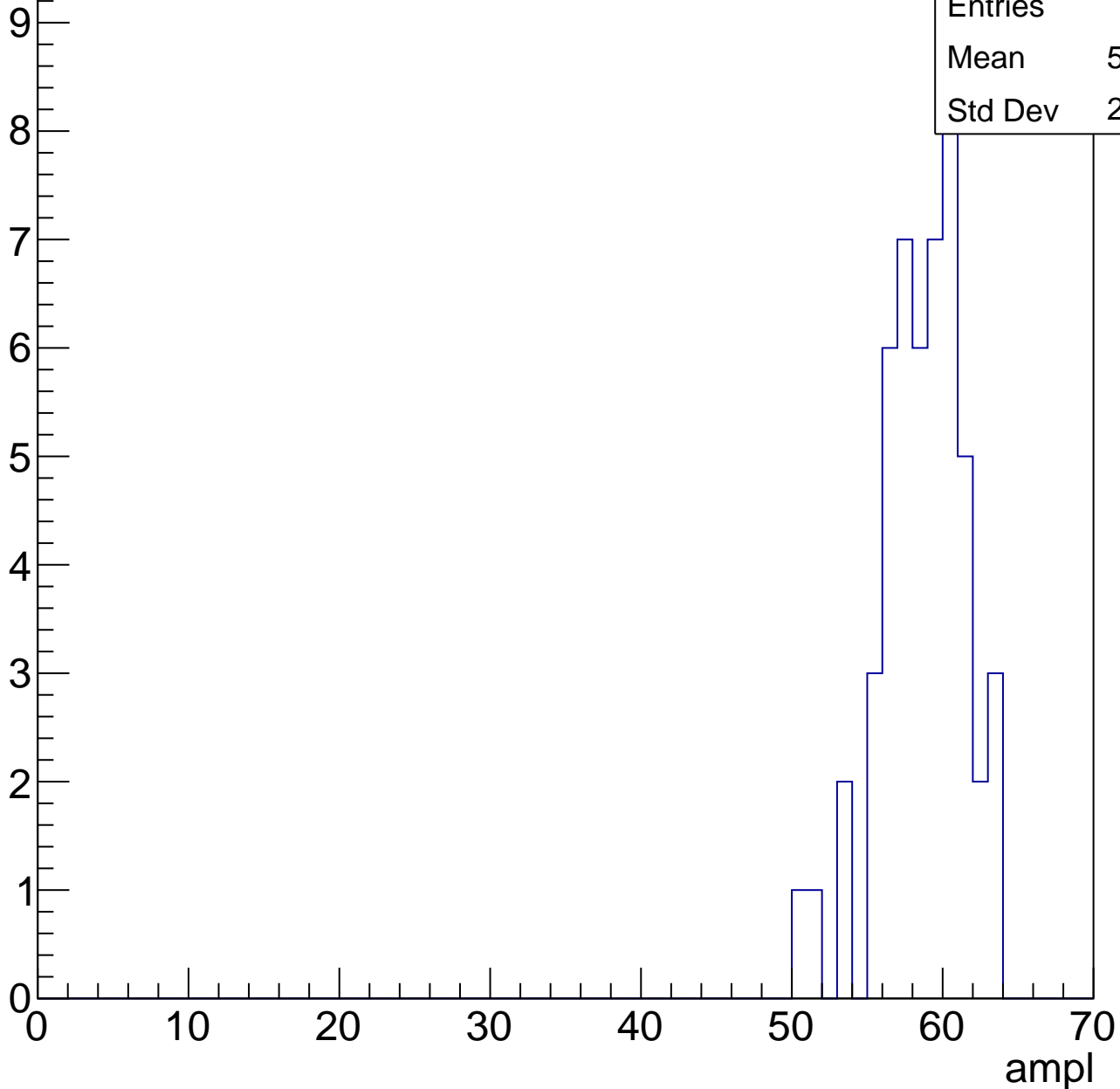


# B1L002S, U18-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.19
Std Dev	2.822

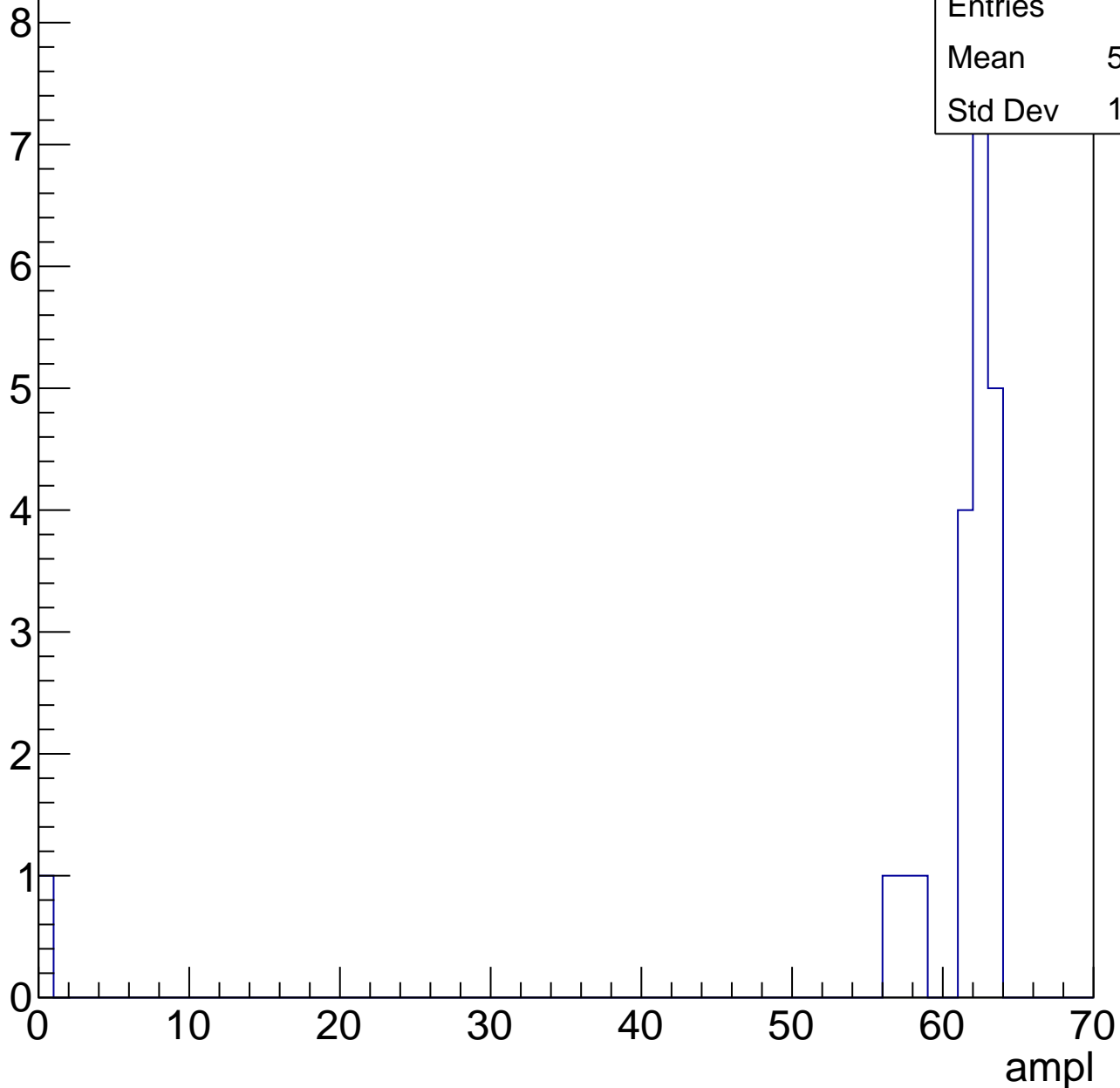


# B1L002S, U18-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.38
Std Dev	13.19

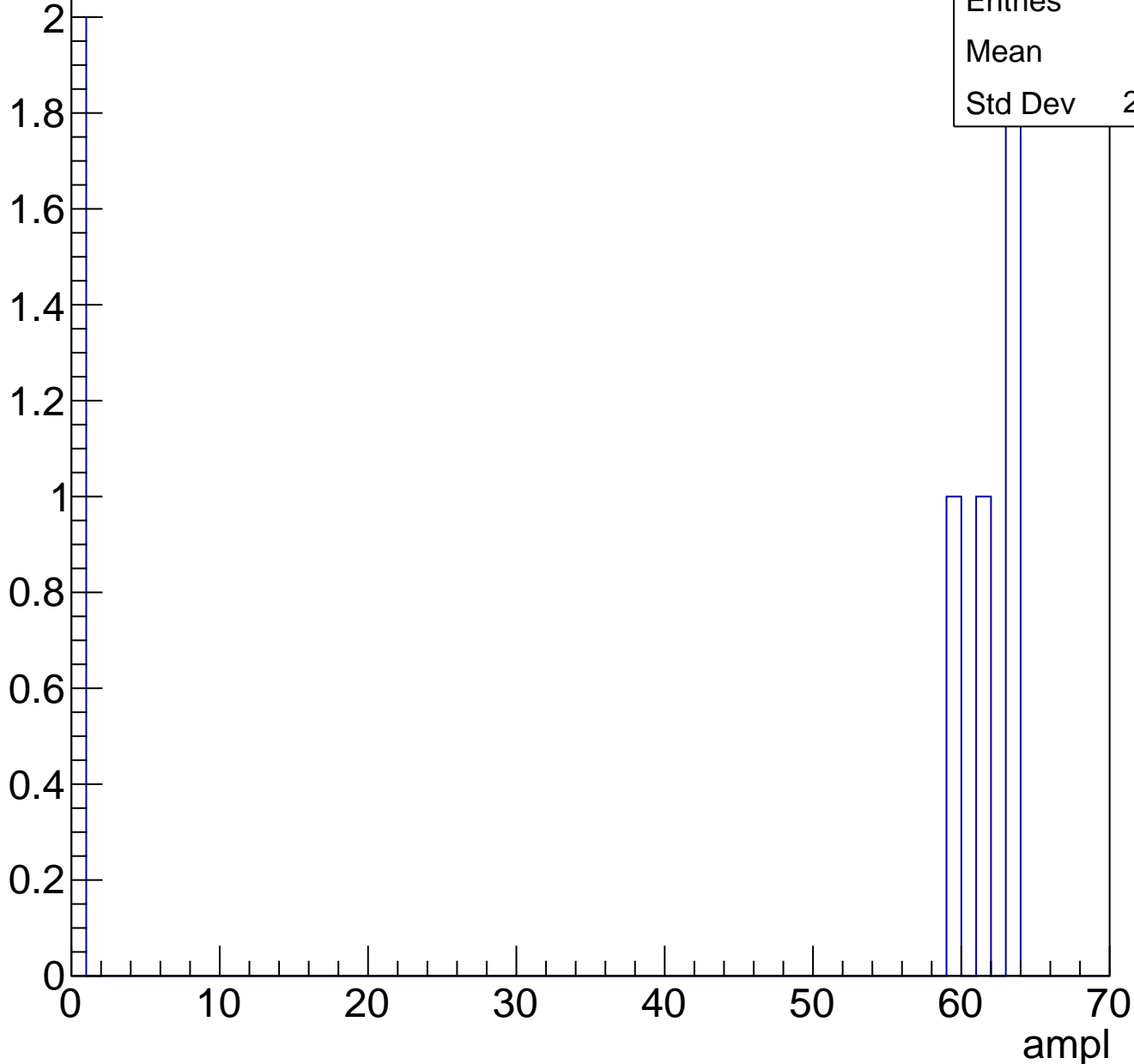




# B1L002S, U18-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch37, adc0

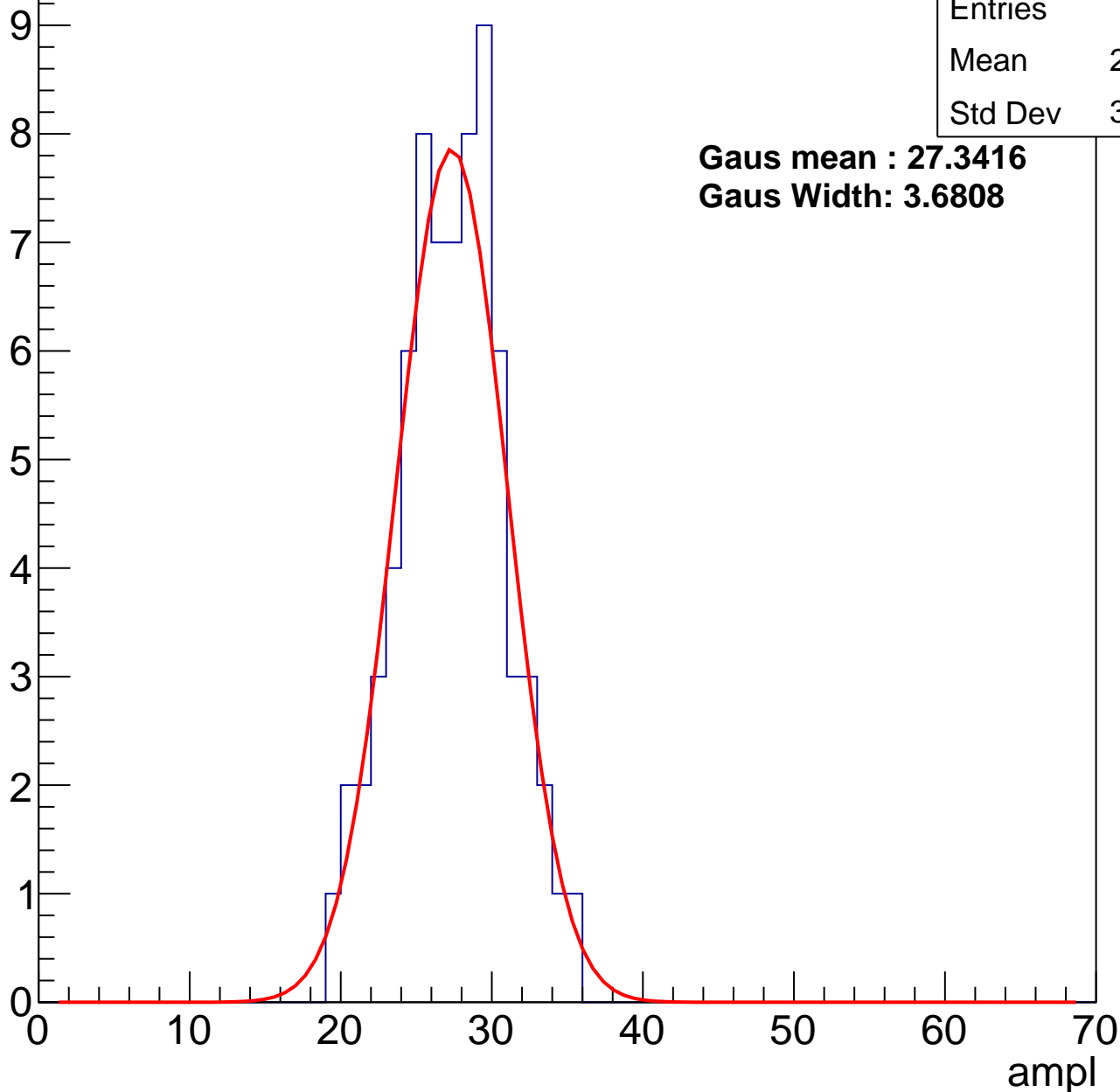
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	26.89
Std Dev	3.454

**Gaus mean : 27.3416**

**Gaus Width: 3.6808**



# B1L002S, U18-ch37, adc1

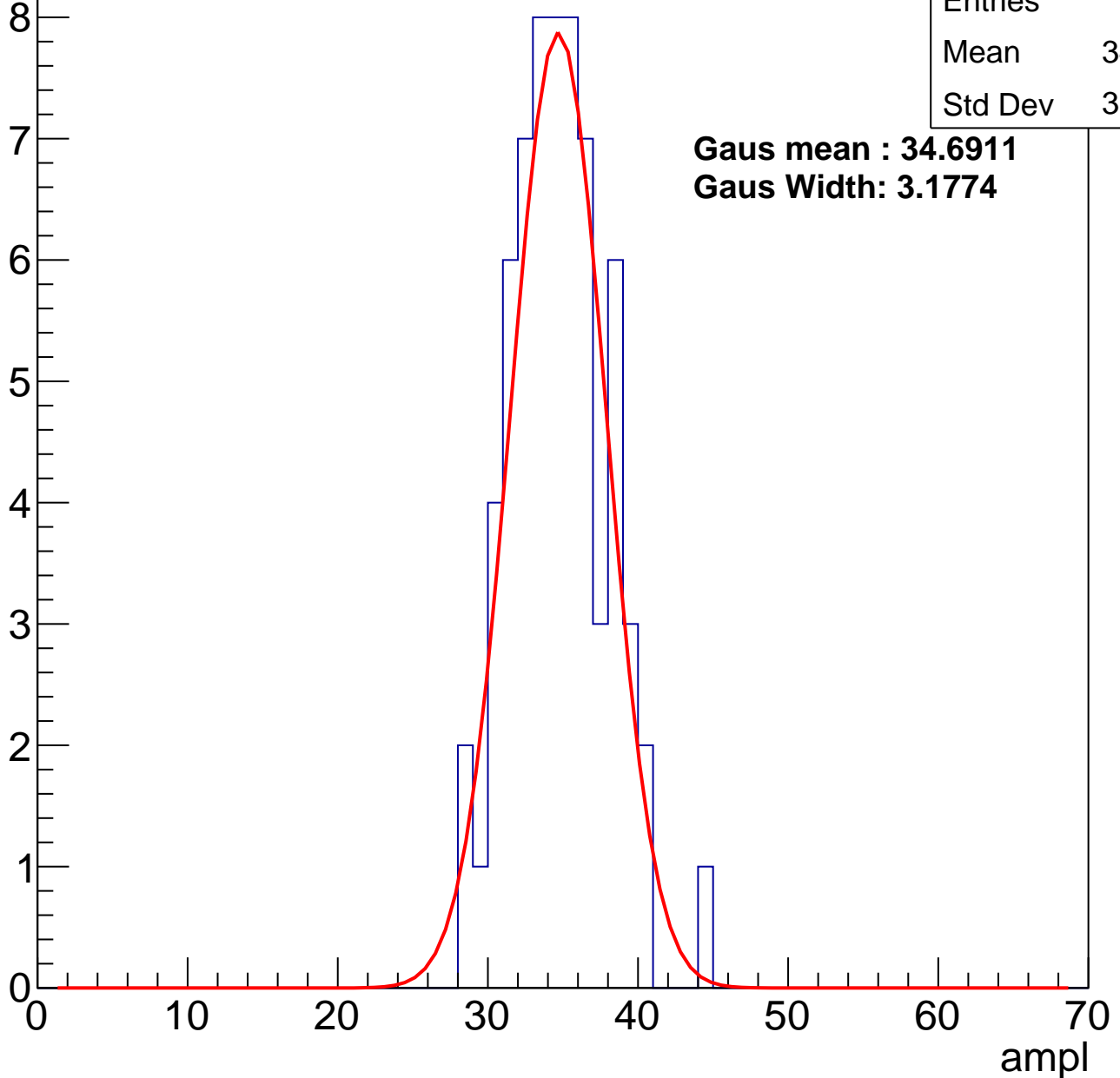
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.29
Std Dev	3.142

**Gaus mean : 34.6911**

**Gaus Width: 3.1774**



# B1L002S, U18-ch37, adc2

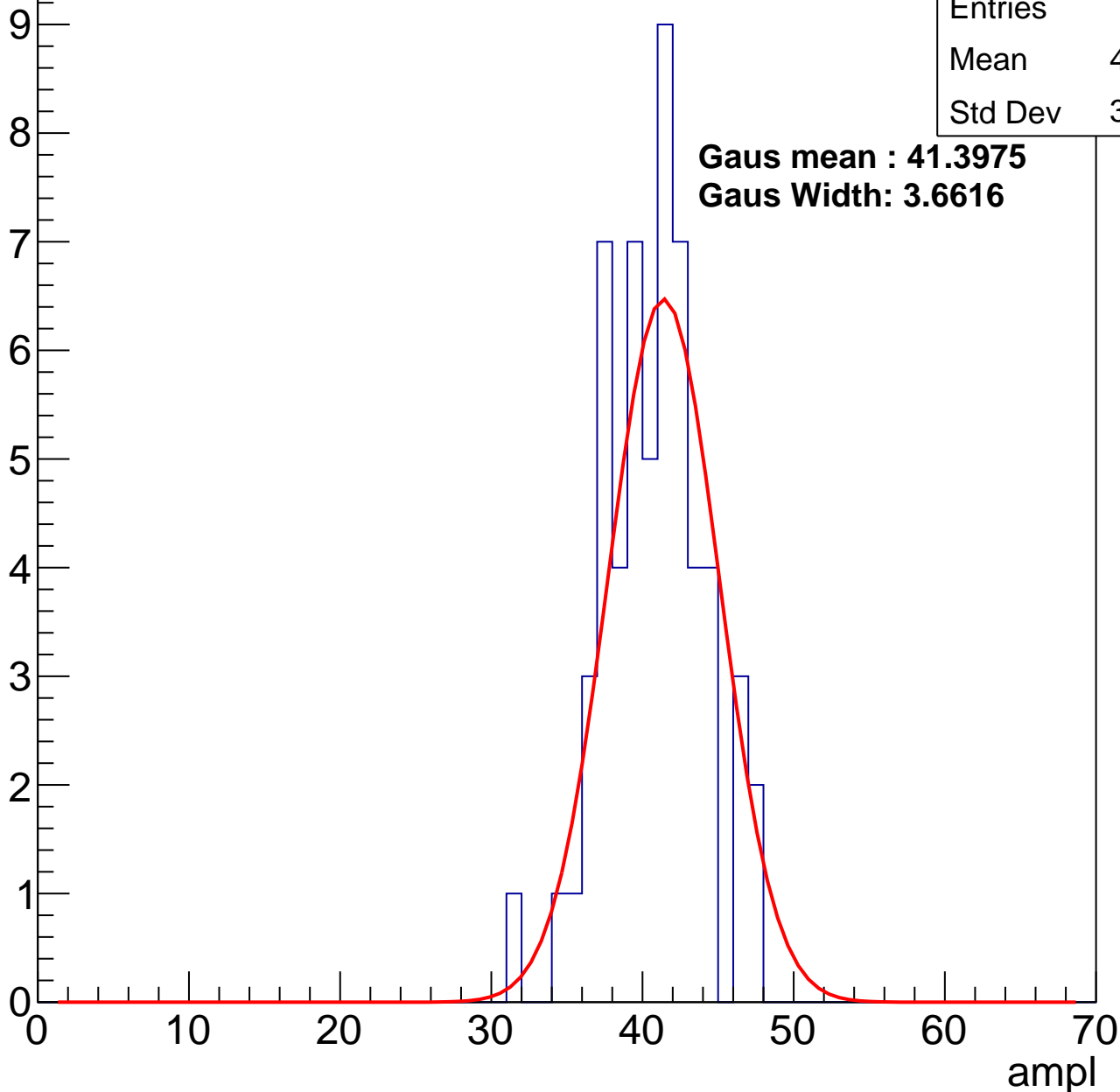
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.26
Std Dev	3.257

**Gaus mean : 41.3975**

**Gaus Width: 3.6616**

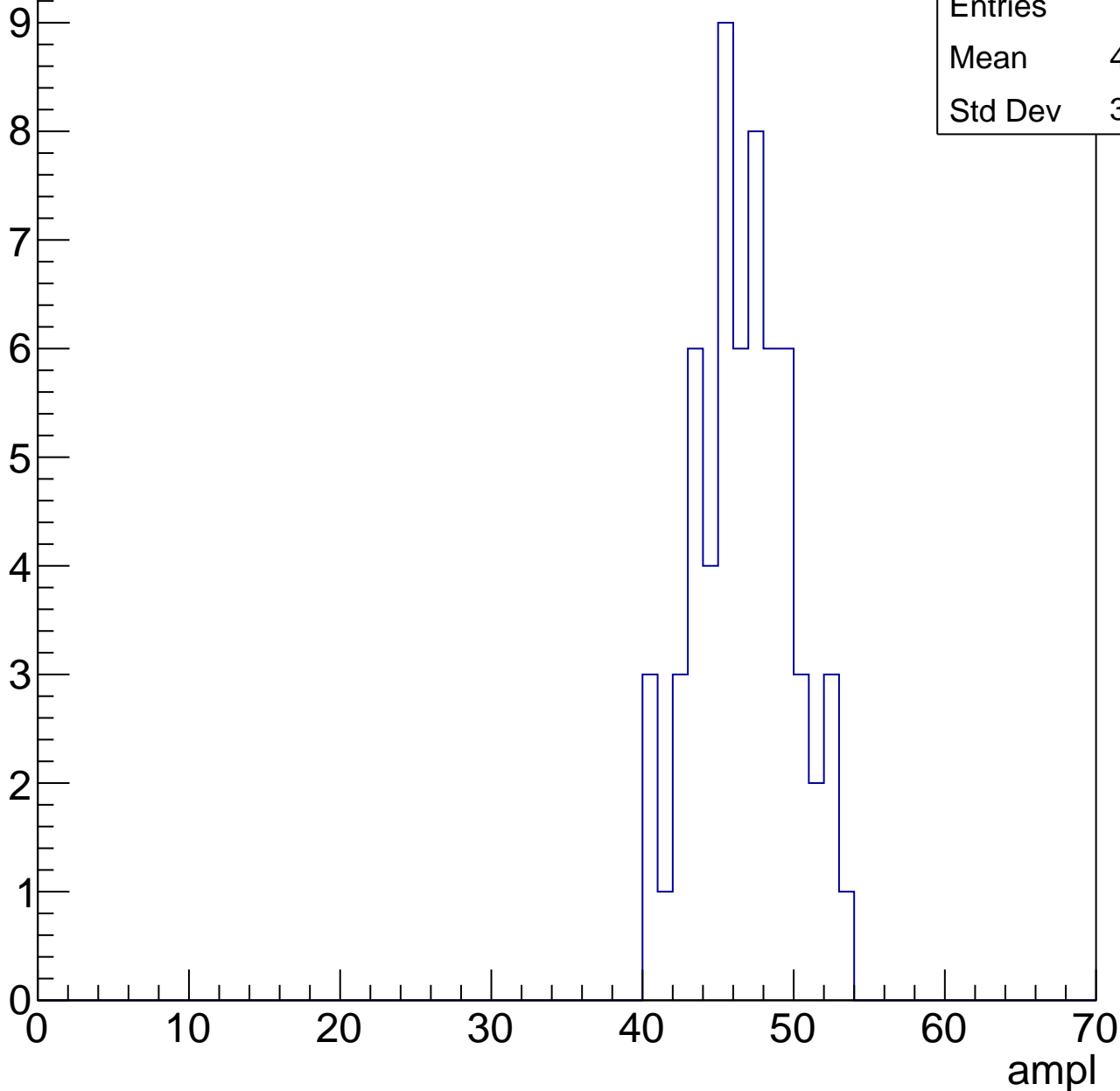


# B1L002S, U18-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.25
Std Dev	3.129

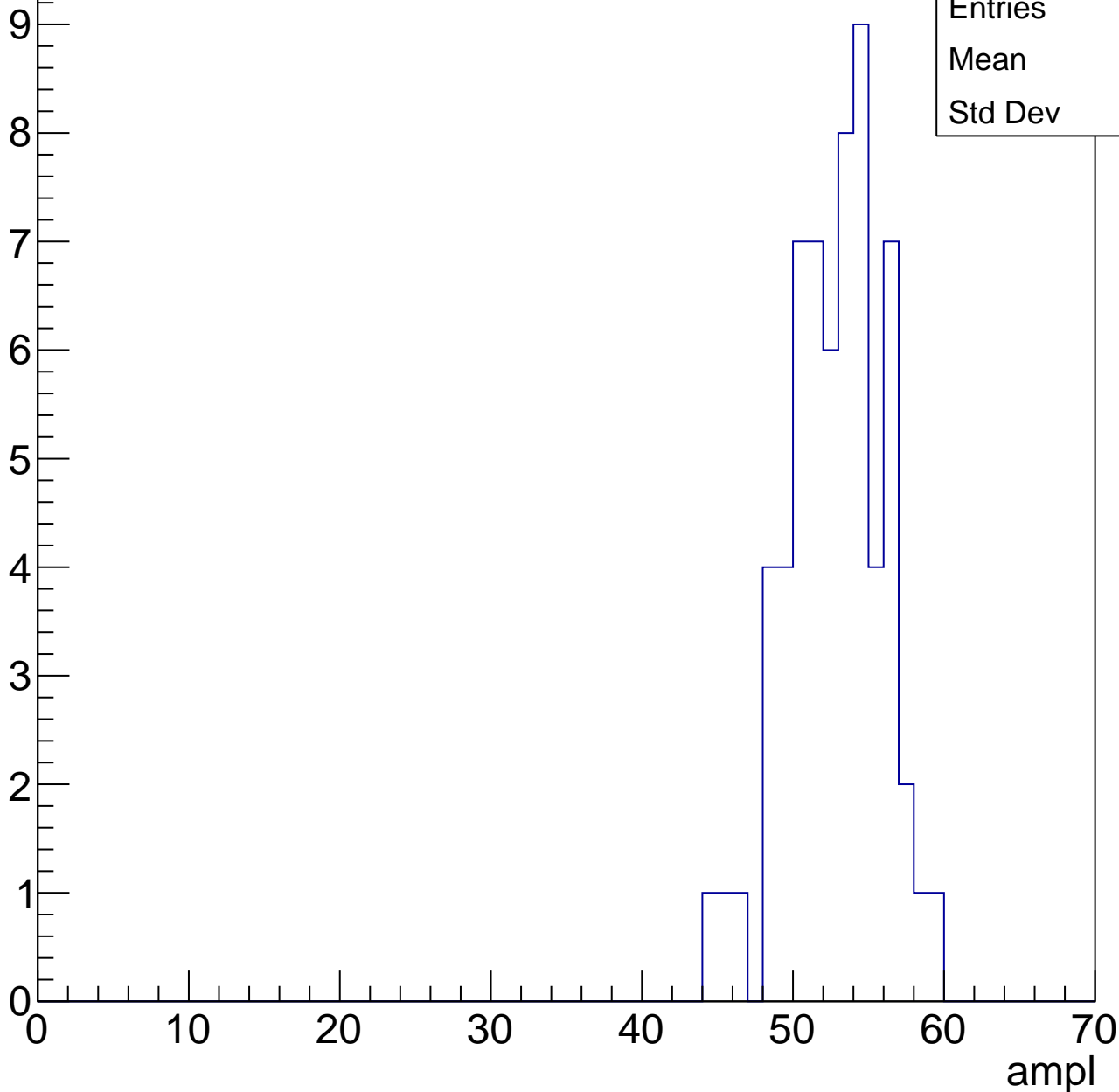


# B1L002S, U18-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	52.3
Std Dev	3.1

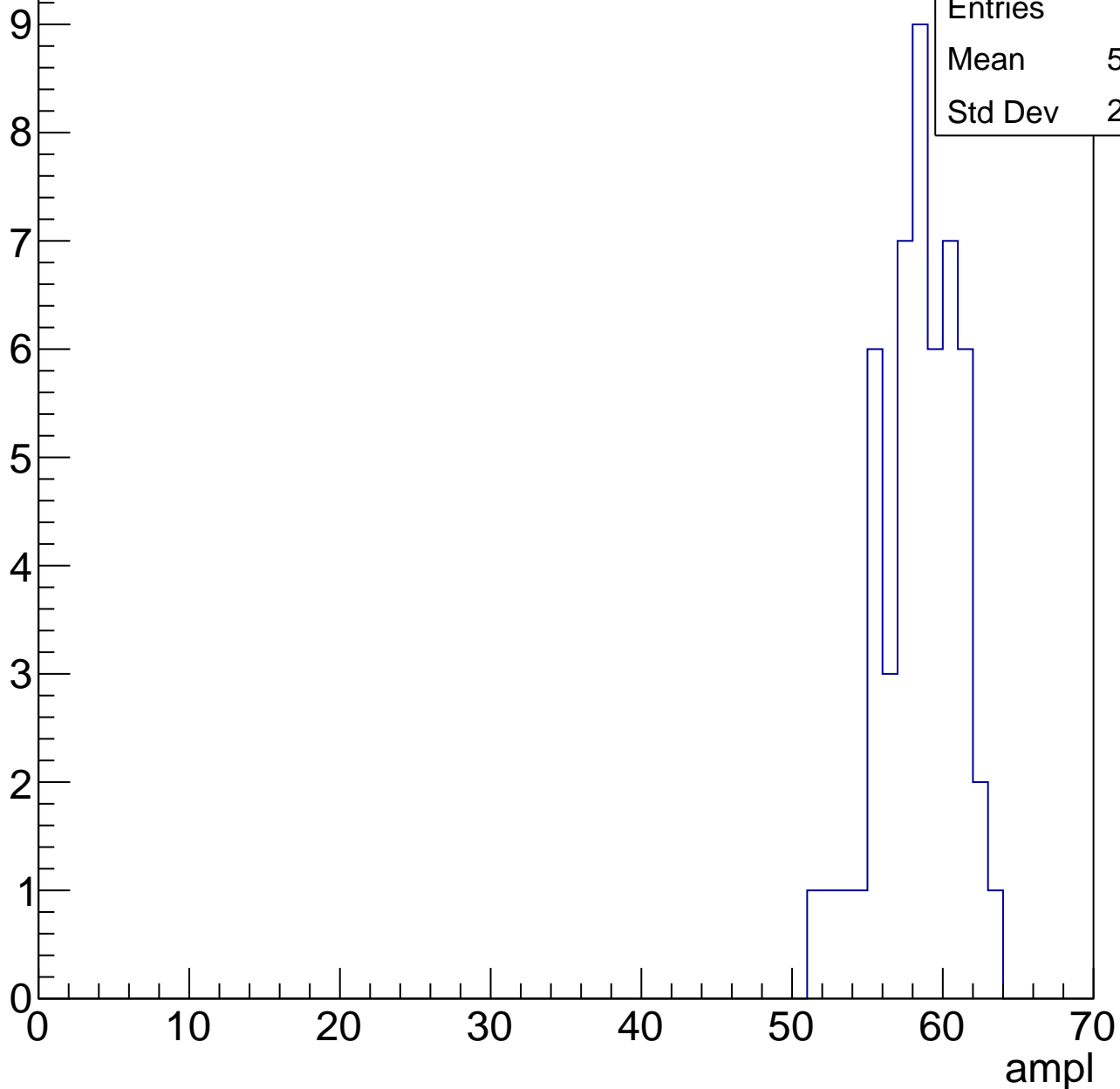


# B1L002S, U18-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.96
Std Dev	2.597

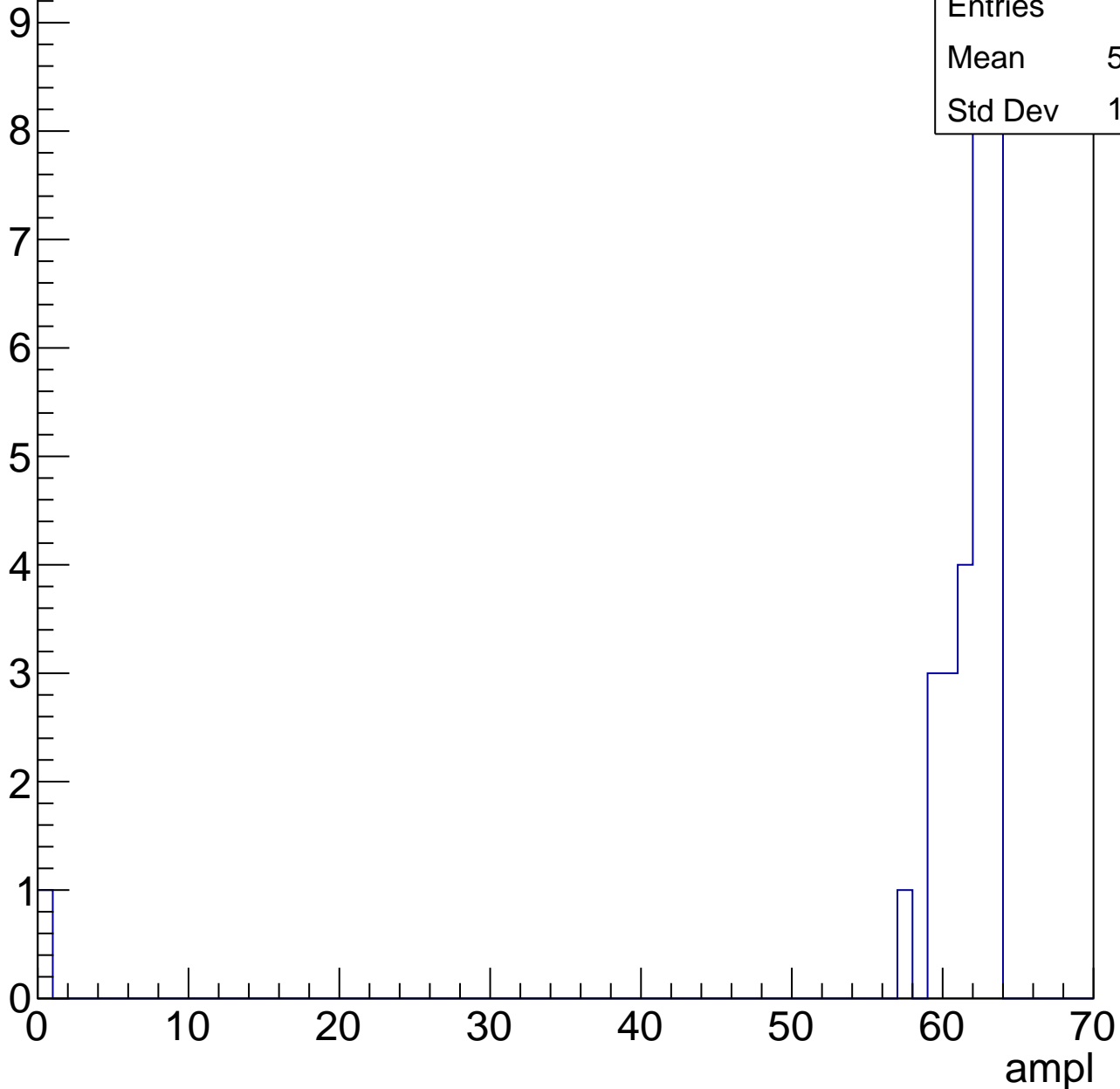


# B1L002S, U18-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	59.34
Std Dev	11.32





# B1L002S, U18-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch38, adc0

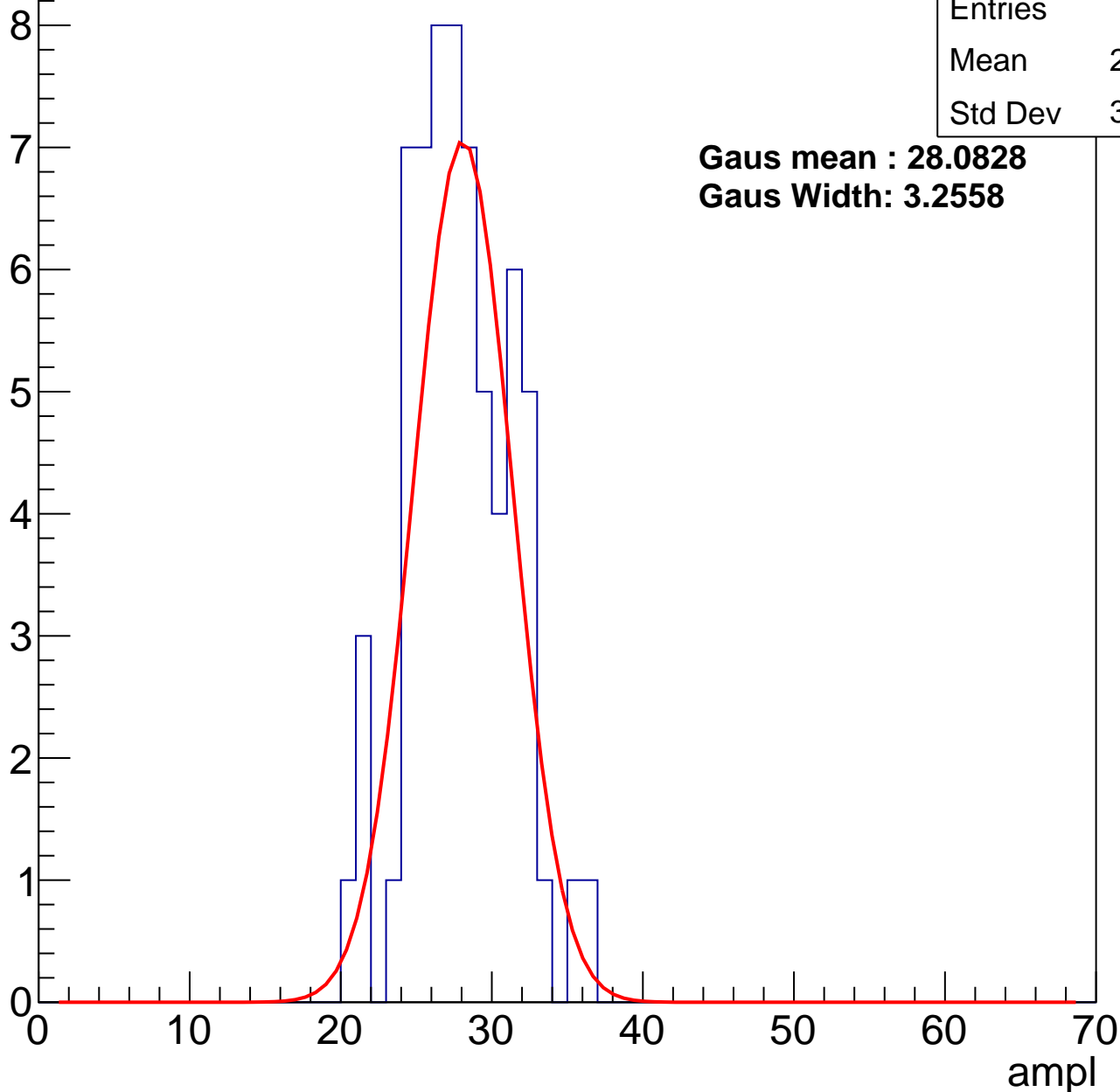
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.45
Std Dev	3.338

**Gaus mean : 28.0828**

**Gaus Width: 3.2558**



# B1L002S, U18-ch38, adc1

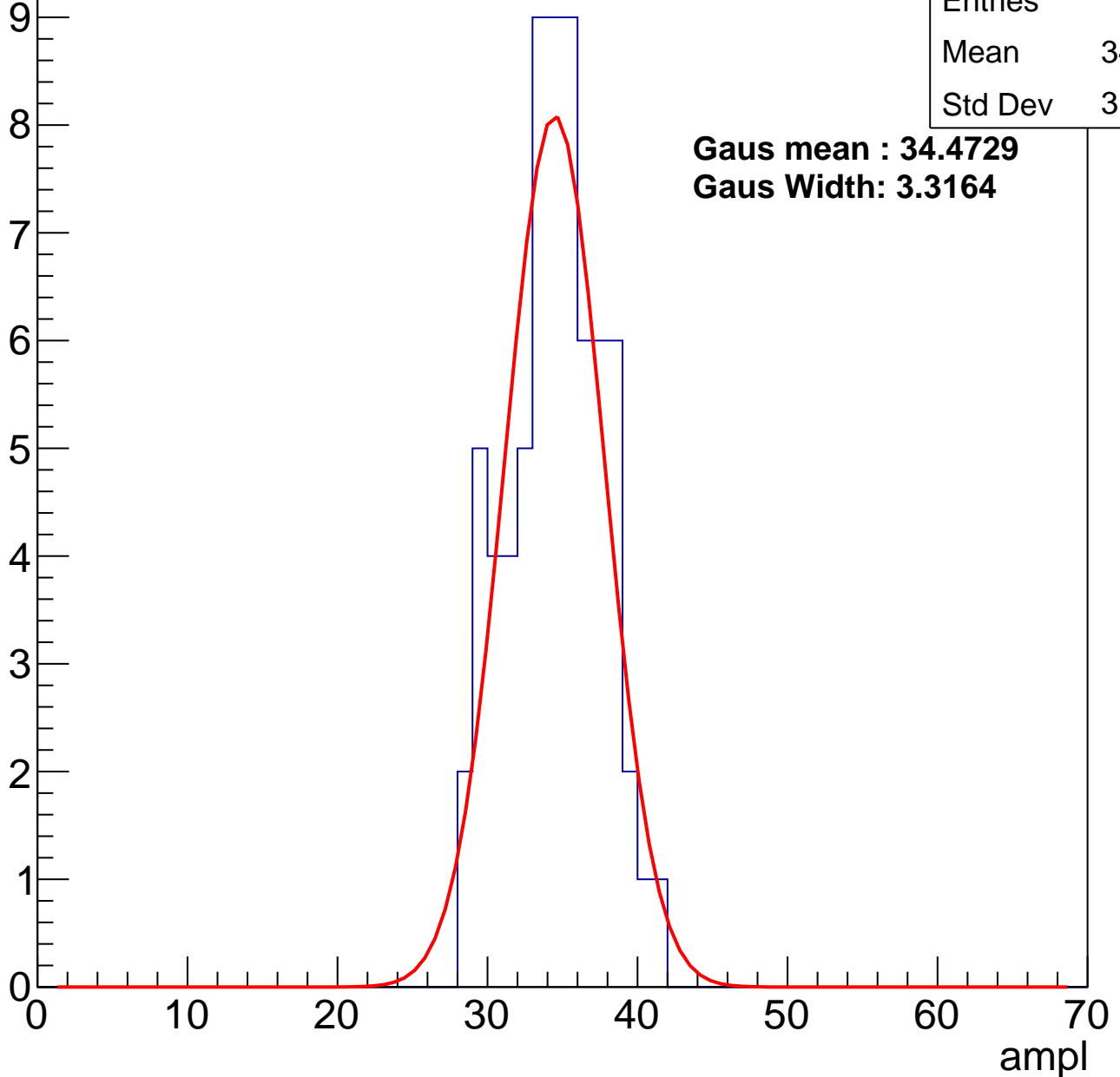
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.03
Std Dev	3.055

**Gaus mean : 34.4729**

**Gaus Width: 3.3164**



# B1L002S, U18-ch38, adc2

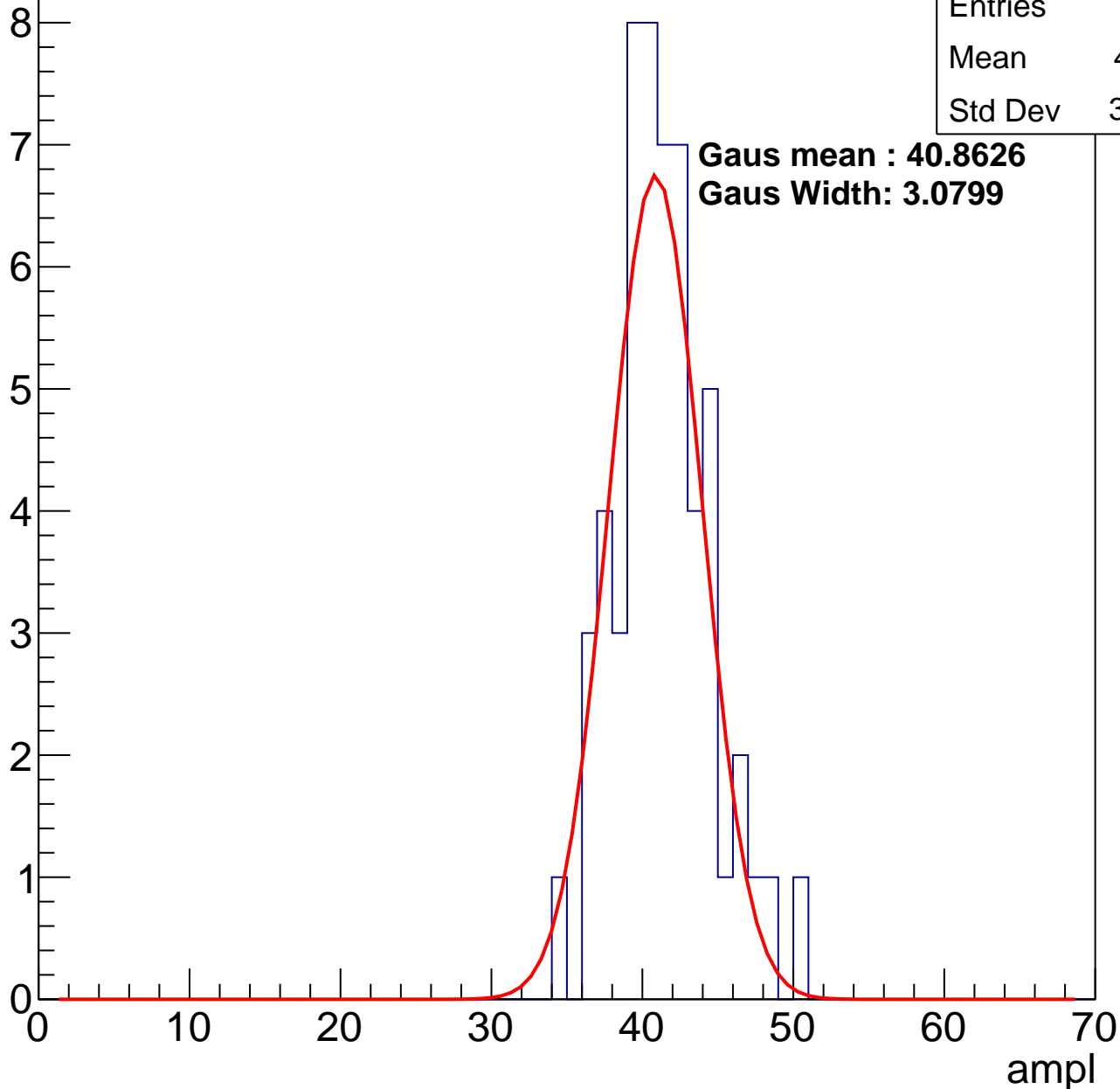
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.91
Std Dev	3.147

**Gaus mean : 40.8626**

**Gaus Width: 3.0799**

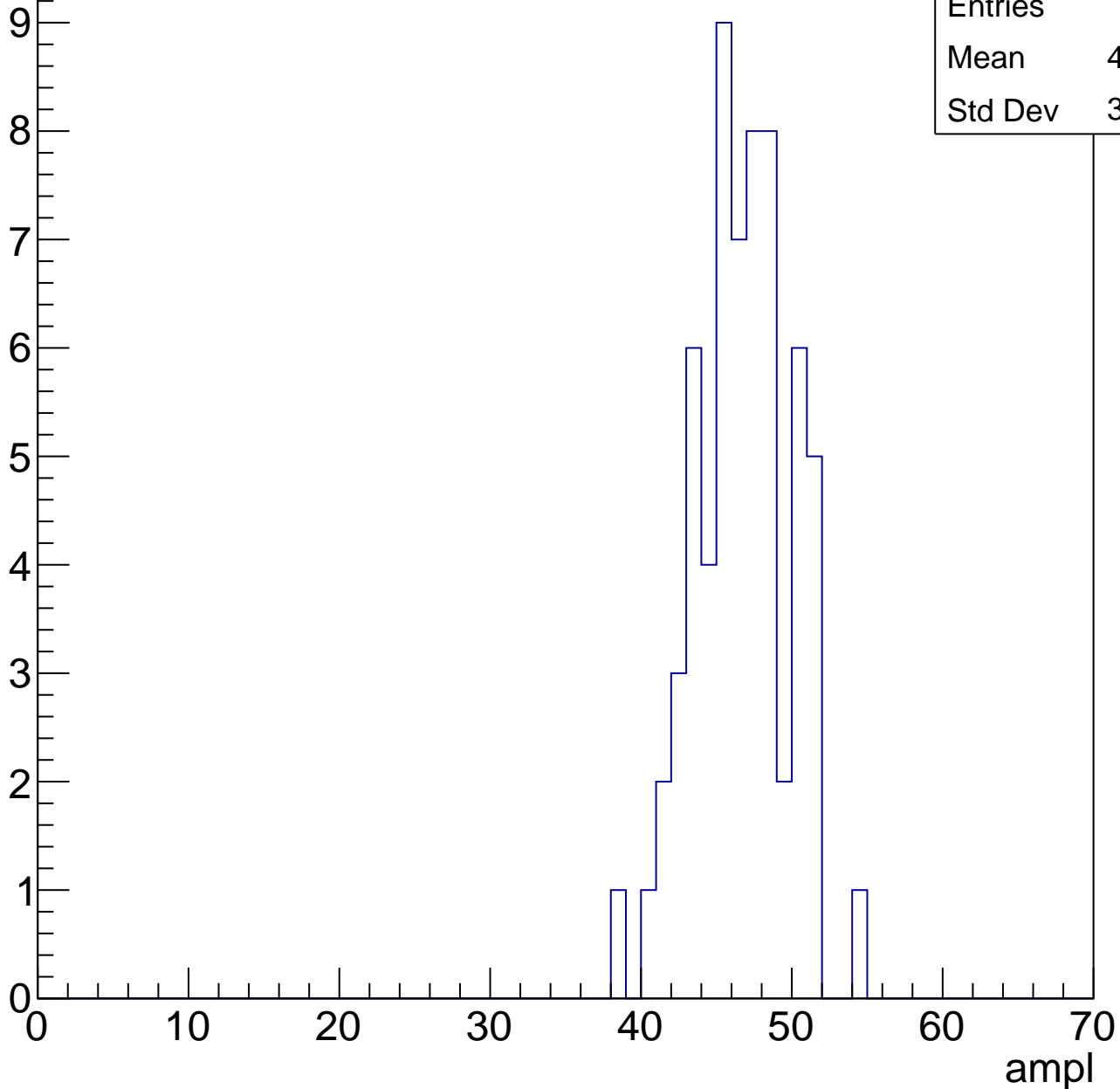


# B1L002S, U18-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.25
Std Dev	3.127

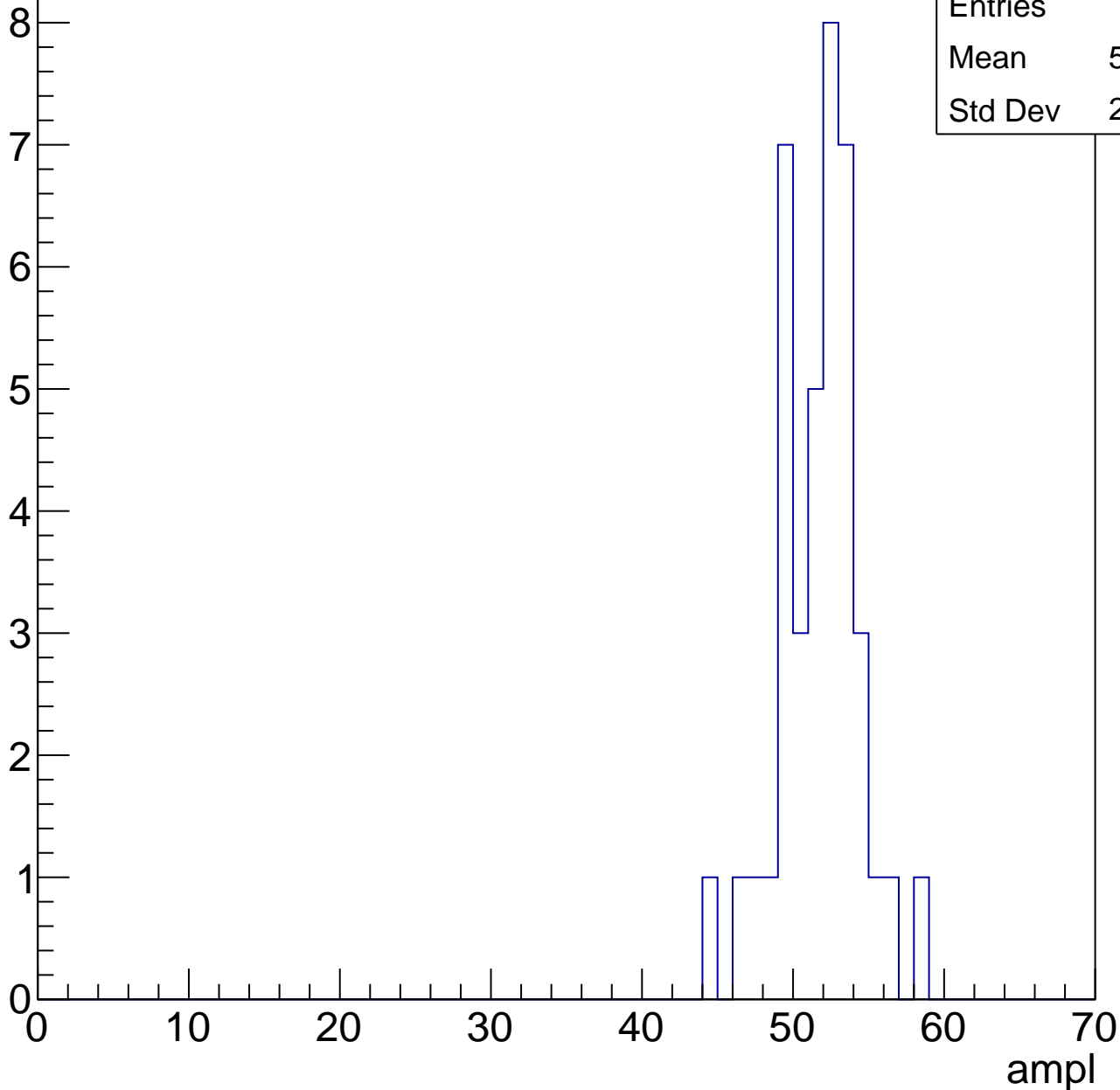


# B1L002S, U18-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

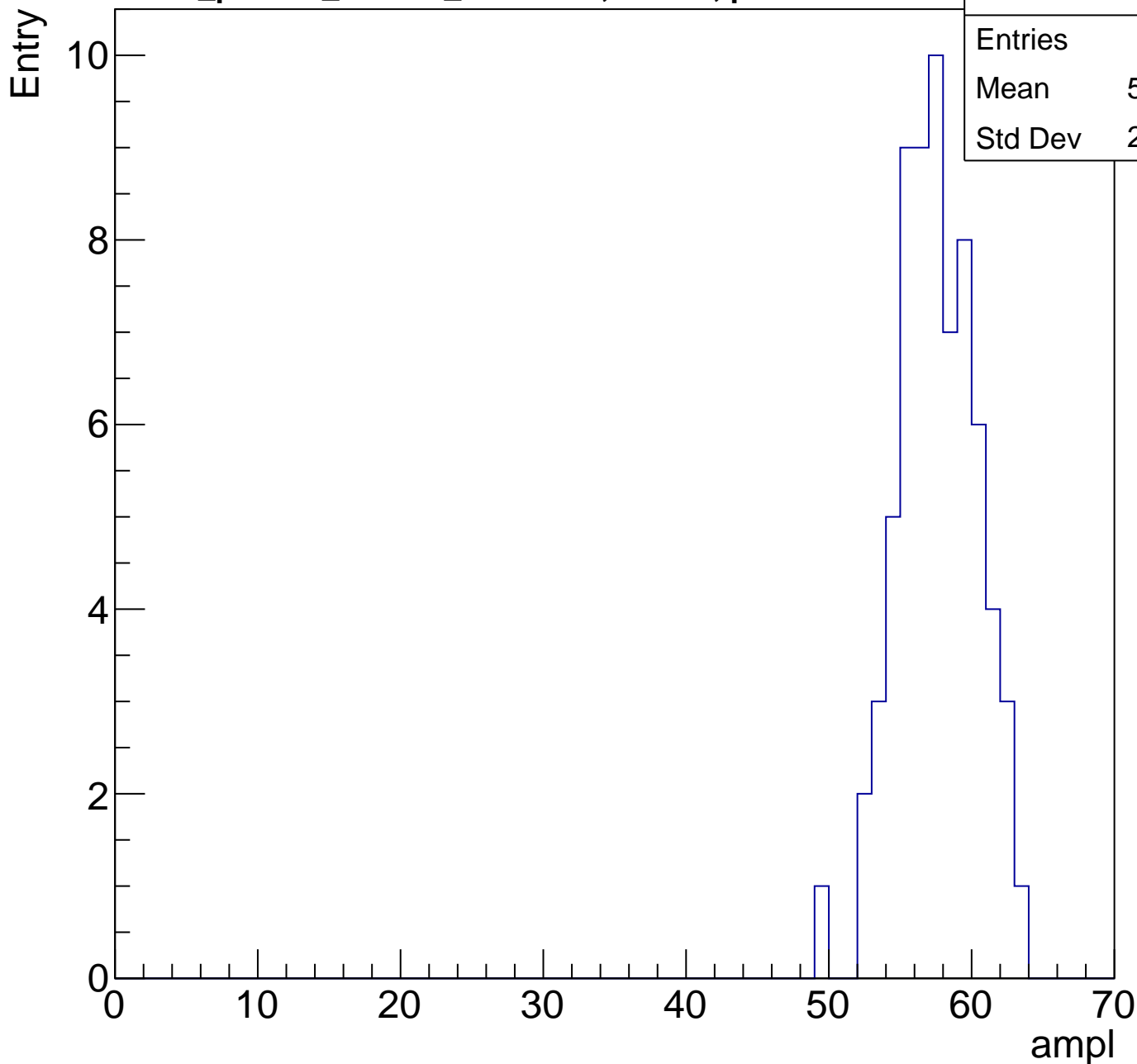
Entries	40
Mean	51.27
Std Dev	2.646



# B1L002S, U18-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	57.09
Std Dev	2.764

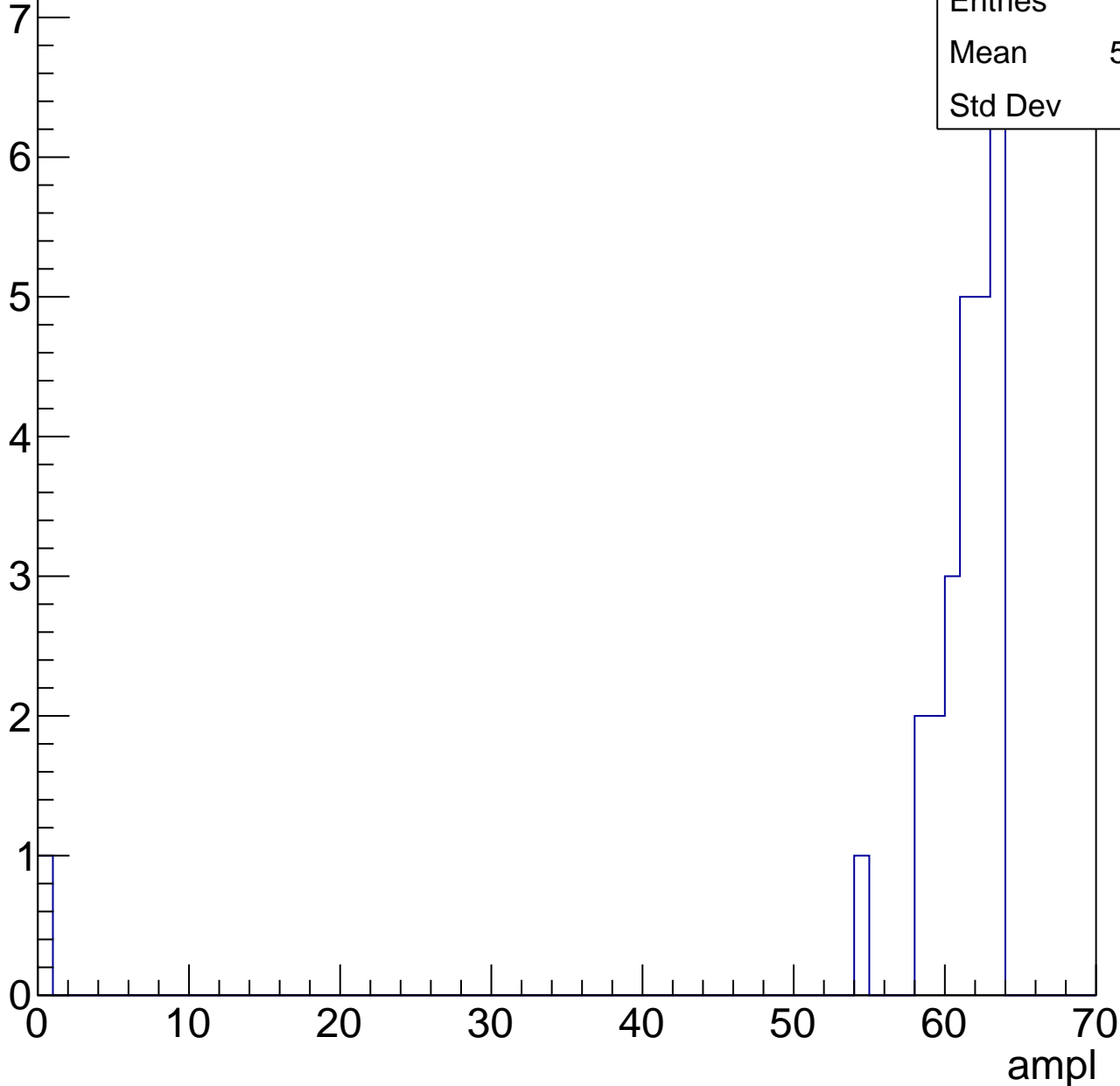


# B1L002S, U18-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.62
Std Dev	11.9

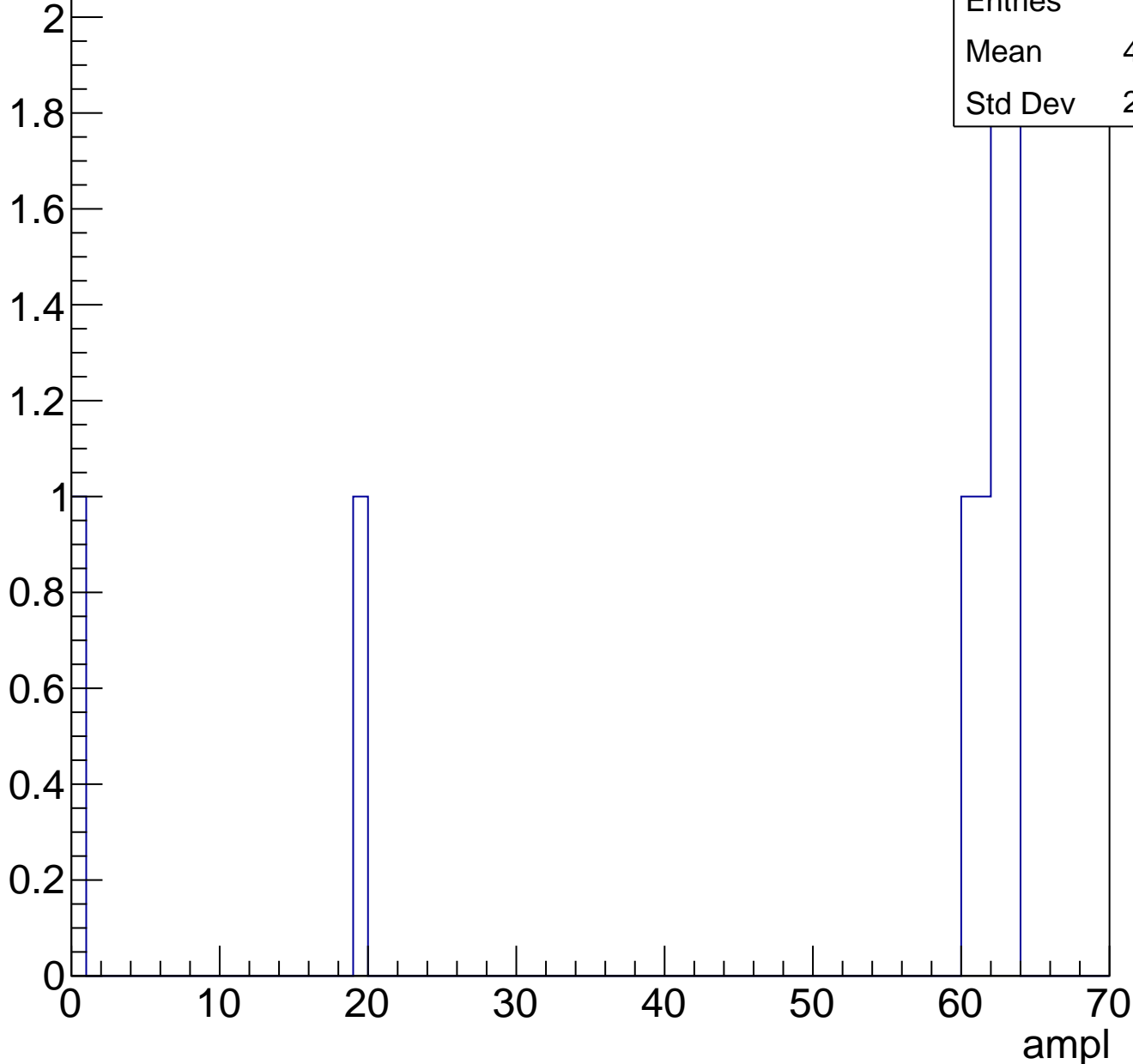




# B1L002S, U18-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	8
Mean	48.75
Std Dev	23.17

# B1L002S, U18-ch39, adc0

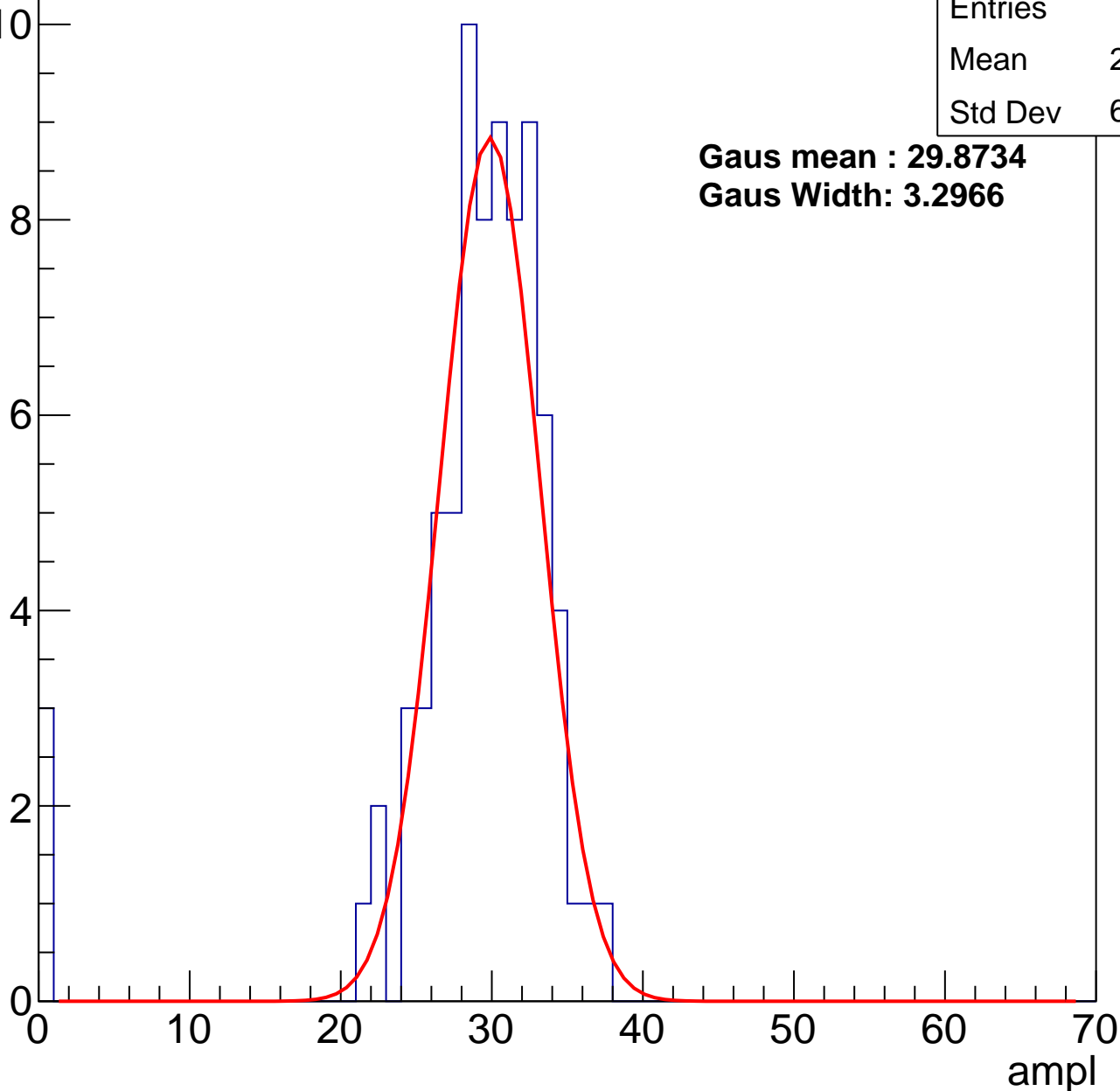
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	28.32
Std Dev	6.476

**Gaus mean : 29.8734**

**Gaus Width: 3.2966**



# B1L002S, U18-ch39, adc1

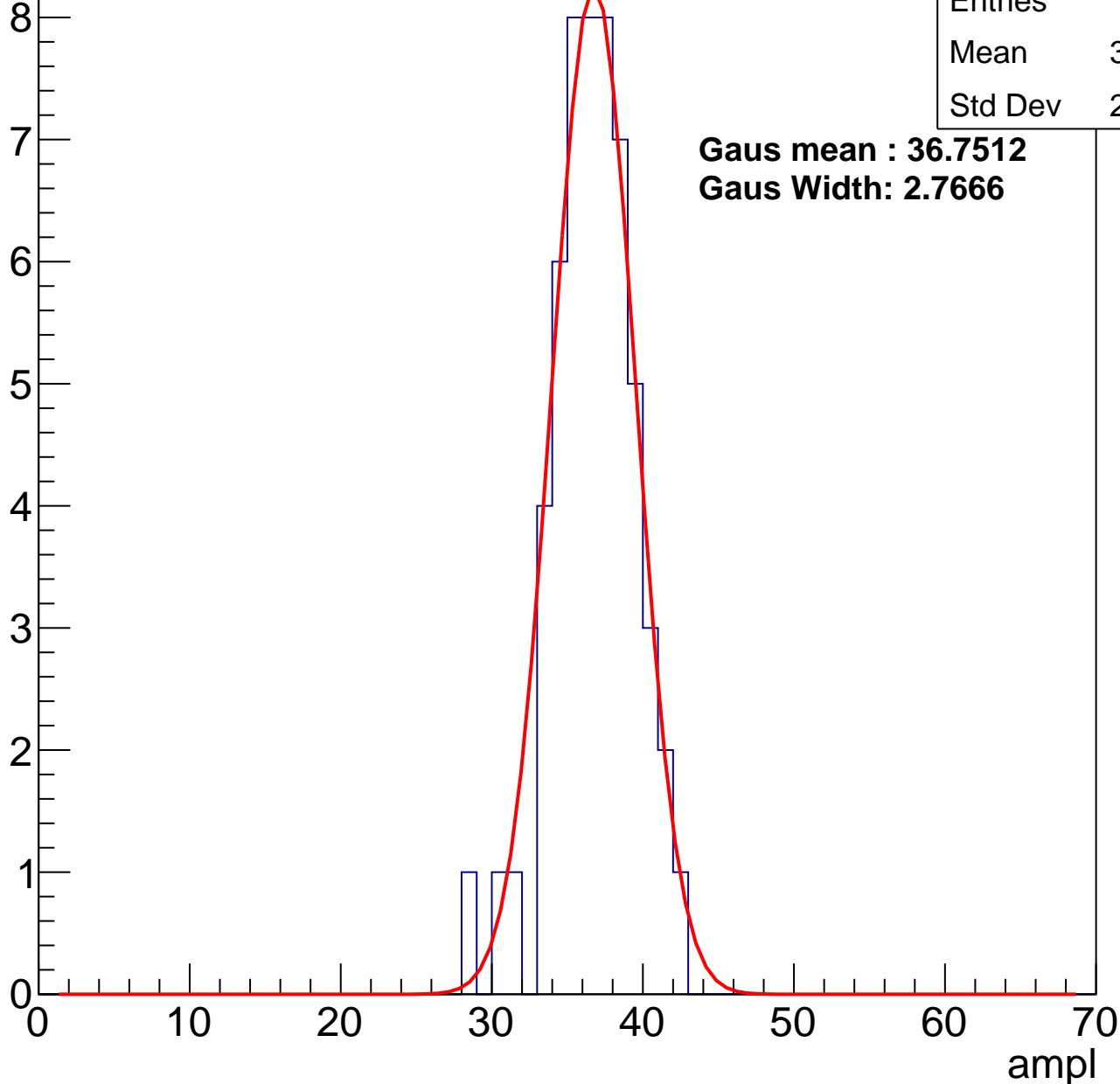
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	36.25
Std Dev	2.712

**Gaus mean : 36.7512**

**Gaus Width: 2.7666**



# B1L002S, U18-ch39, adc2

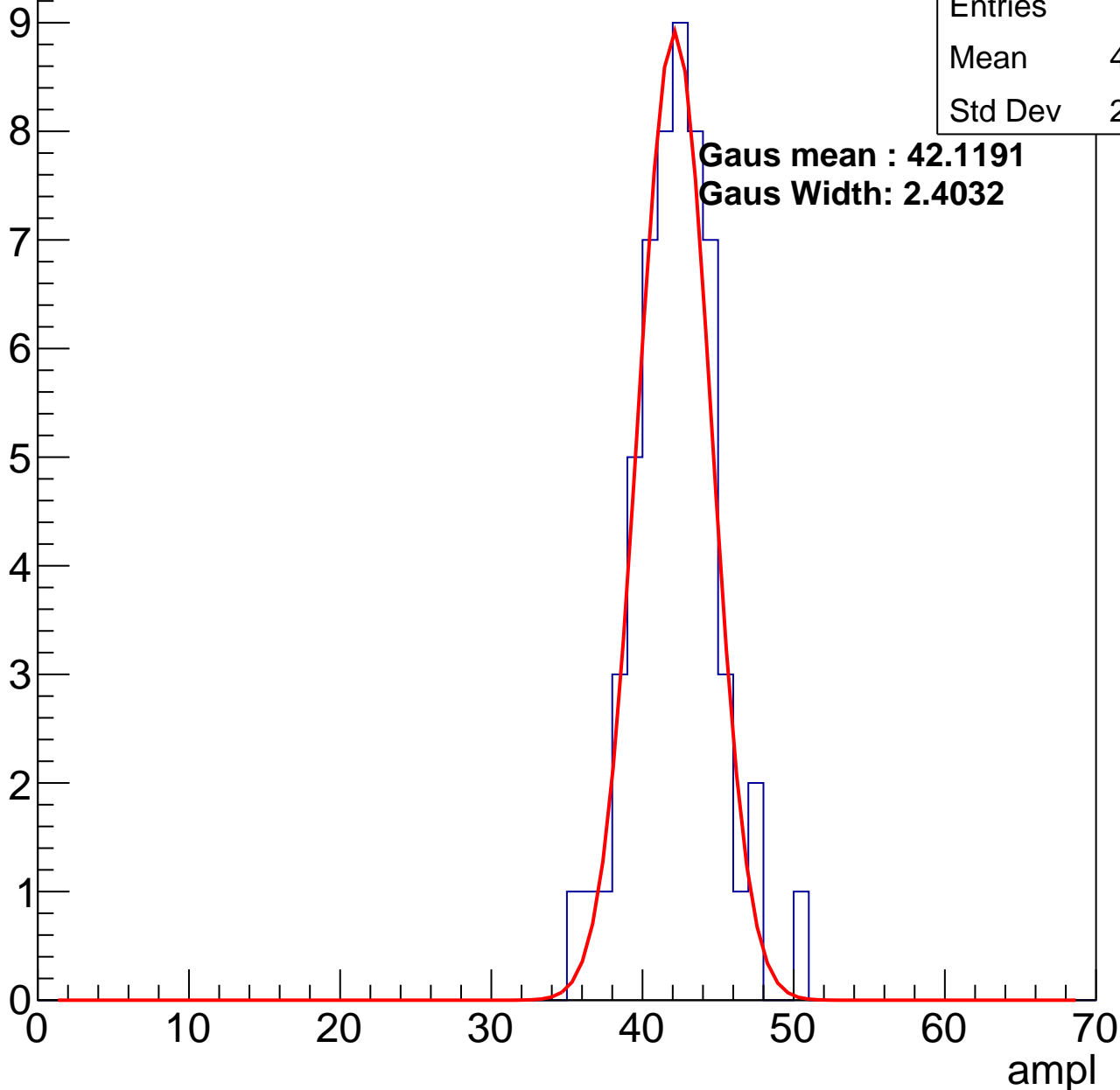
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.75
Std Dev	2.742

**Gaus mean : 42.1191**

**Gaus Width: 2.4032**

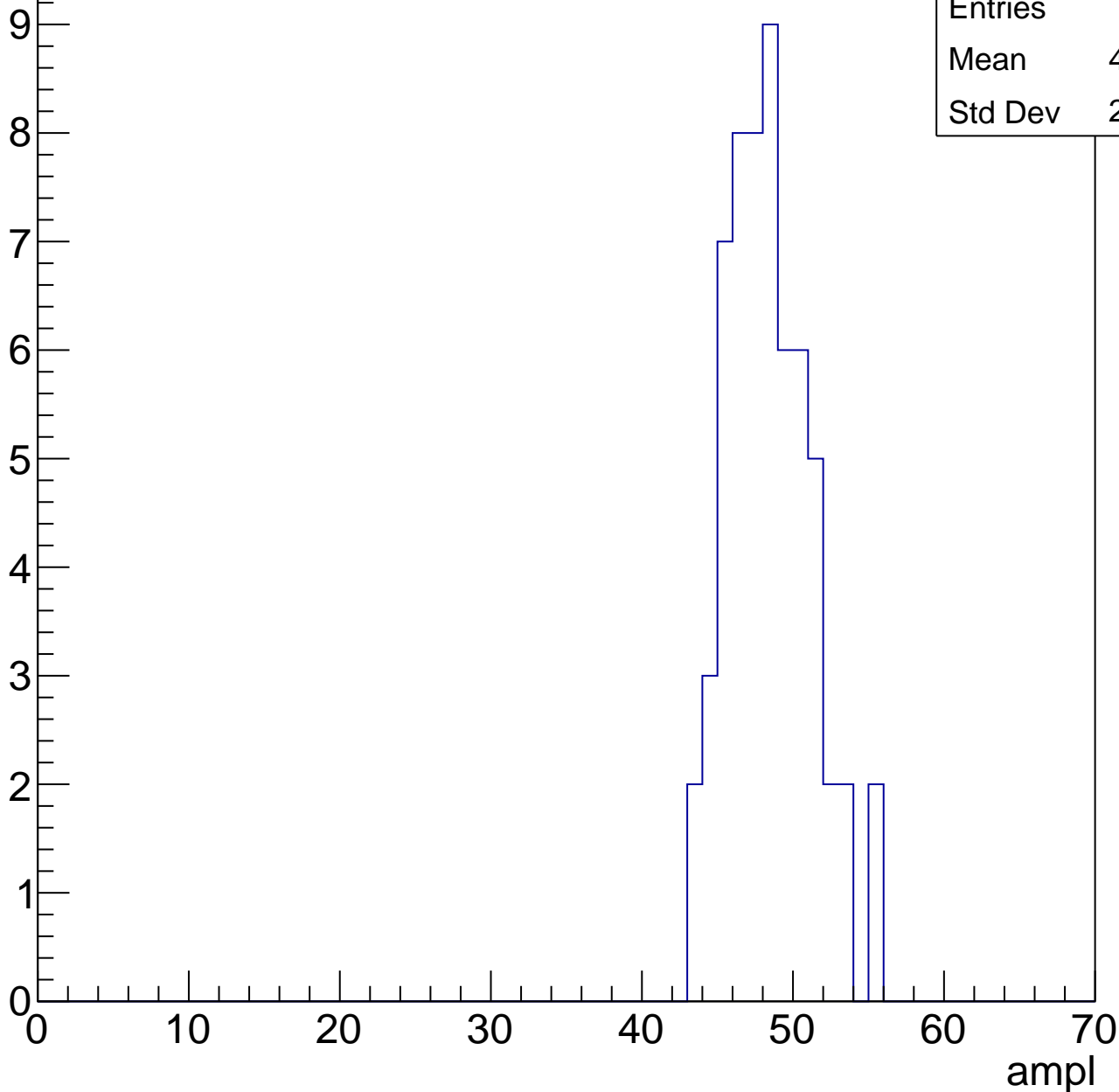


# B1L002S, U18-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	47.97
Std Dev	2.757

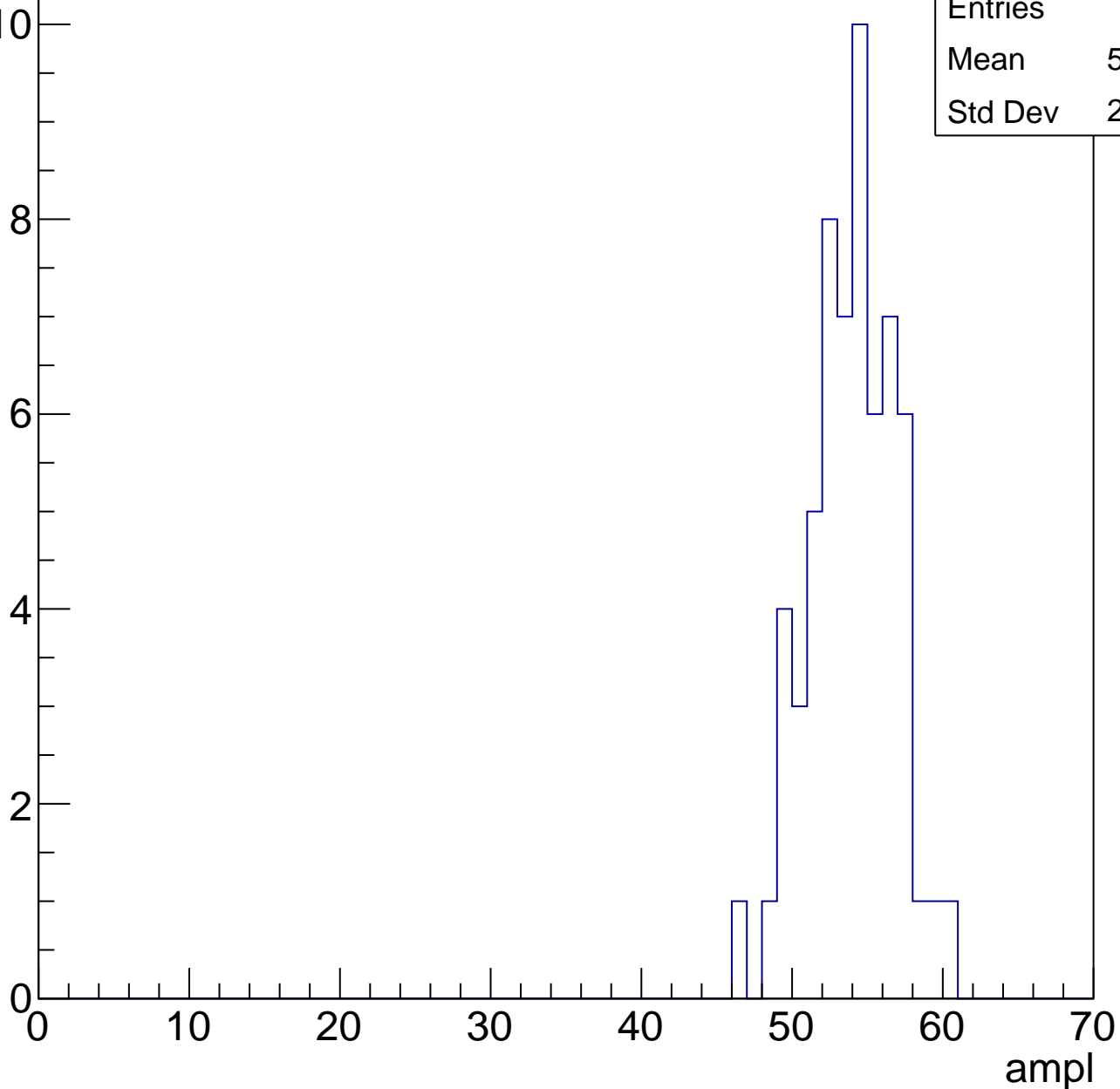


# B1L002S, U18-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	53.49
Std Dev	2.809

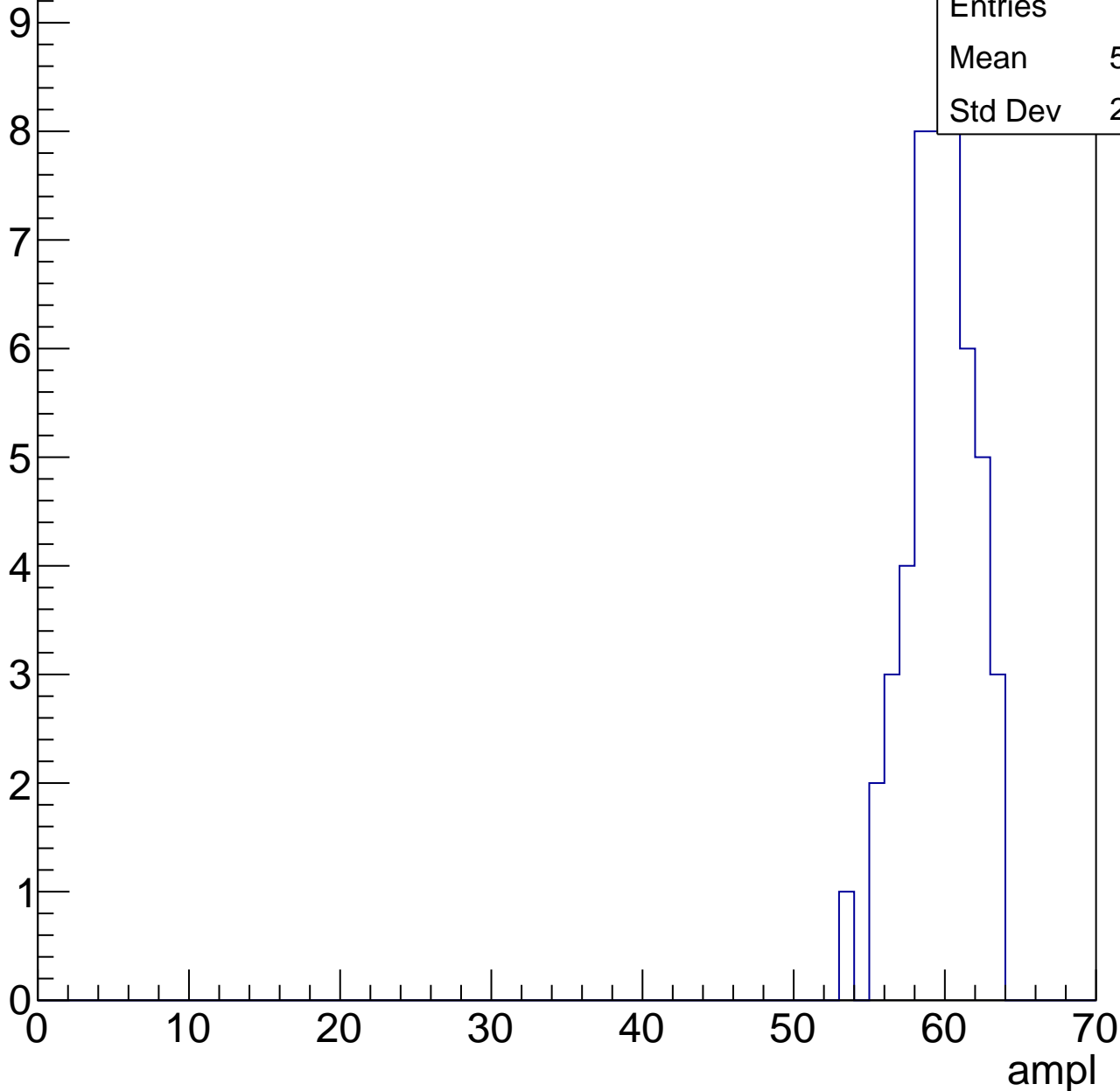


# B1L002S, U18-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	59.18
Std Dev	2.229

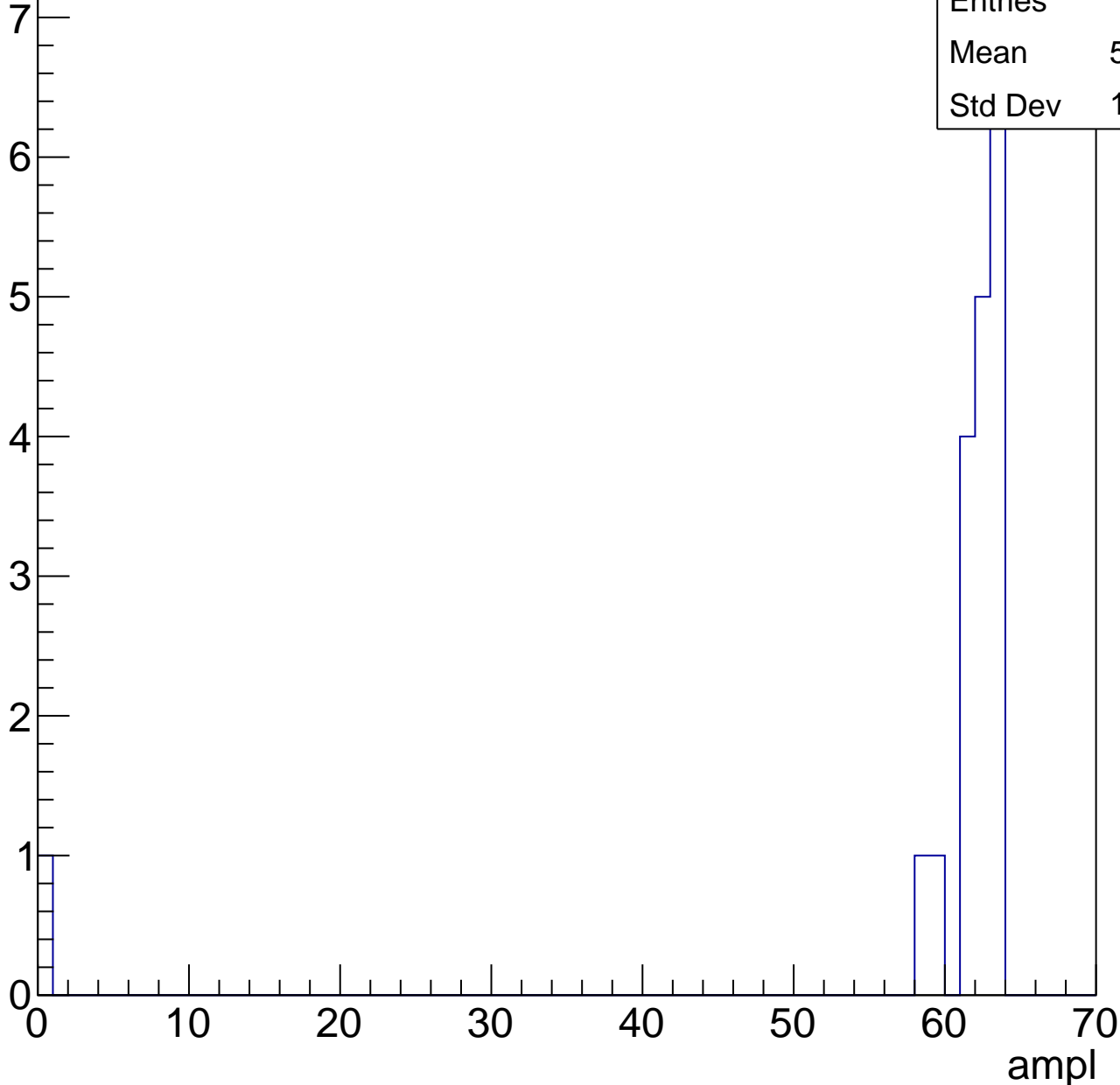


# B1L002S, U18-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.53
Std Dev	13.86





# B1L002S, U18-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch40, adc0

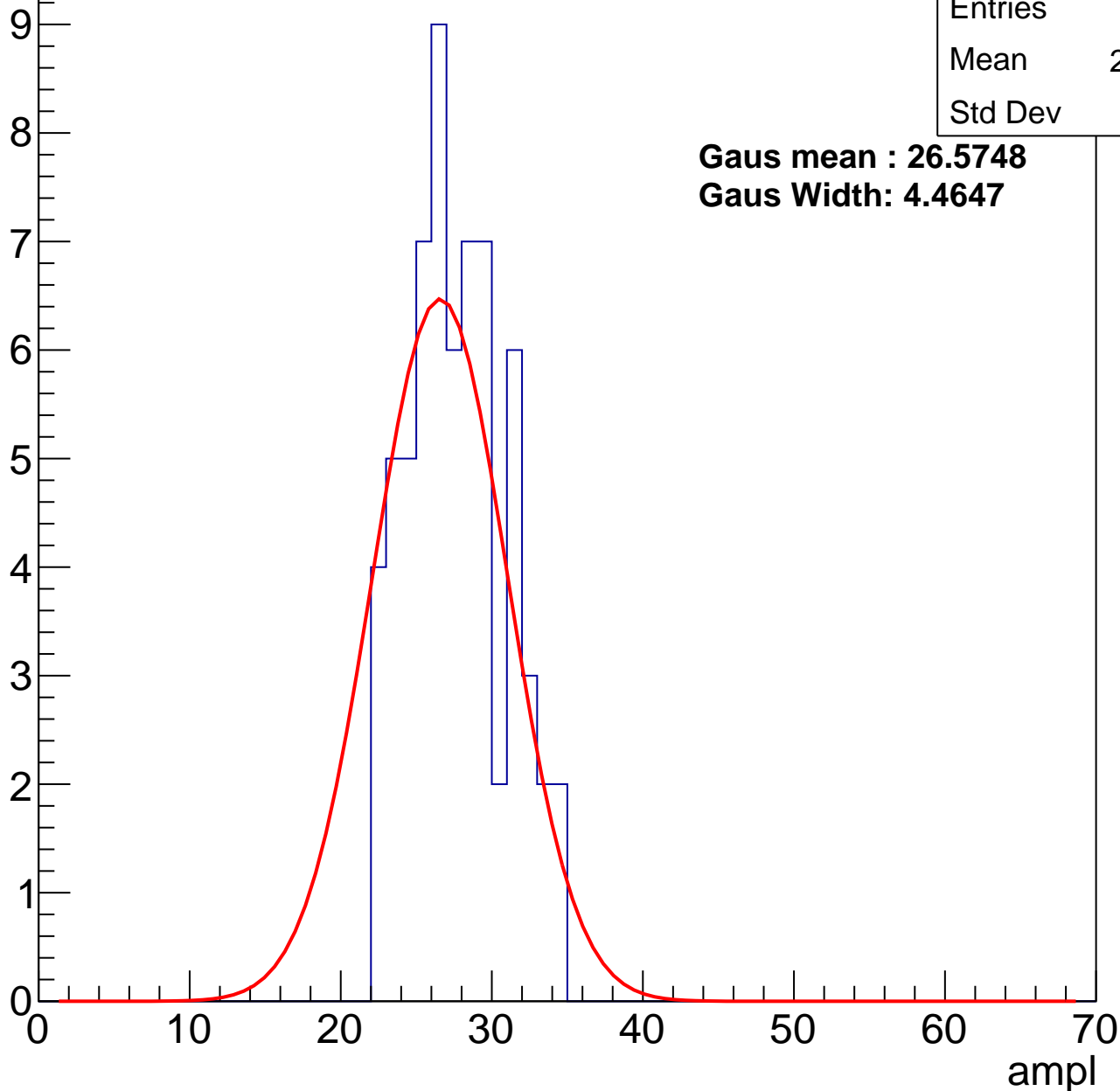
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.22
Std Dev	3.17

**Gaus mean : 26.5748**

**Gaus Width: 4.4647**



# B1L002S, U18-ch40, adc1

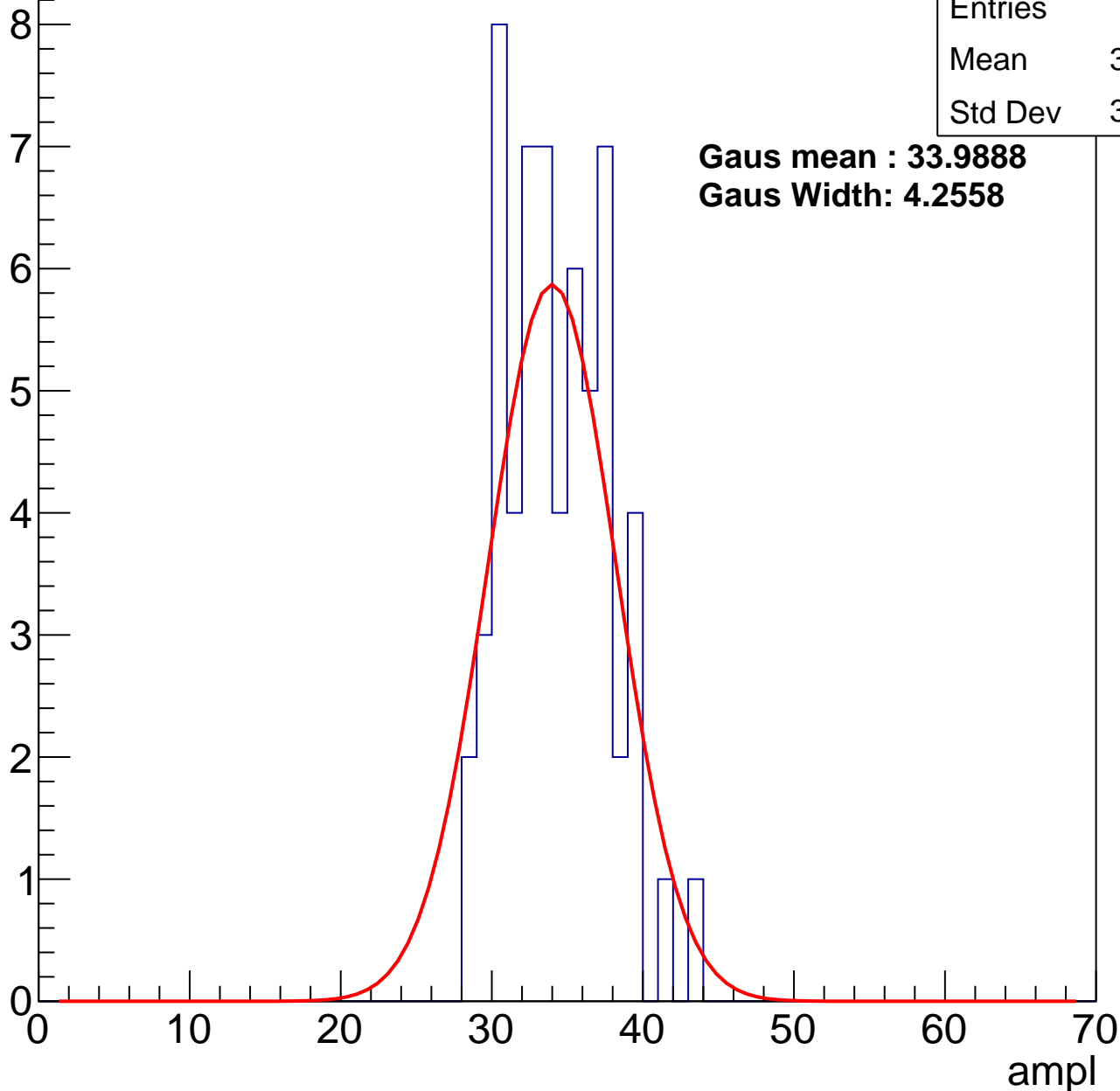
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	33.82
Std Dev	3.375

**Gaus mean : 33.9888**

**Gaus Width: 4.2558**



# B1L002S, U18-ch40, adc2

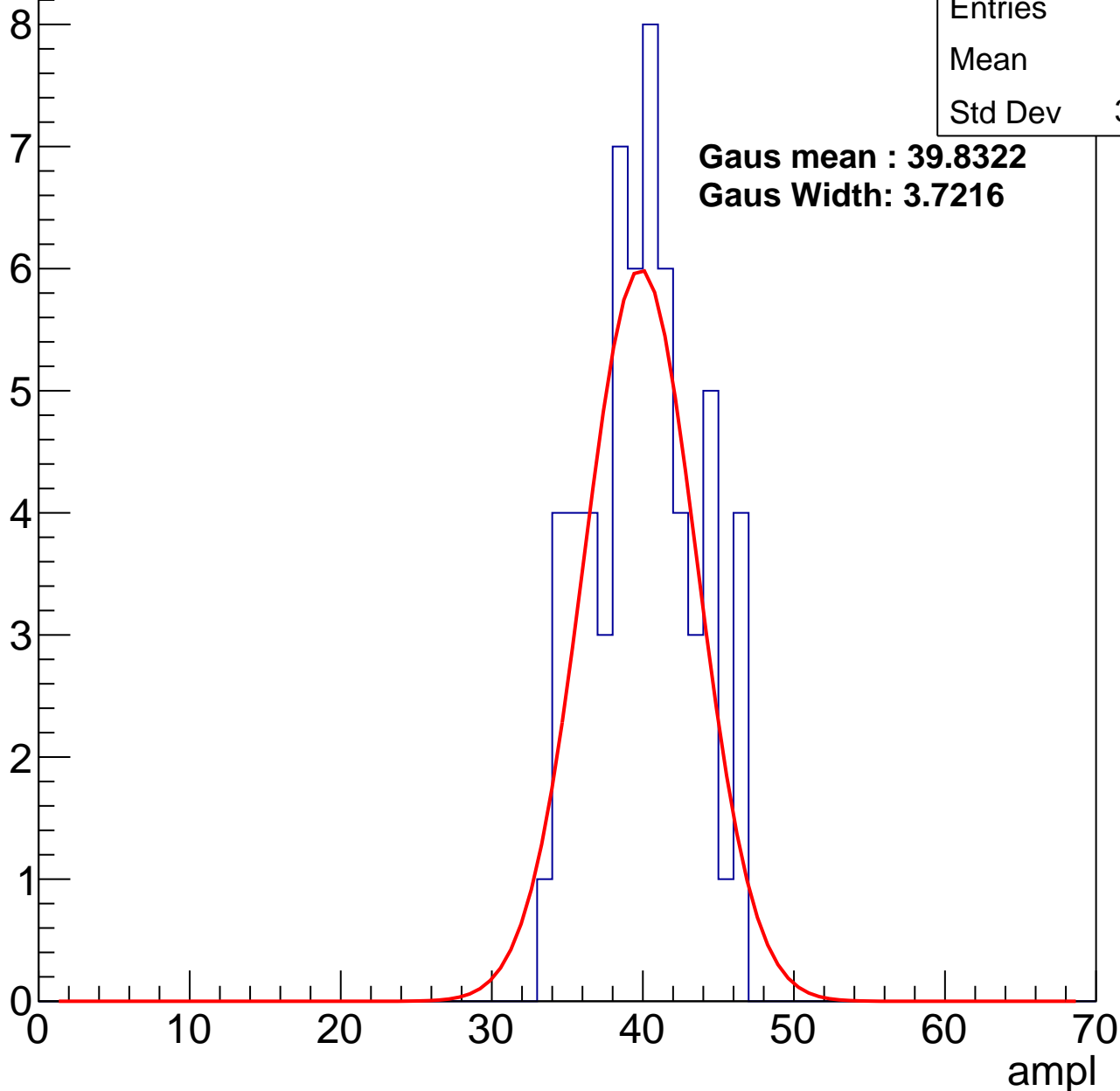
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	39.6
Std Dev	3.431

**Gaus mean : 39.8322**

**Gaus Width: 3.7216**

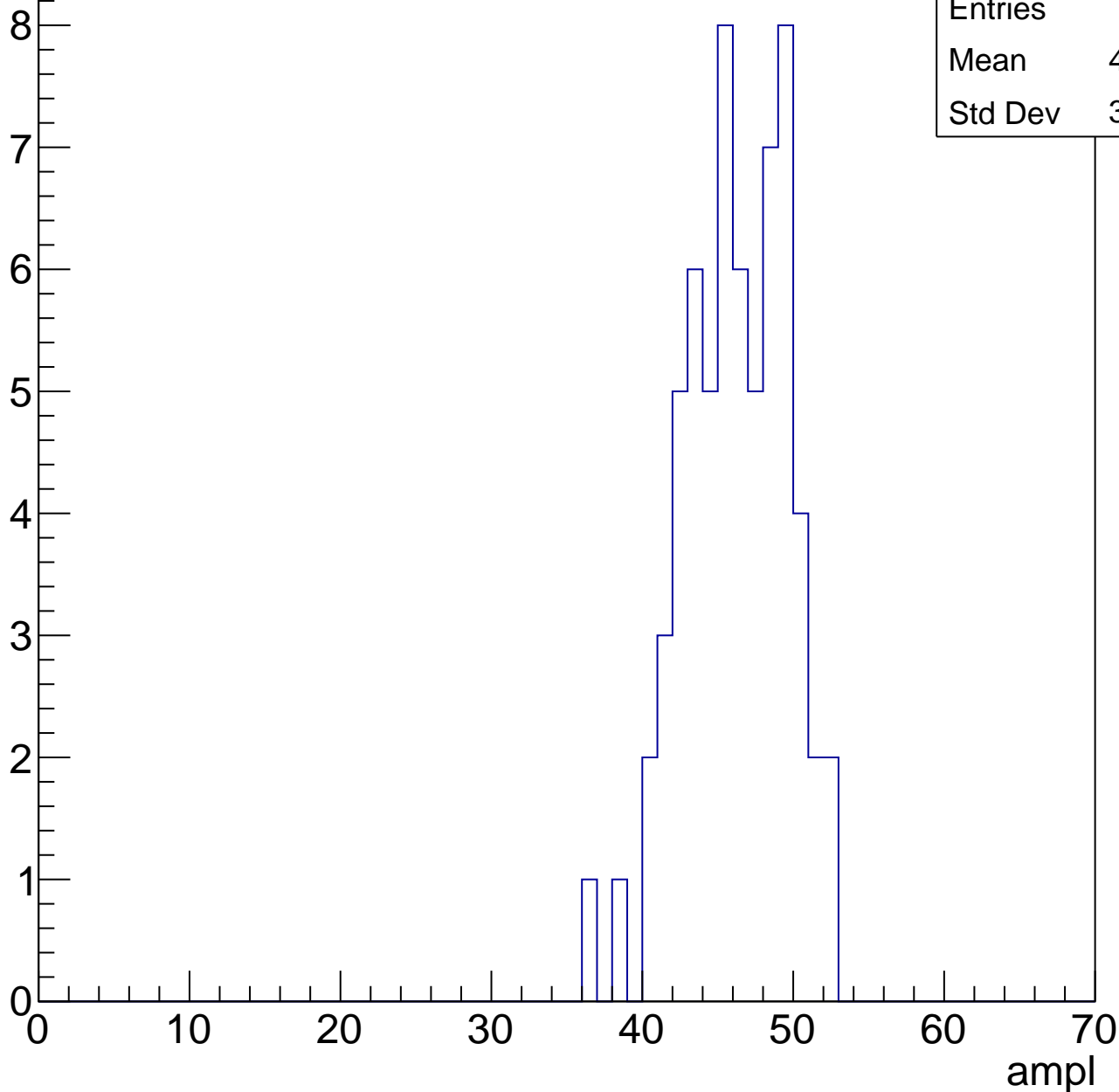


# B1L002S, U18-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

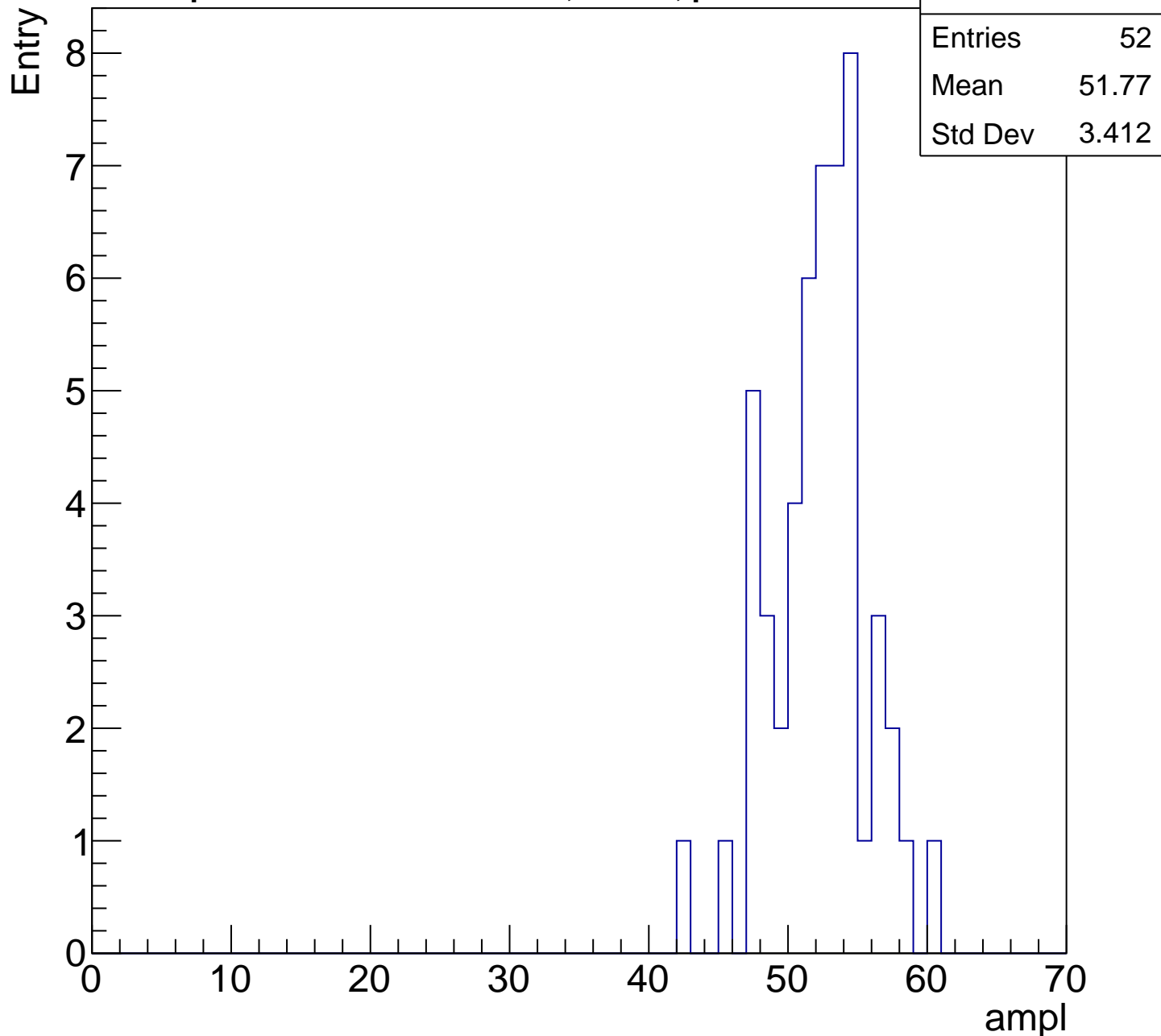
Entry

Entries	65
Mean	45.69
Std Dev	3.415



# B1L002S, U18-ch40, adc4

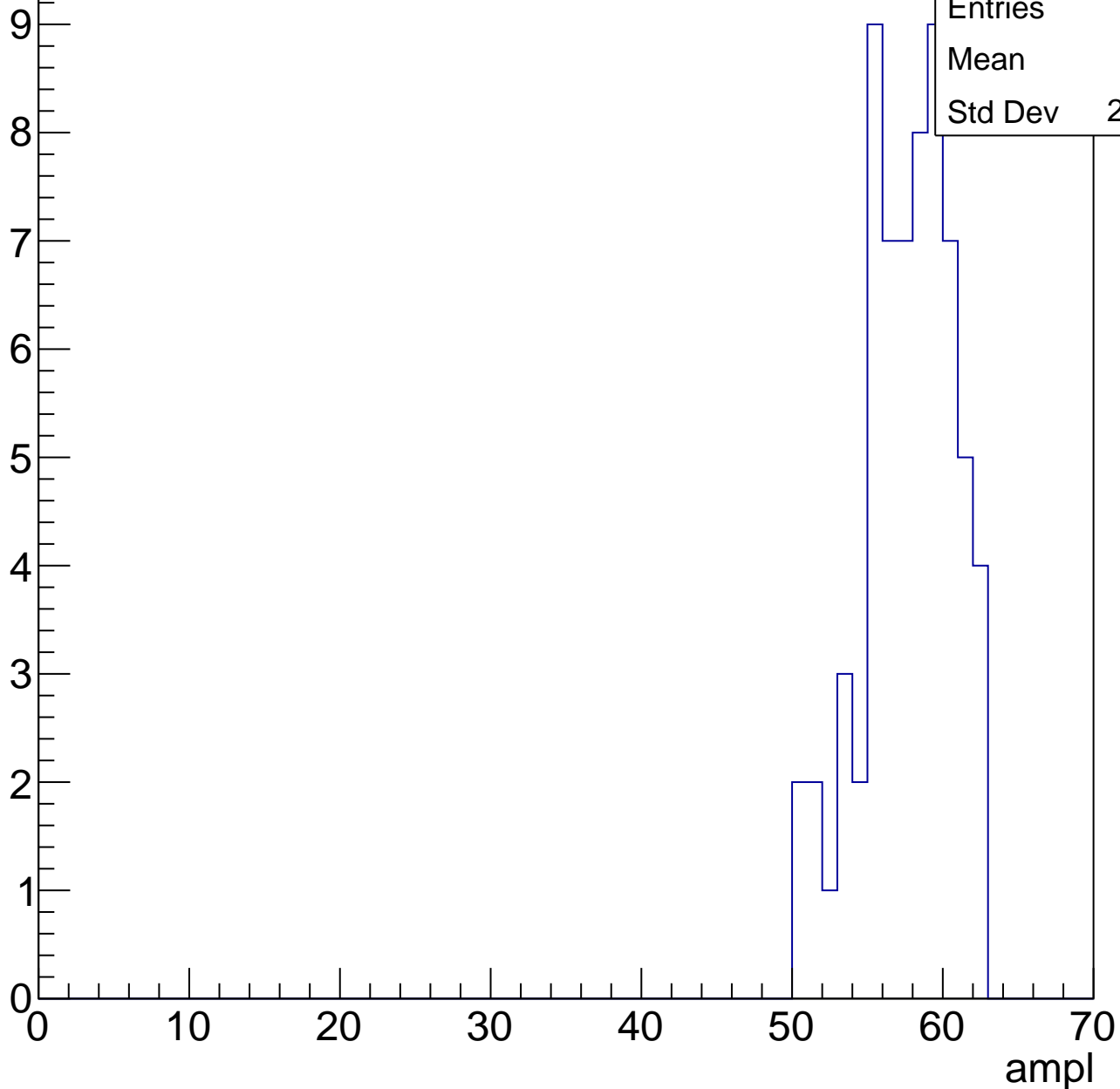
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



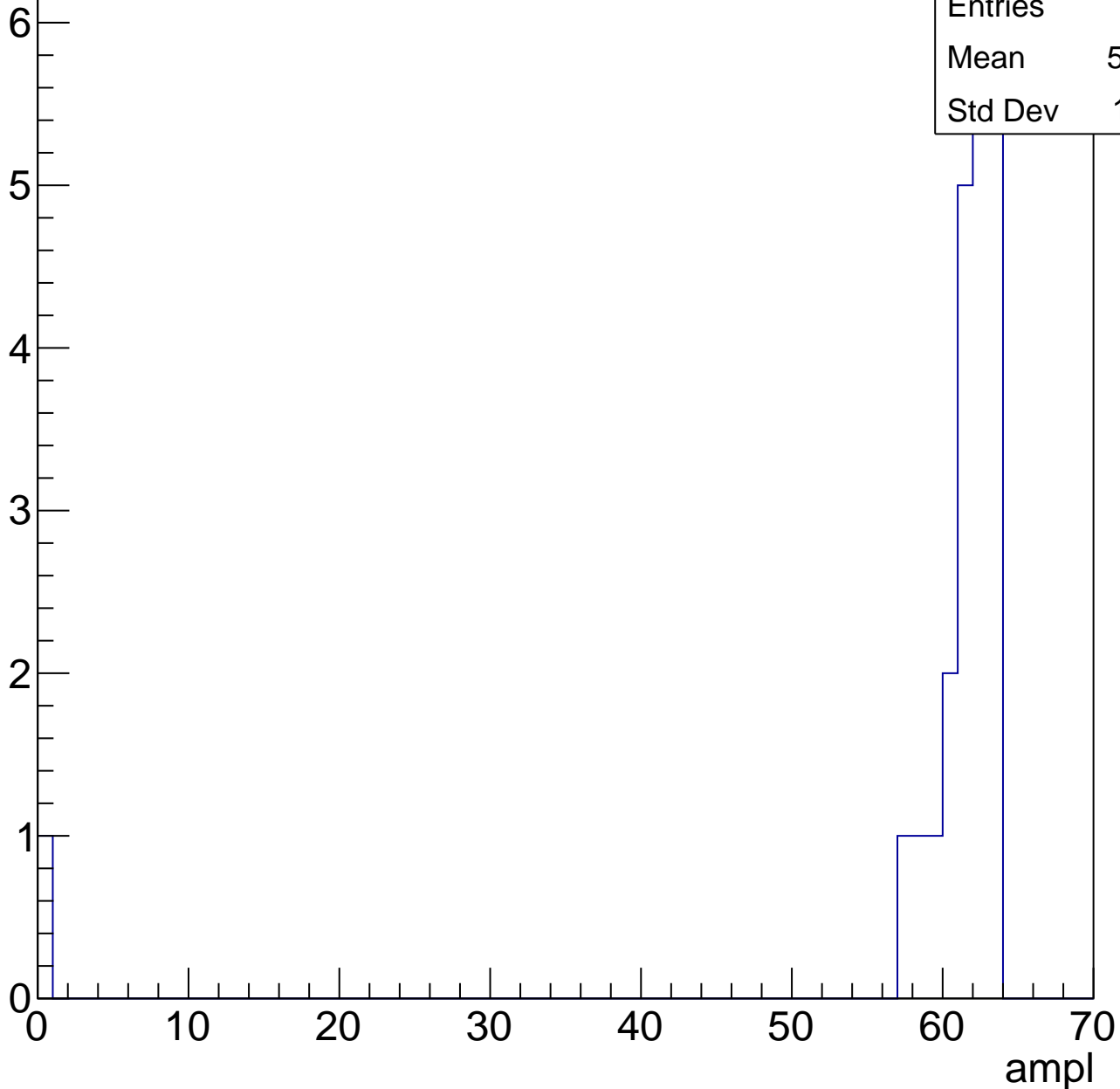
Entries	66
Mean	57.2
Std Dev	2.986

# B1L002S, U18-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.65
Std Dev	12.61

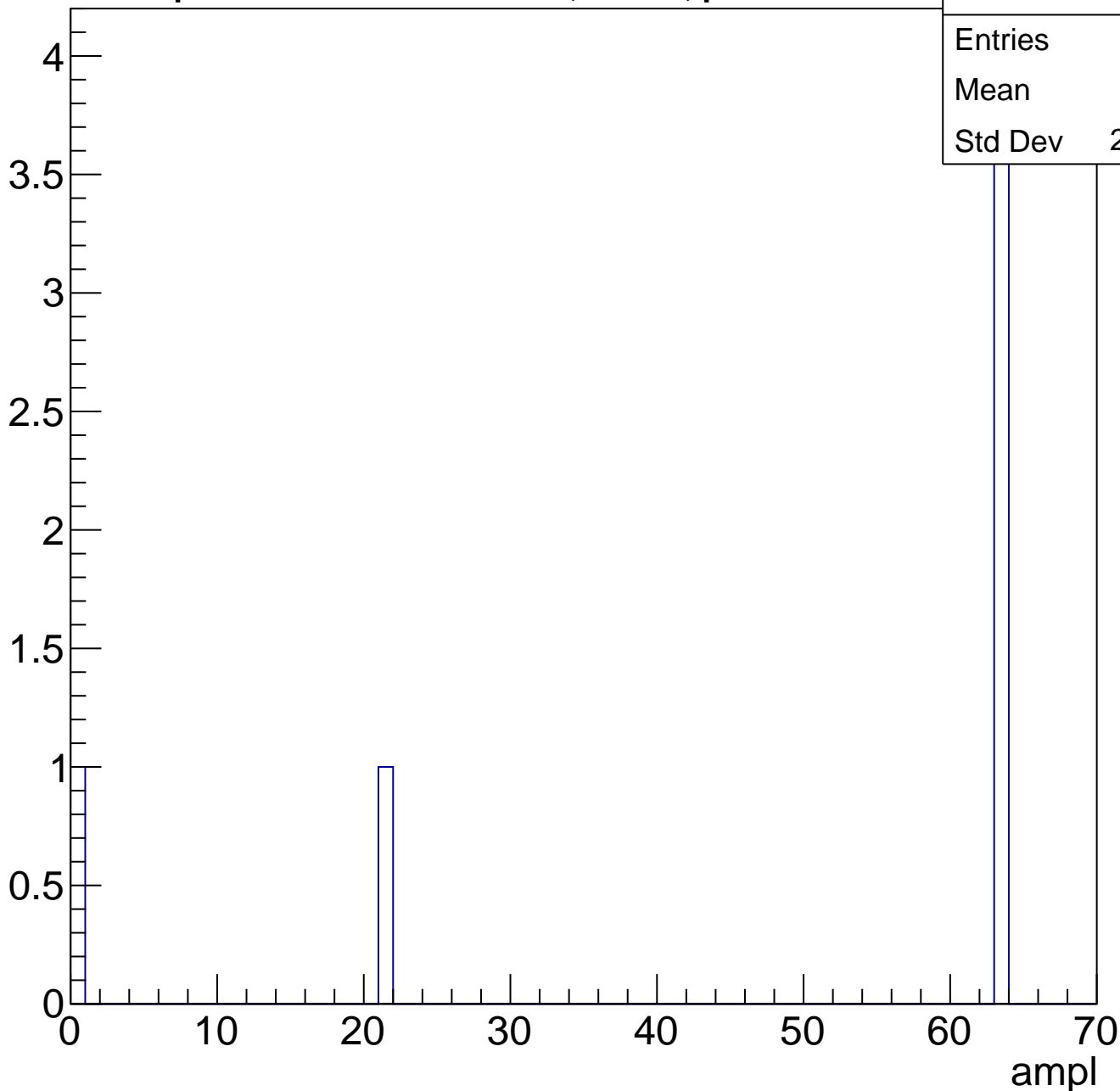




# B1L002S, U18-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	45.5
Std Dev	25.48

# B1L002S, U18-ch41, adc0

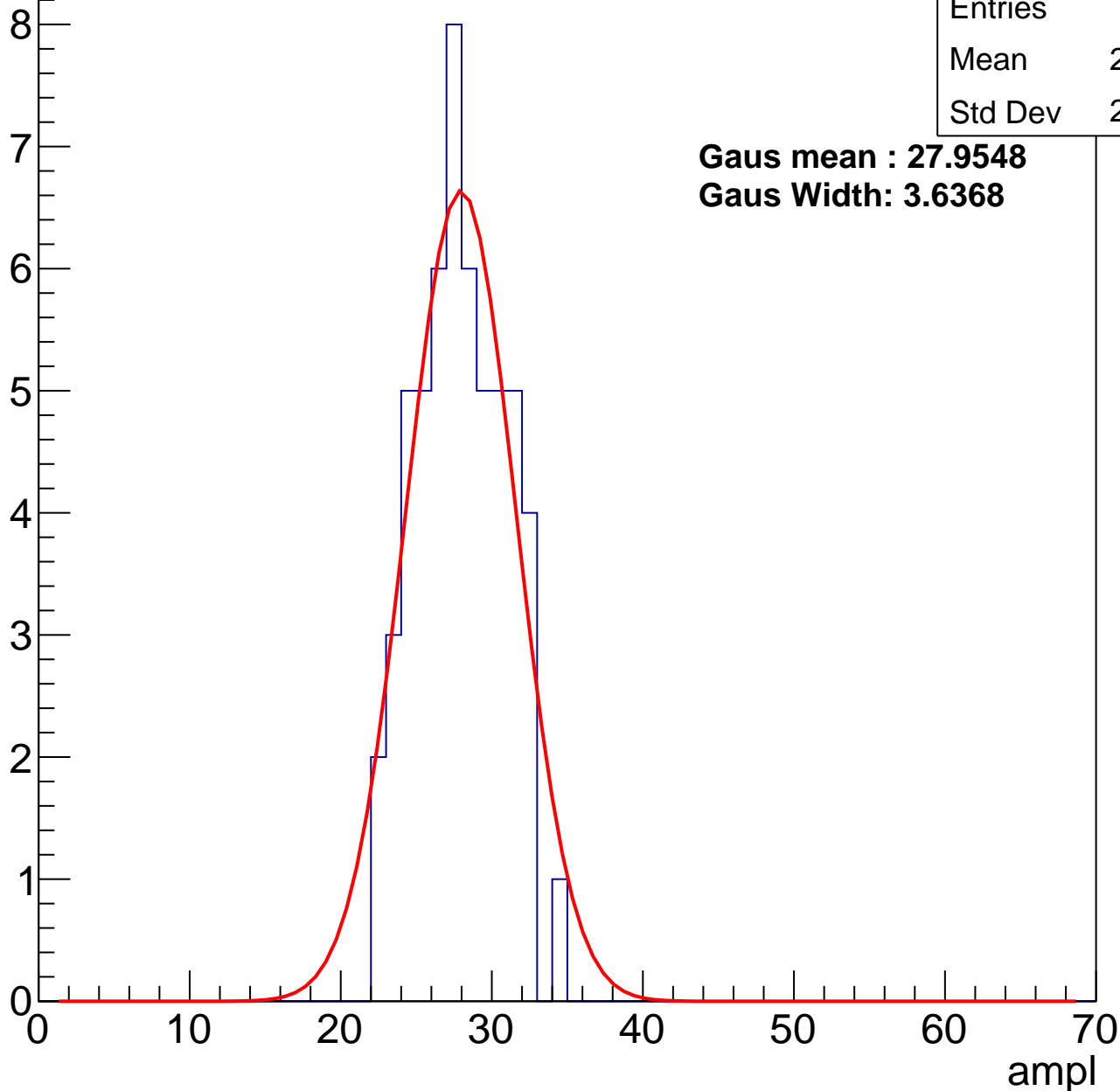
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.45
Std Dev	2.885

**Gaus mean : 27.9548**

**Gaus Width: 3.6368**



# B1L002S, U18-ch41, adc1

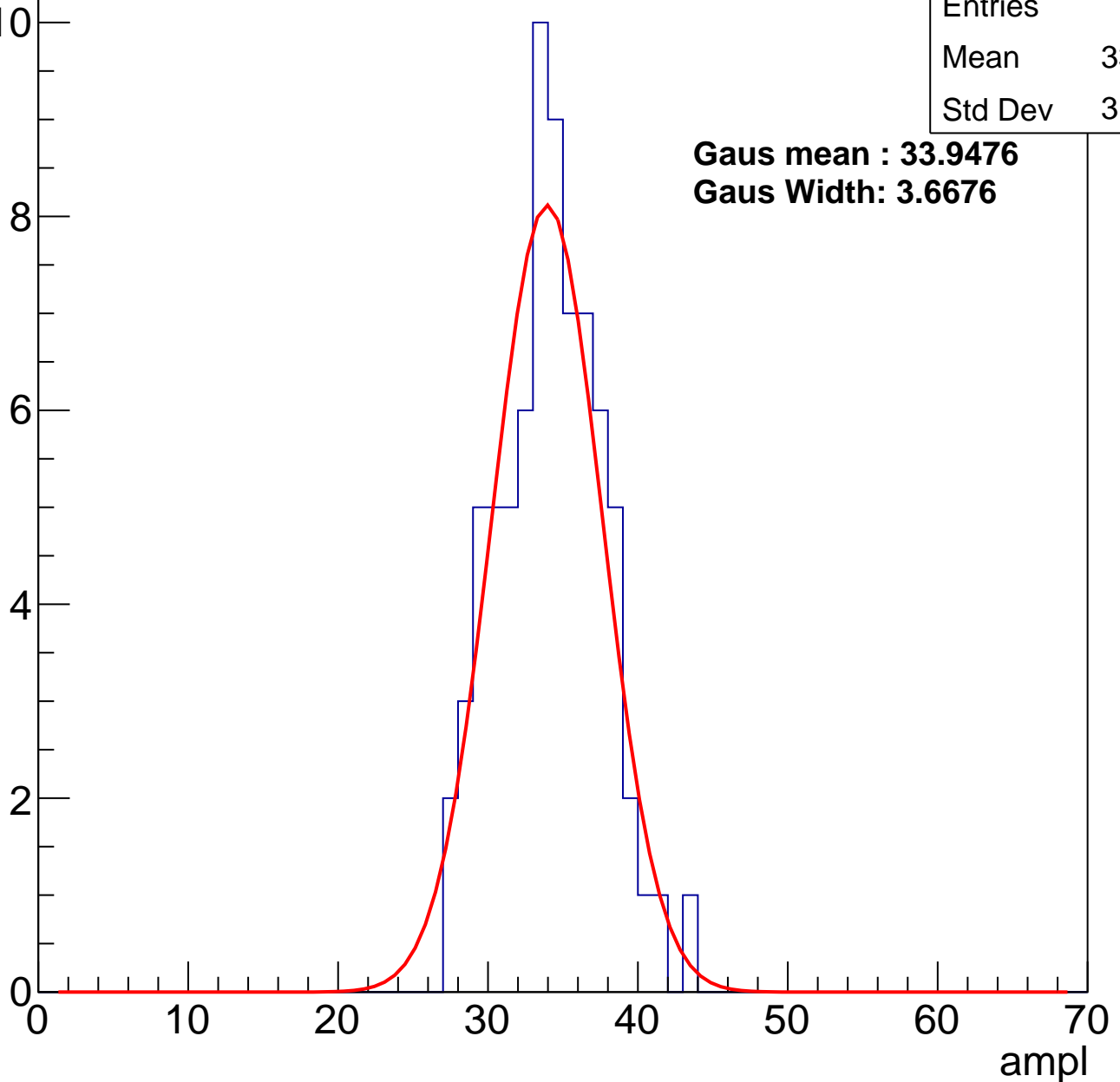
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	33.69
Std Dev	3.402

**Gaus mean : 33.9476**

**Gaus Width: 3.6676**



# B1L002S, U18-ch41, adc2

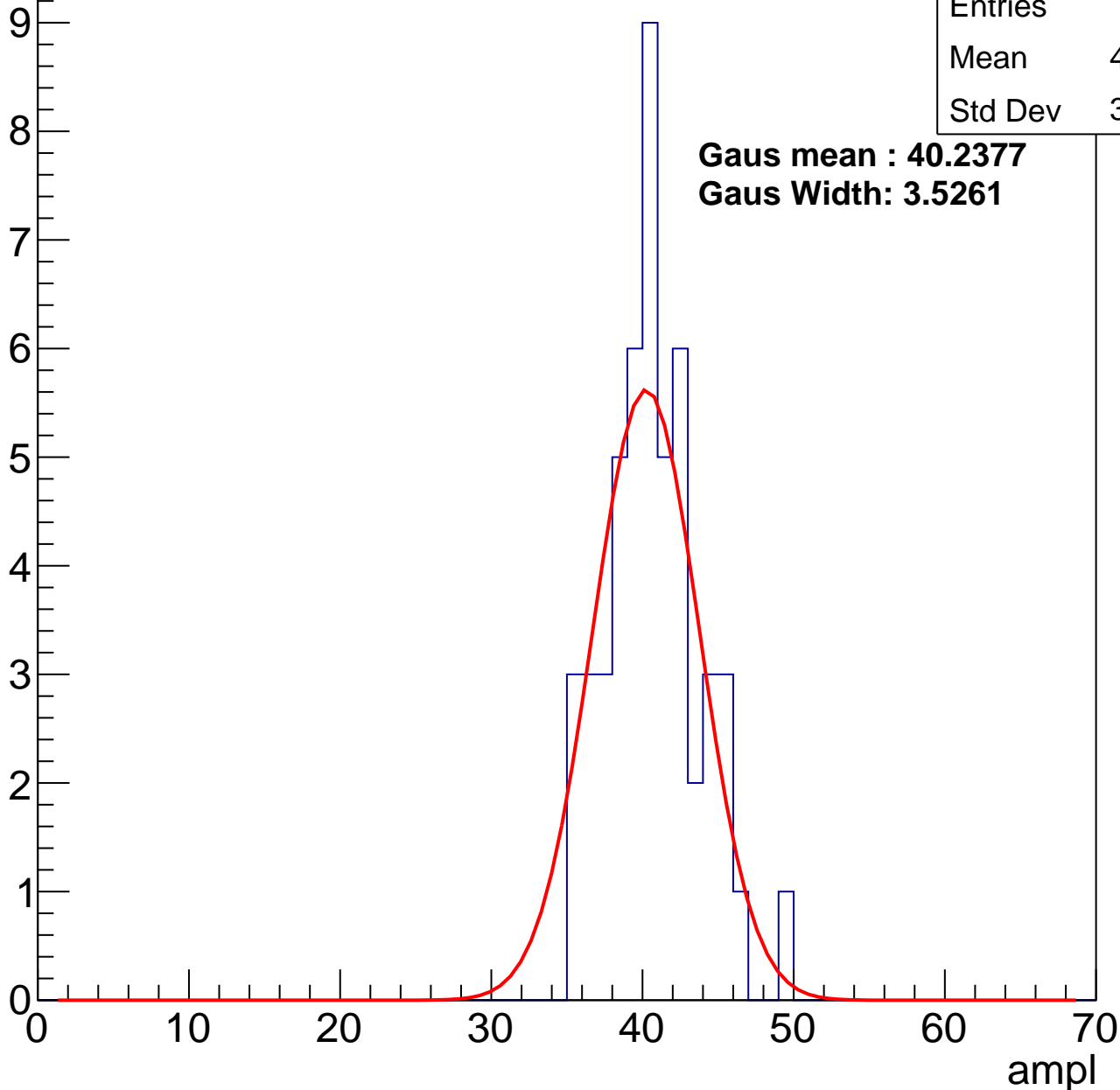
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.26
Std Dev	3.032

**Gaus mean : 40.2377**

**Gaus Width: 3.5261**

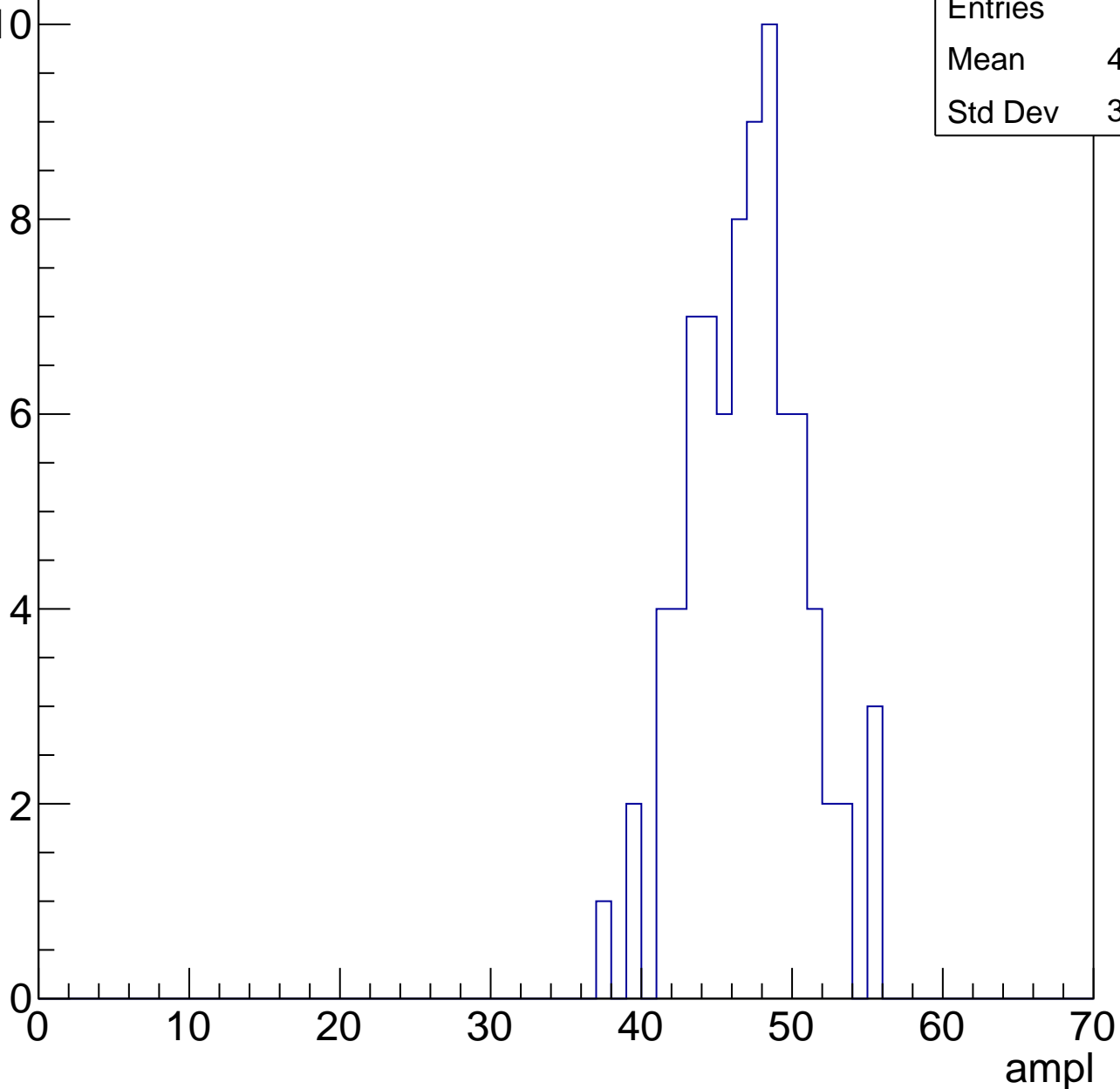


# B1L002S, U18-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	46.54
Std Dev	3.732

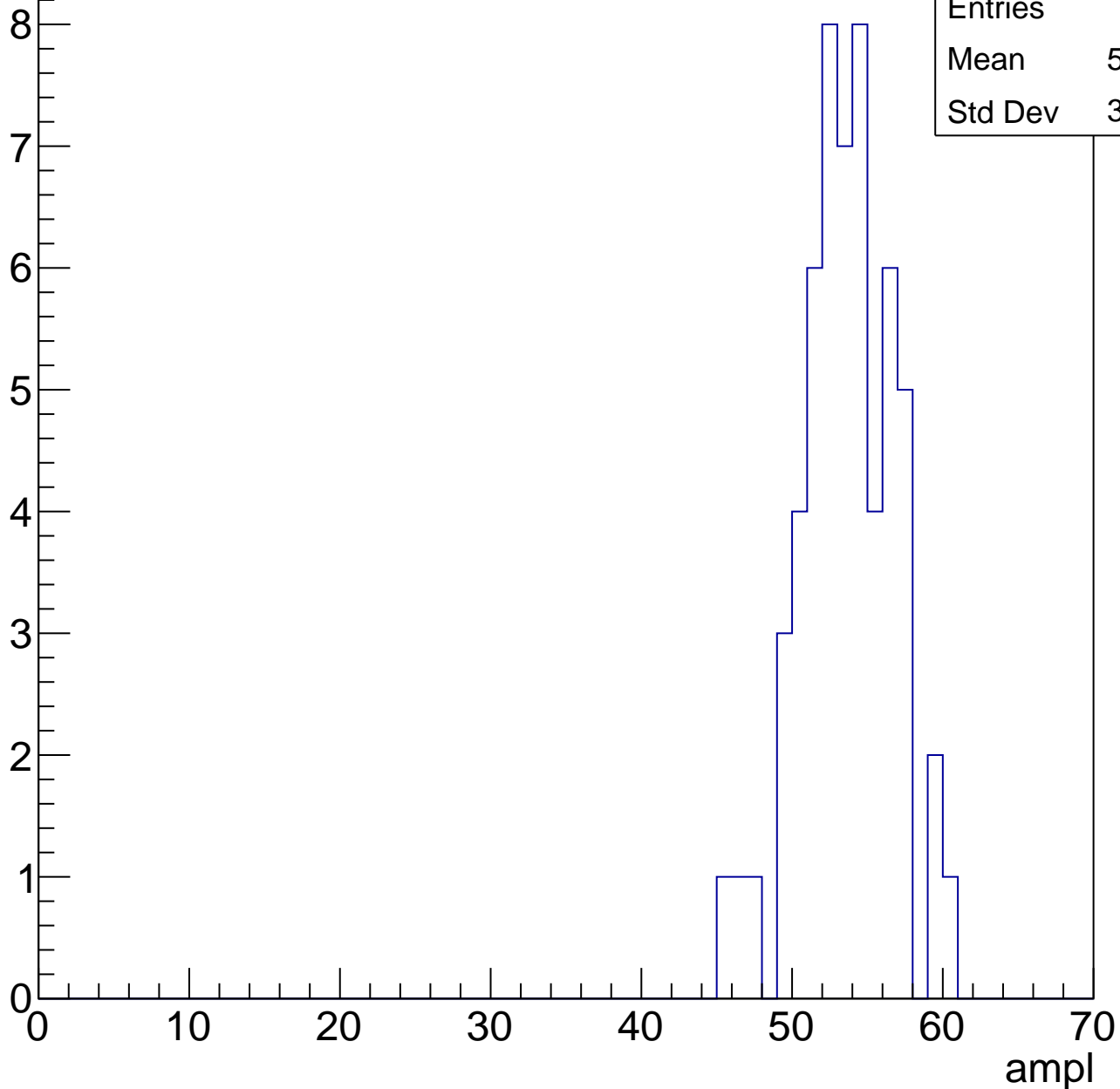


# B1L002S, U18-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.14
Std Dev	3.086



# B1L002S, U18-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	57
Mean	59.04
Std Dev	2.52

Entry

10

8

6

4

2

0

0

10

20

30

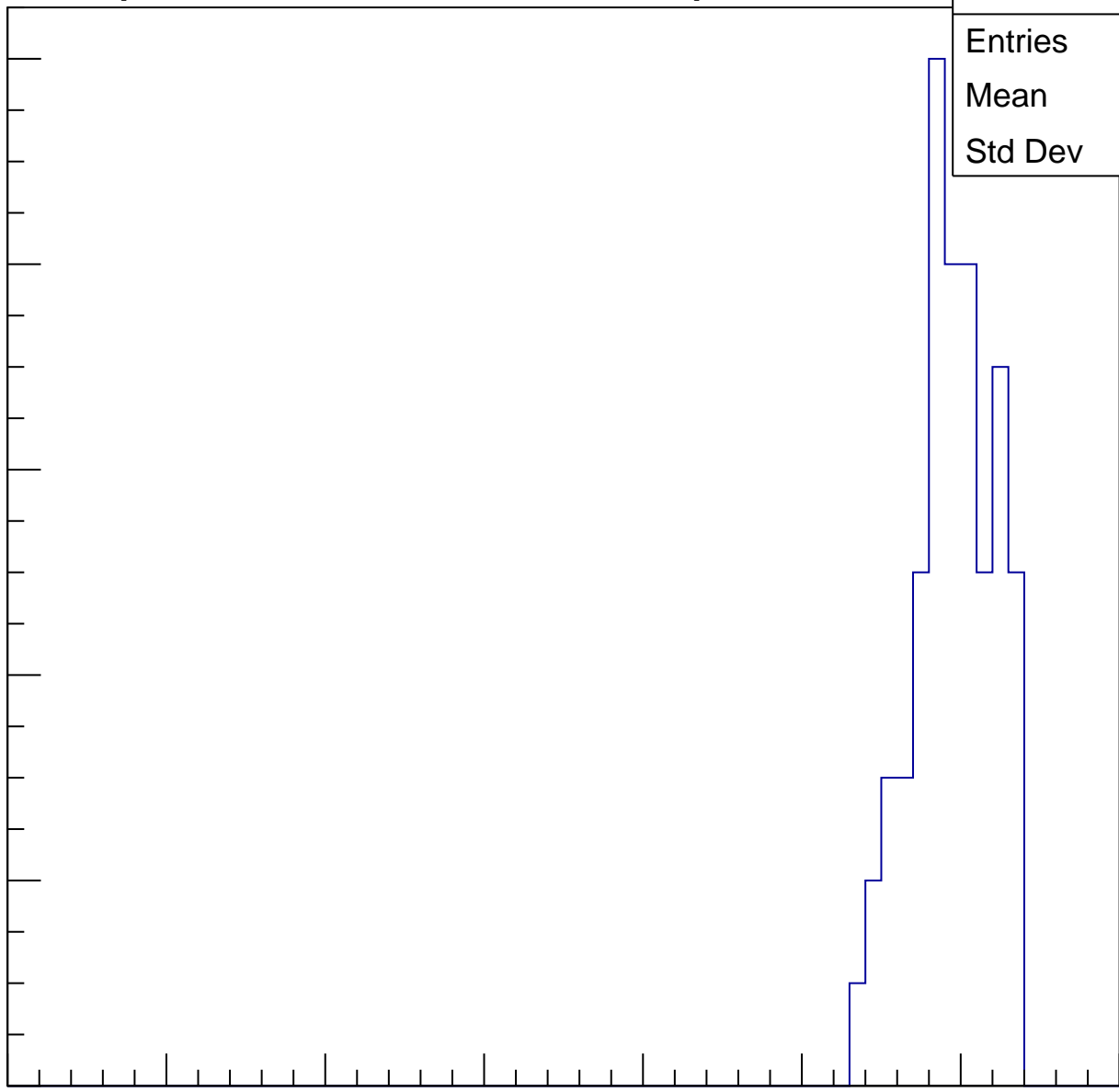
40

50

60

70

ampl

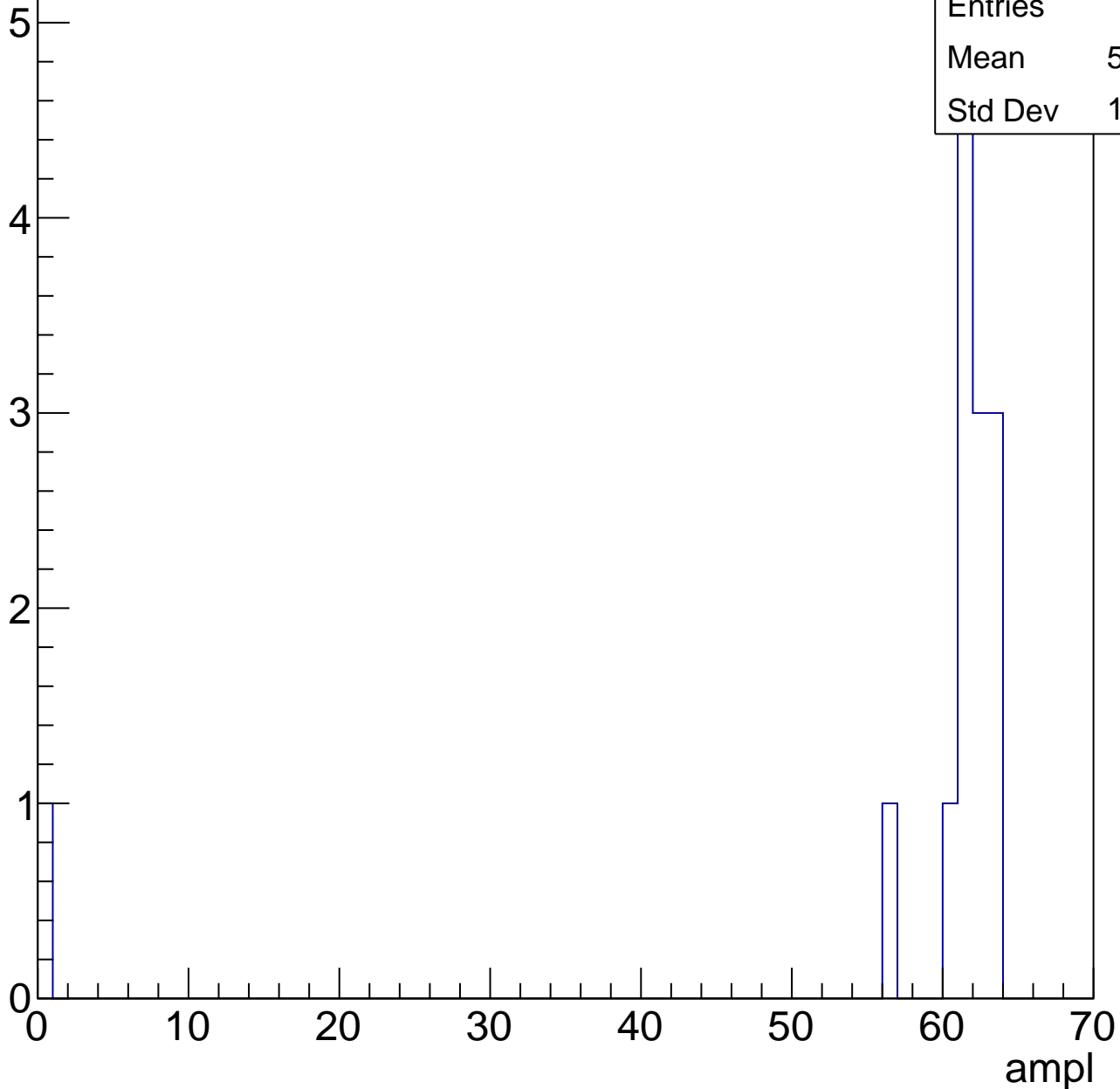


# B1L002S, U18-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	56.86
Std Dev	15.86

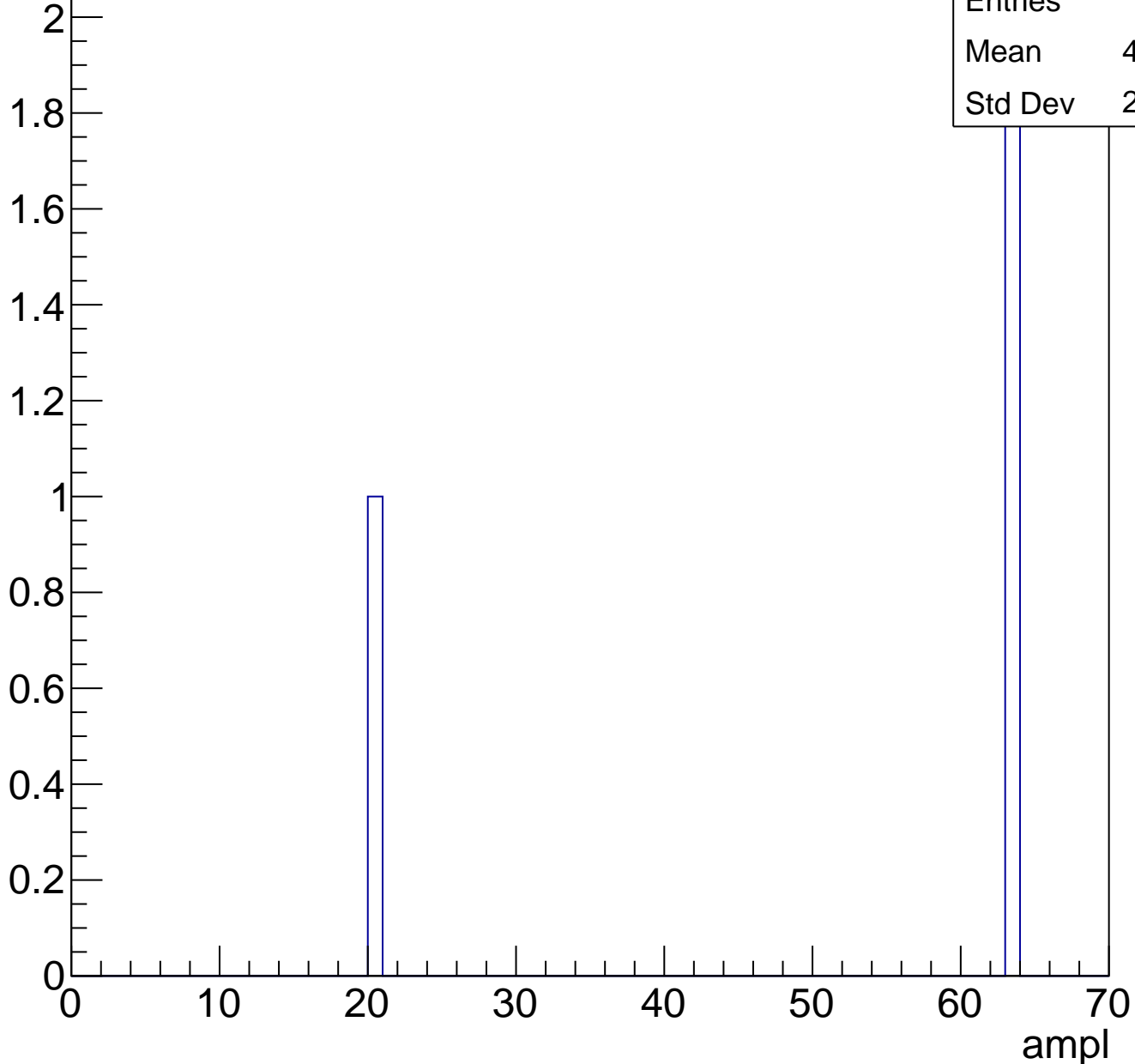




# B1L002S, U18-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	48.67
Std Dev	20.27

# B1L002S, U18-ch42, adc0

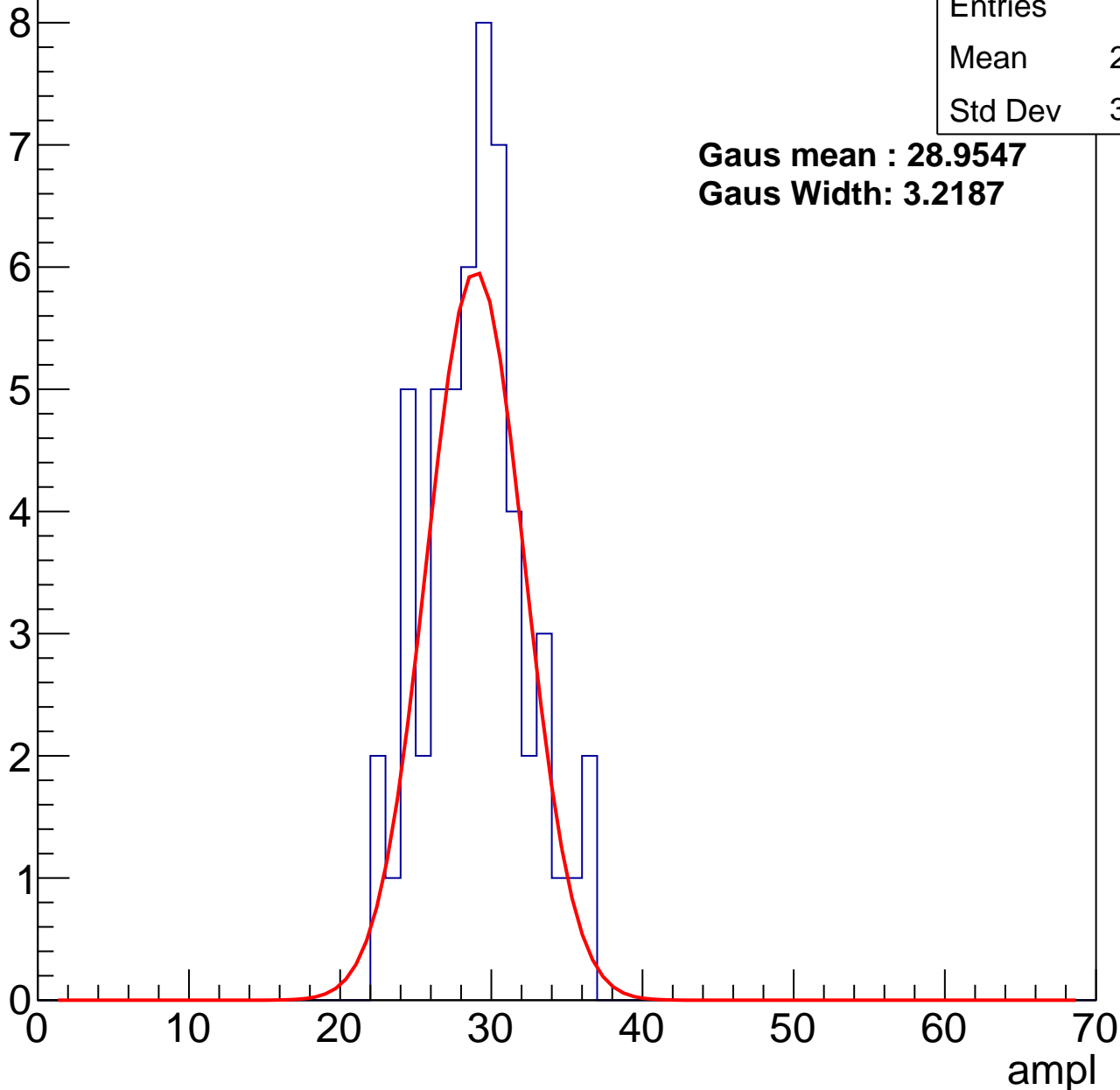
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.52
Std Dev	3.326

**Gaus mean : 28.9547**

**Gaus Width: 3.2187**



# B1L002S, U18-ch42, adc1

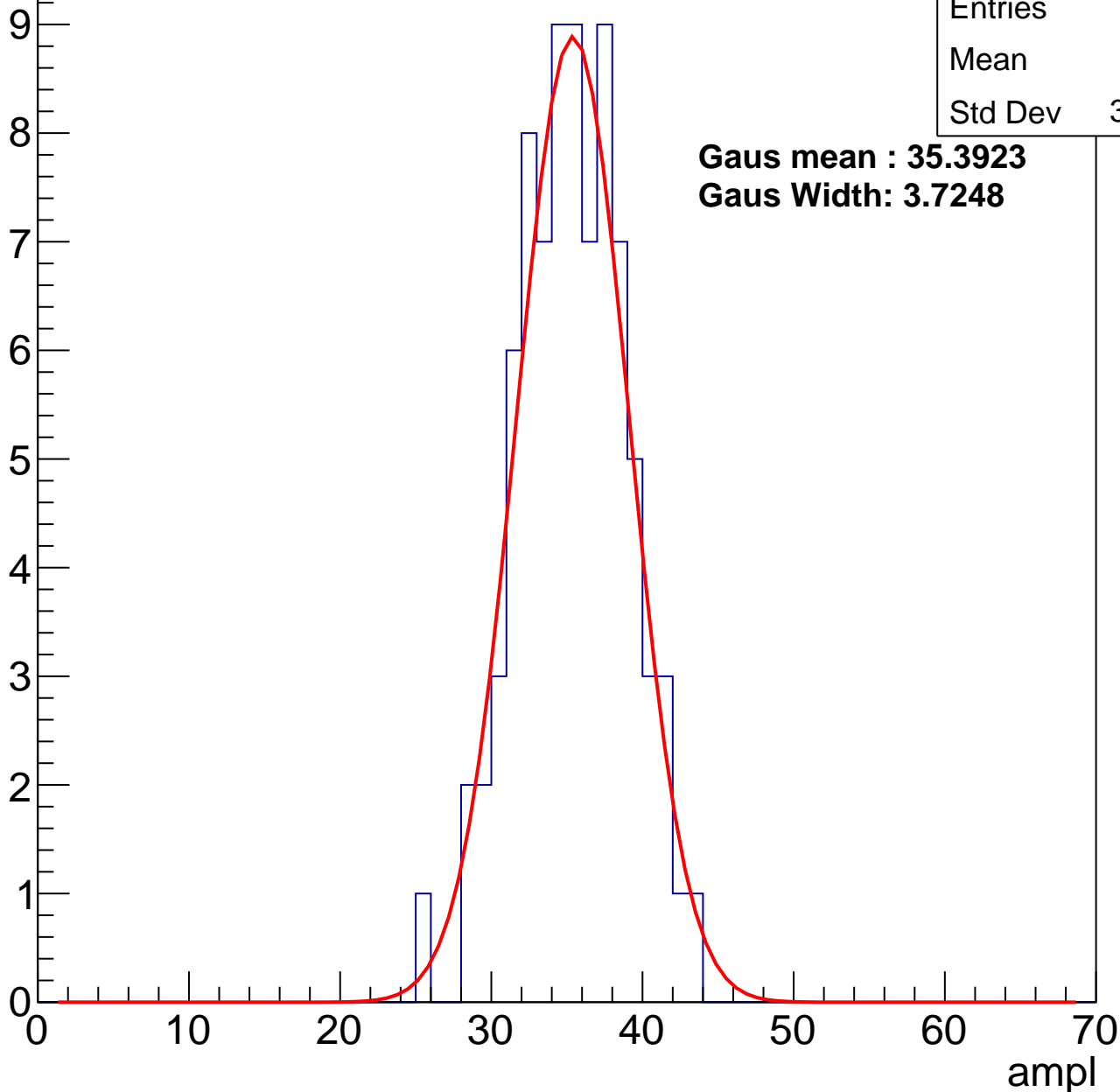
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	34.9
Std Dev	3.525

**Gaus mean : 35.3923**

**Gaus Width: 3.7248**



# B1L002S, U18-ch42, adc2

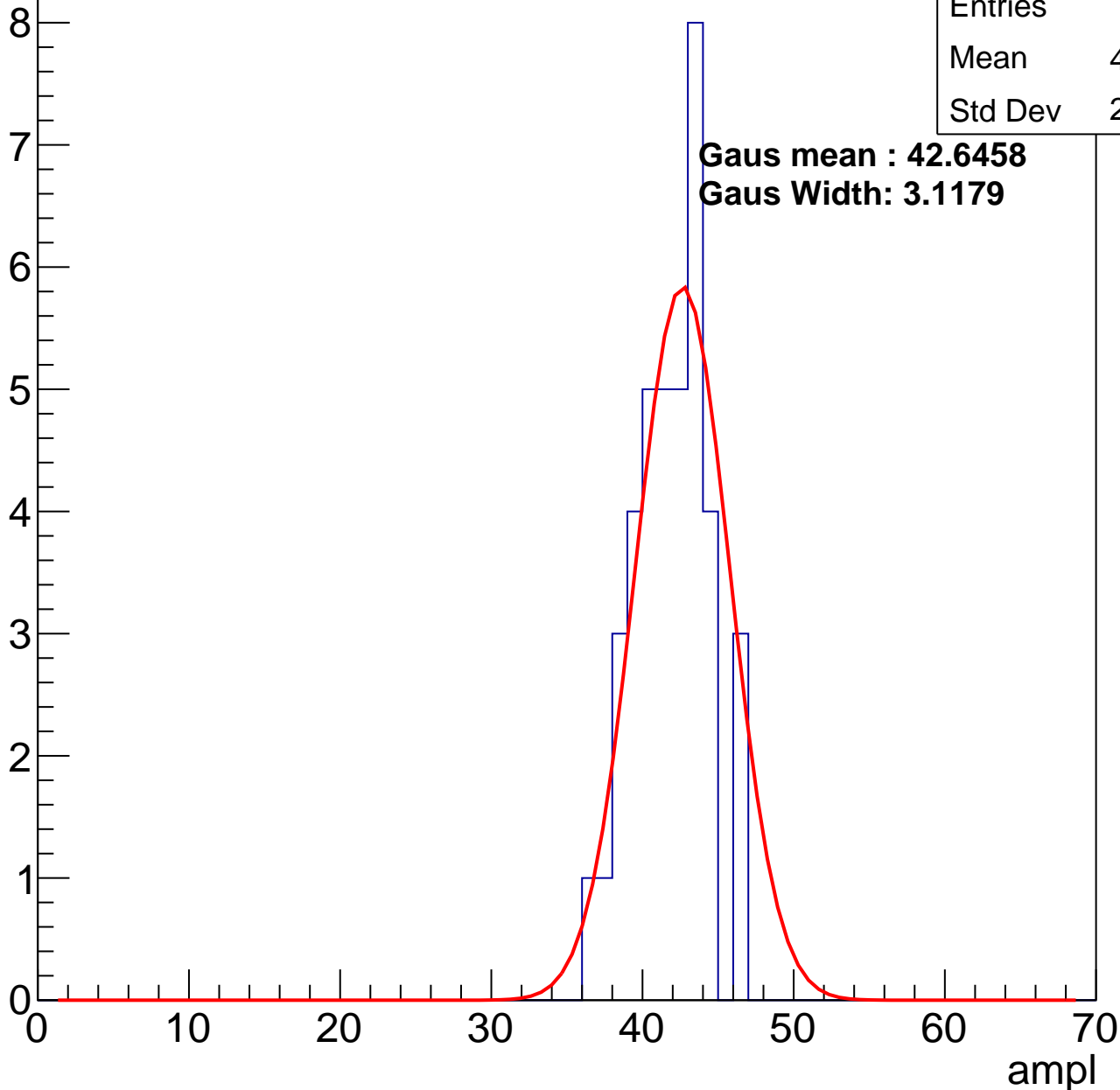
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	41.44
Std Dev	2.426

**Gaus mean : 42.6458**

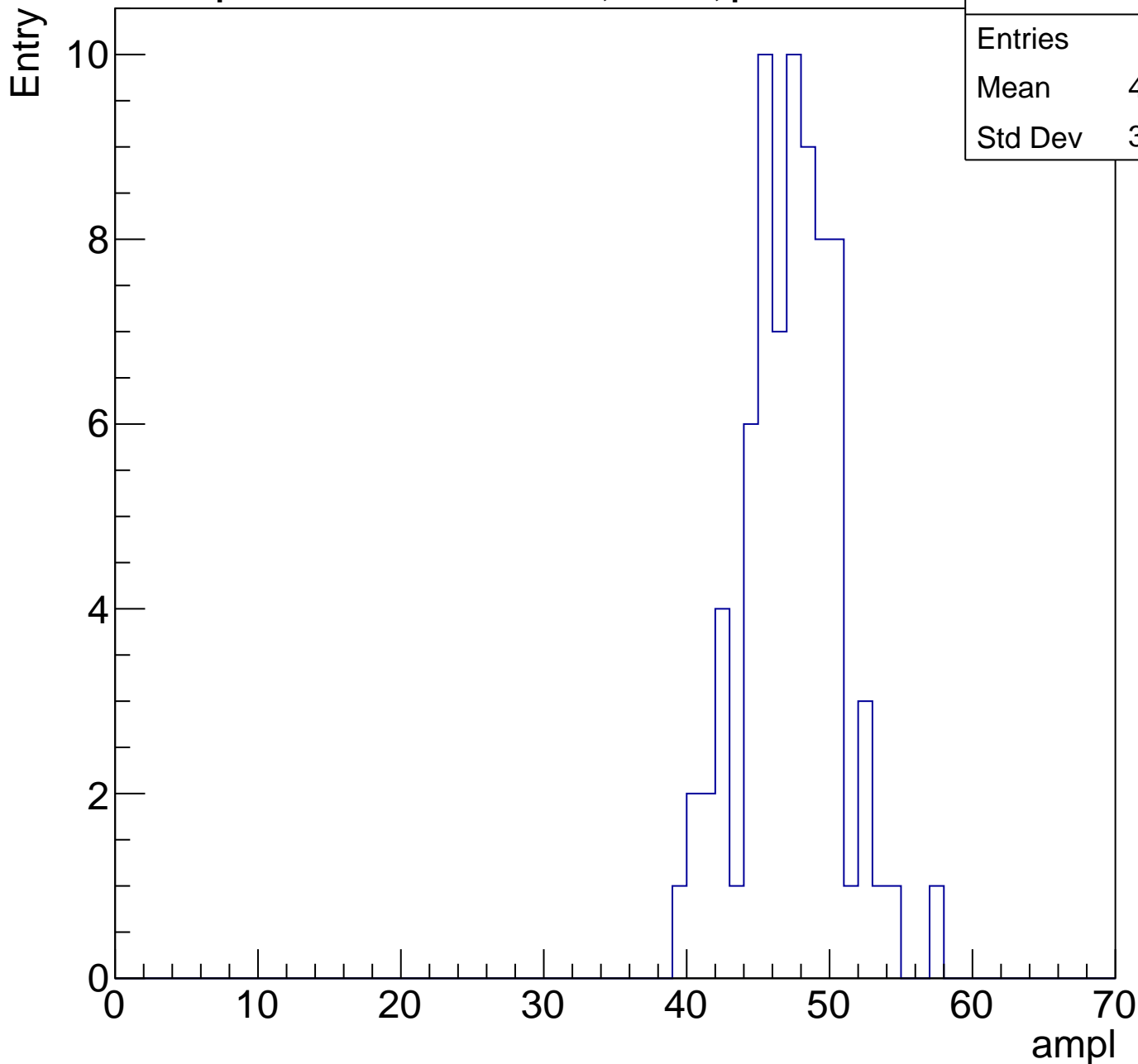
**Gaus Width: 3.1179**



# B1L002S, U18-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	46.84
Std Dev	3.343

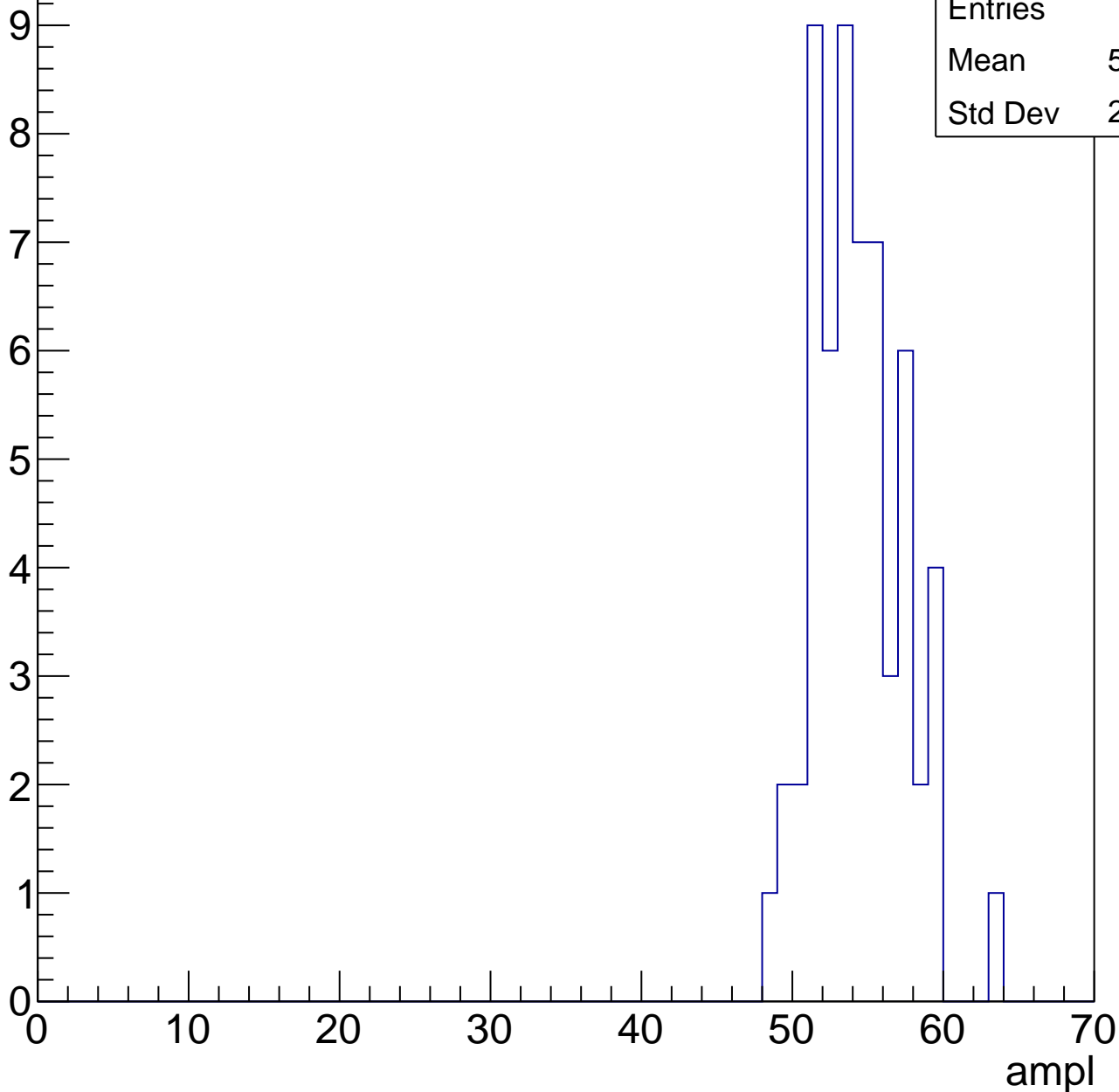


# B1L002S, U18-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	53.93
Std Dev	2.962

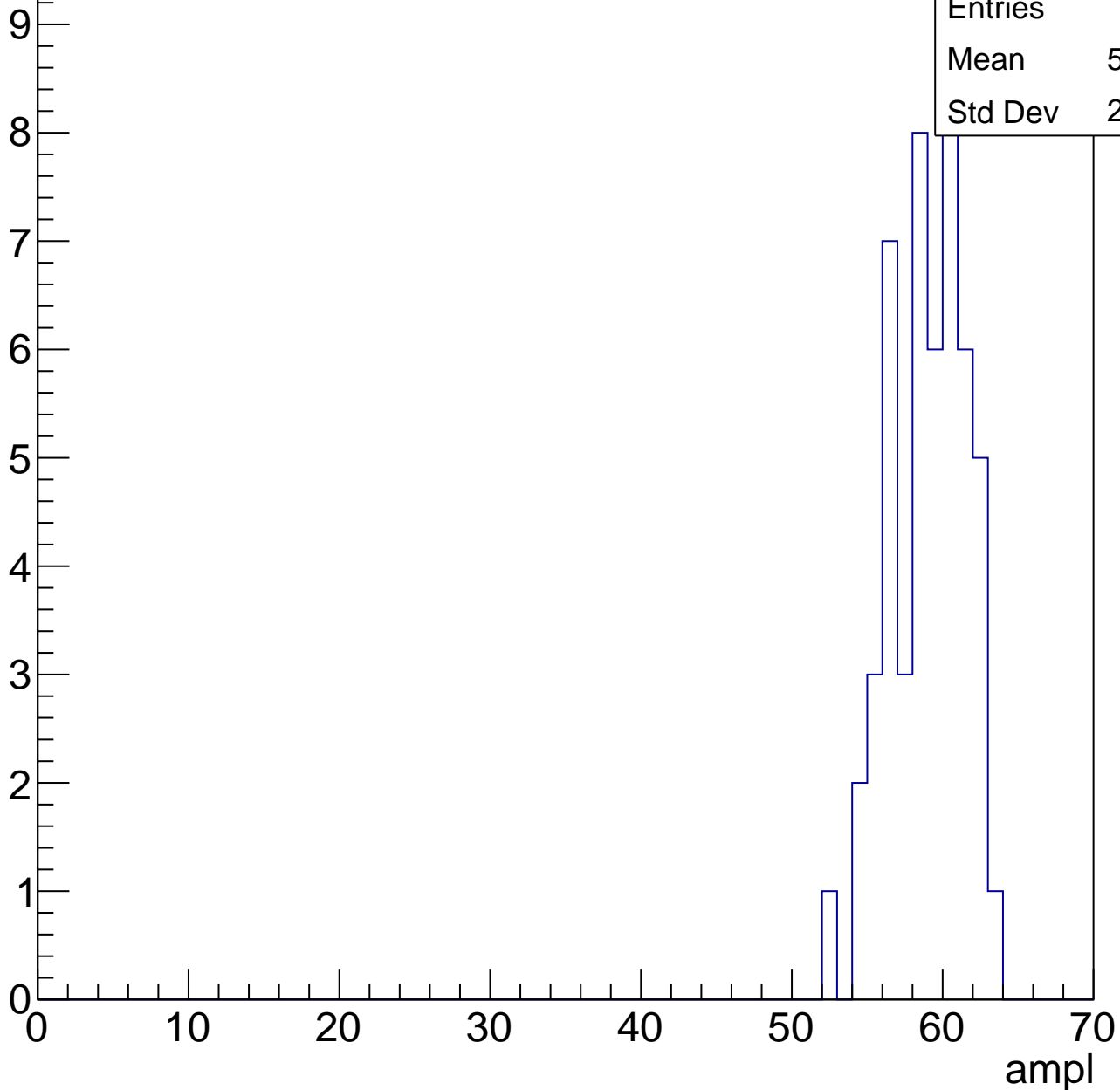


# B1L002S, U18-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.53
Std Dev	2.476

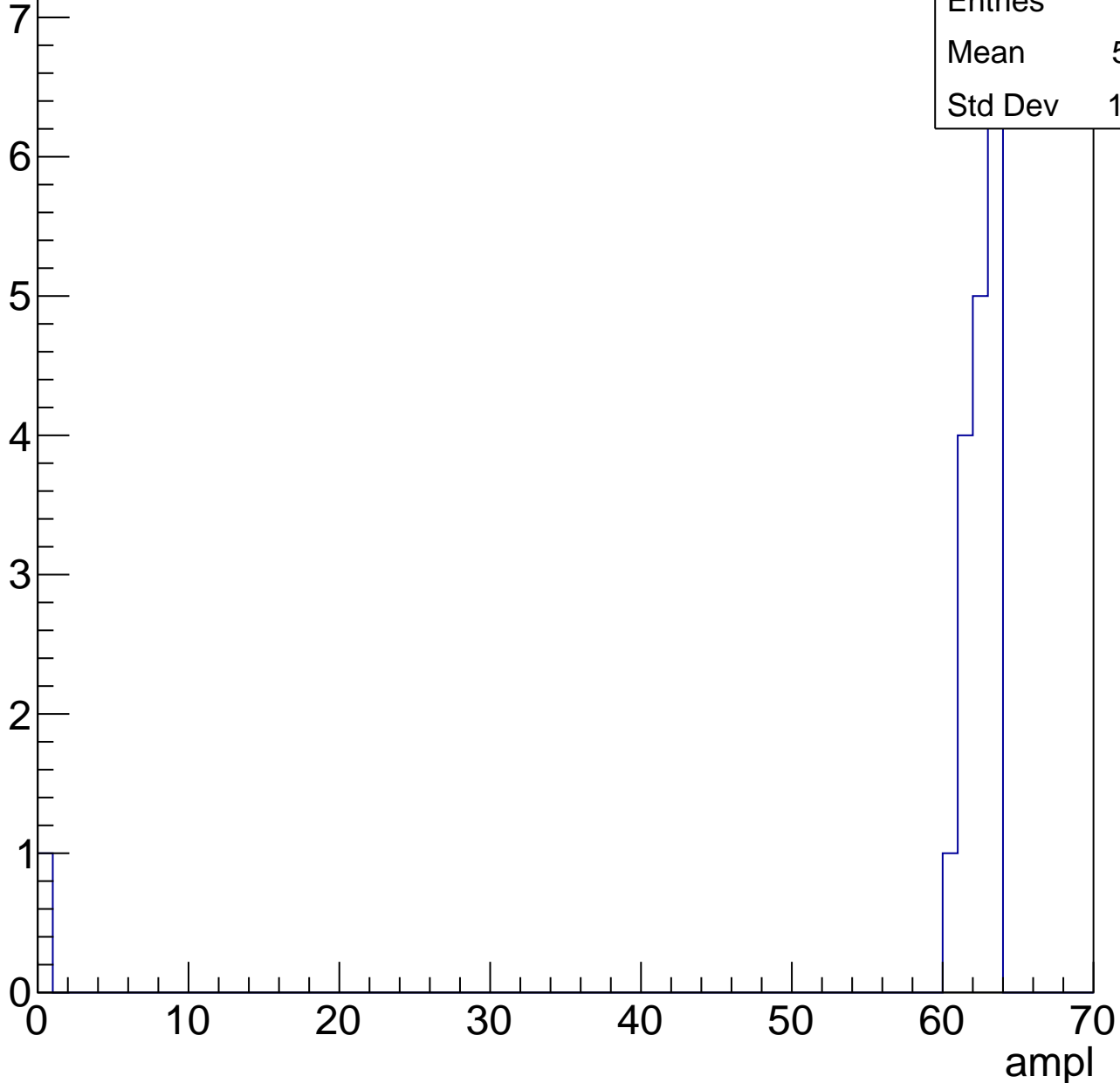


# B1L002S, U18-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.61
Std Dev	14.24

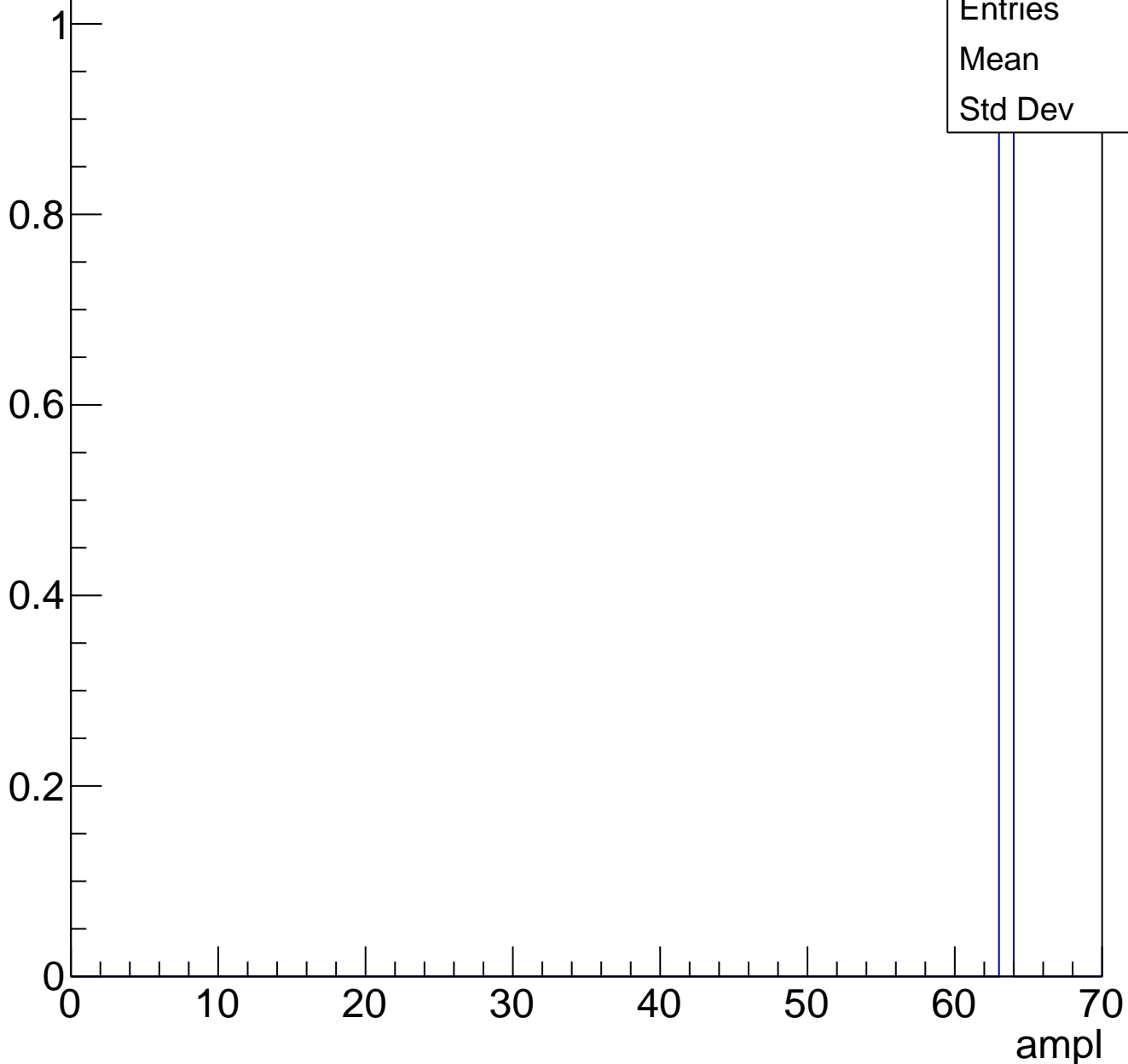




# B1L002S, U18-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch43, adc0

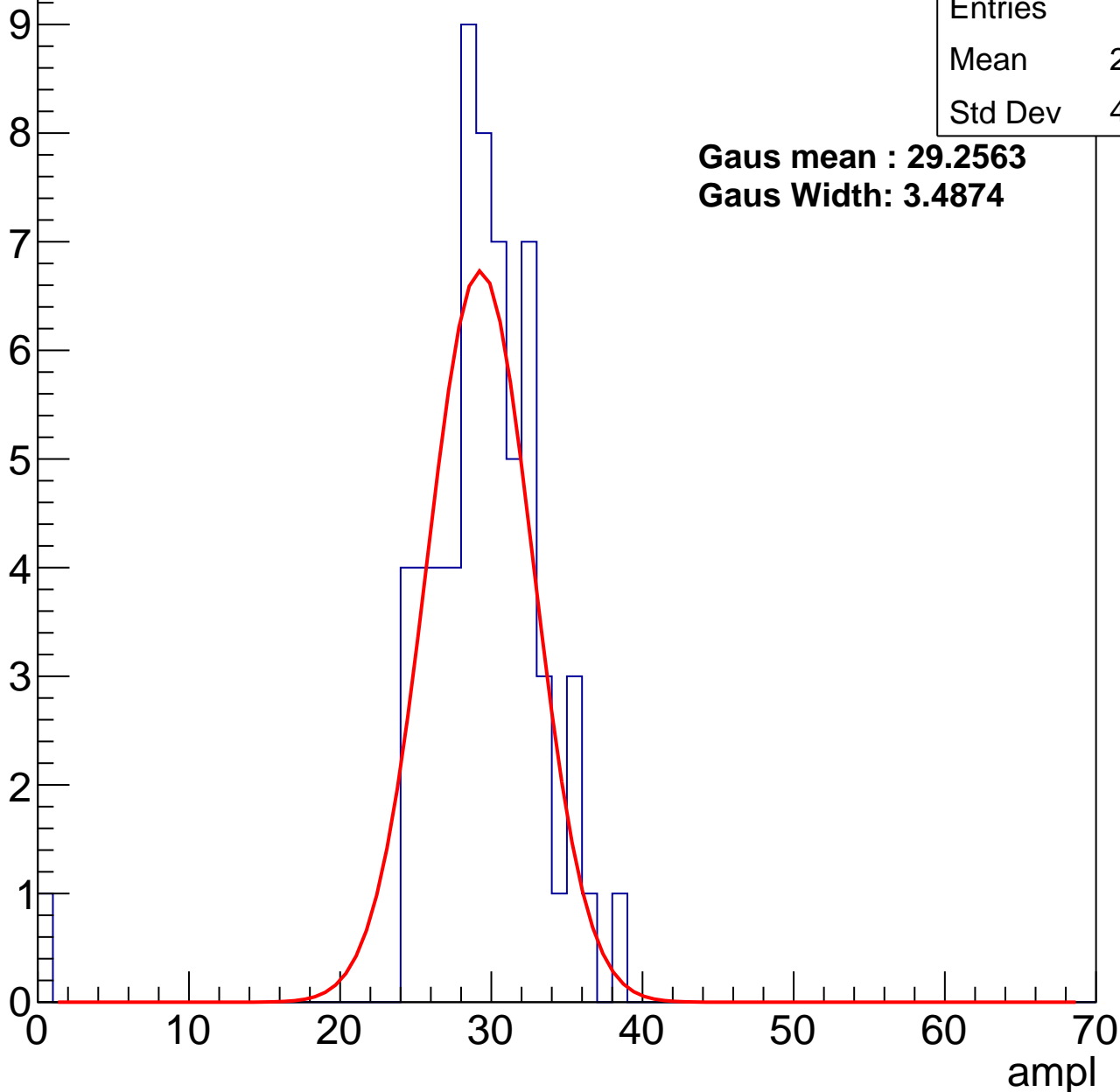
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.92
Std Dev	4.864

**Gaus mean : 29.2563**

**Gaus Width: 3.4874**



# B1L002S, U18-ch43, adc1

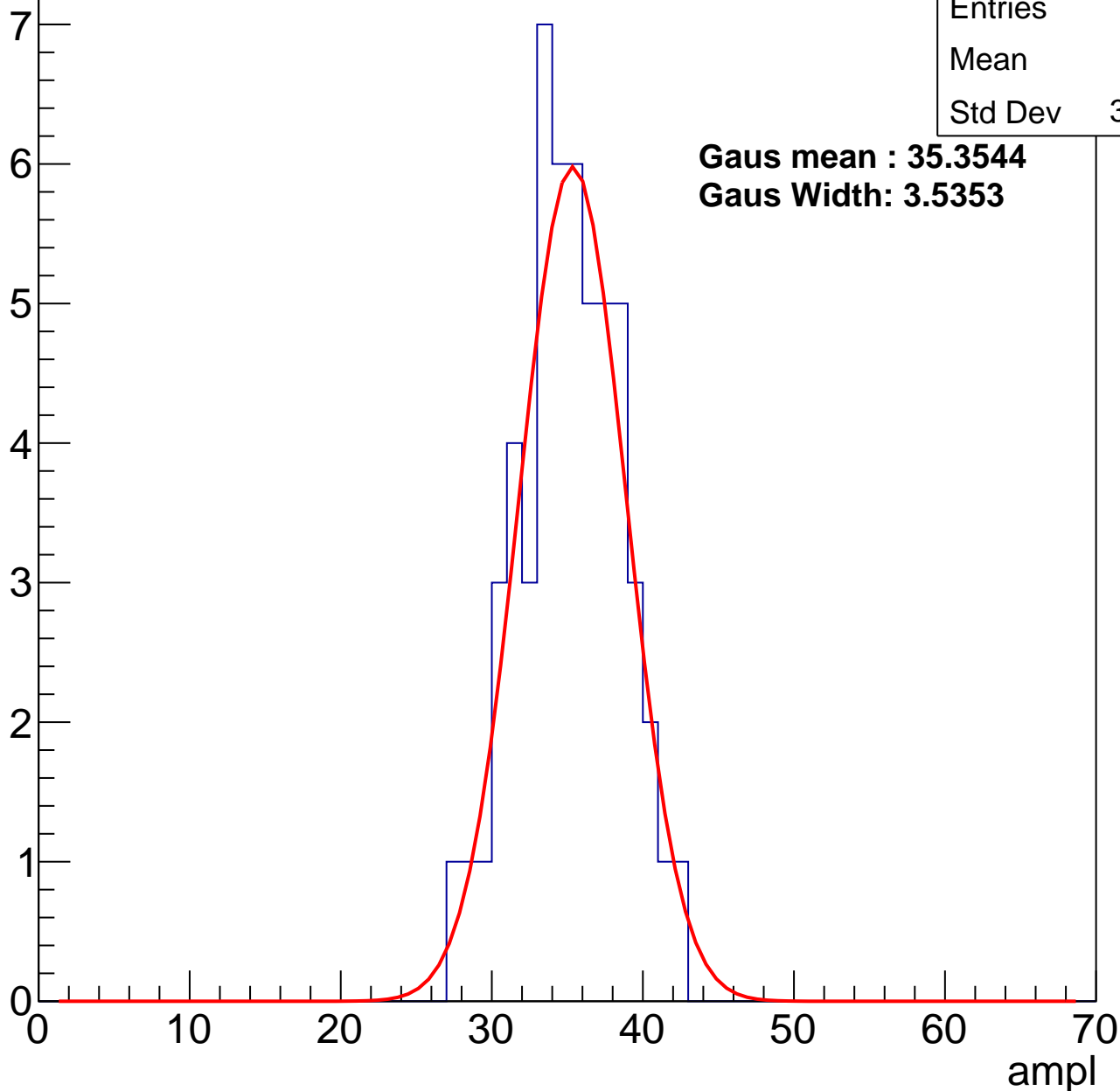
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.7
Std Dev	3.326

**Gaus mean : 35.3544**

**Gaus Width: 3.5353**



# B1L002S, U18-ch43, adc2

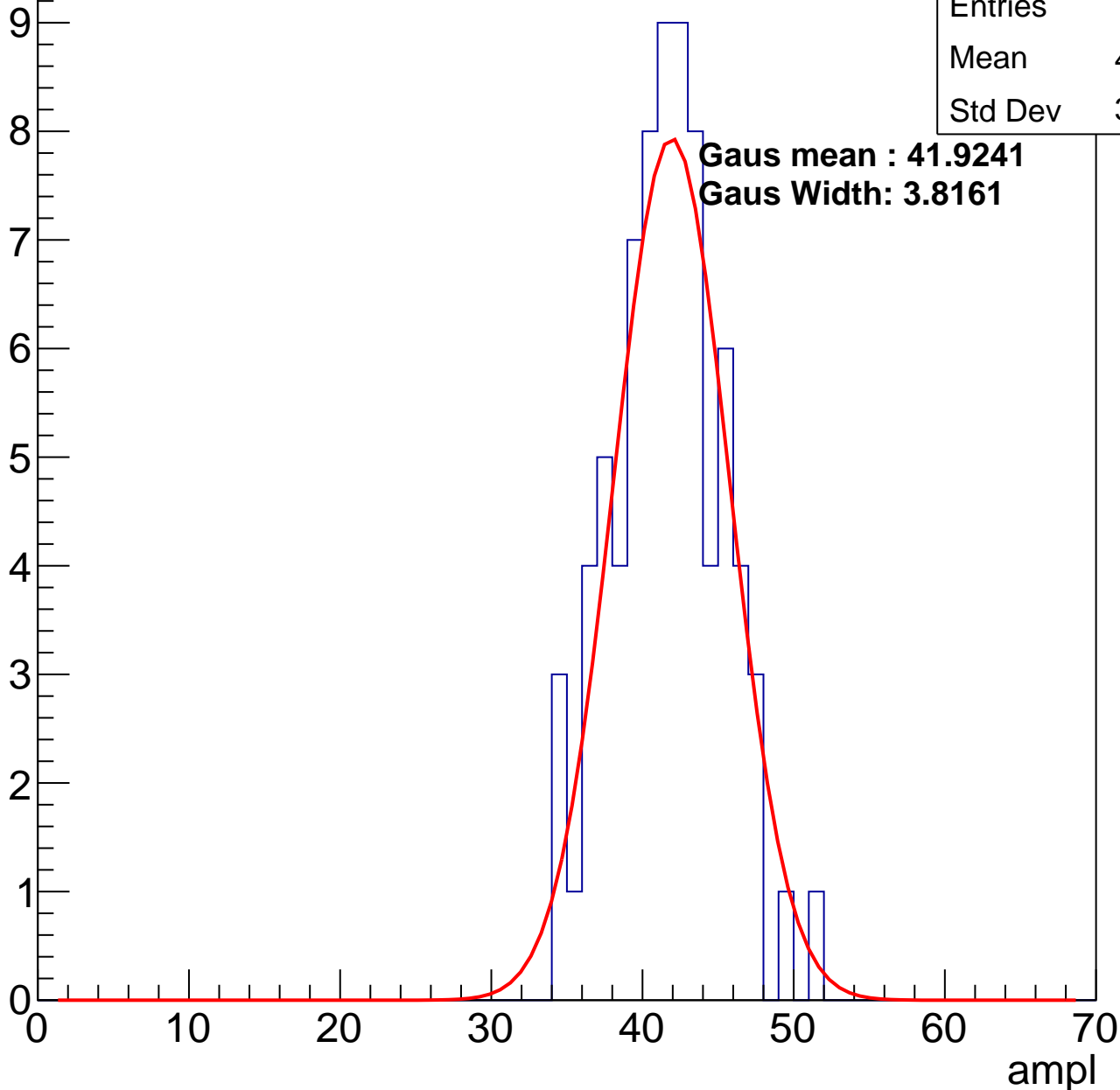
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	41.21
Std Dev	3.561

**Gaus mean : 41.9241**

**Gaus Width: 3.8161**

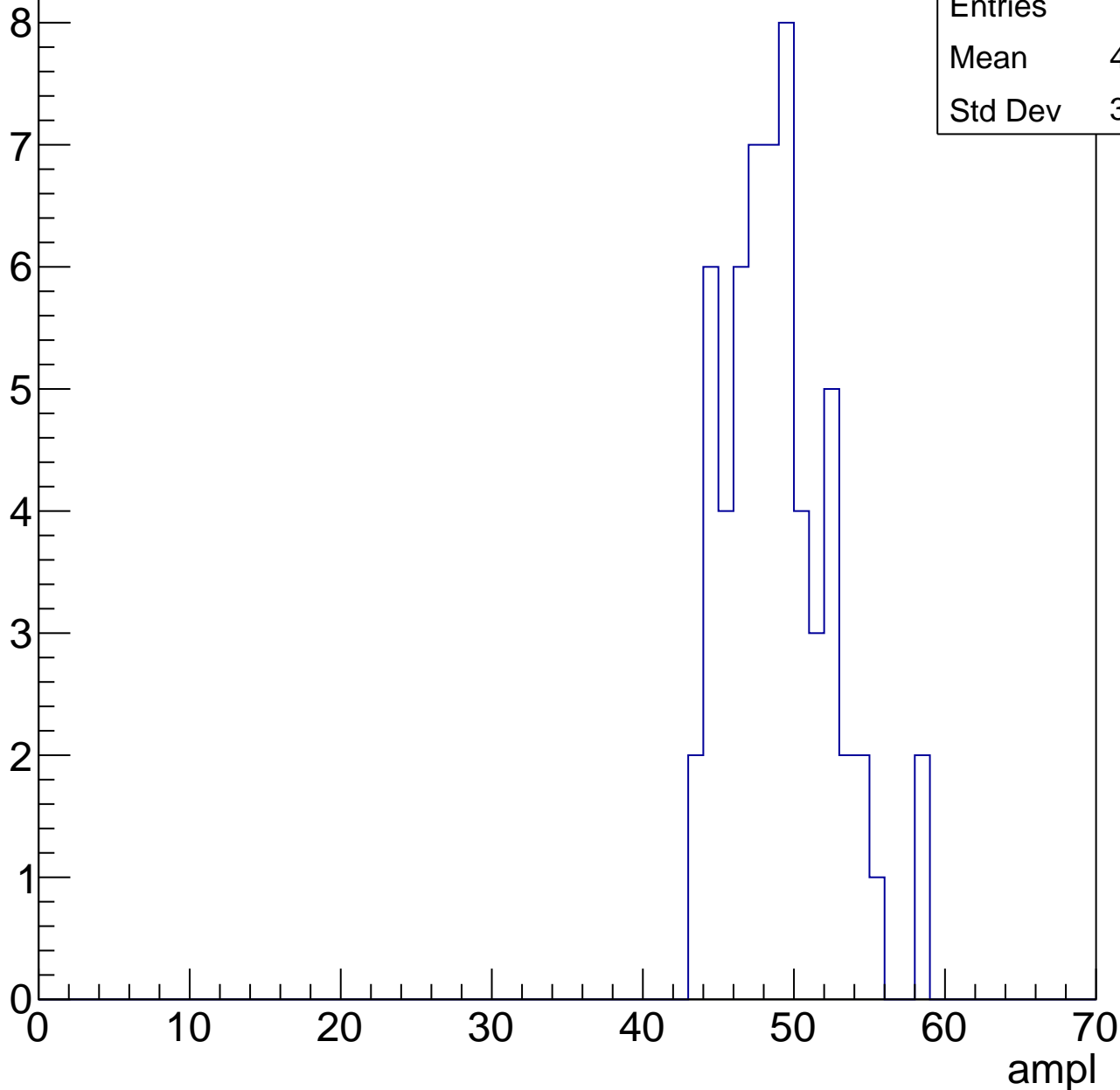


# B1L002S, U18-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	48.49
Std Dev	3.446

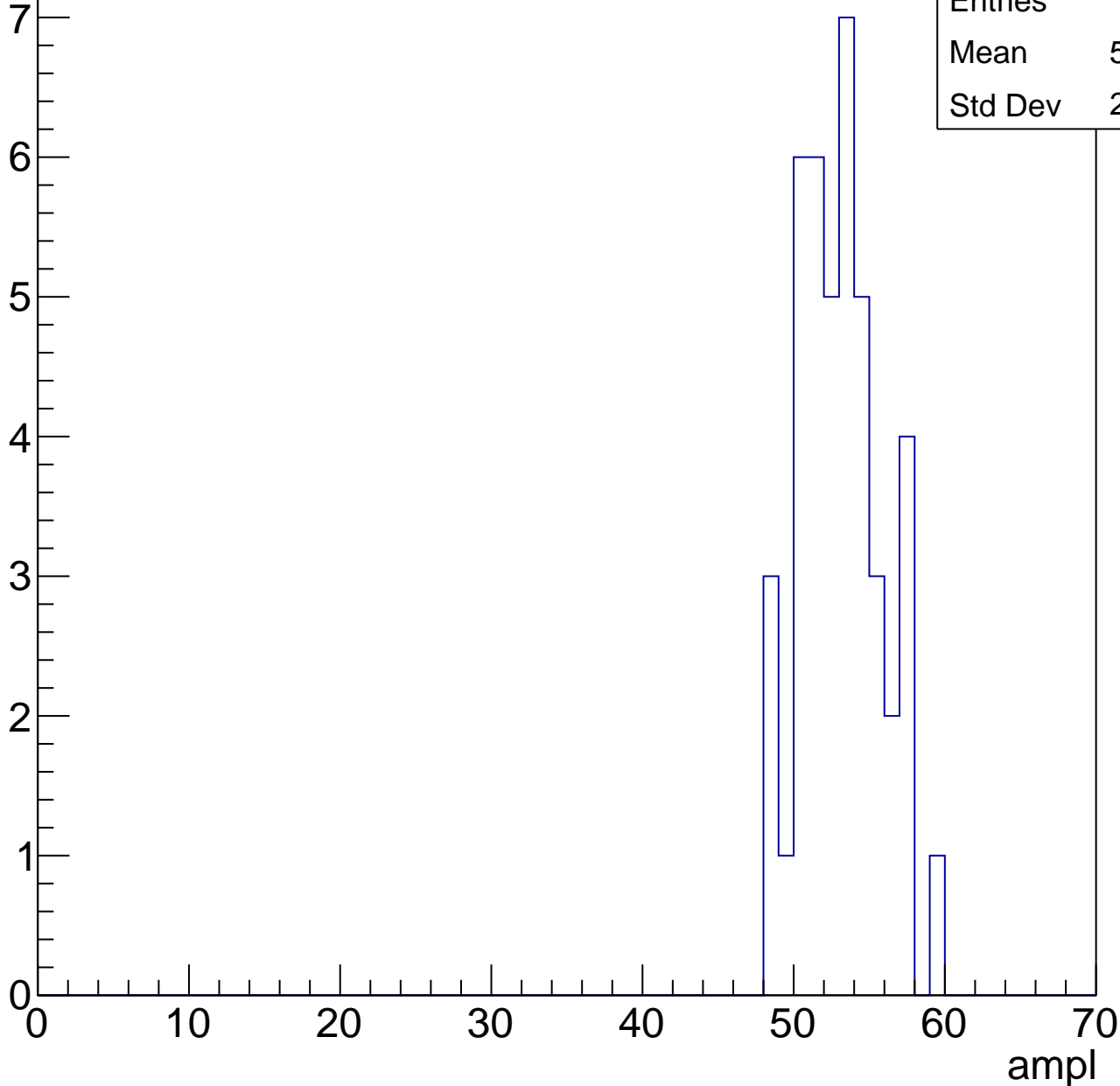


# B1L002S, U18-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	52.65
Std Dev	2.658

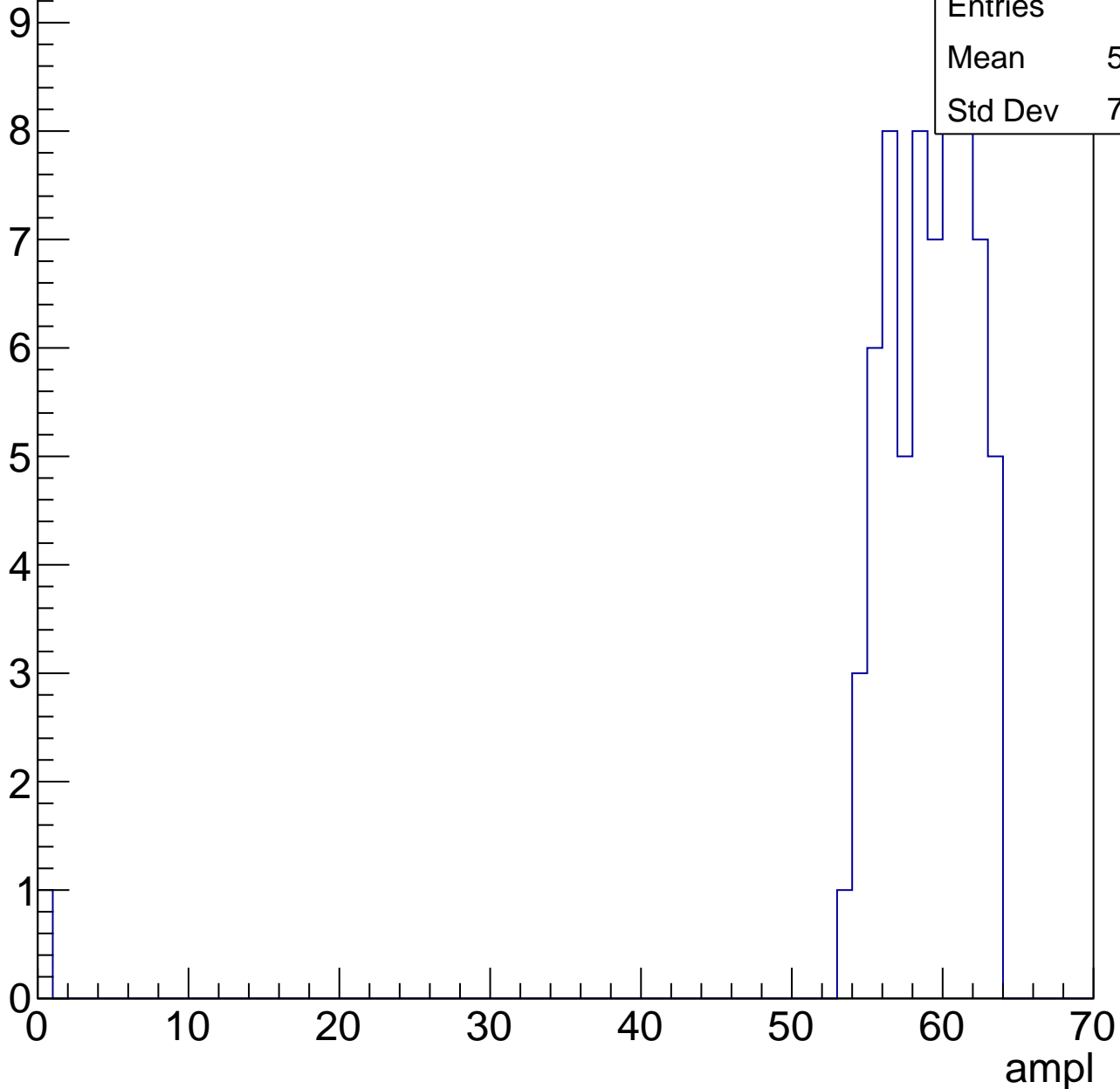


# B1L002S, U18-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

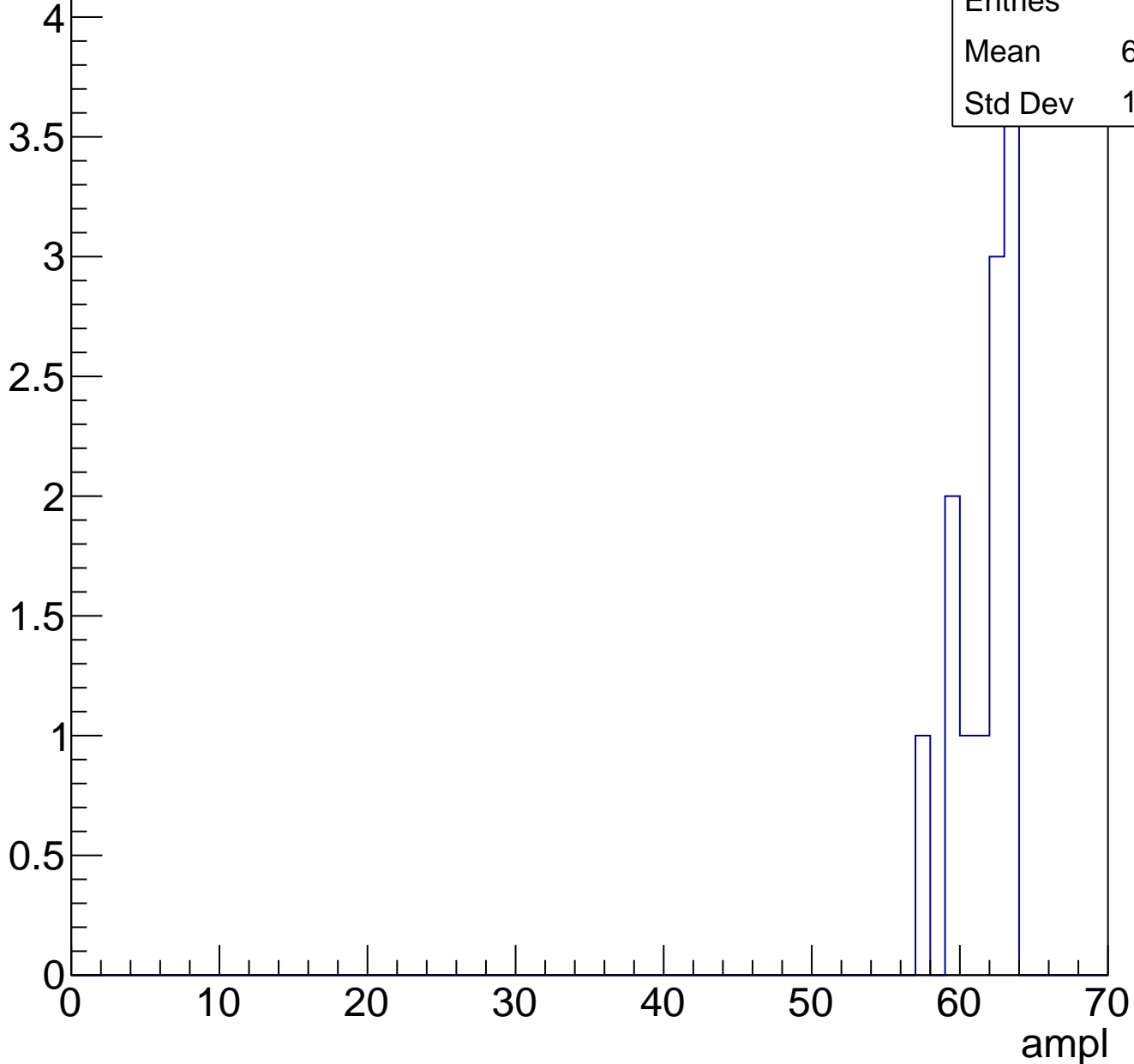
Entries	69
Mean	57.87
Std Dev	7.506



# B1L002S, U18-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U18-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch44, adc0

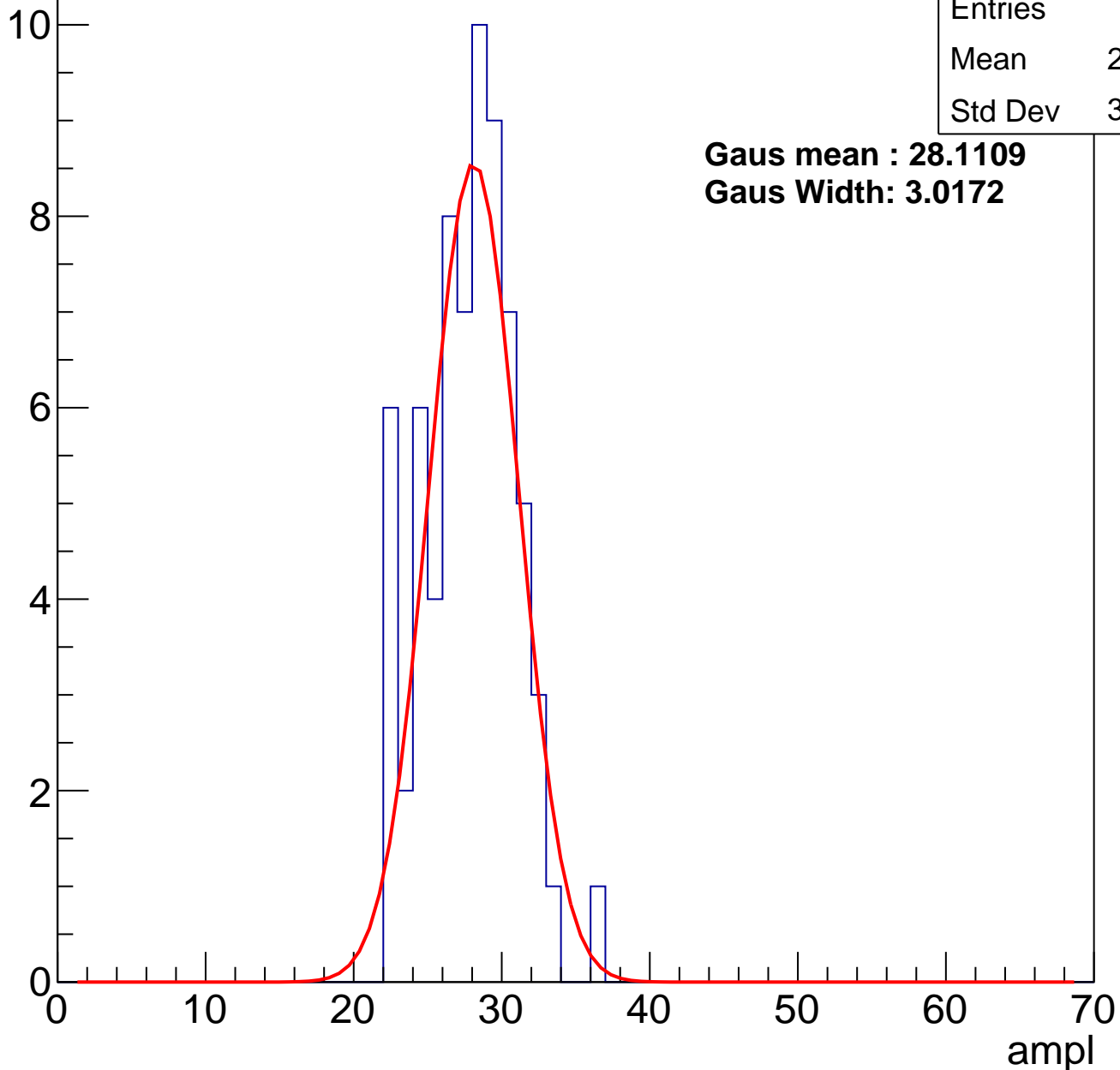
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27.39
Std Dev	3.023

**Gaus mean : 28.1109**

**Gaus Width: 3.0172**

Entry



# B1L002S, U18-ch44, adc1

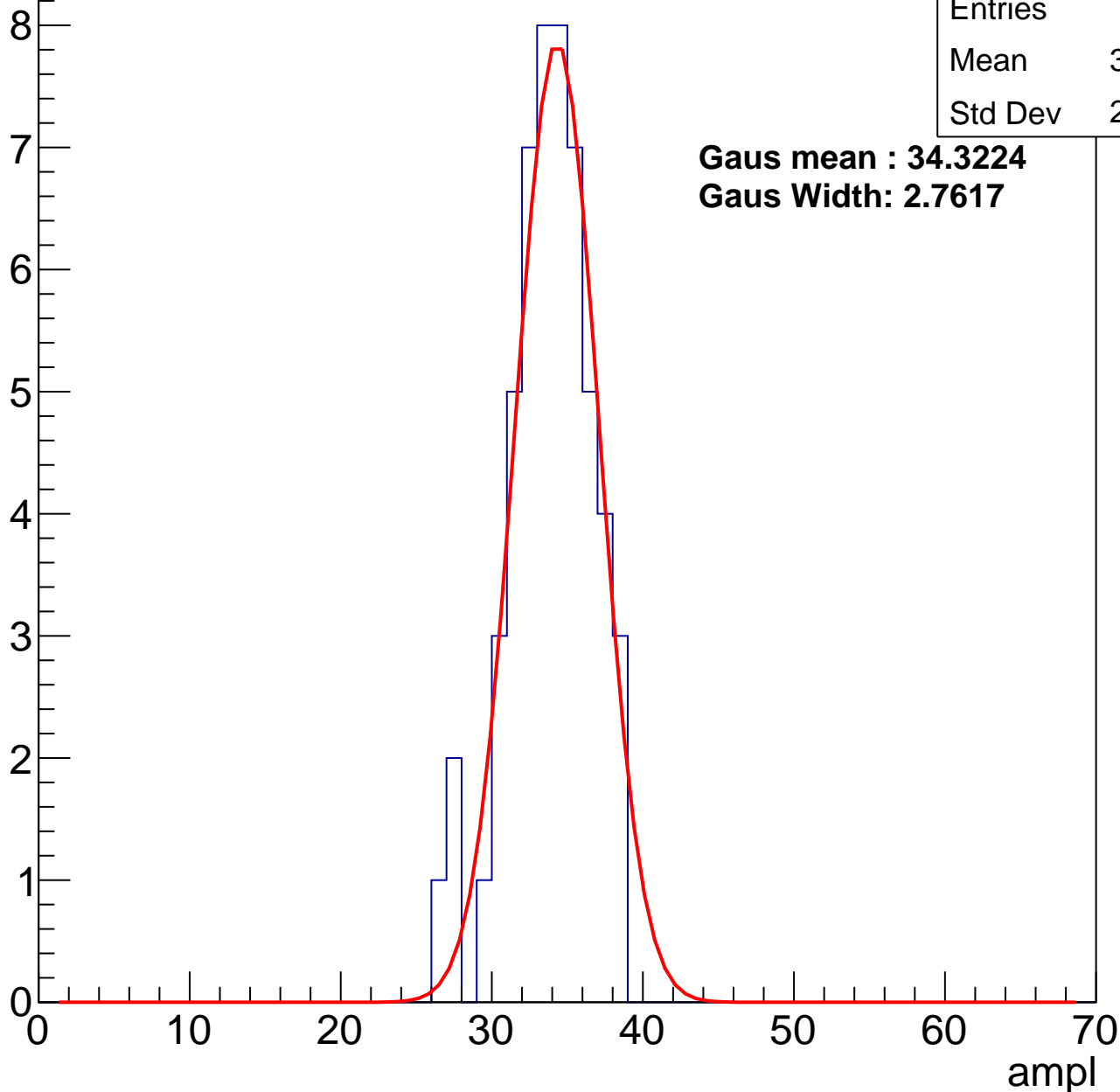
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	33.35
Std Dev	2.736

**Gaus mean : 34.3224**

**Gaus Width: 2.7617**



# B1L002S, U18-ch44, adc2

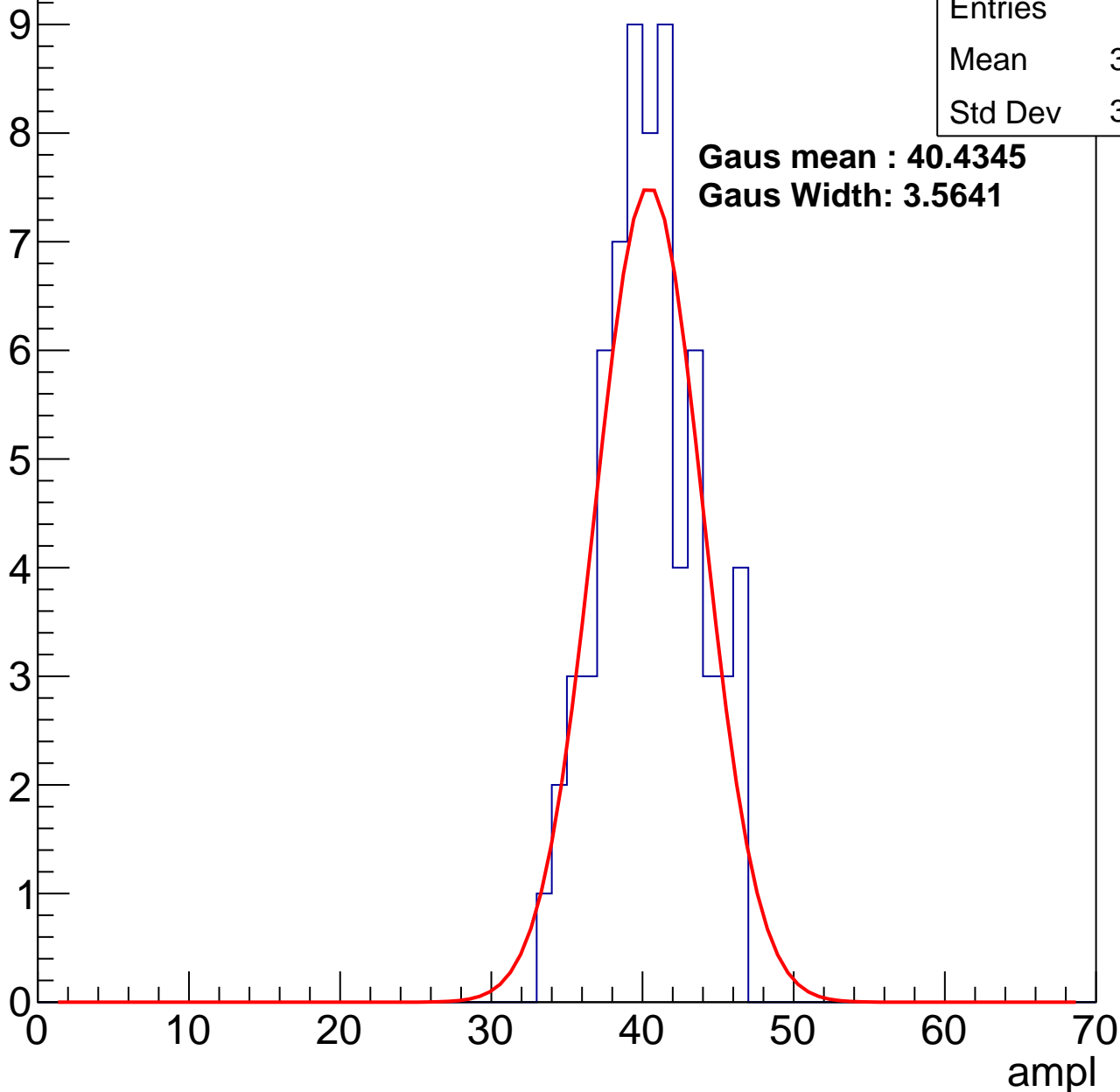
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	39.99
Std Dev	3.165

**Gaus mean : 40.4345**

**Gaus Width: 3.5641**

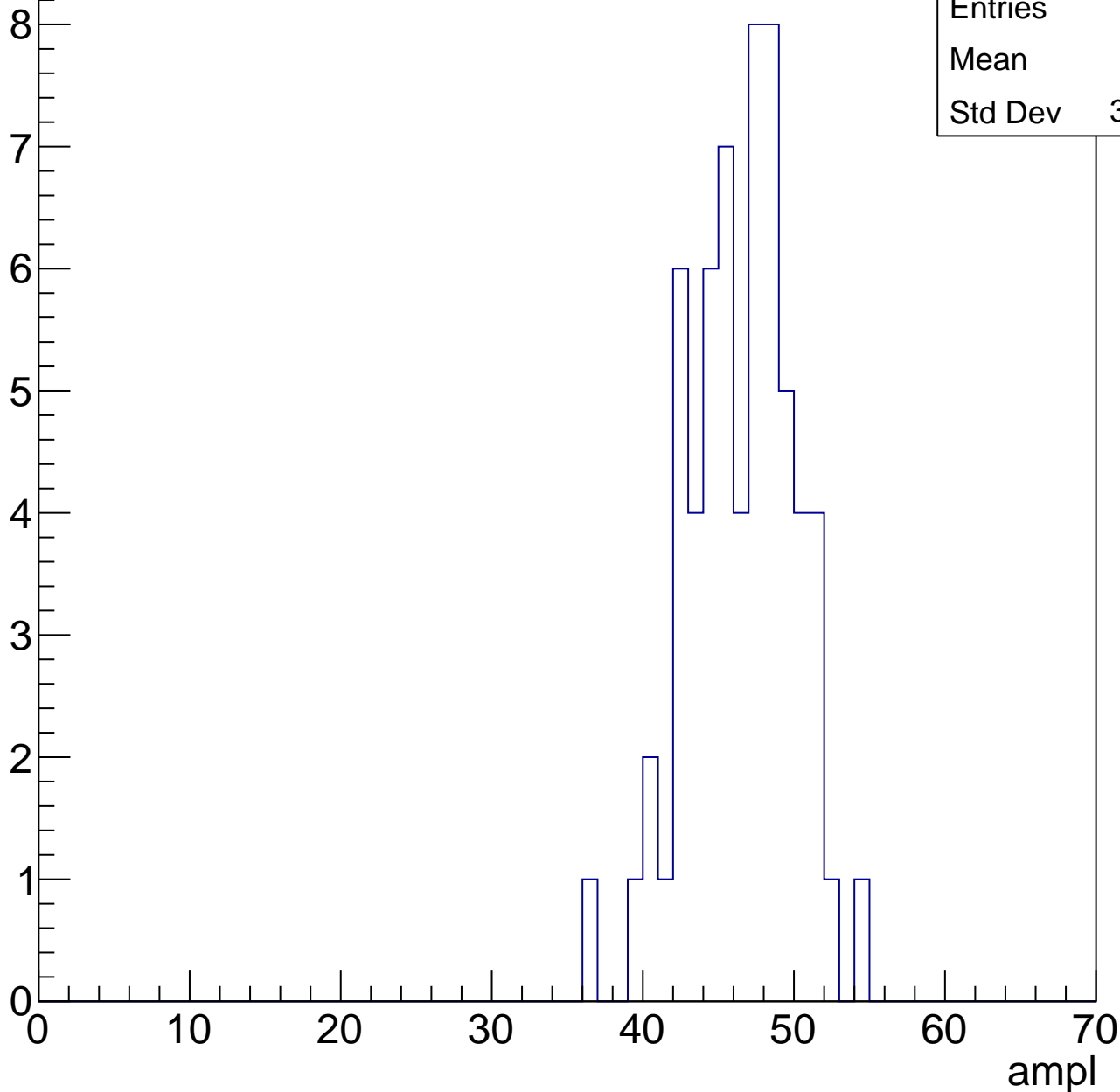


# B1L002S, U18-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46
Std Dev	3.469

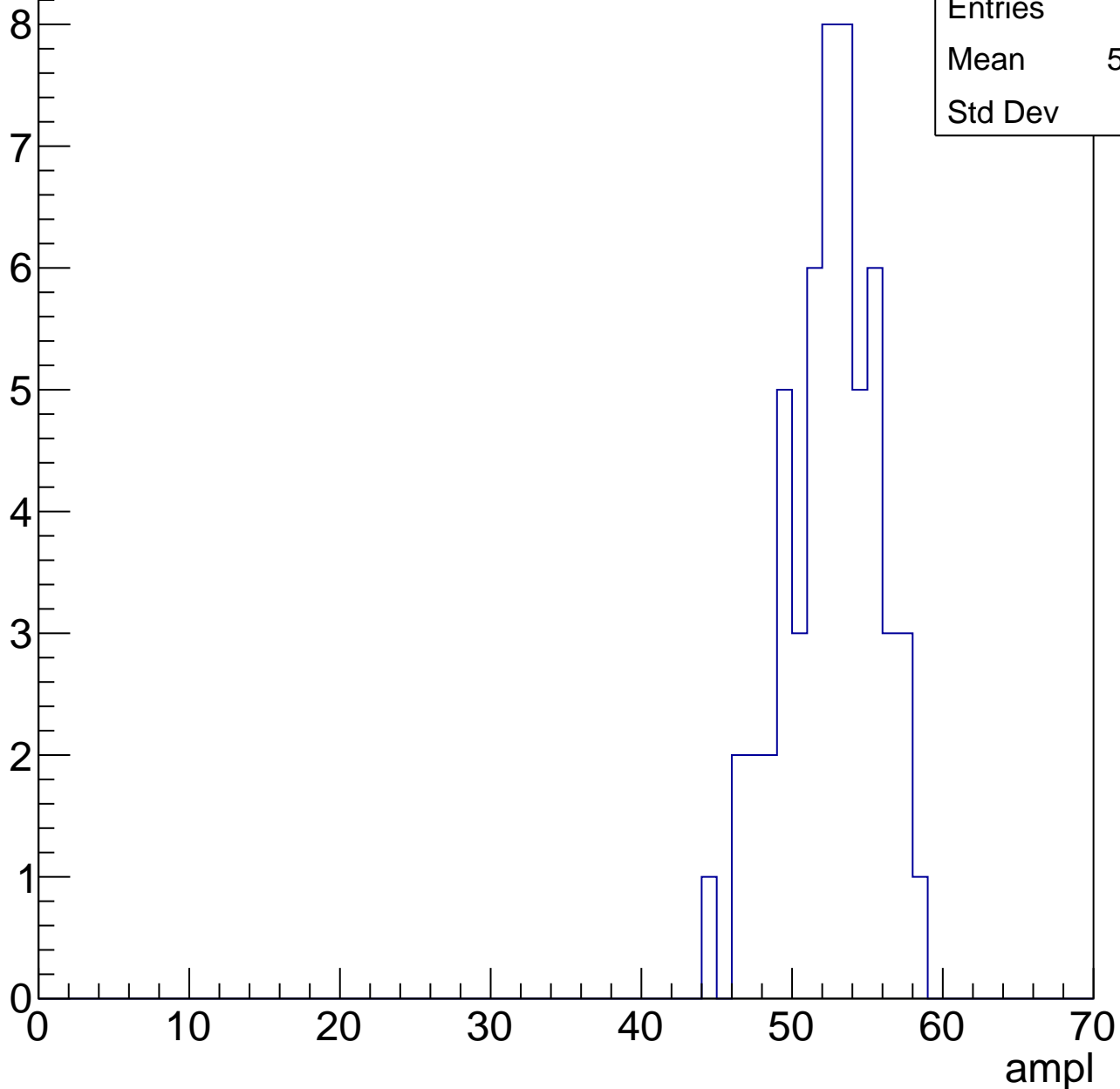


# B1L002S, U18-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.07
Std Dev	3.08

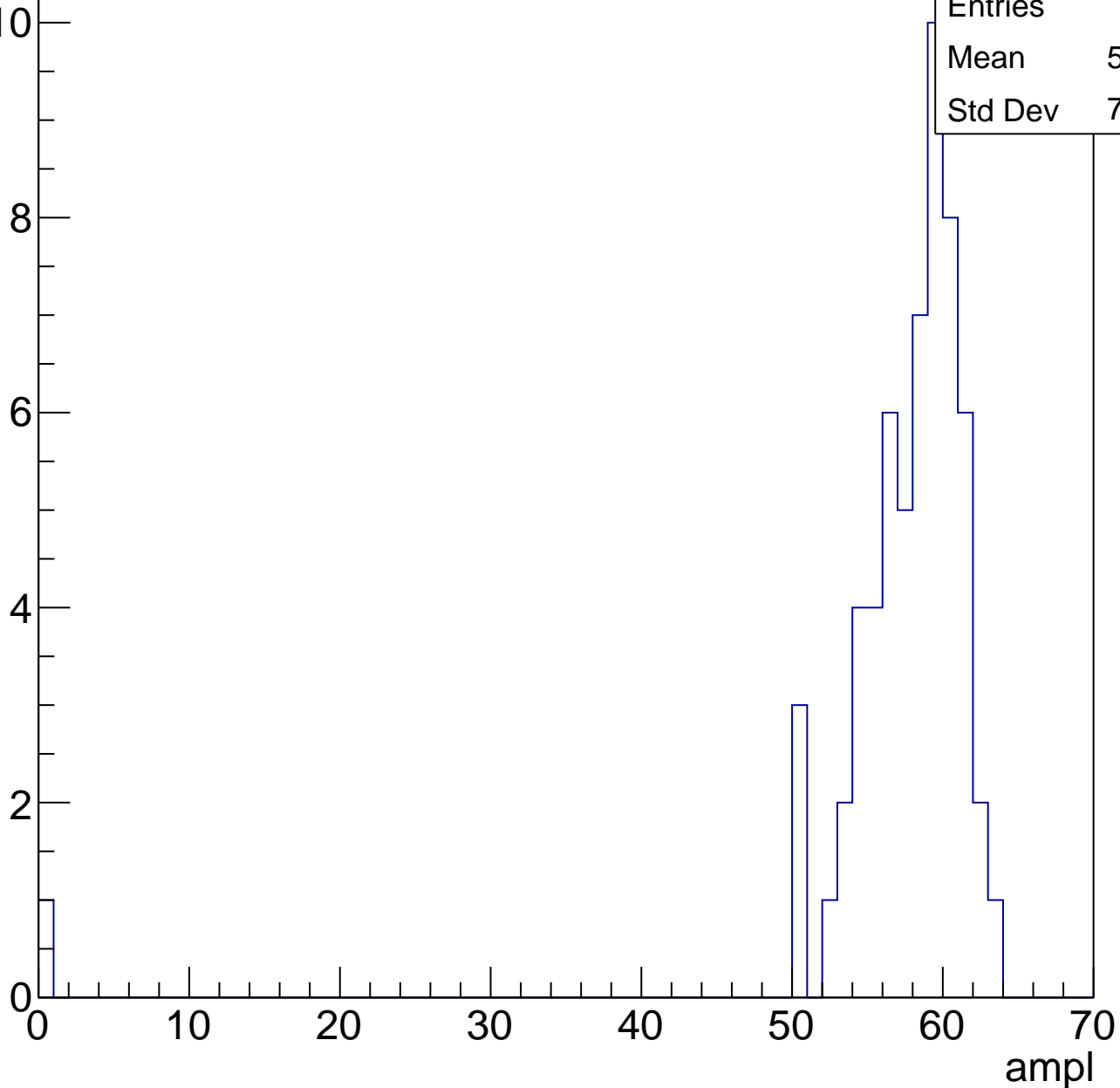


# B1L002S, U18-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.57
Std Dev	7.959

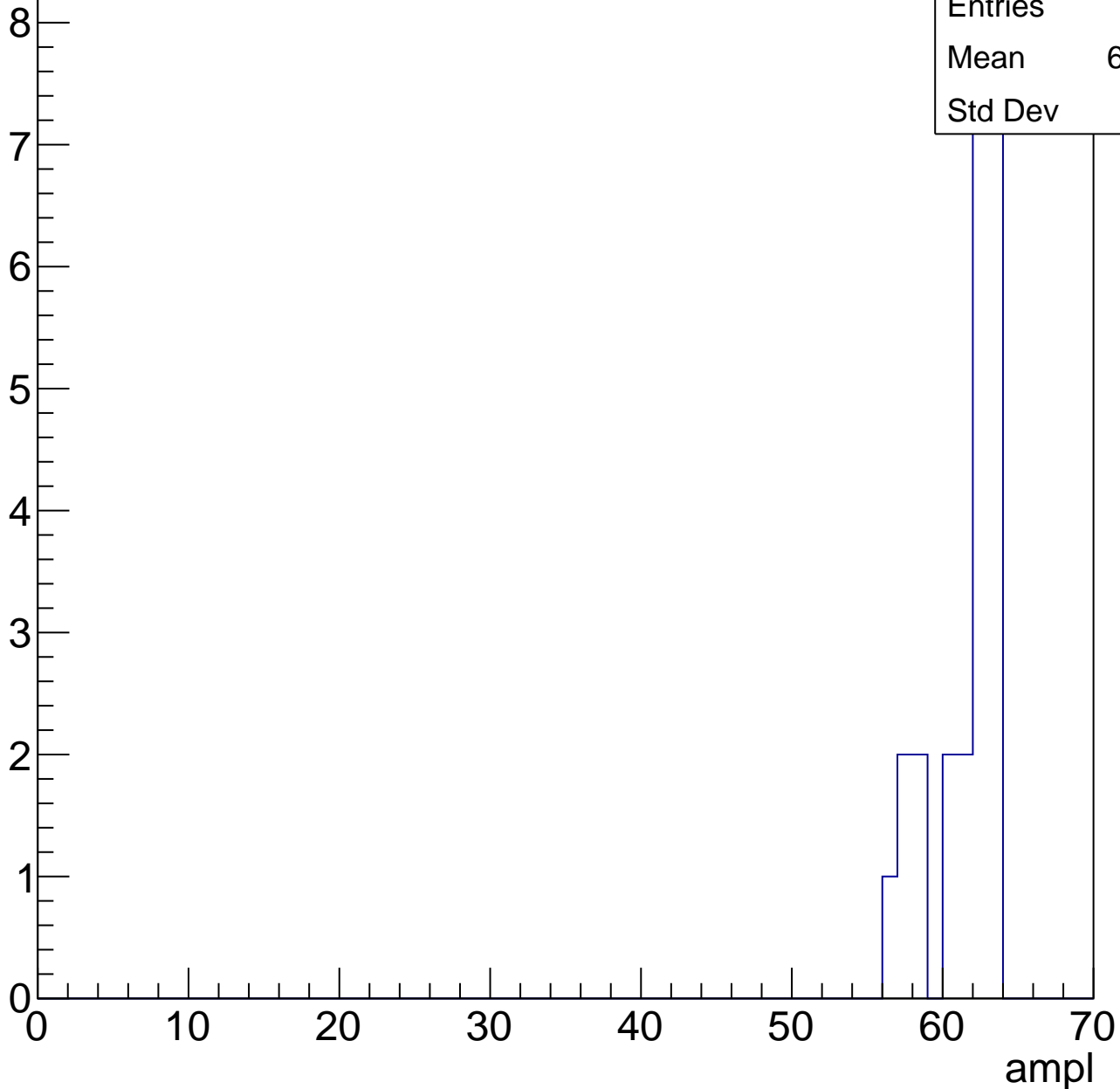


# B1L002S, U18-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.12
Std Dev	2.16

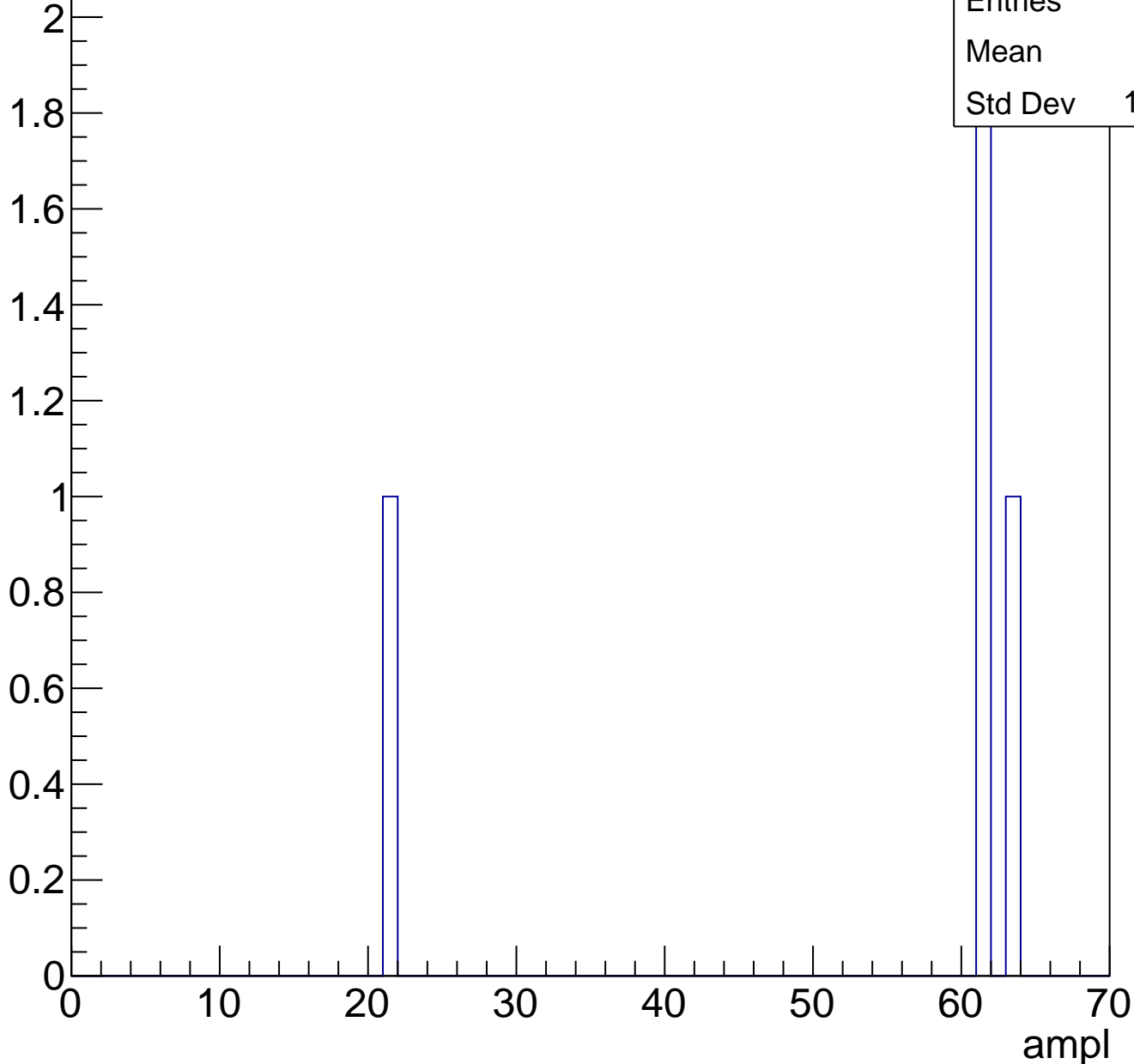




# B1L002S, U18-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch45, adc0

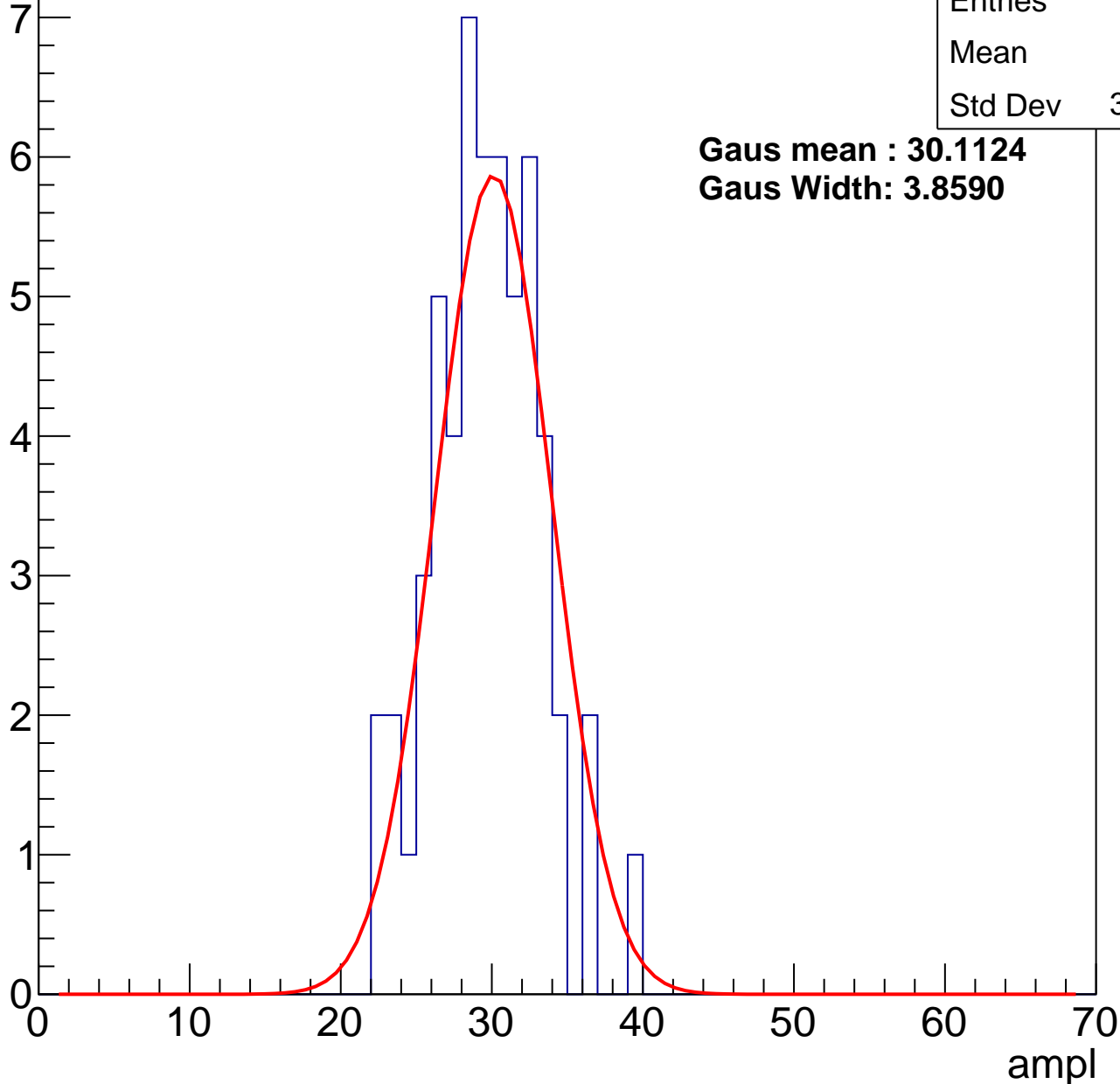
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	29.2
Std Dev	3.538

**Gaus mean : 30.1124**

**Gaus Width: 3.8590**



# B1L002S, U18-ch45, adc1

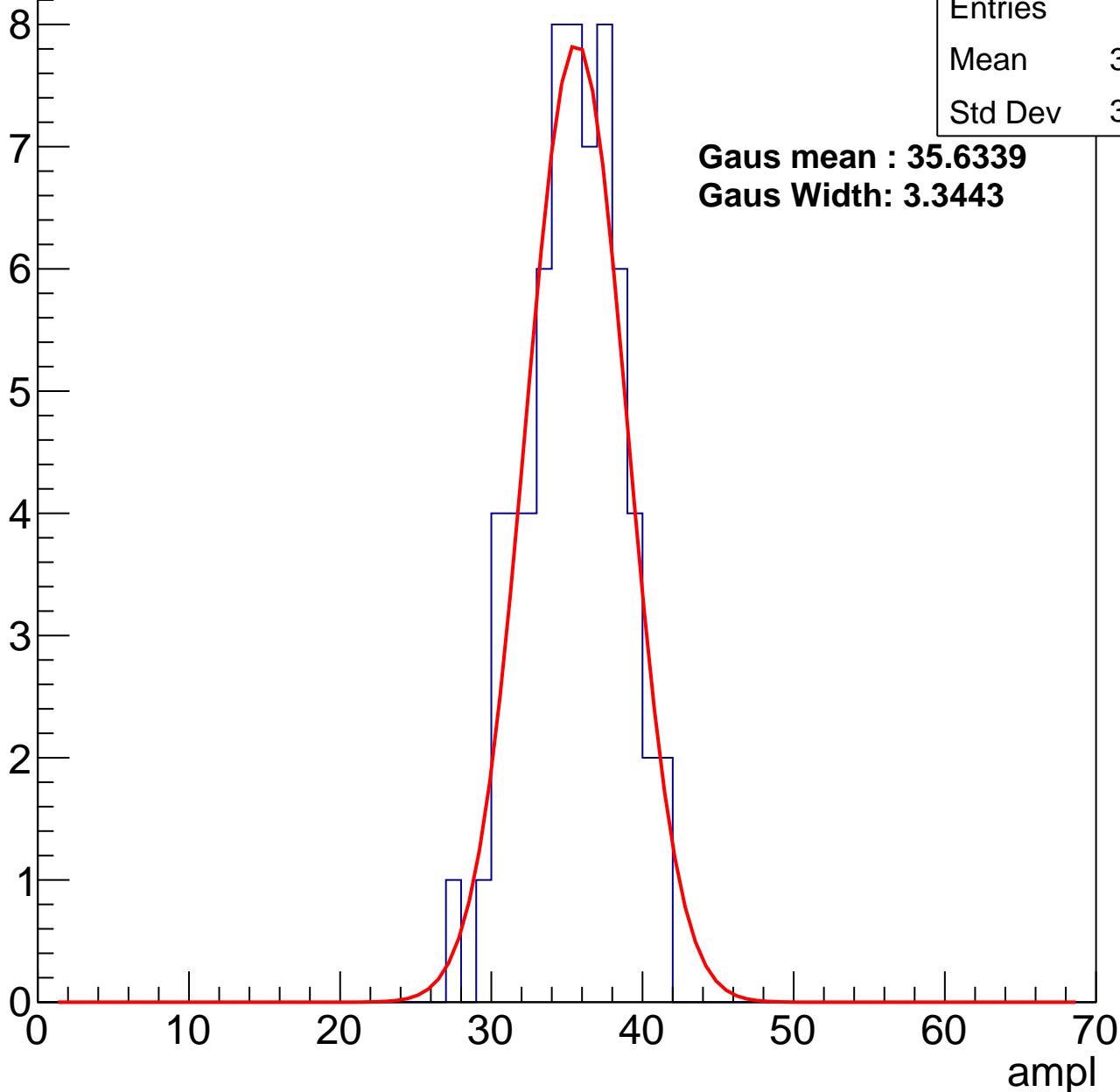
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.95
Std Dev	3.066

**Gaus mean : 35.6339**

**Gaus Width: 3.3443**



# B1L002S, U18-ch45, adc2

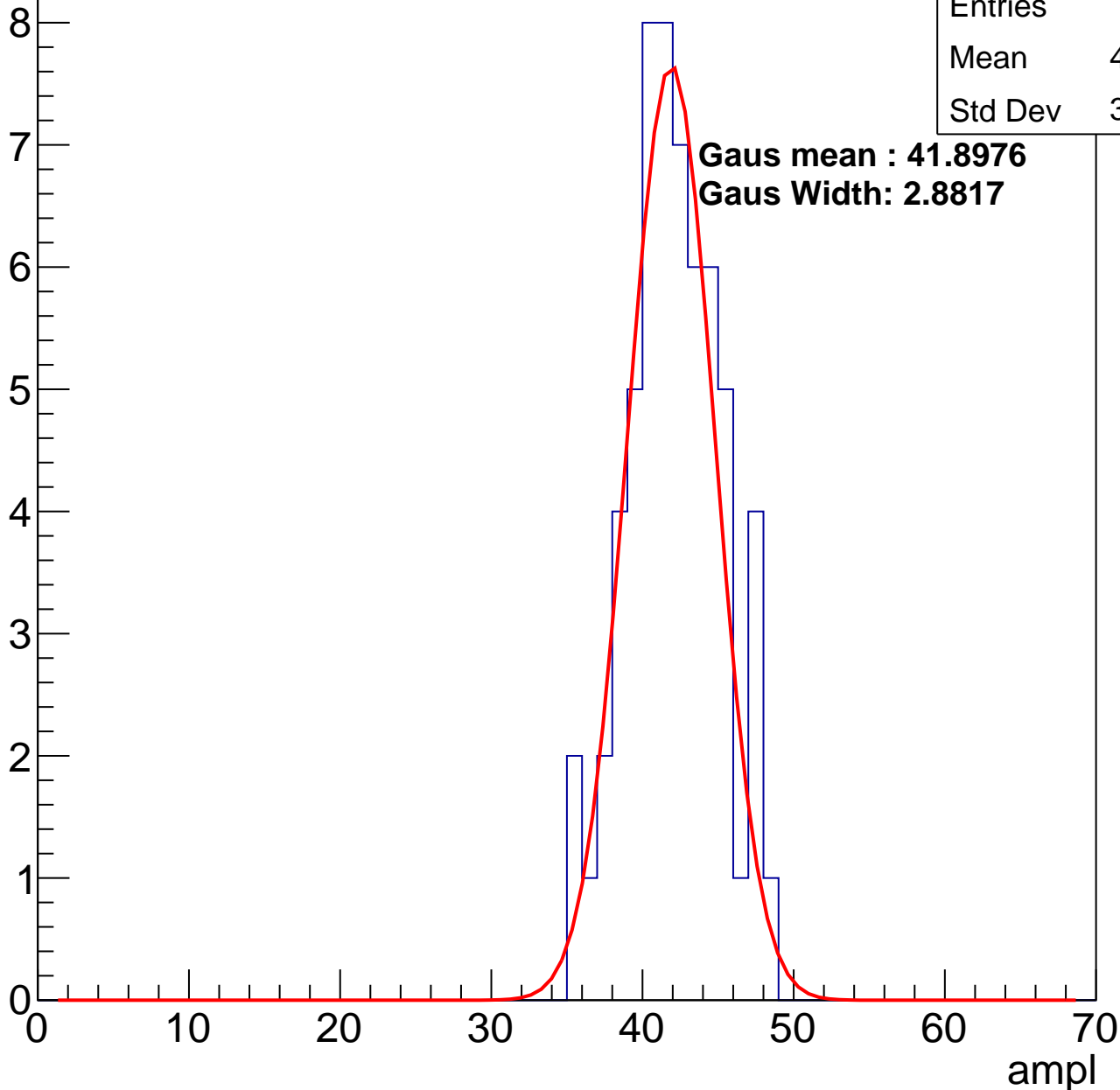
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.63
Std Dev	3.033

**Gaus mean : 41.8976**

**Gaus Width: 2.8817**

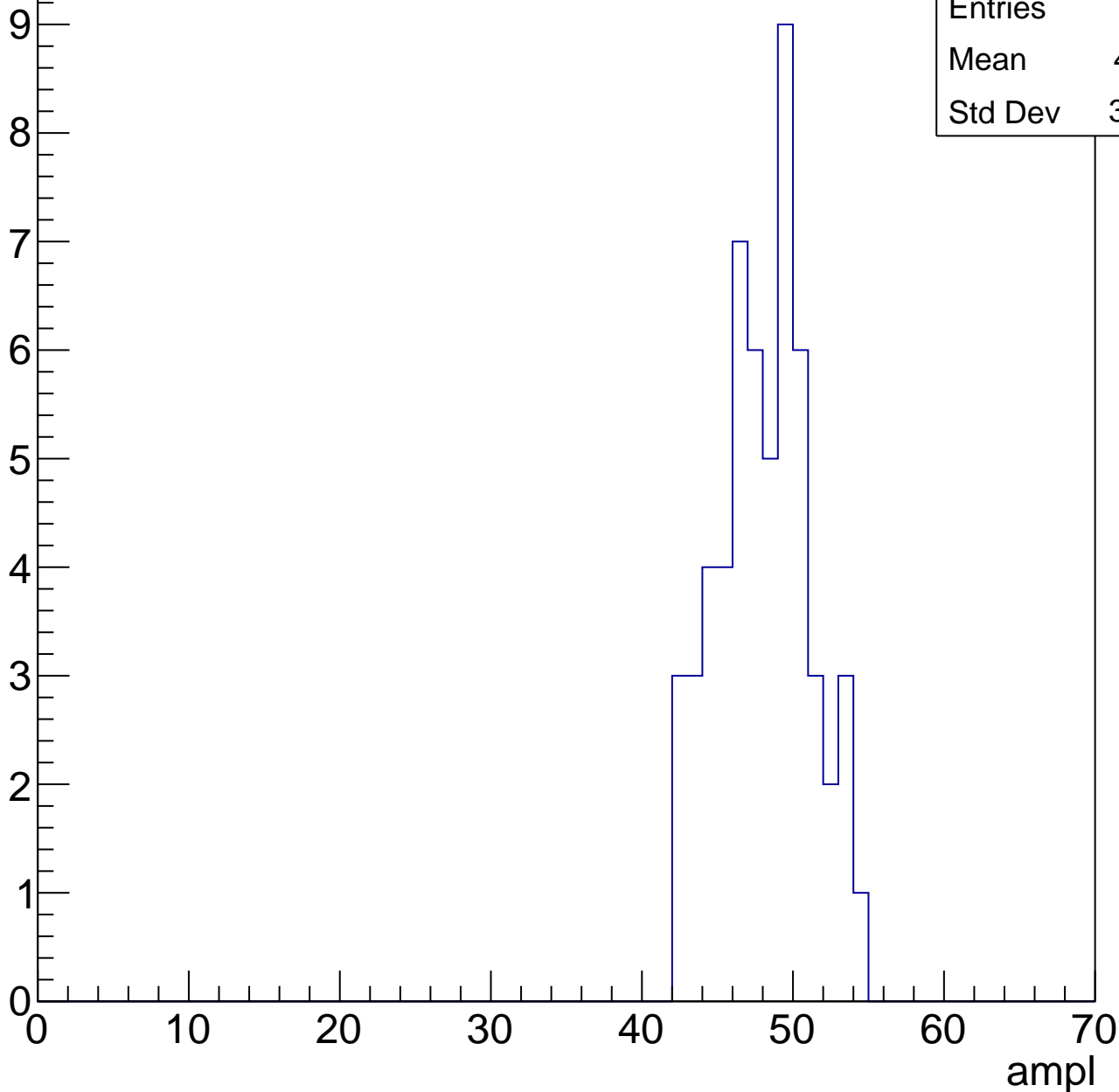


# B1L002S, U18-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	47.61
Std Dev	3.022

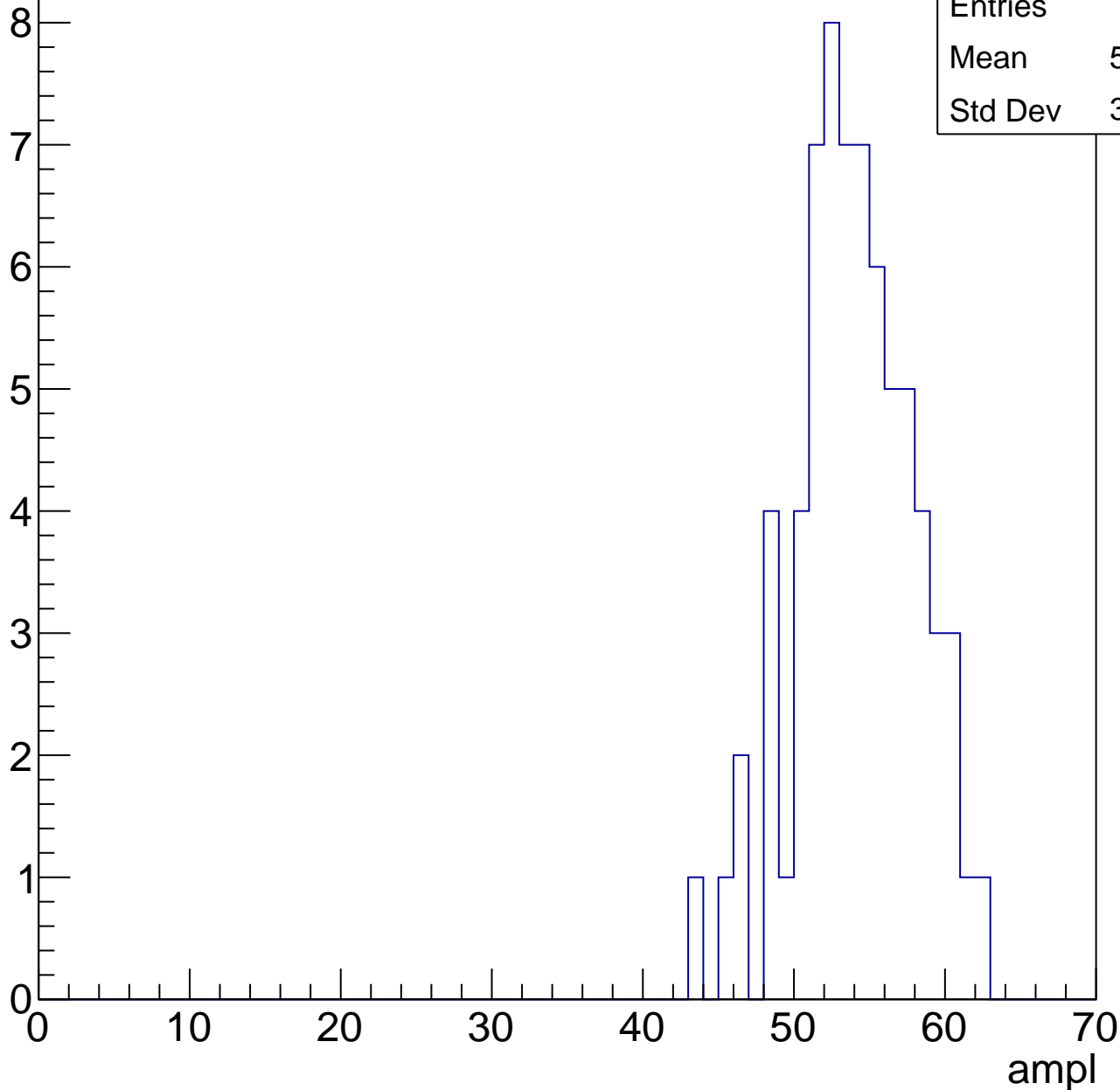


# B1L002S, U18-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	53.57
Std Dev	3.934

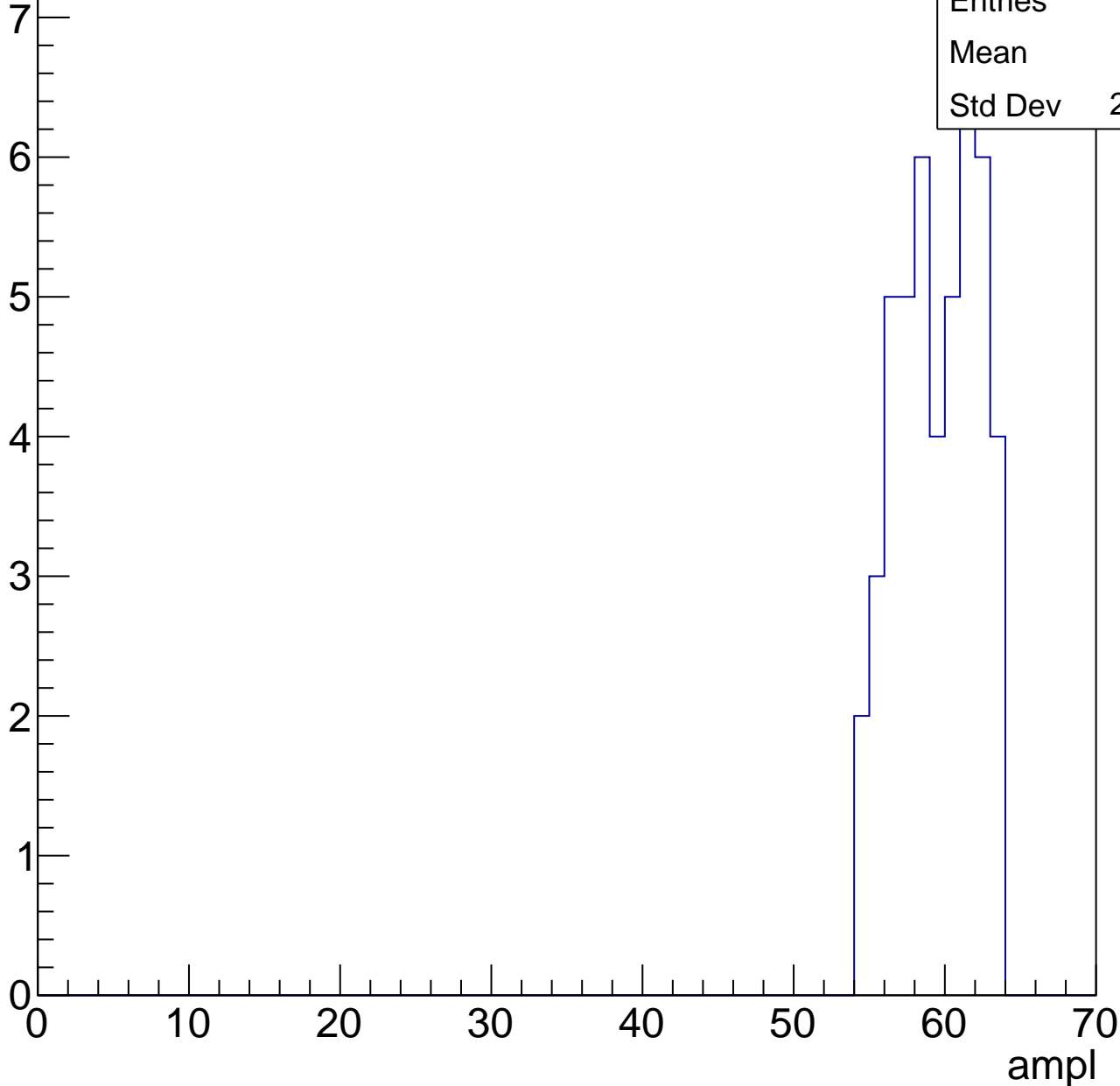


# B1L002S, U18-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	59
Std Dev	2.609

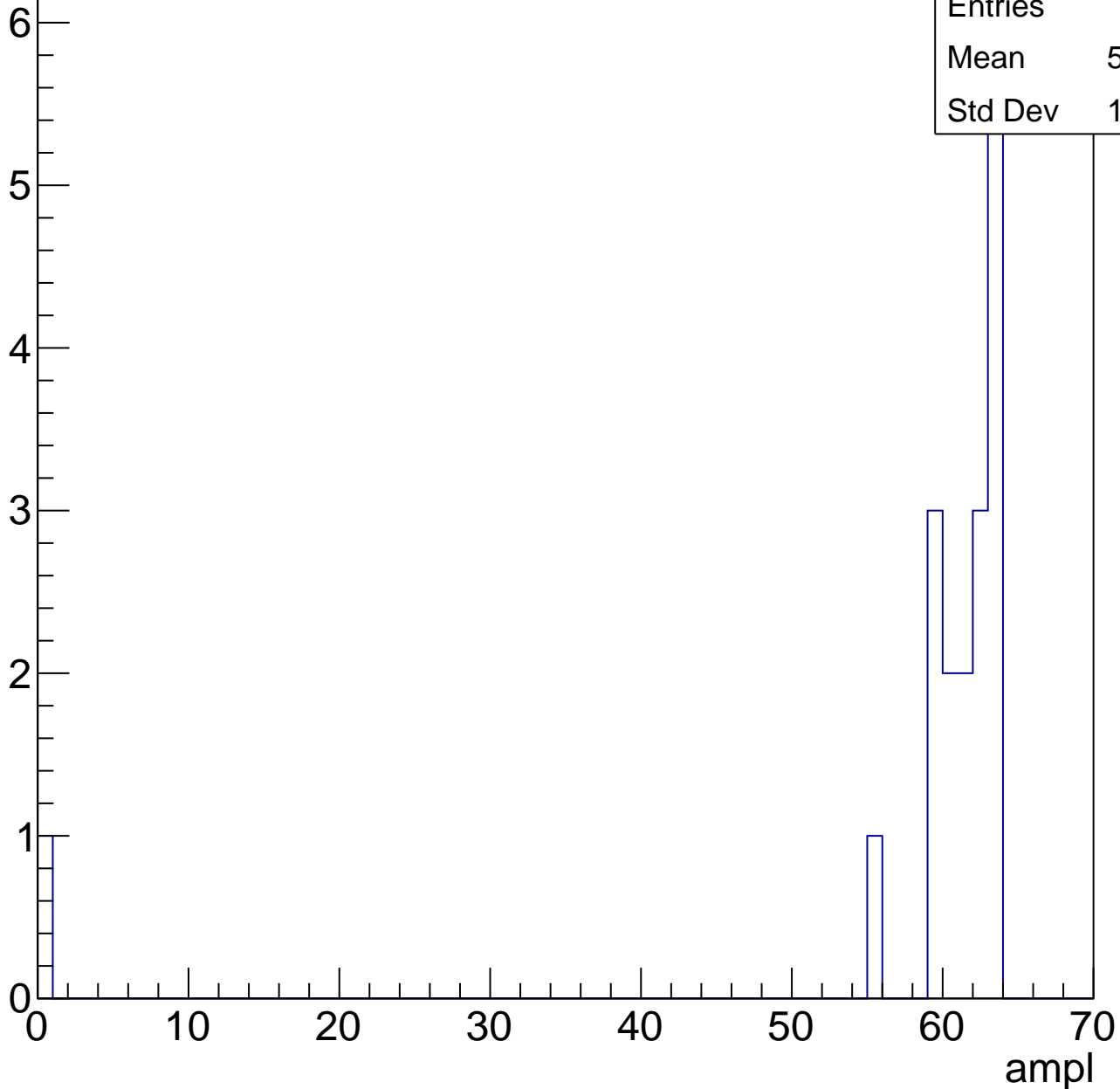


# B1L002S, U18-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	57.67
Std Dev	14.14

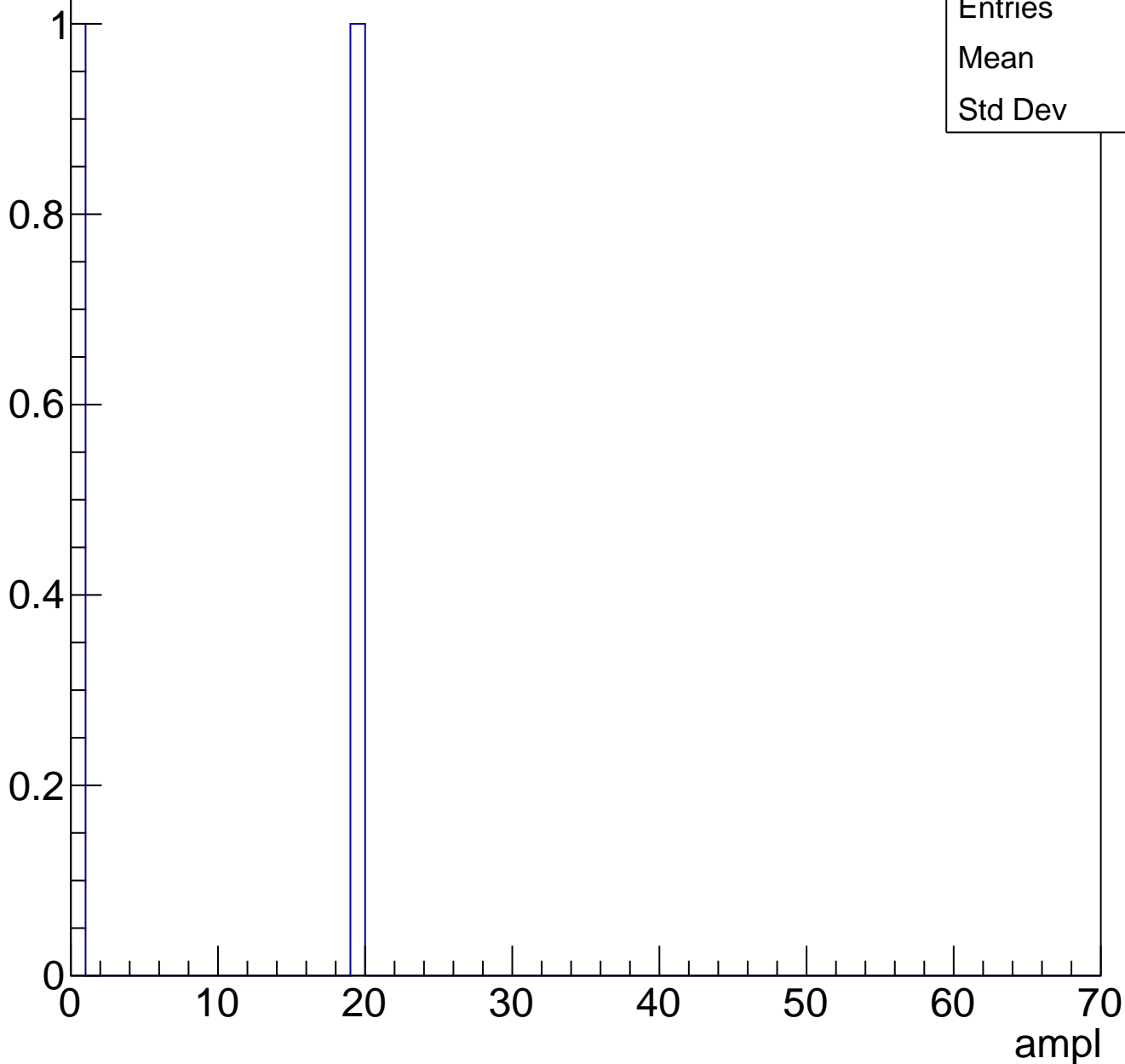




# B1L002S, U18-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	9.5
Std Dev	9.5

# B1L002S, U18-ch46, adc0

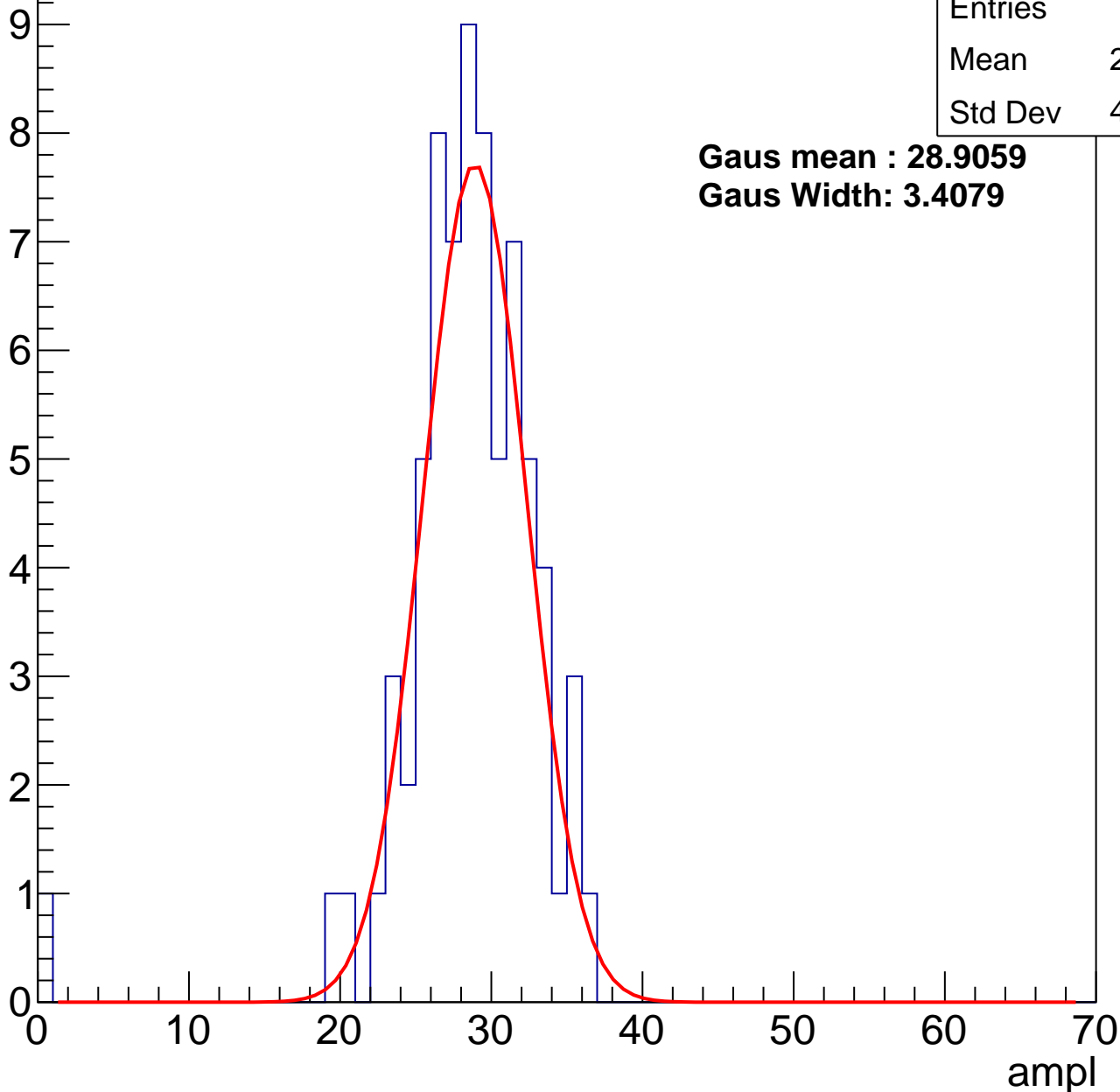
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	28.03
Std Dev	4.822

**Gaus mean : 28.9059**

**Gaus Width: 3.4079**



# B1L002S, U18-ch46, adc1

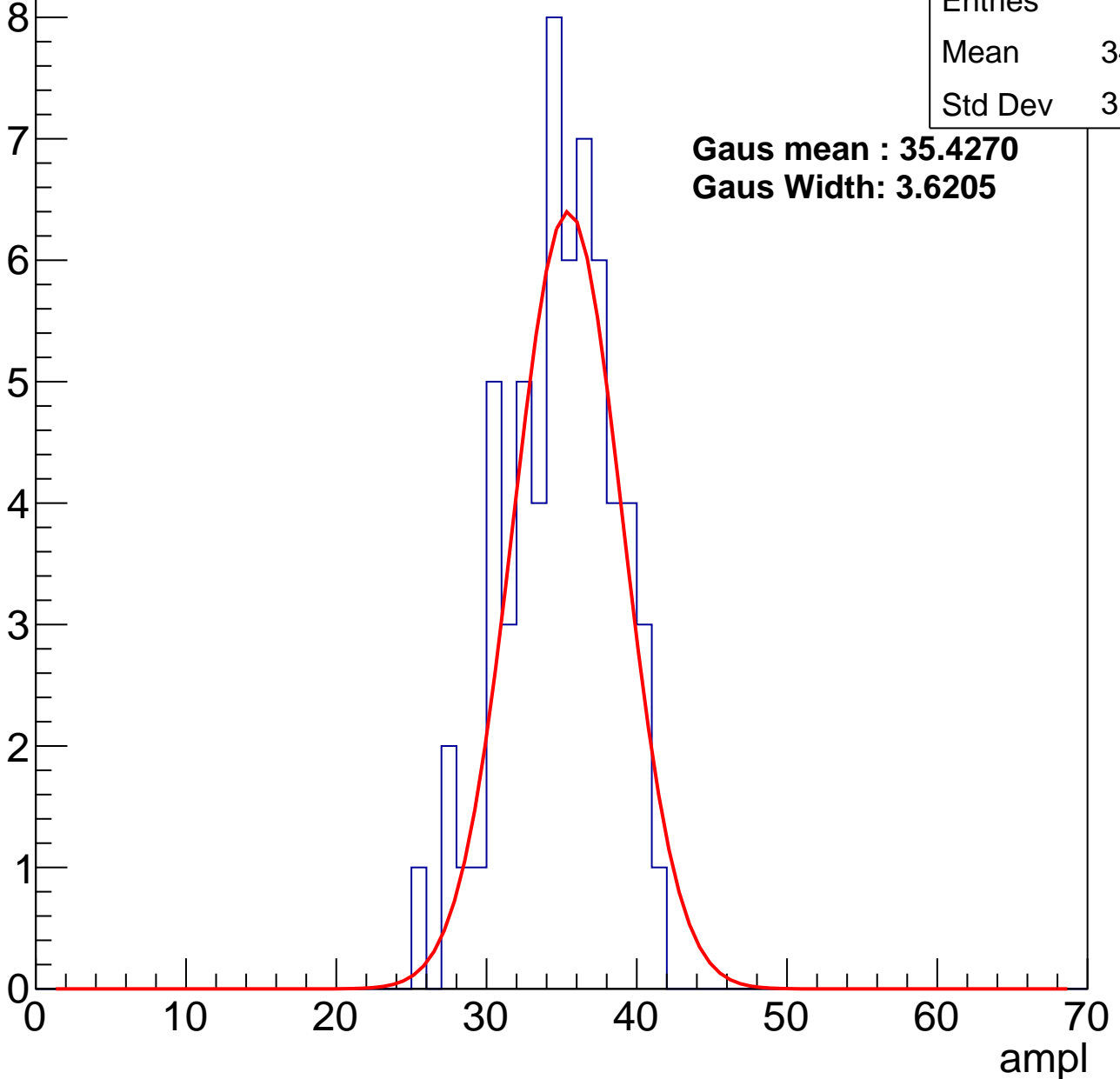
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.36
Std Dev	3.558

**Gaus mean : 35.4270**

**Gaus Width: 3.6205**



# B1L002S, U18-ch46, adc2

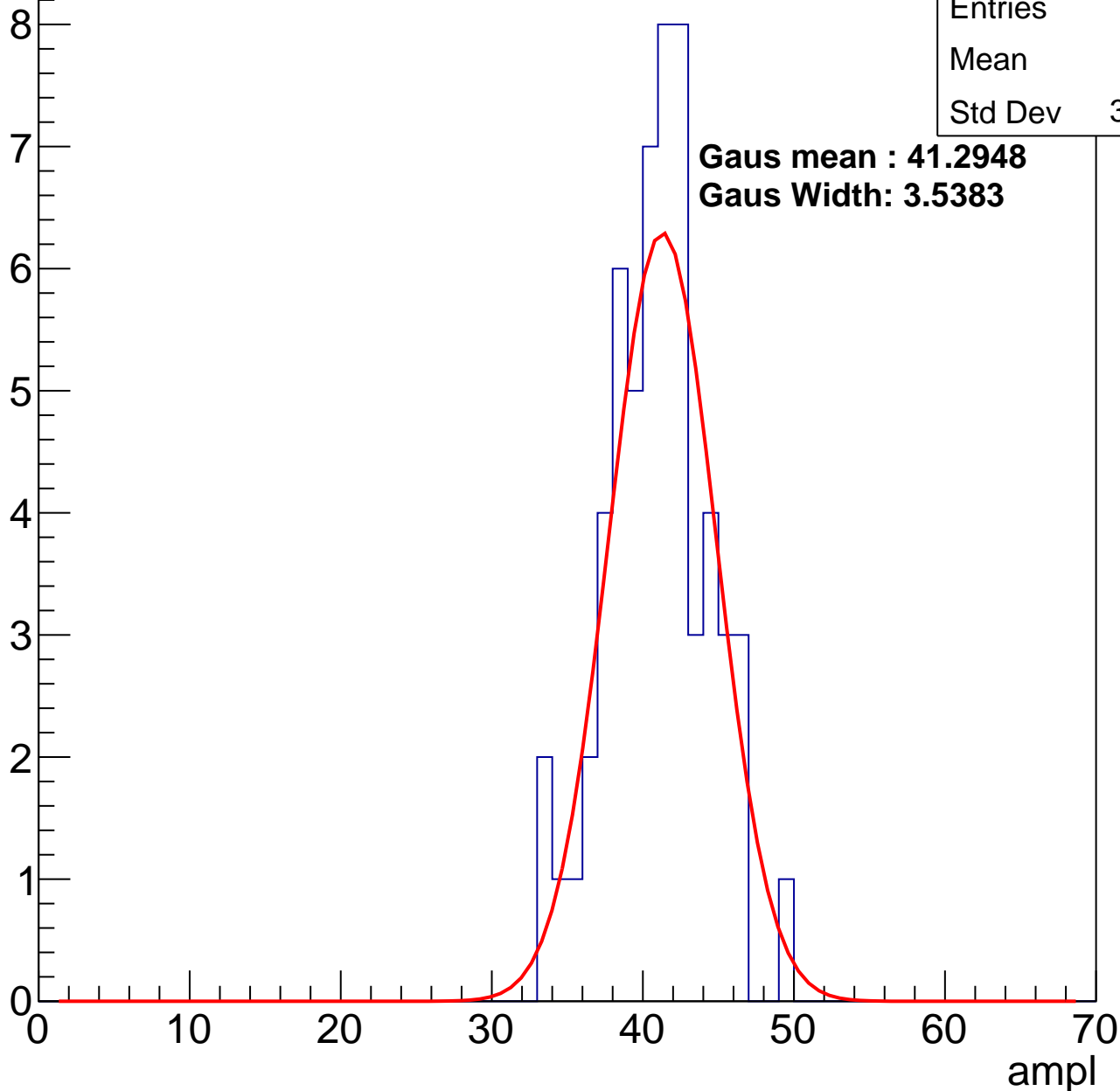
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.5
Std Dev	3.313

**Gaus mean : 41.2948**

**Gaus Width: 3.5383**

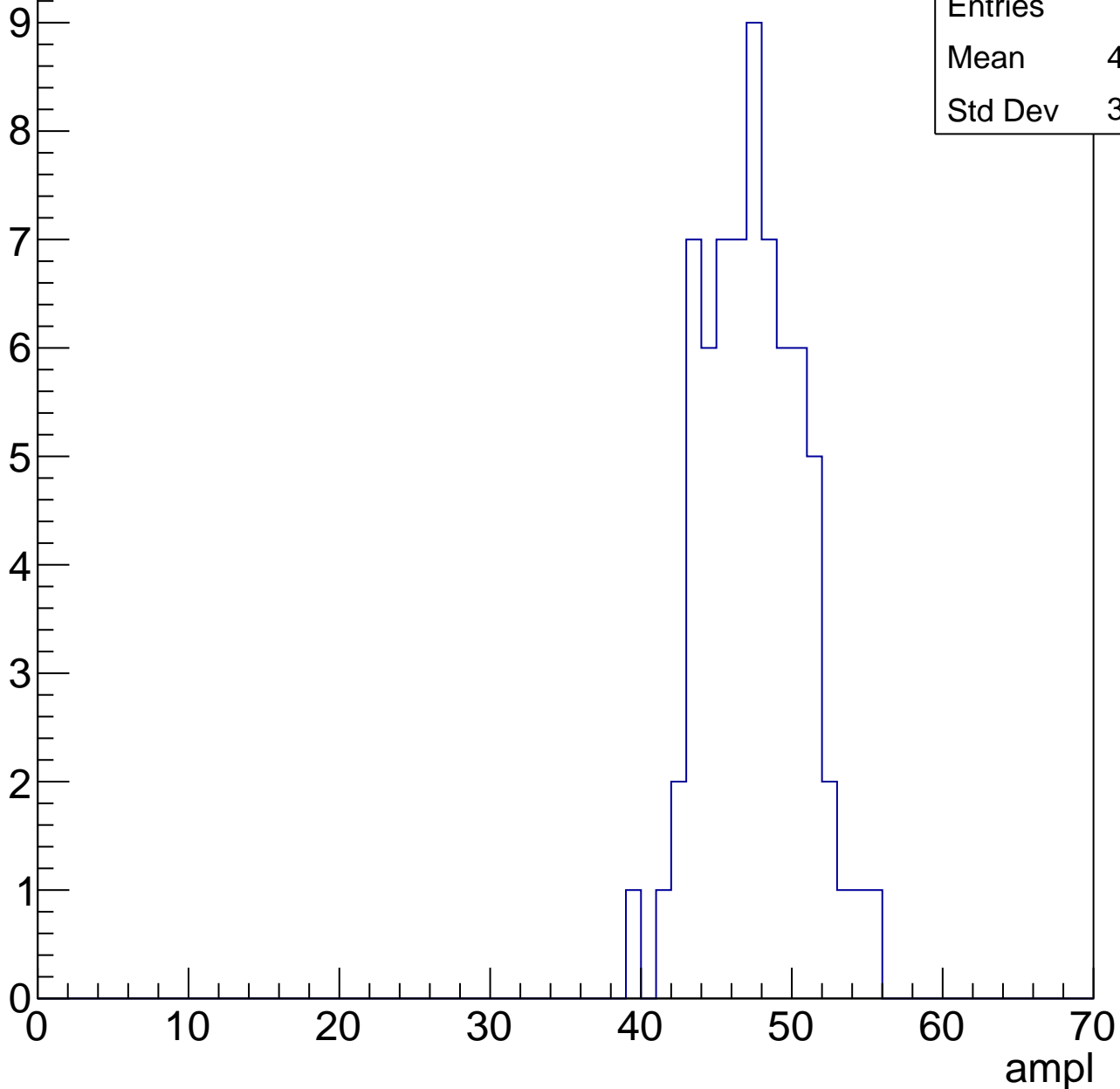


# B1L002S, U18-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.96
Std Dev	3.219

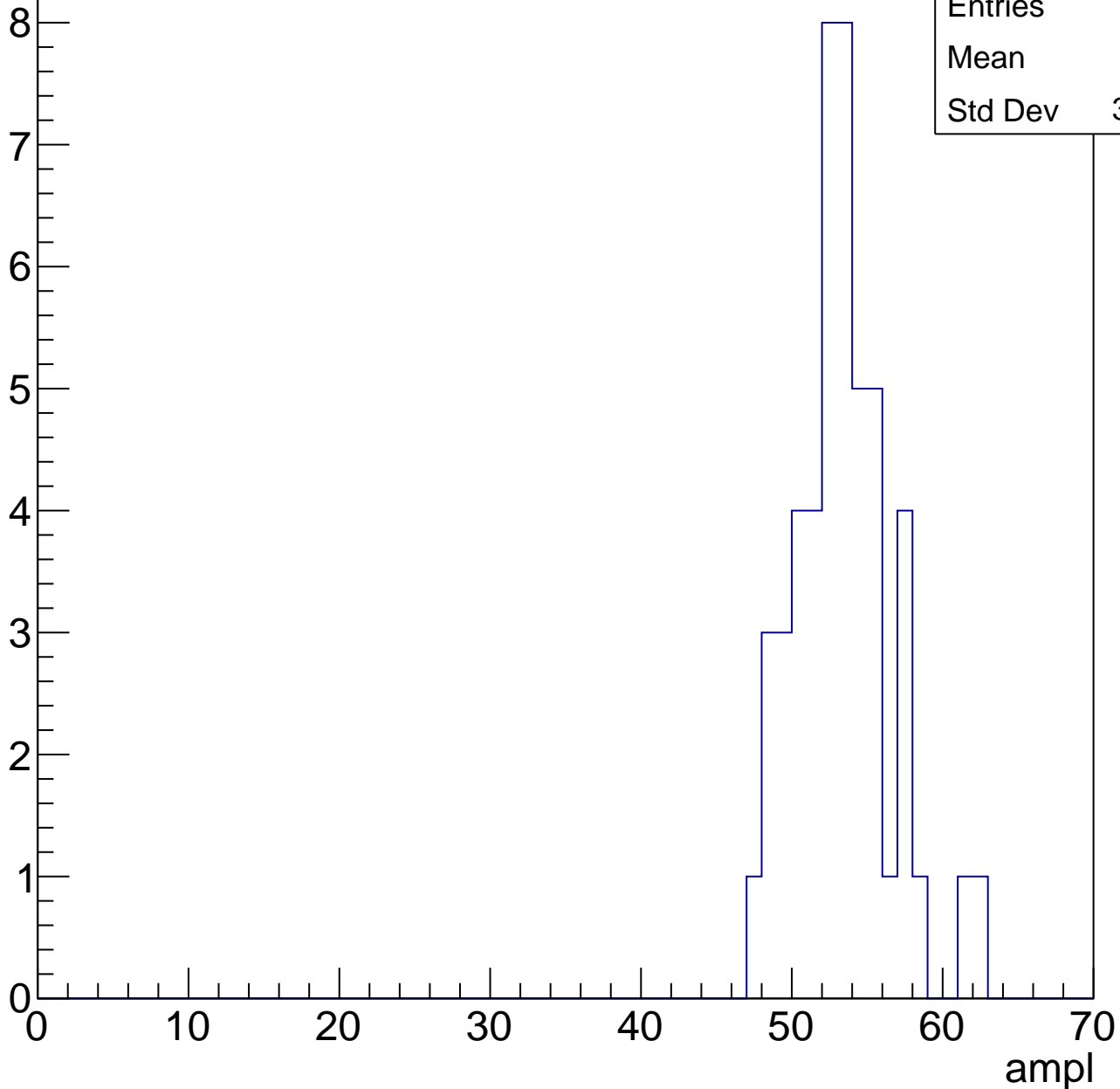


# B1L002S, U18-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.9
Std Dev	3.151

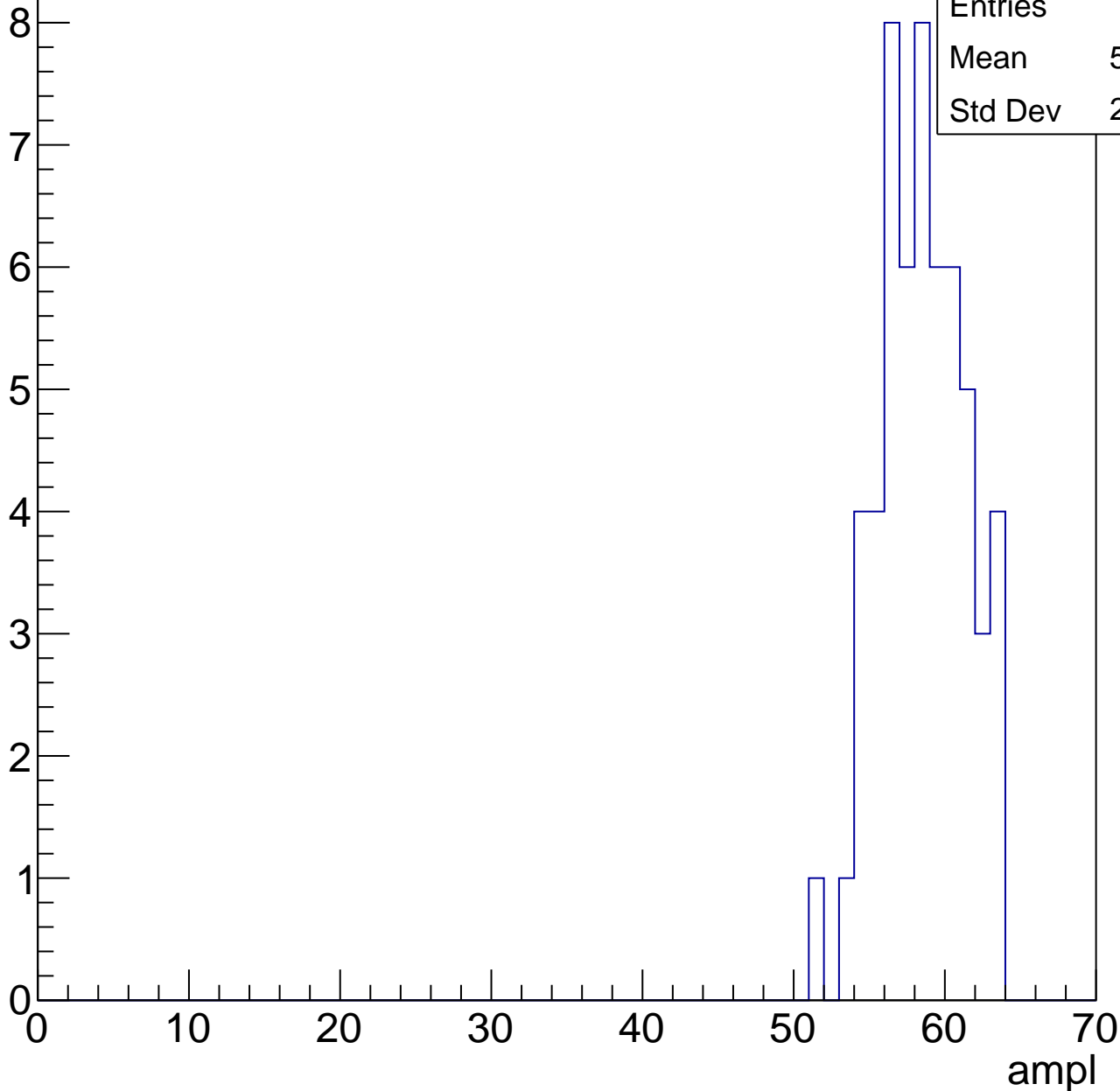


# B1L002S, U18-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	58.05
Std Dev	2.787

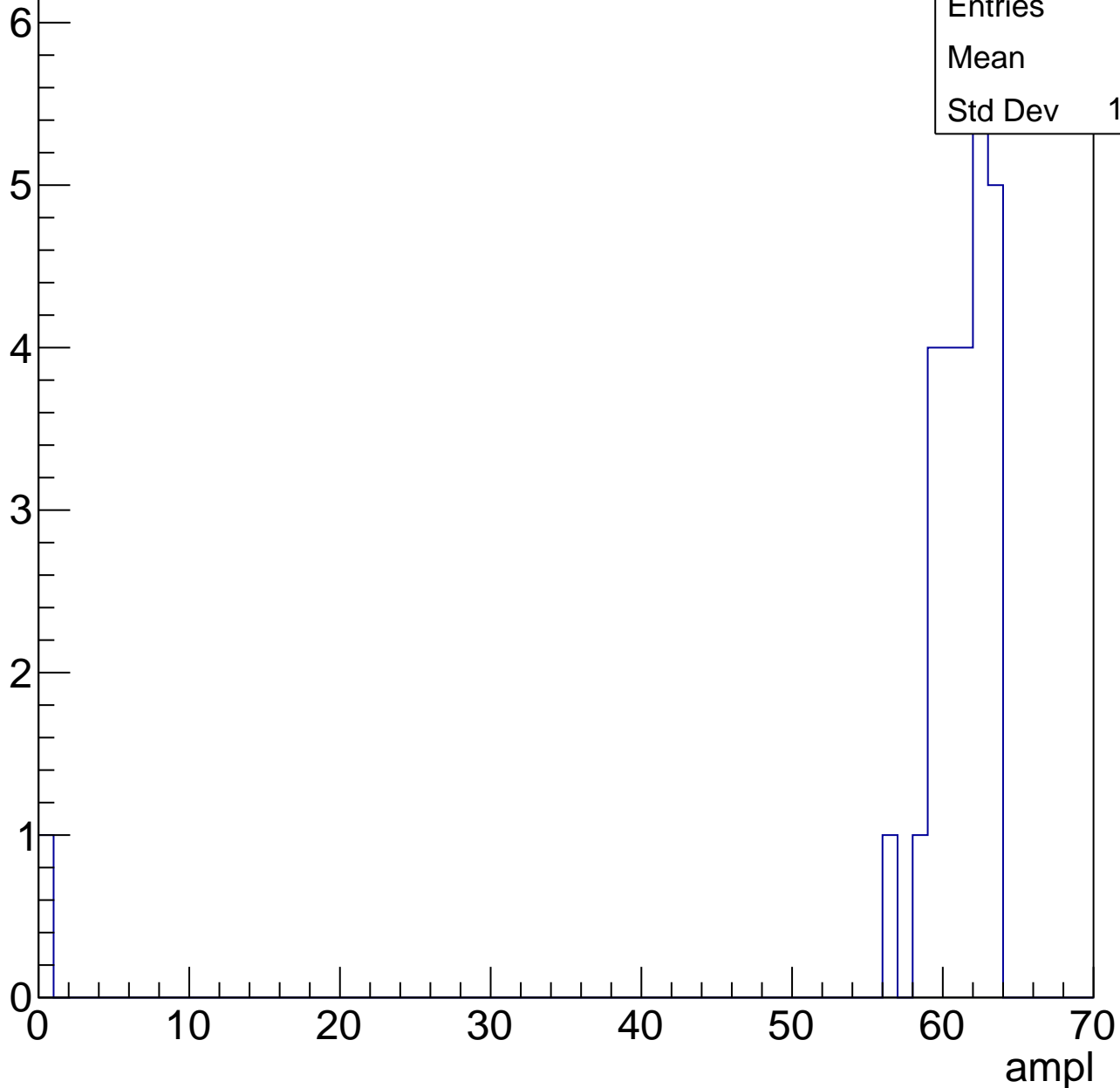


# B1L002S, U18-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.5
Std Dev	11.83





# B1L002S, U18-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L002S, U18-ch47, adc0

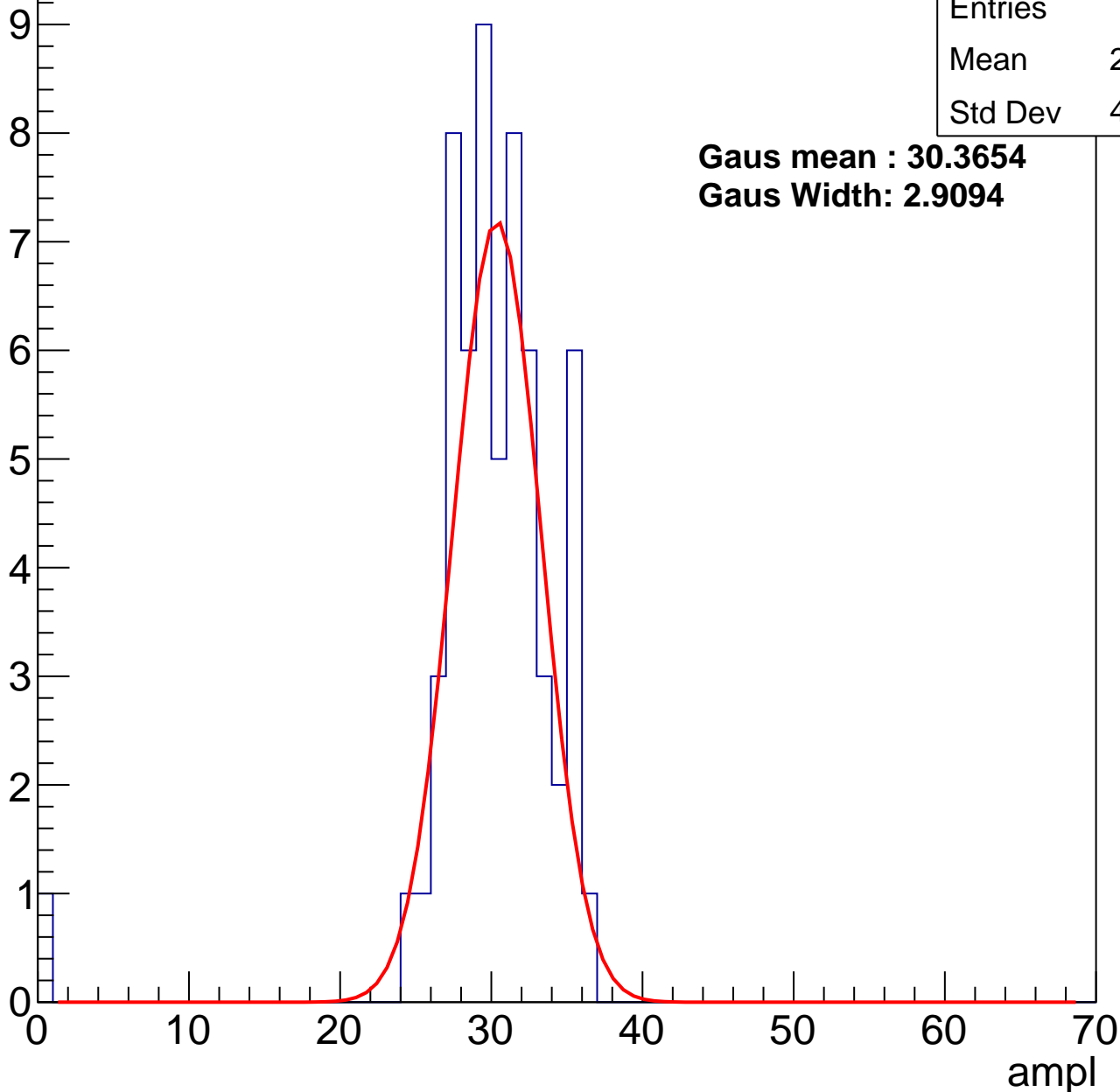
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	29.58
Std Dev	4.797

**Gaus mean : 30.3654**

**Gaus Width: 2.9094**



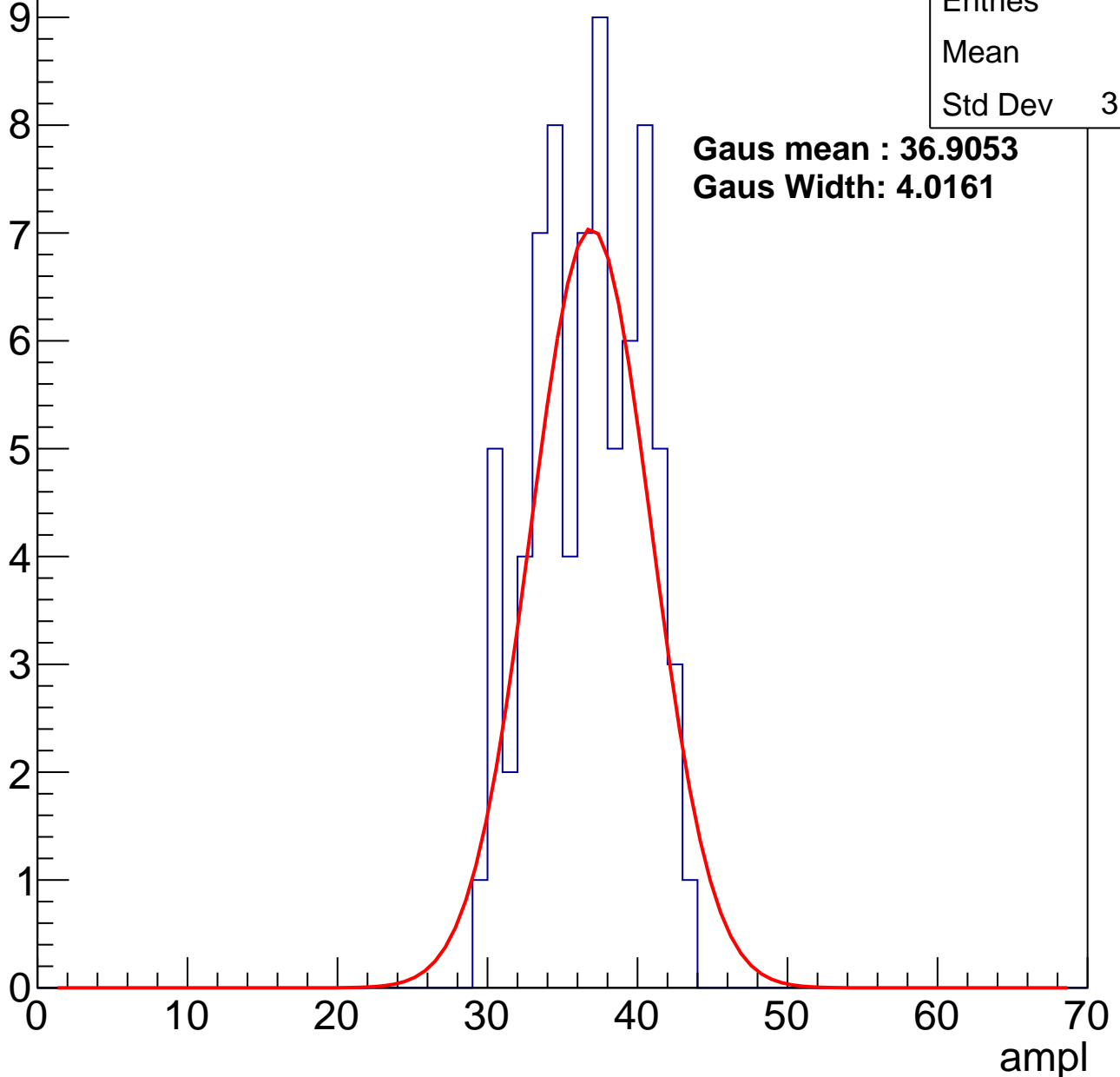
# B1L002S, U18-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	36.2
Std Dev	3.525

**Gaus mean : 36.9053**  
**Gaus Width: 4.0161**



# B1L002S, U18-ch47, adc2

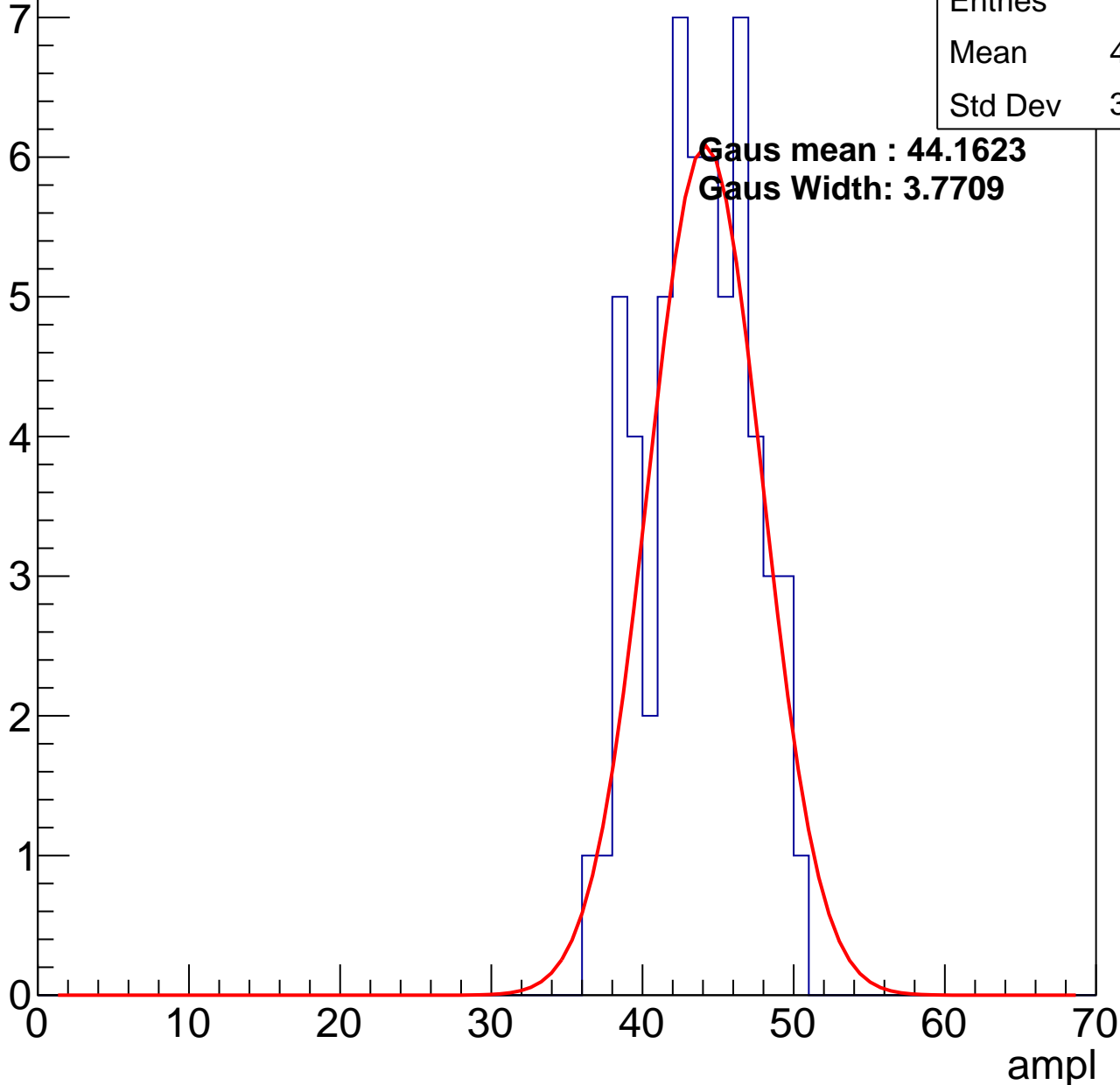
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	43.27
Std Dev	3.415

**Gaus mean : 44.1623**

**Gaus Width: 3.7709**

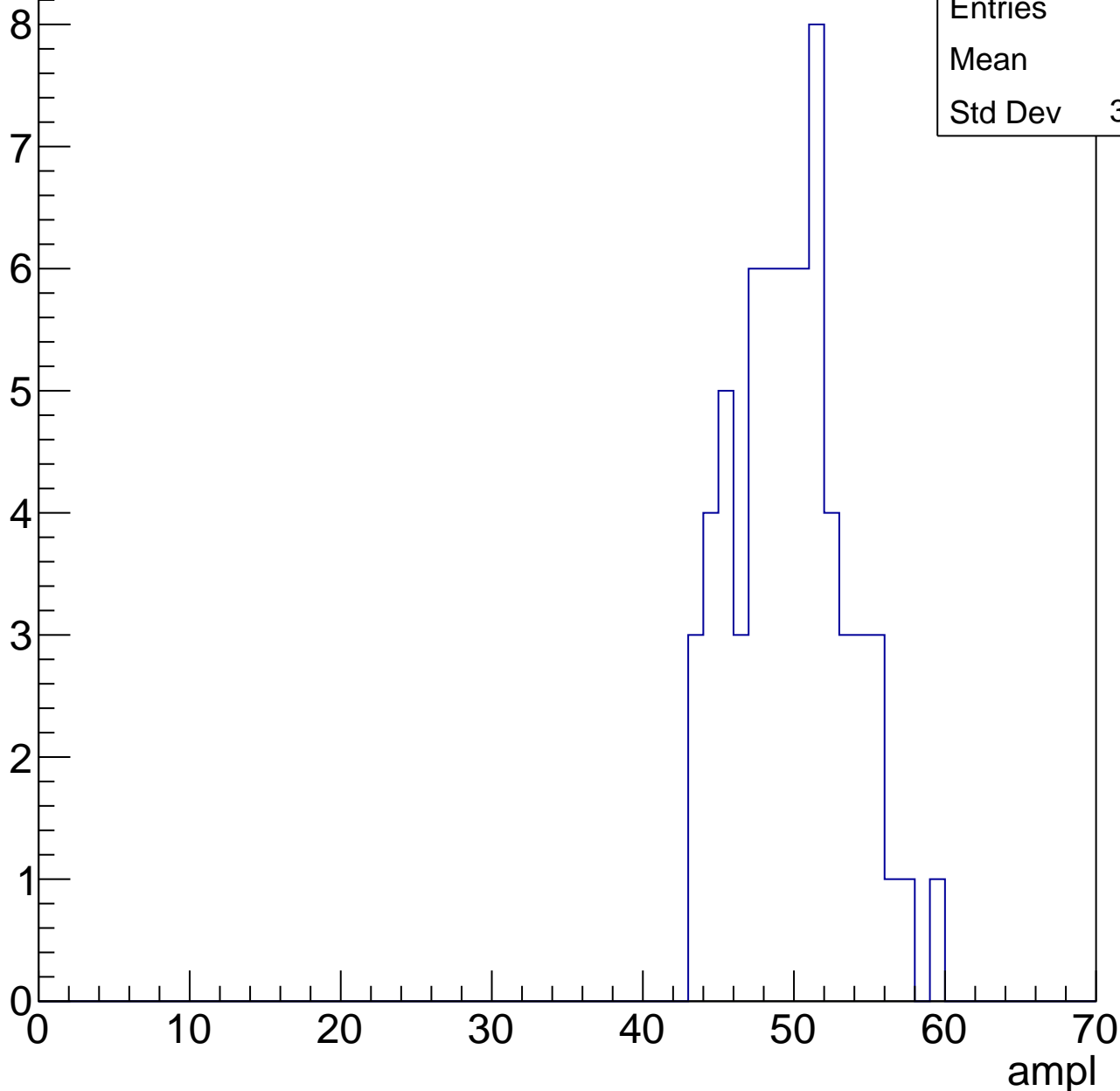


# B1L002S, U18-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	49.3
Std Dev	3.689

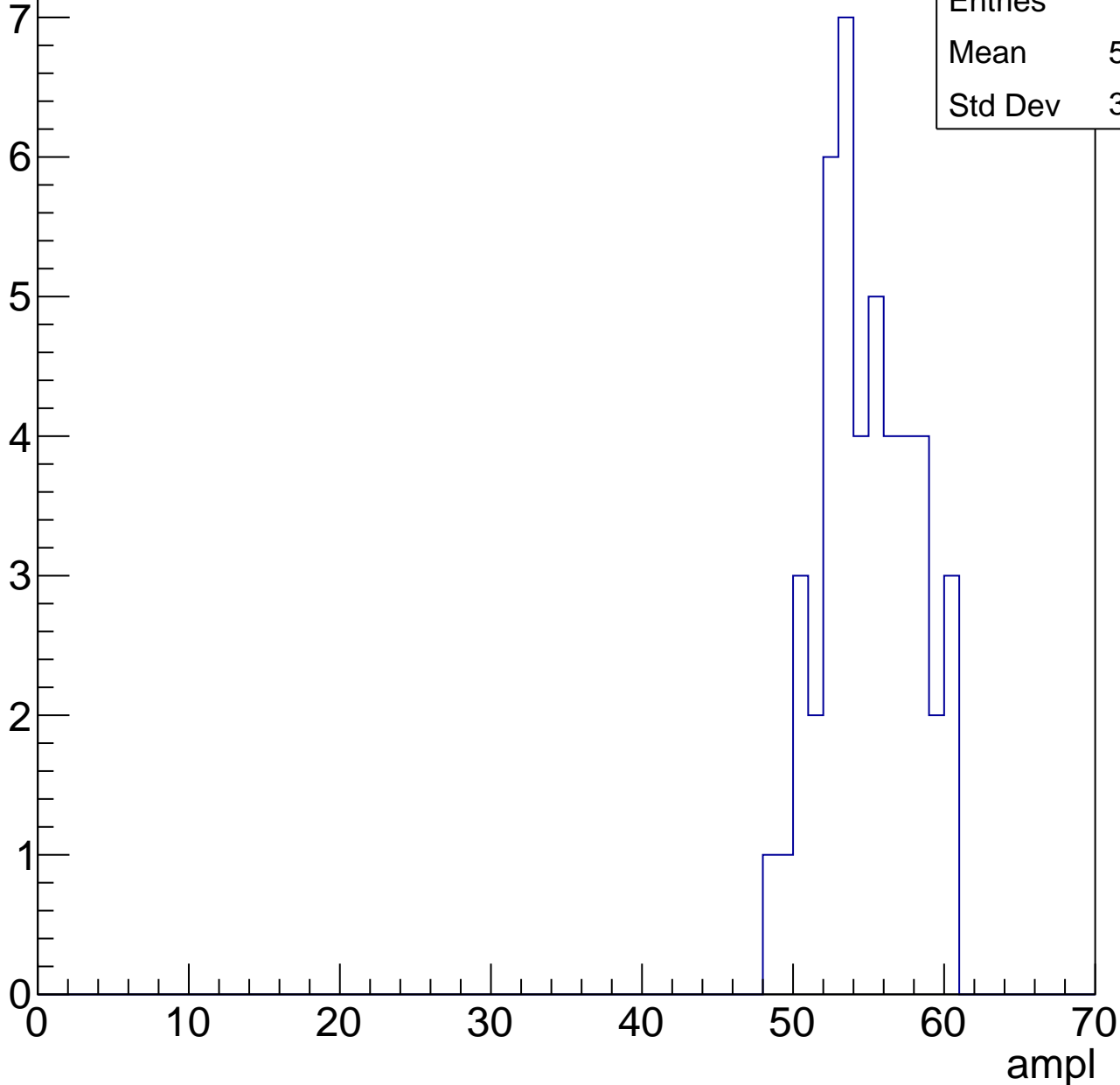


# B1L002S, U18-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	54.46
Std Dev	3.048

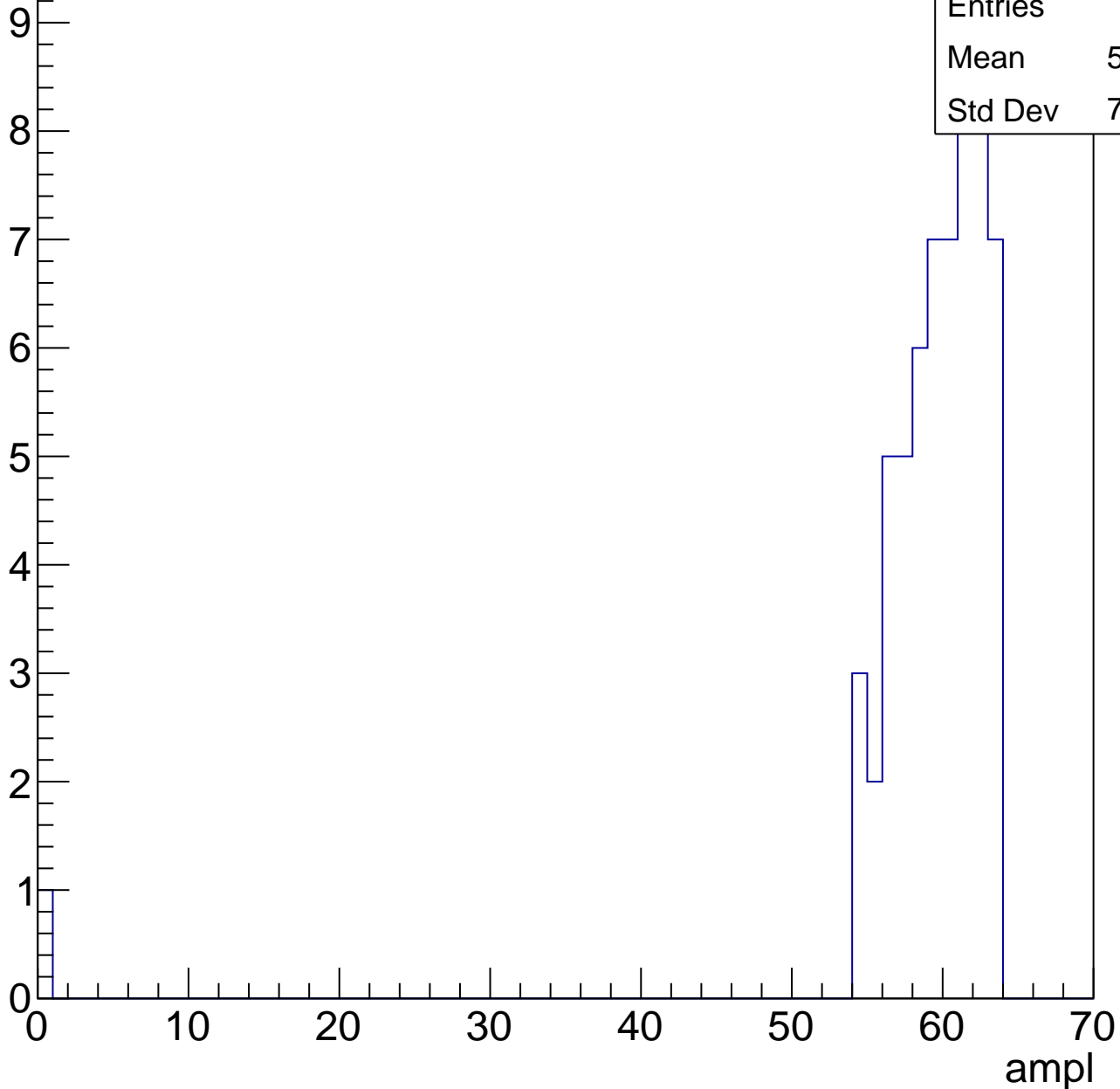


# B1L002S, U18-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	58.46
Std Dev	7.972



# B1L002S, U18-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

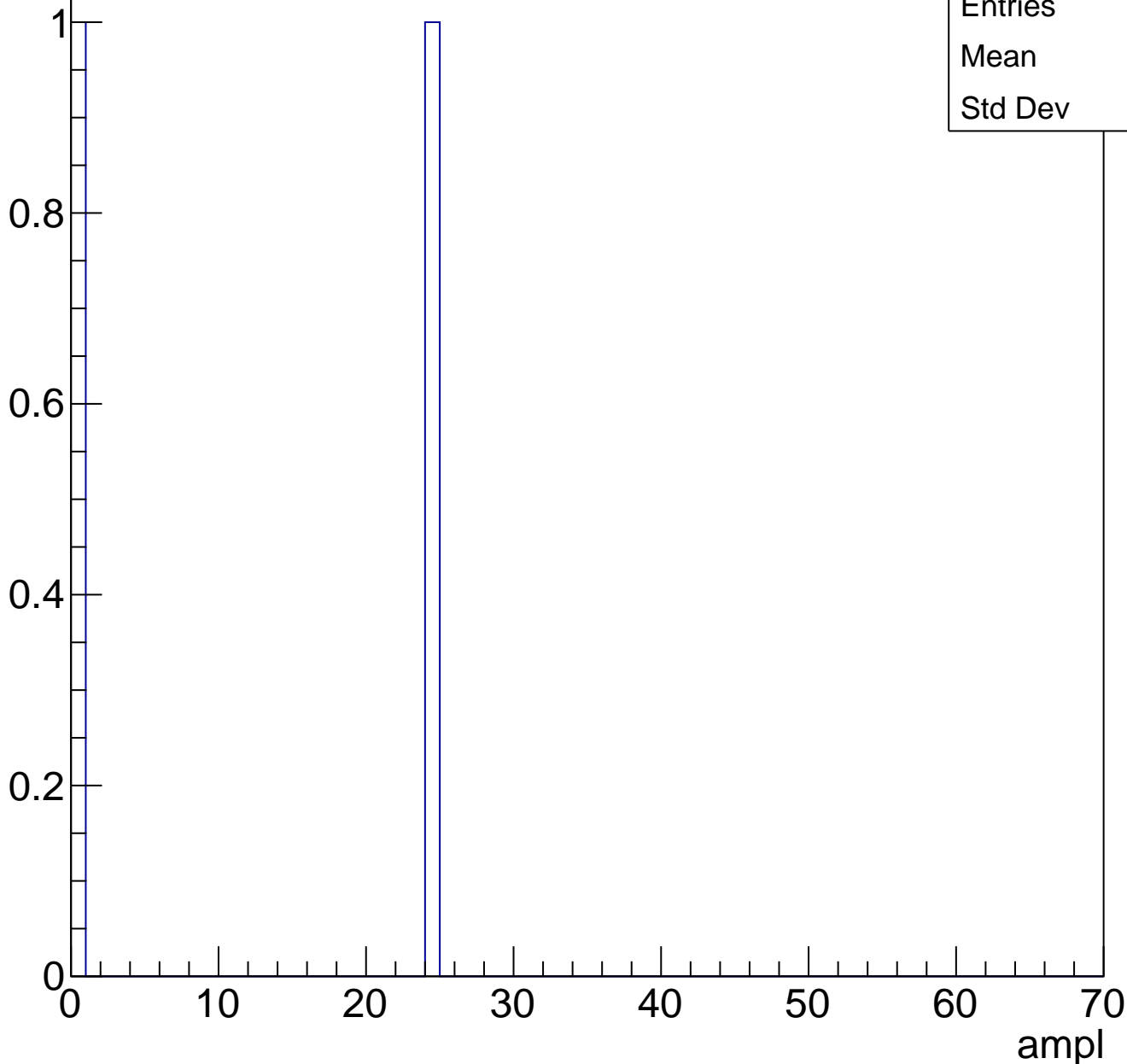




# B1L002S, U18-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch48, adc0

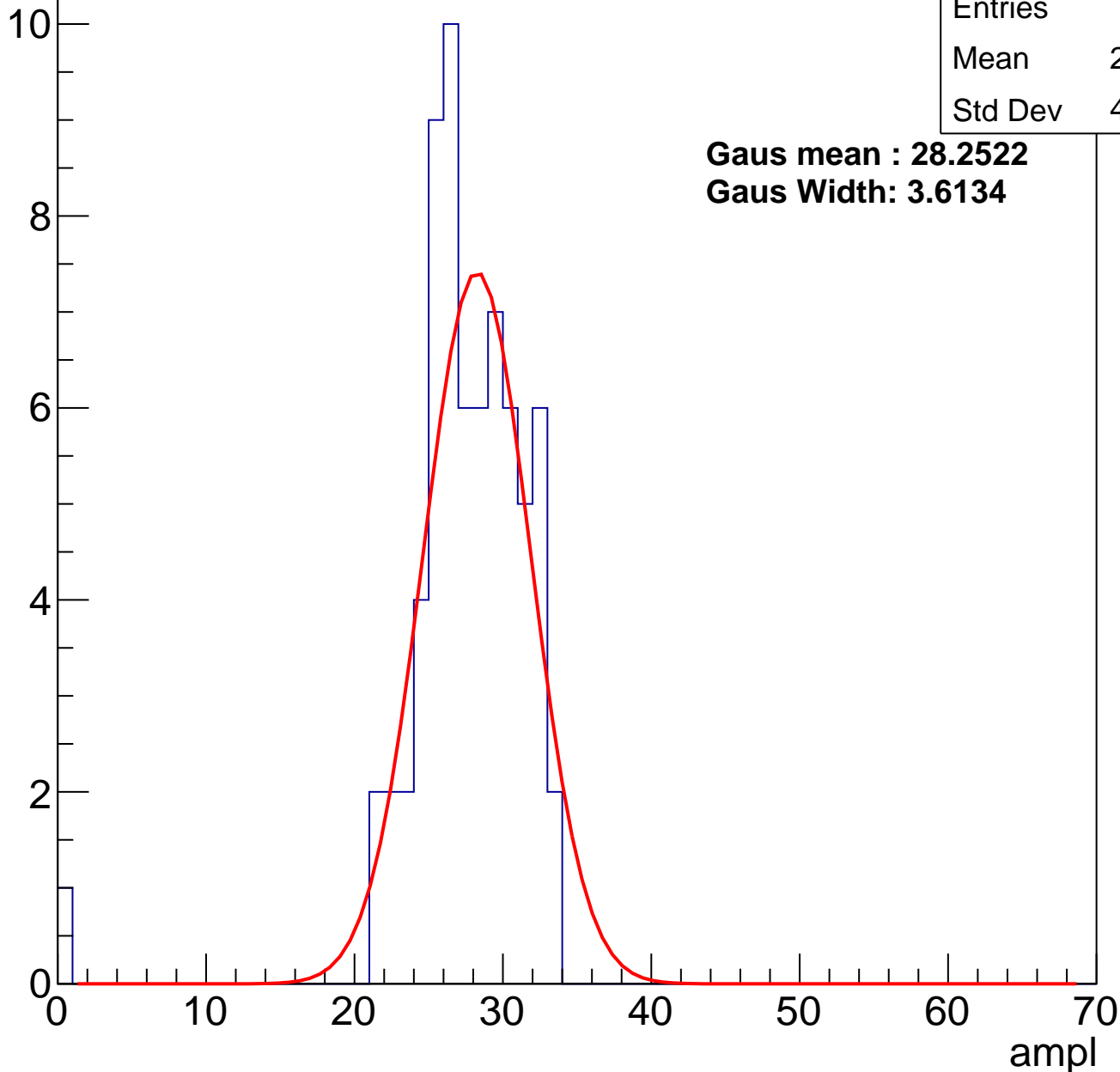
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	27.04
Std Dev	4.464

**Gaus mean : 28.2522**

**Gaus Width: 3.6134**

Entry



# B1L002S, U18-ch48, adc1

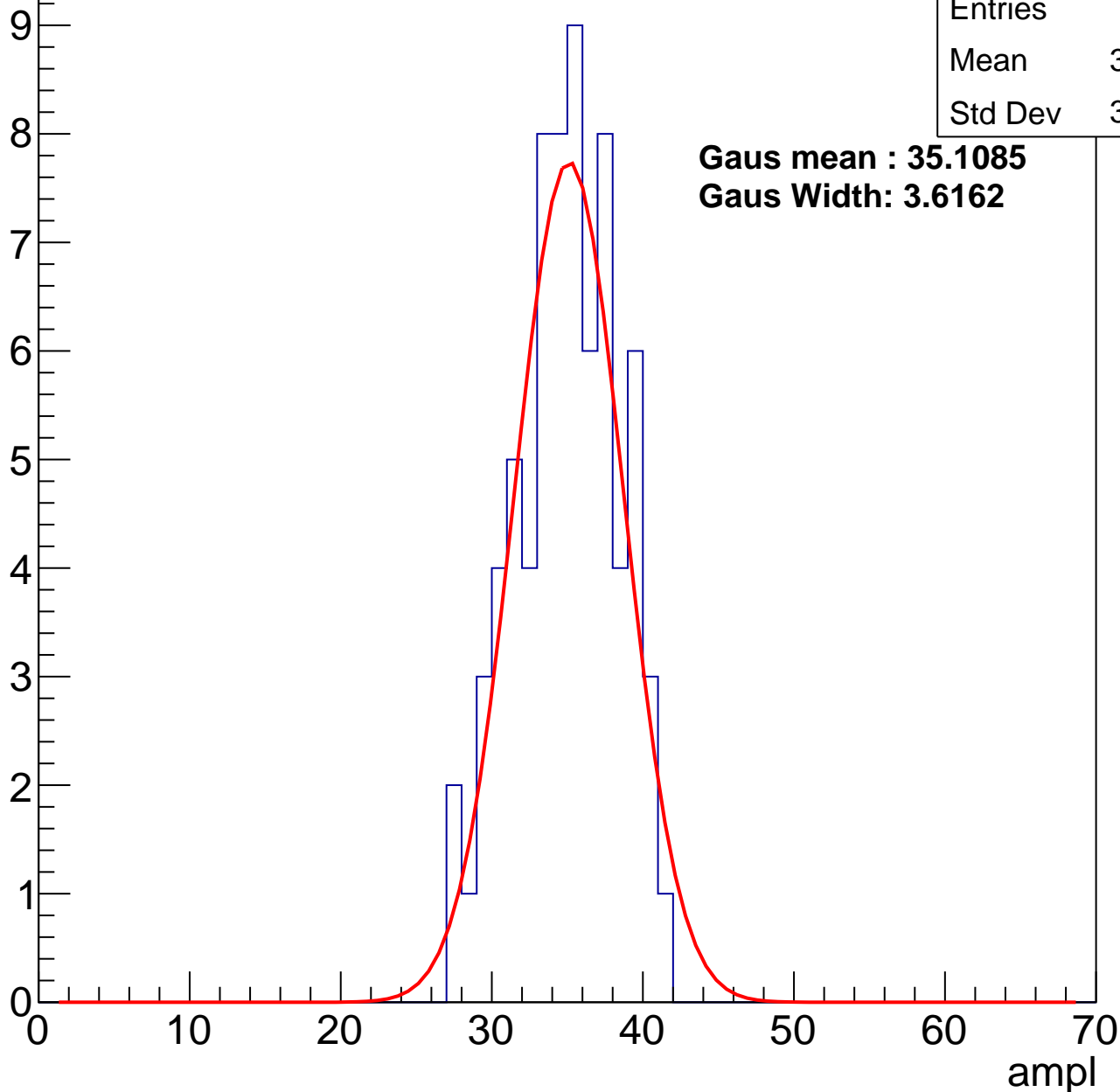
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.47
Std Dev	3.337

**Gaus mean : 35.1085**

**Gaus Width: 3.6162**



# B1L002S, U18-ch48, adc2

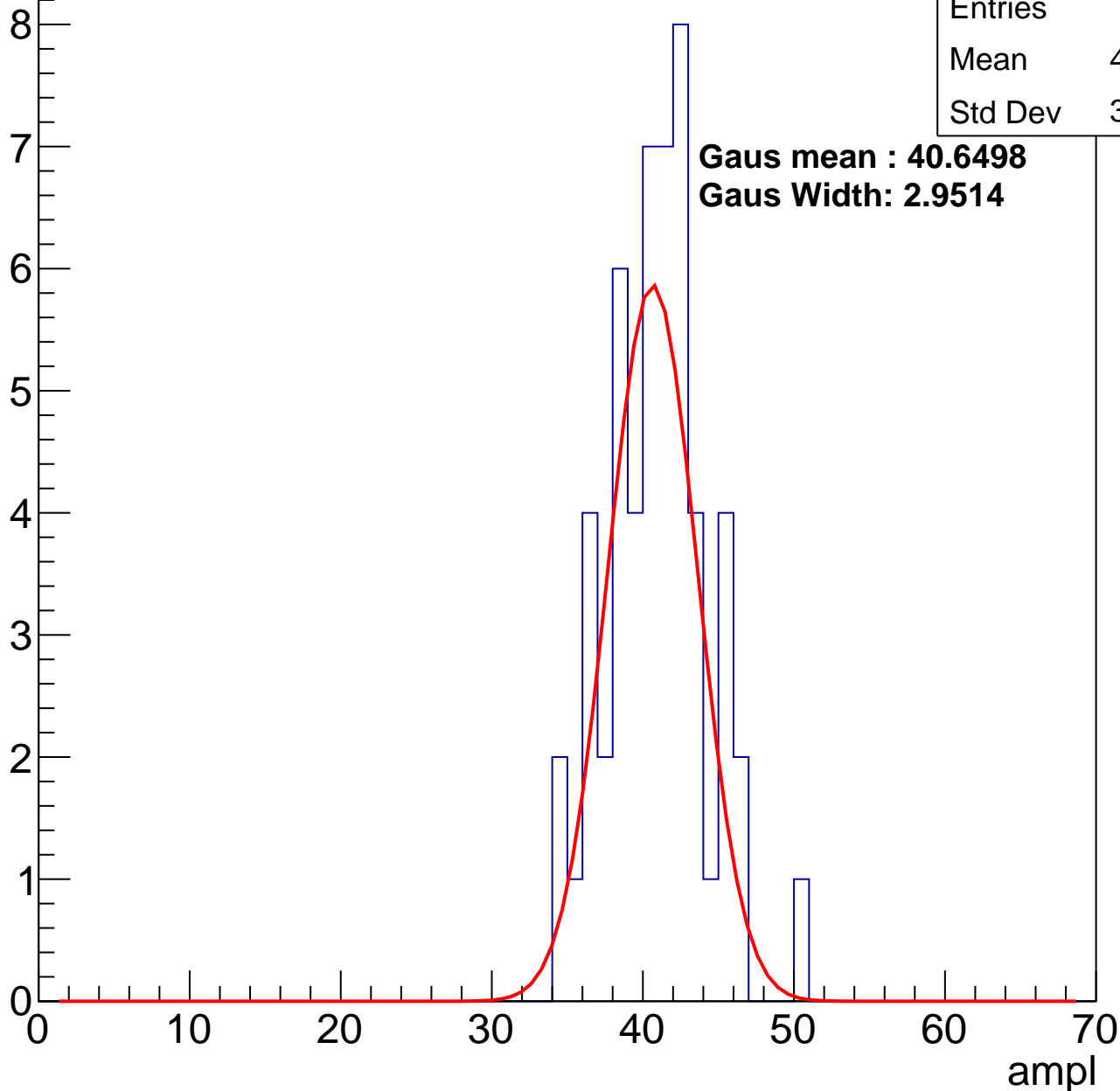
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.49
Std Dev	3.243

**Gaus mean : 40.6498**

**Gaus Width: 2.9514**

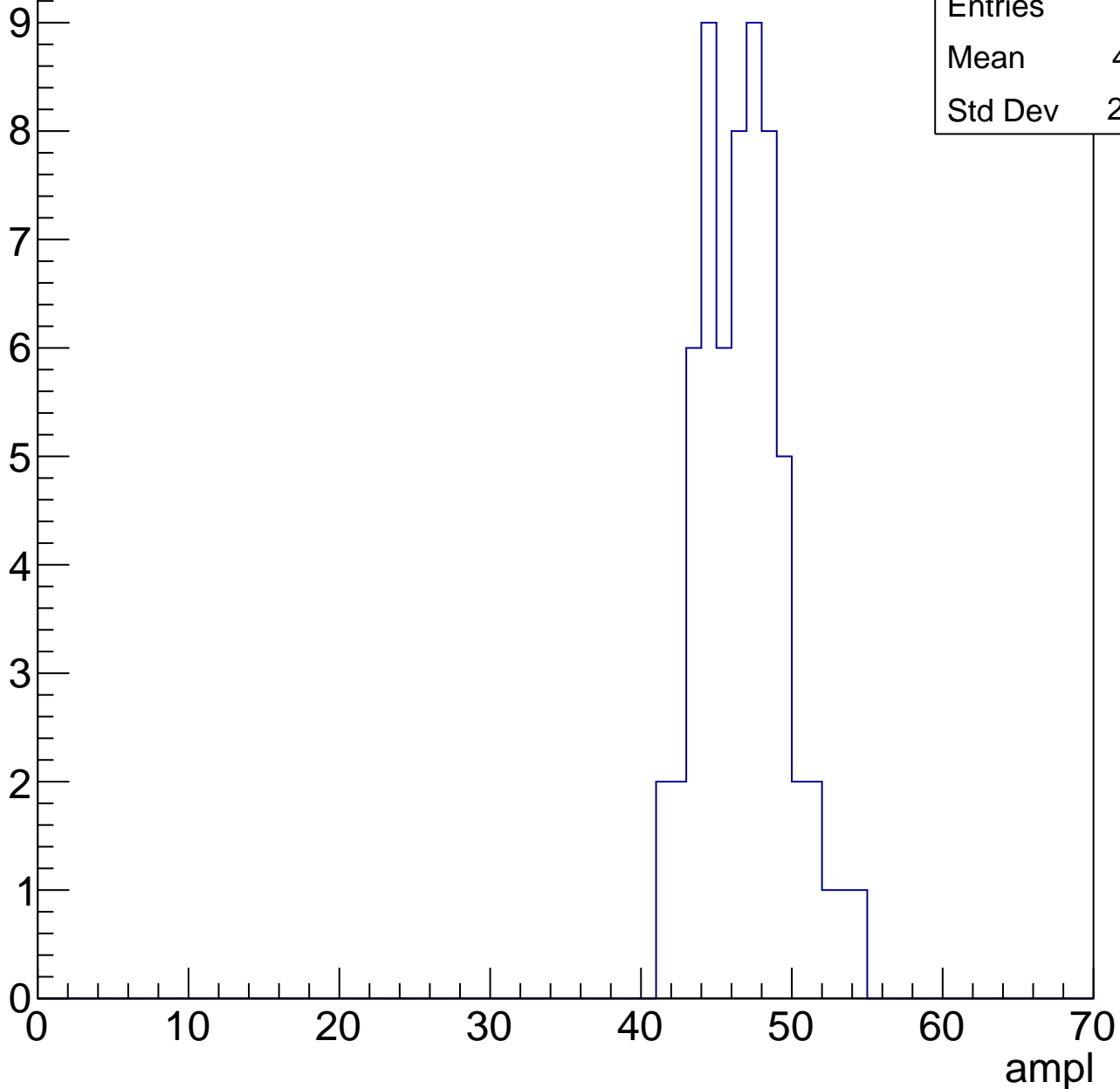


# B1L002S, U18-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.31
Std Dev	2.809



# B1L002S, U18-ch48, adc4

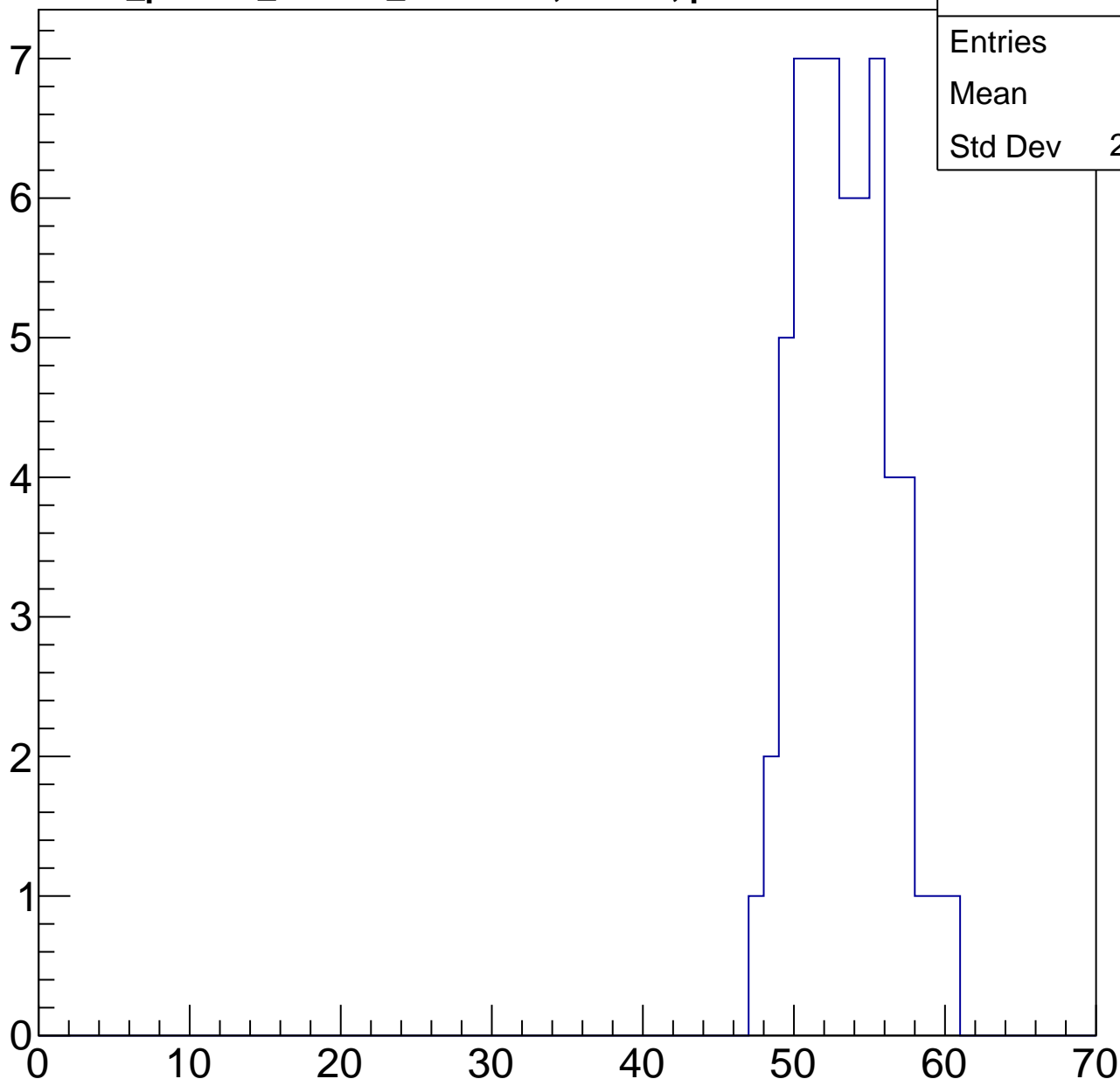
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	52.8
Std Dev	2.927

ampl

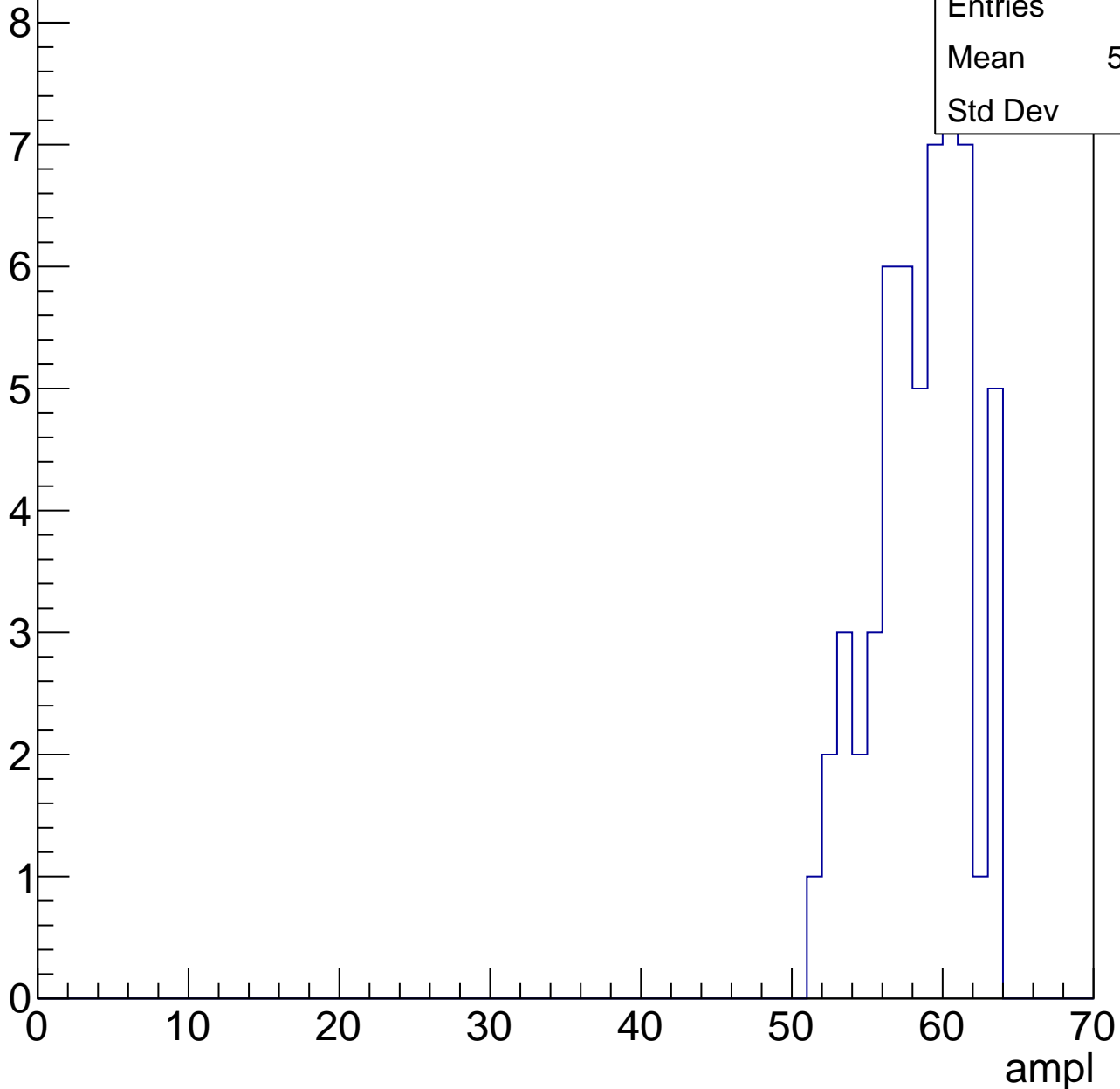


# B1L002S, U18-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	58.07
Std Dev	3.07

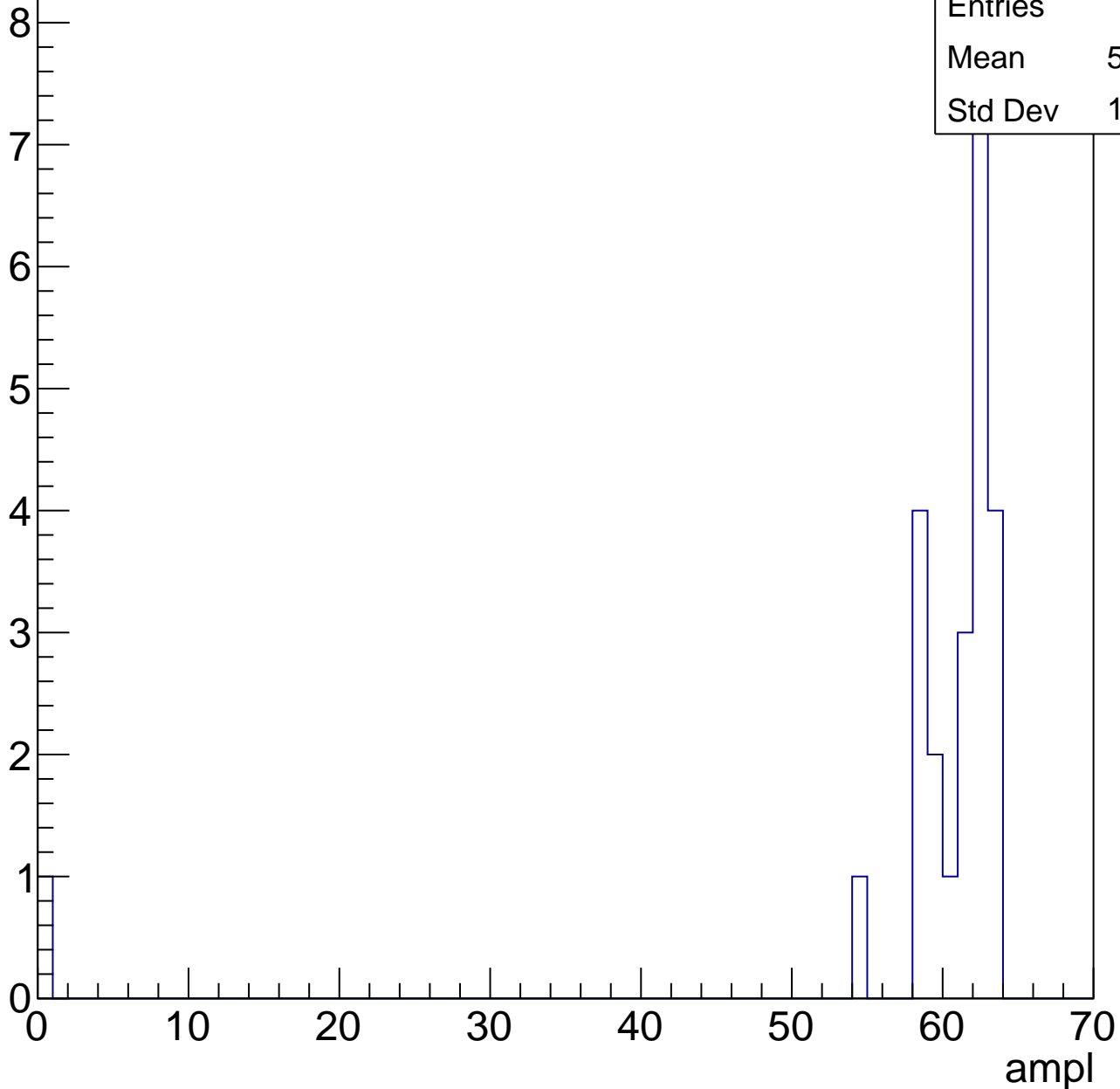


# B1L002S, U18-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.12
Std Dev	12.32





# B1L002S, U18-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch49, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	53
Mean	28.81
Std Dev	2.734

**Gaus mean : 29.2269**

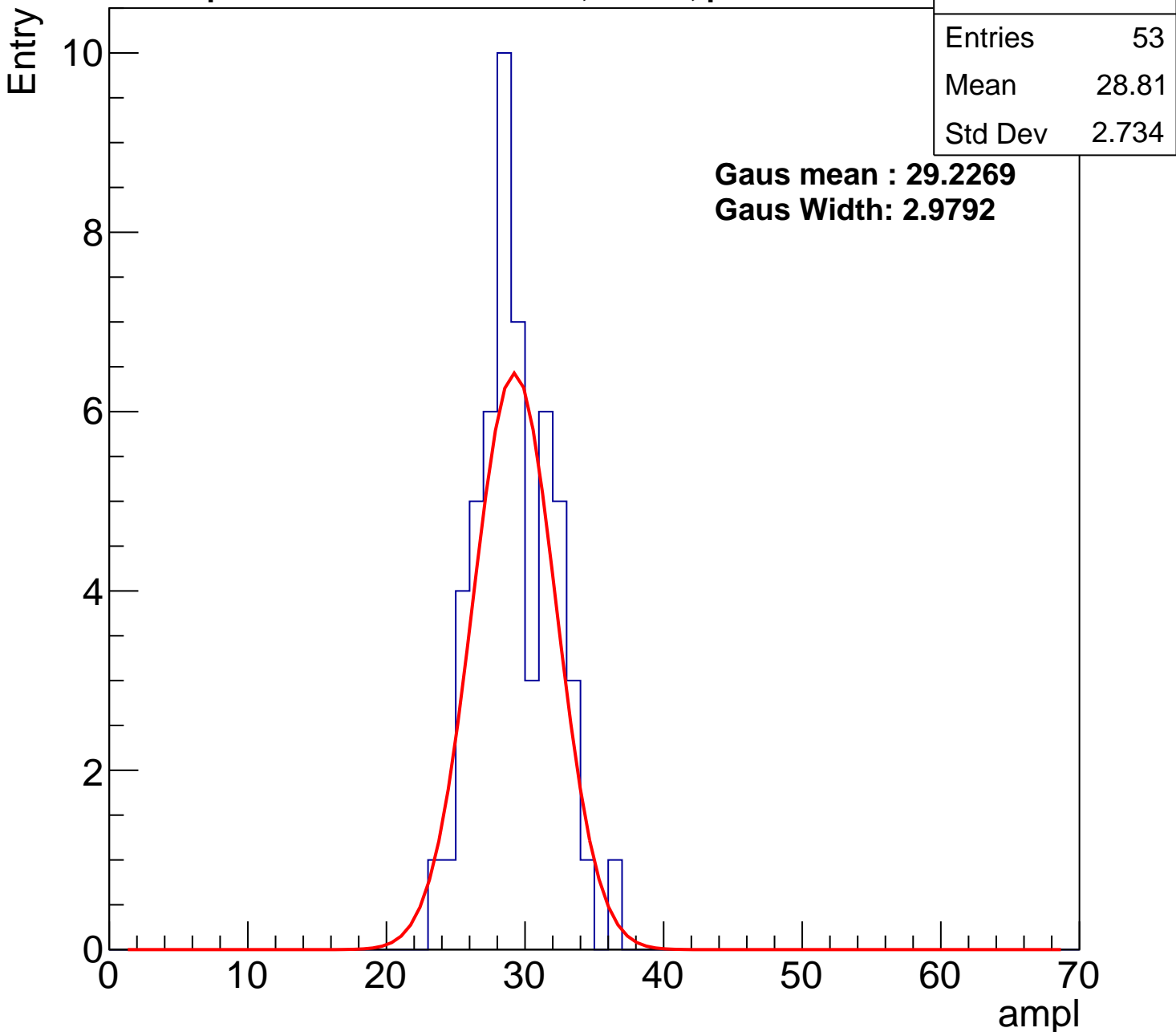
**Gaus Width: 2.9792**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch49, adc1

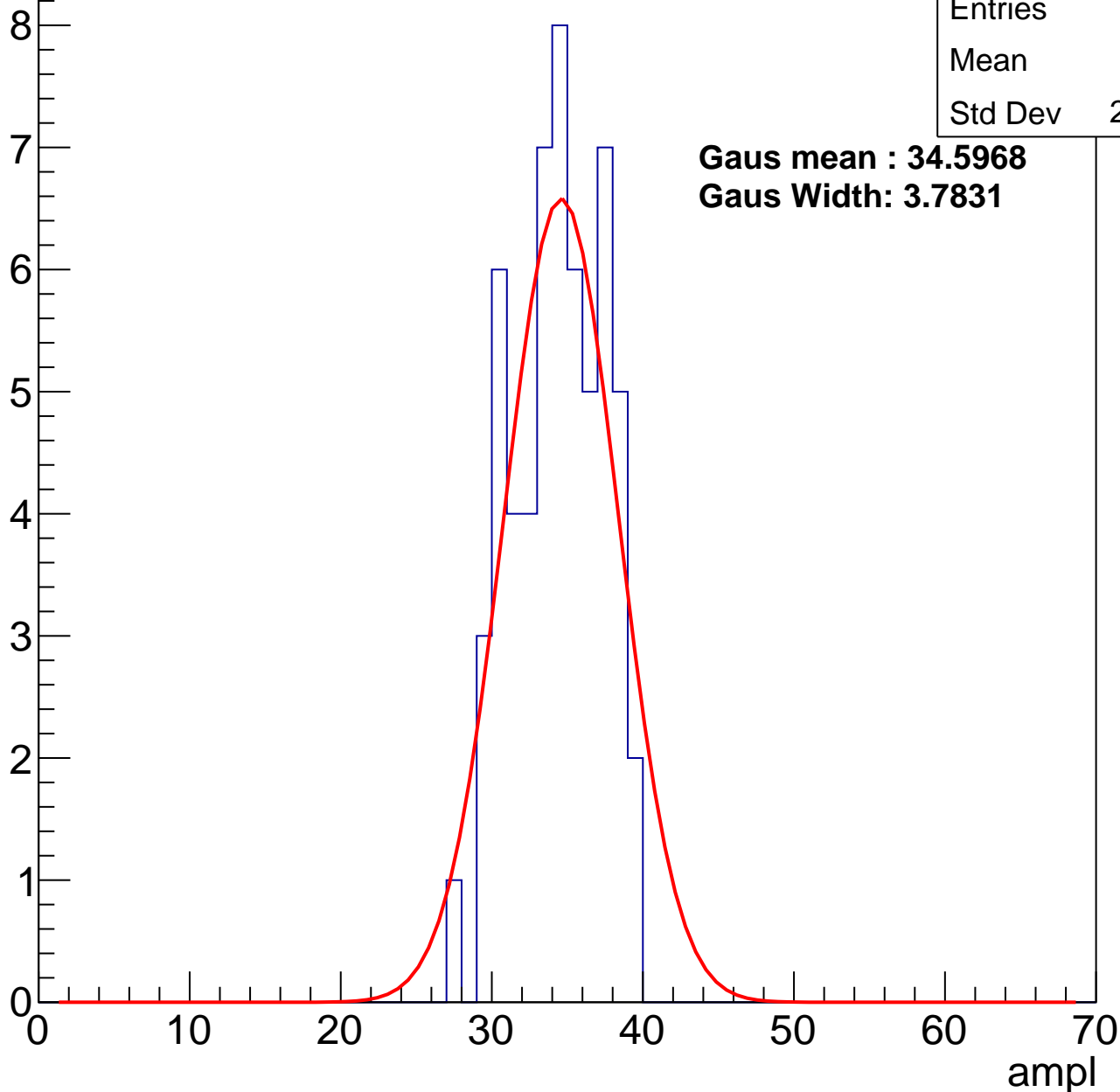
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	33.9
Std Dev	2.928

**Gaus mean : 34.5968**

**Gaus Width: 3.7831**



# B1L002S, U18-ch49, adc2

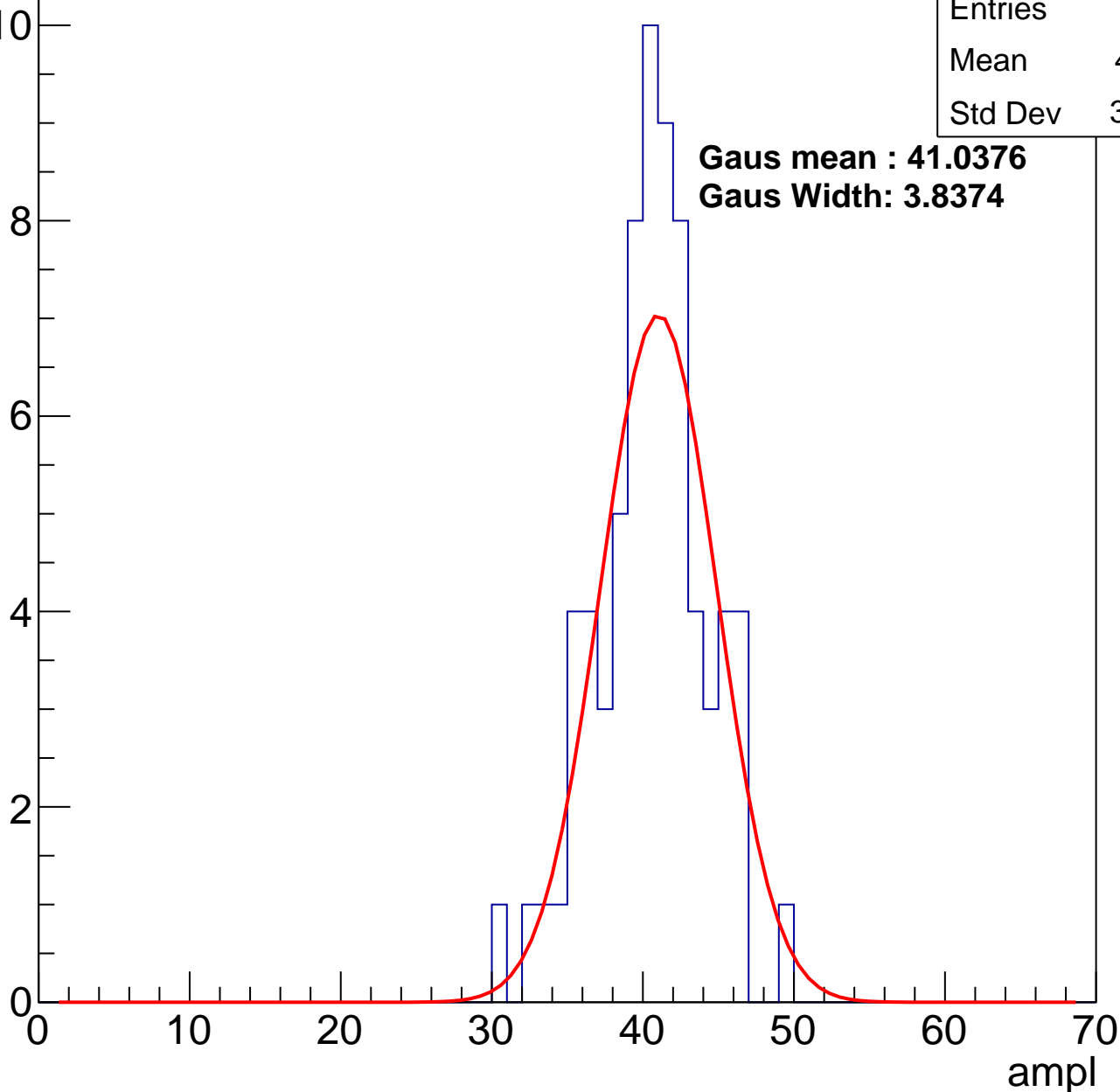
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.11
Std Dev	3.594

**Gaus mean : 41.0376**

**Gaus Width: 3.8374**

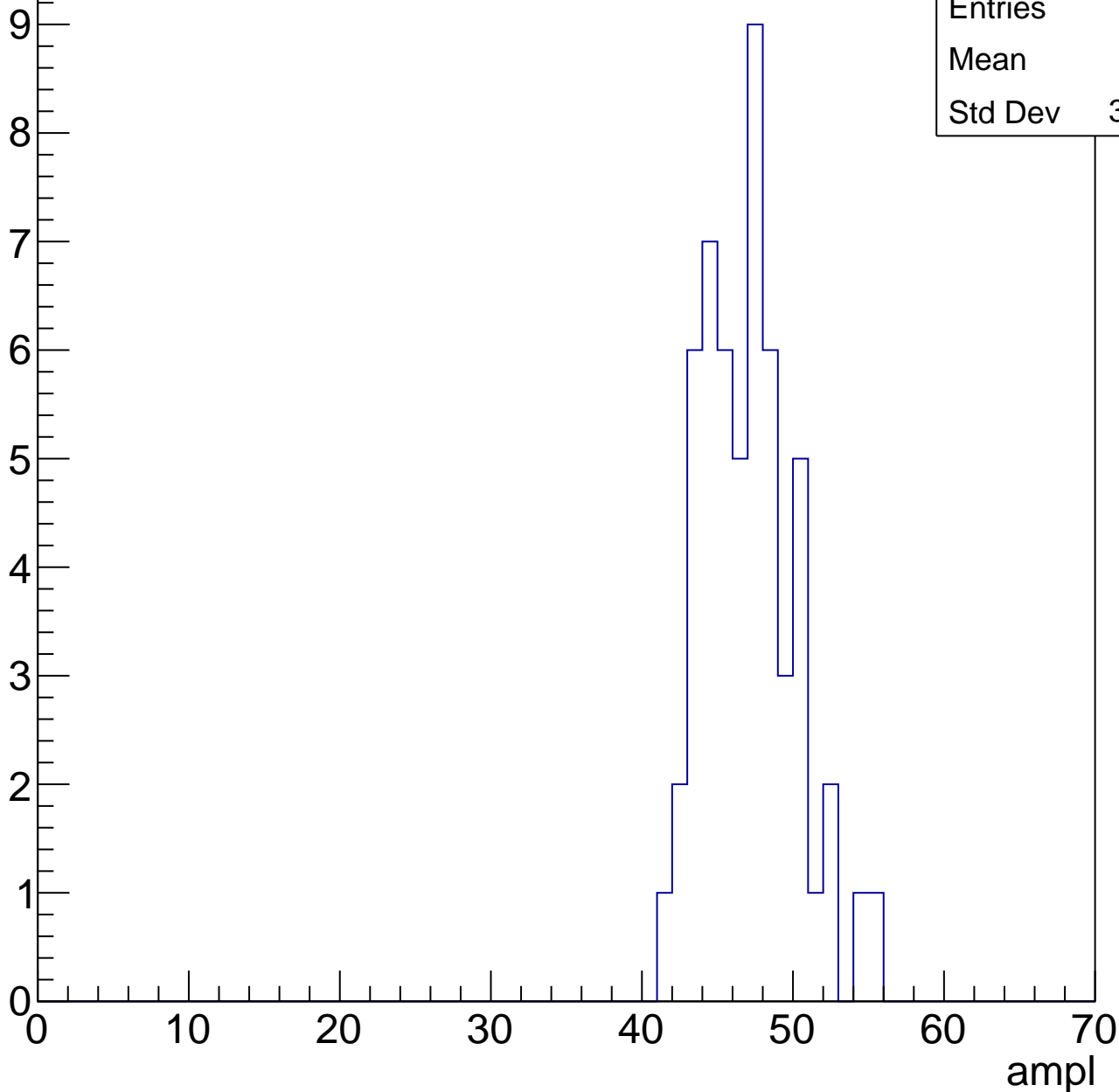


# B1L002S, U18-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.6
Std Dev	3.037

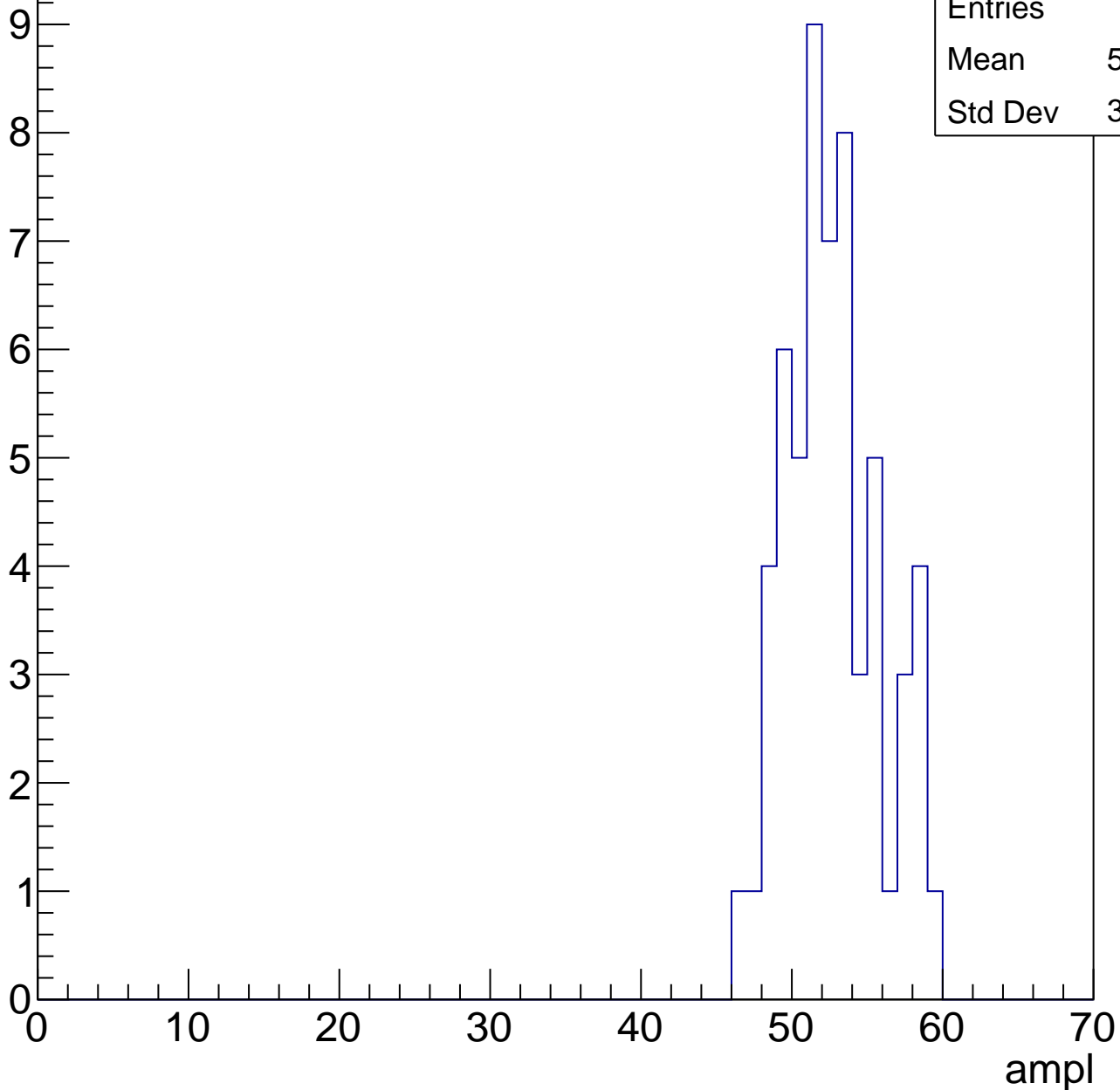


# B1L002S, U18-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

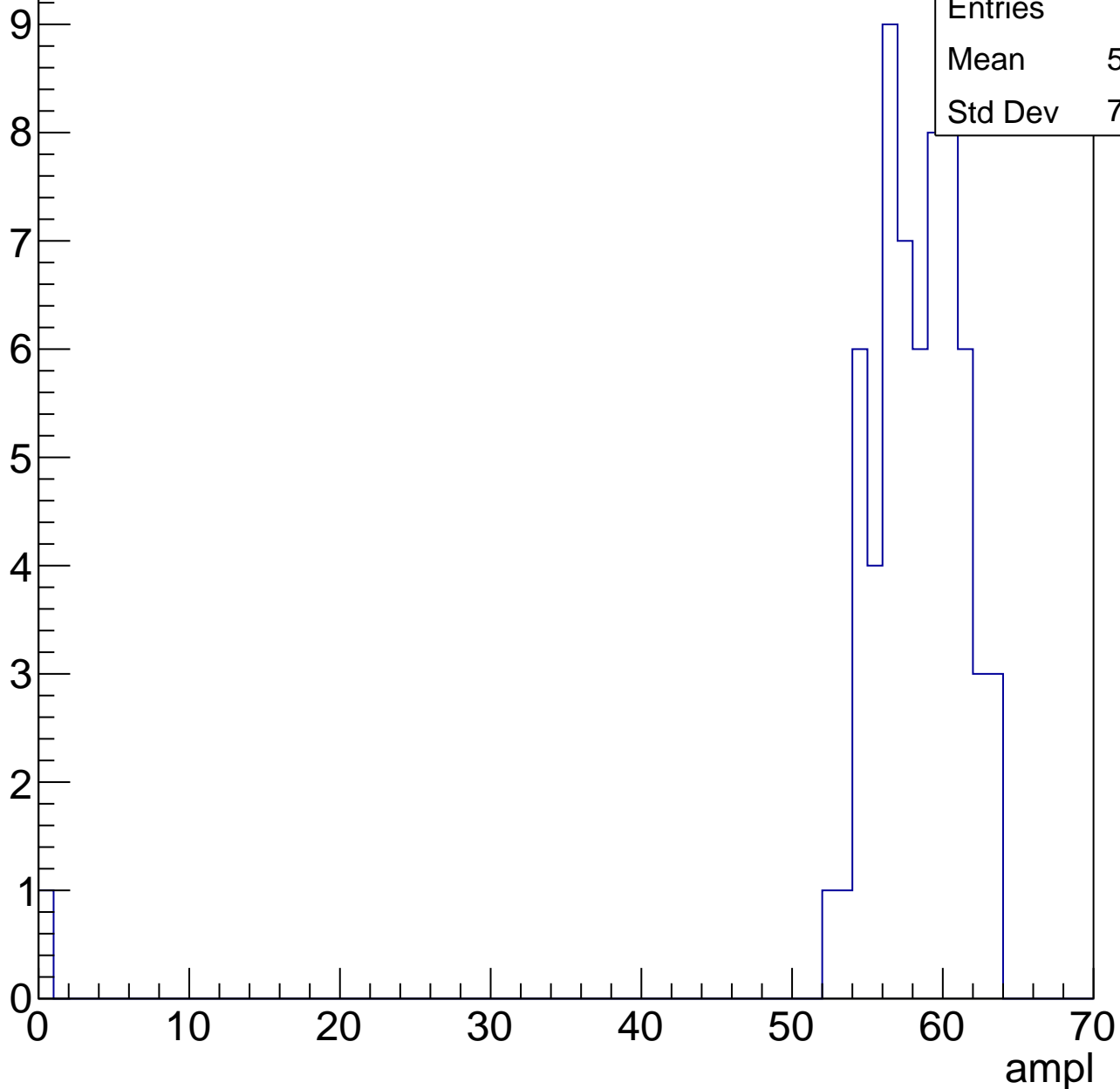
Entries	58
Mean	52.26
Std Dev	3.088



# B1L002S, U18-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

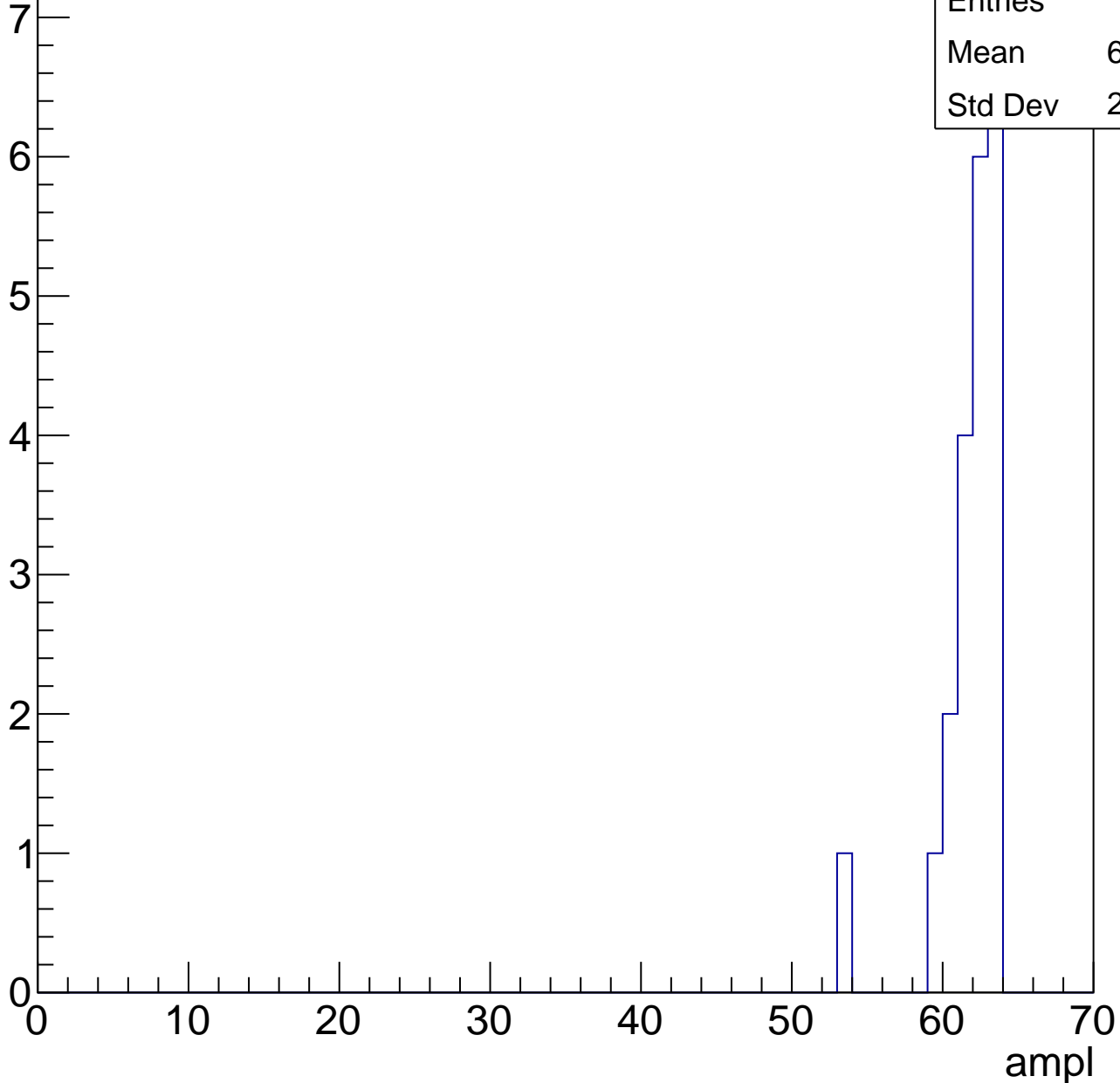


# B1L002S, U18-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.38
Std Dev	2.193





# B1L002S, U18-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch50, adc0

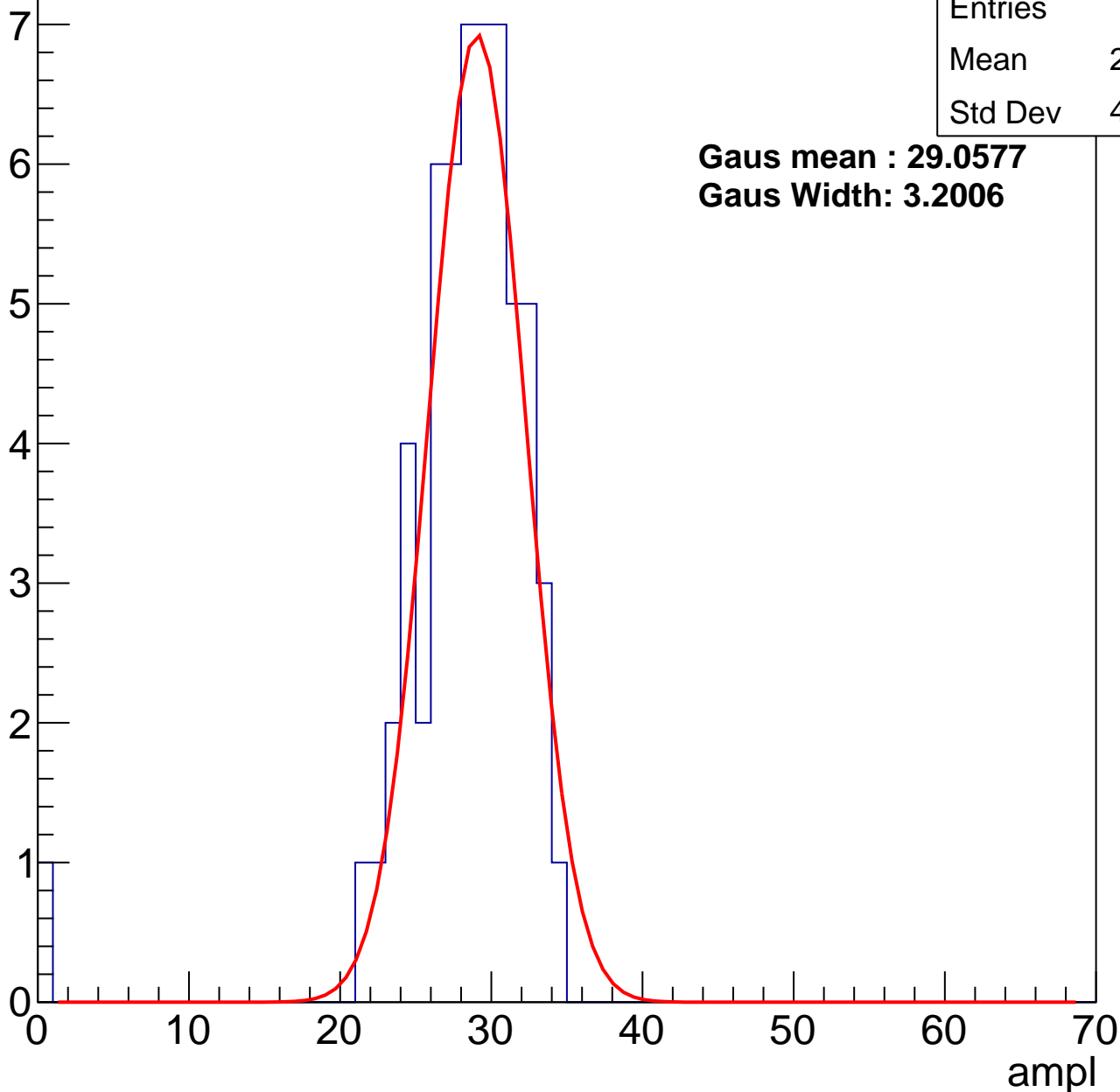
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.76
Std Dev	4.732

**Gaus mean : 29.0577**

**Gaus Width: 3.2006**



# B1L002S, U18-ch50, adc1

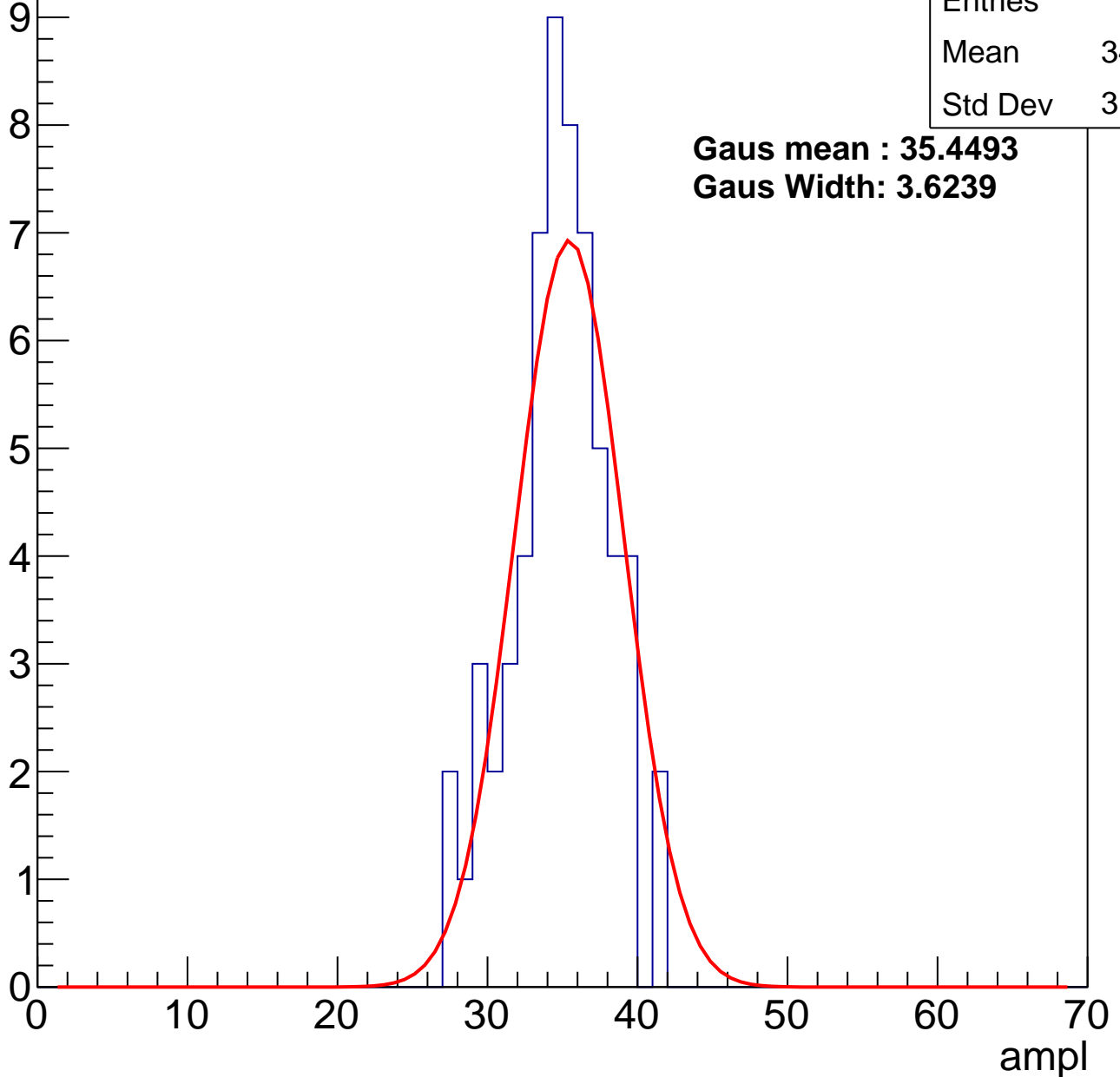
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.33
Std Dev	3.207

**Gaus mean : 35.4493**

**Gaus Width: 3.6239**



# B1L002S, U18-ch50, adc2

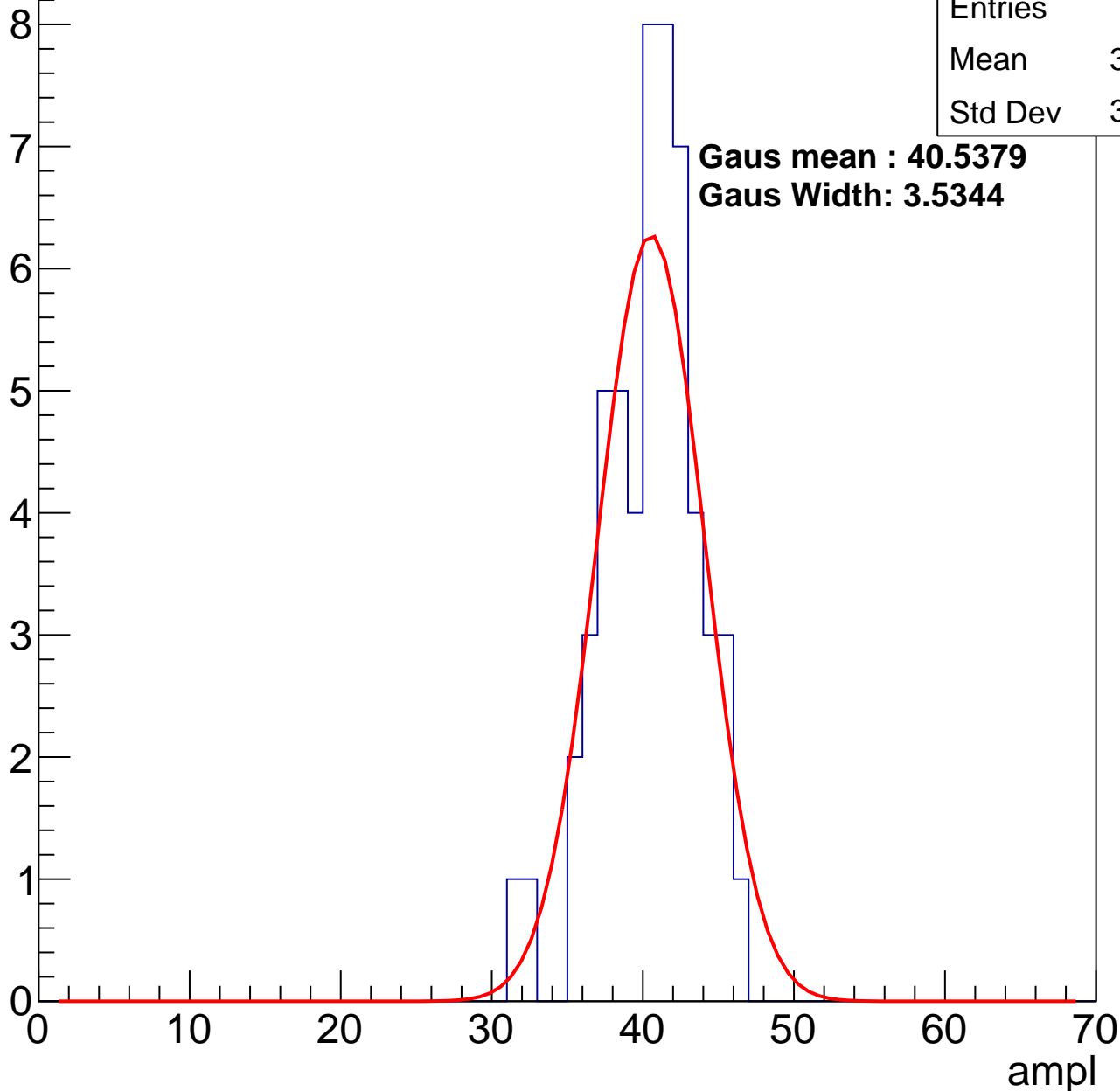
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	39.98
Std Dev	3.142

**Gaus mean : 40.5379**

**Gaus Width: 3.5344**

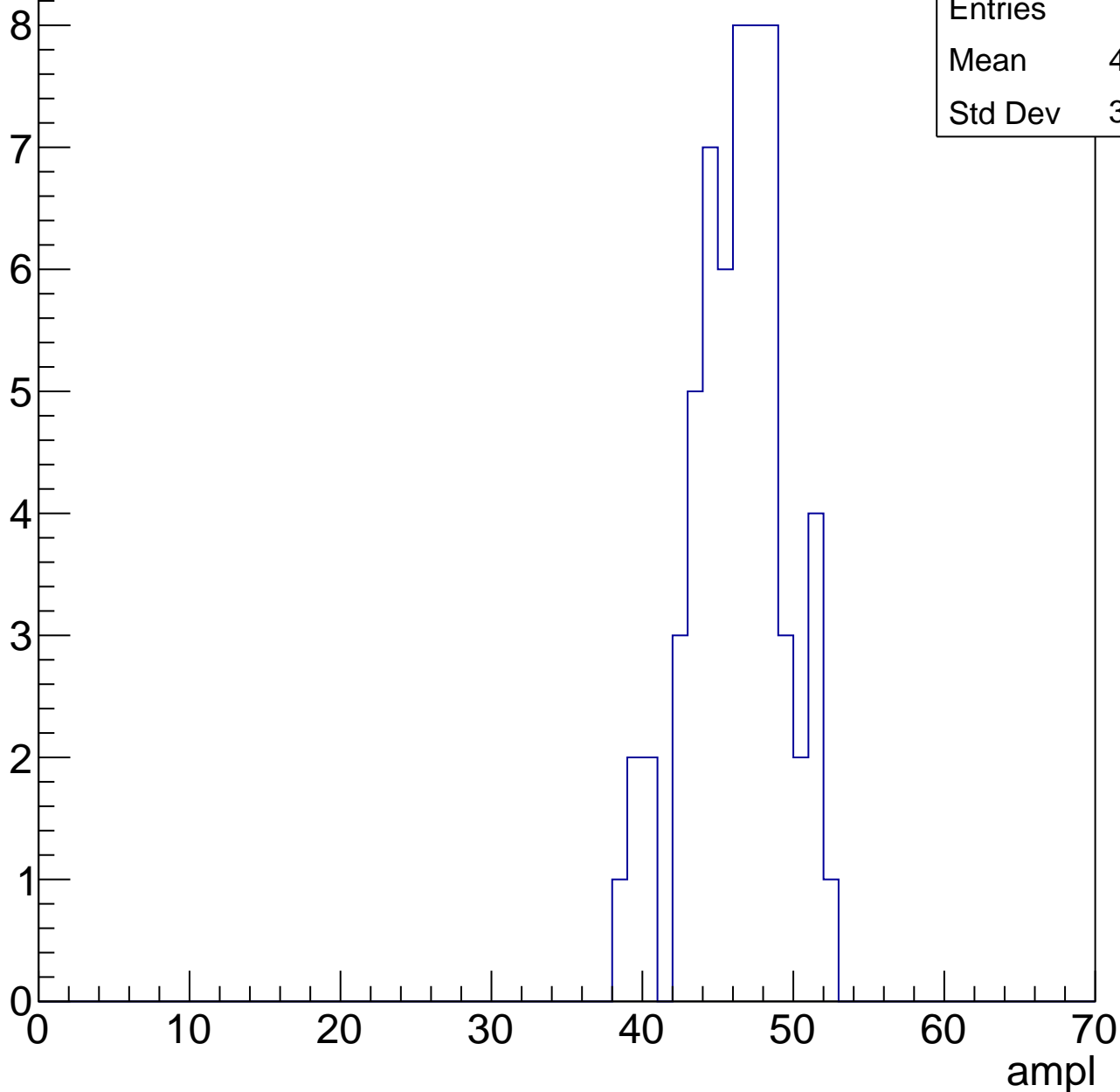


# B1L002S, U18-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.77
Std Dev	3.143

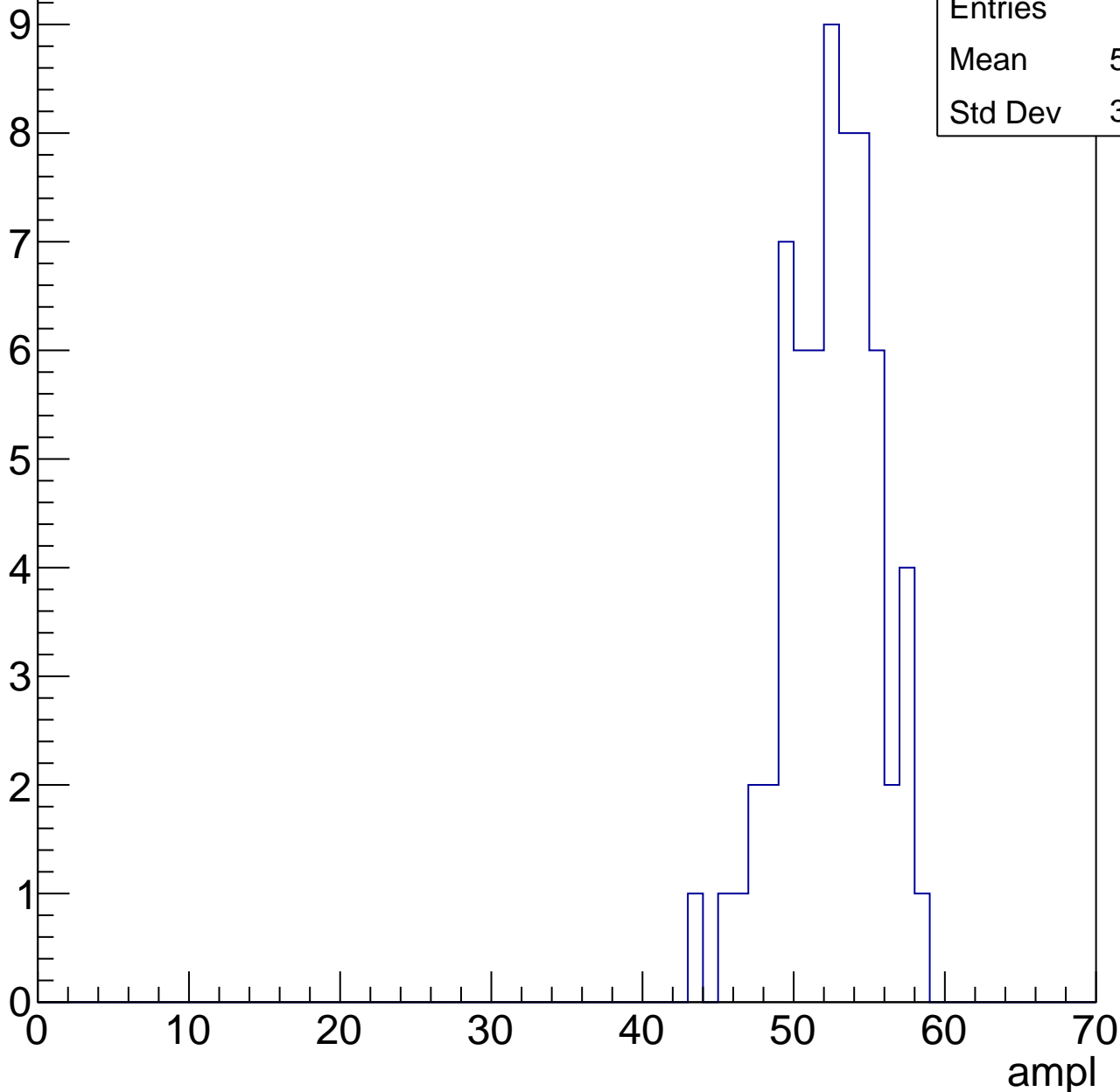


# B1L002S, U18-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

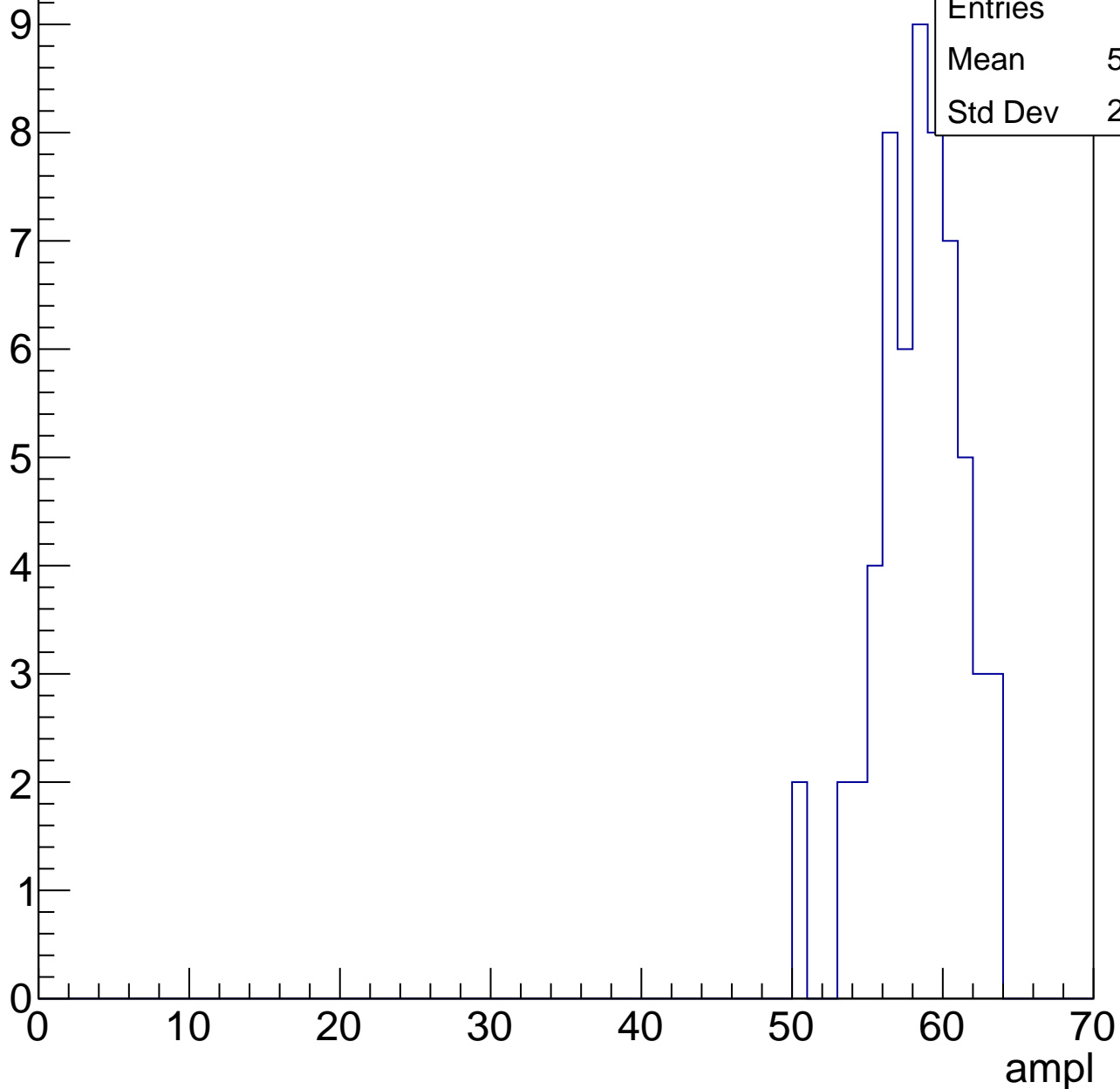
Entries	64
Mean	51.95
Std Dev	3.069



# B1L002S, U18-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

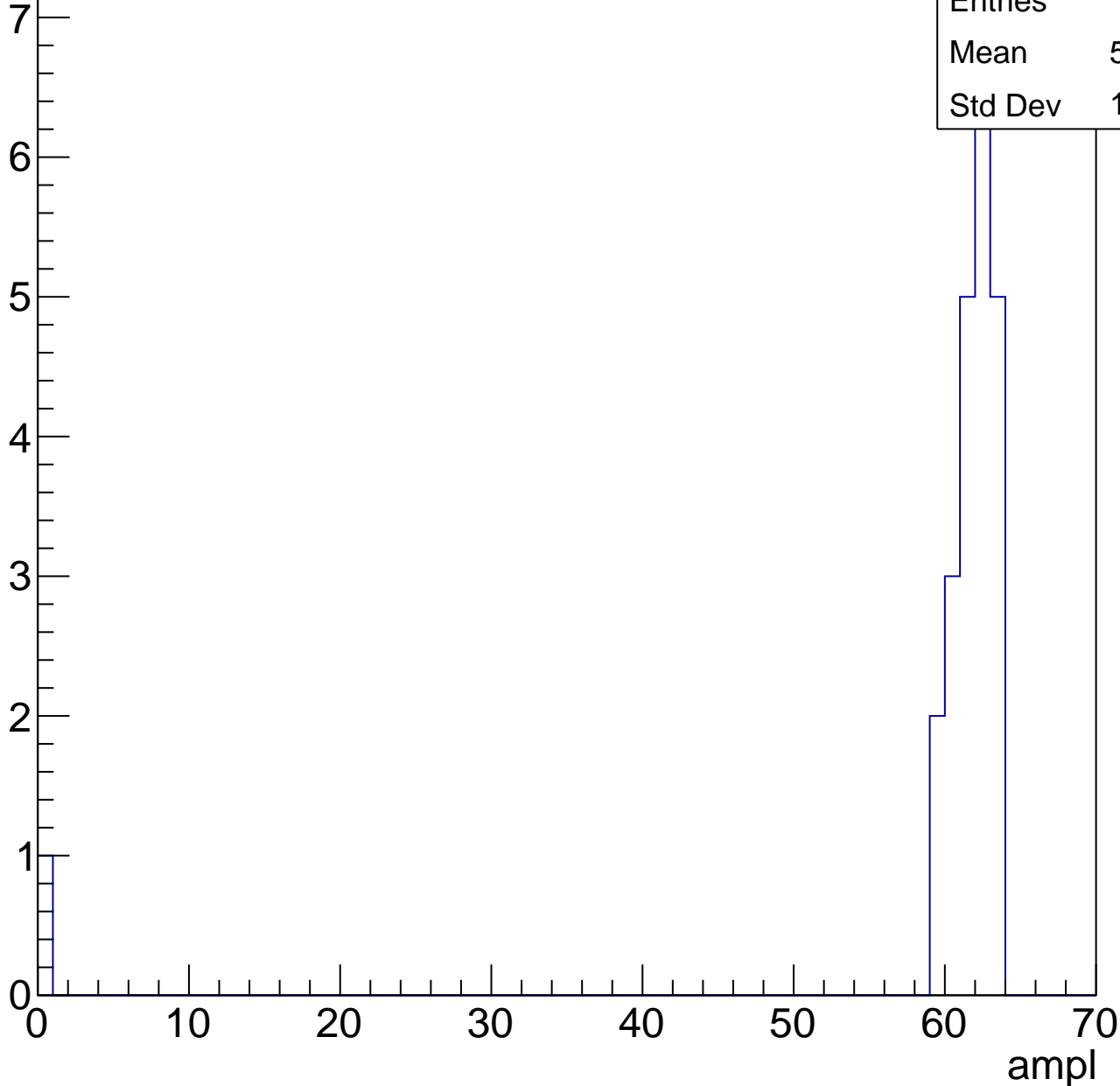


# B1L002S, U18-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.78
Std Dev	12.59





# B1L002S, U18-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch51, adc0

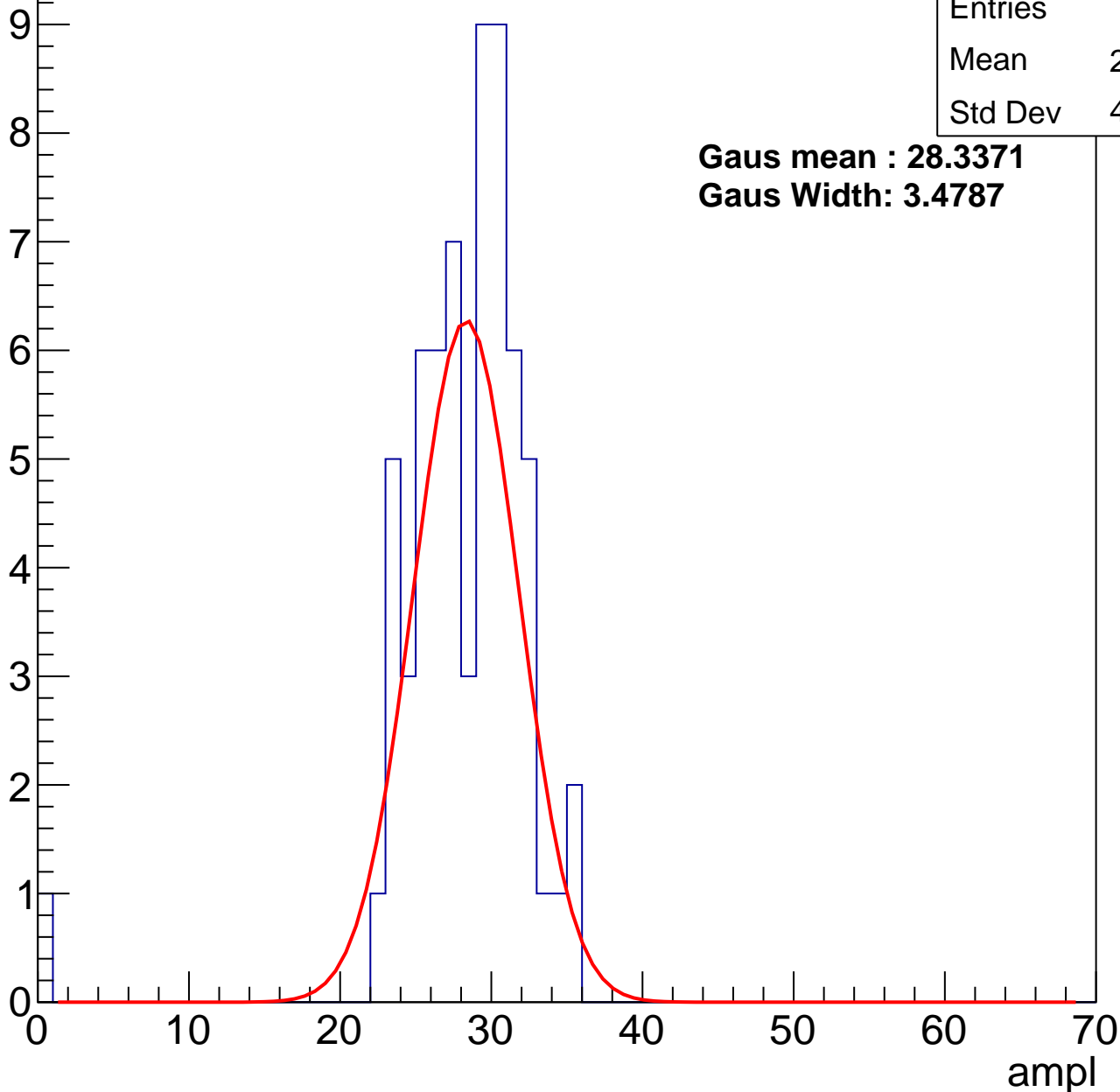
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.72
Std Dev	4.659

**Gaus mean : 28.3371**

**Gaus Width: 3.4787**



# B1L002S, U18-ch51, adc1

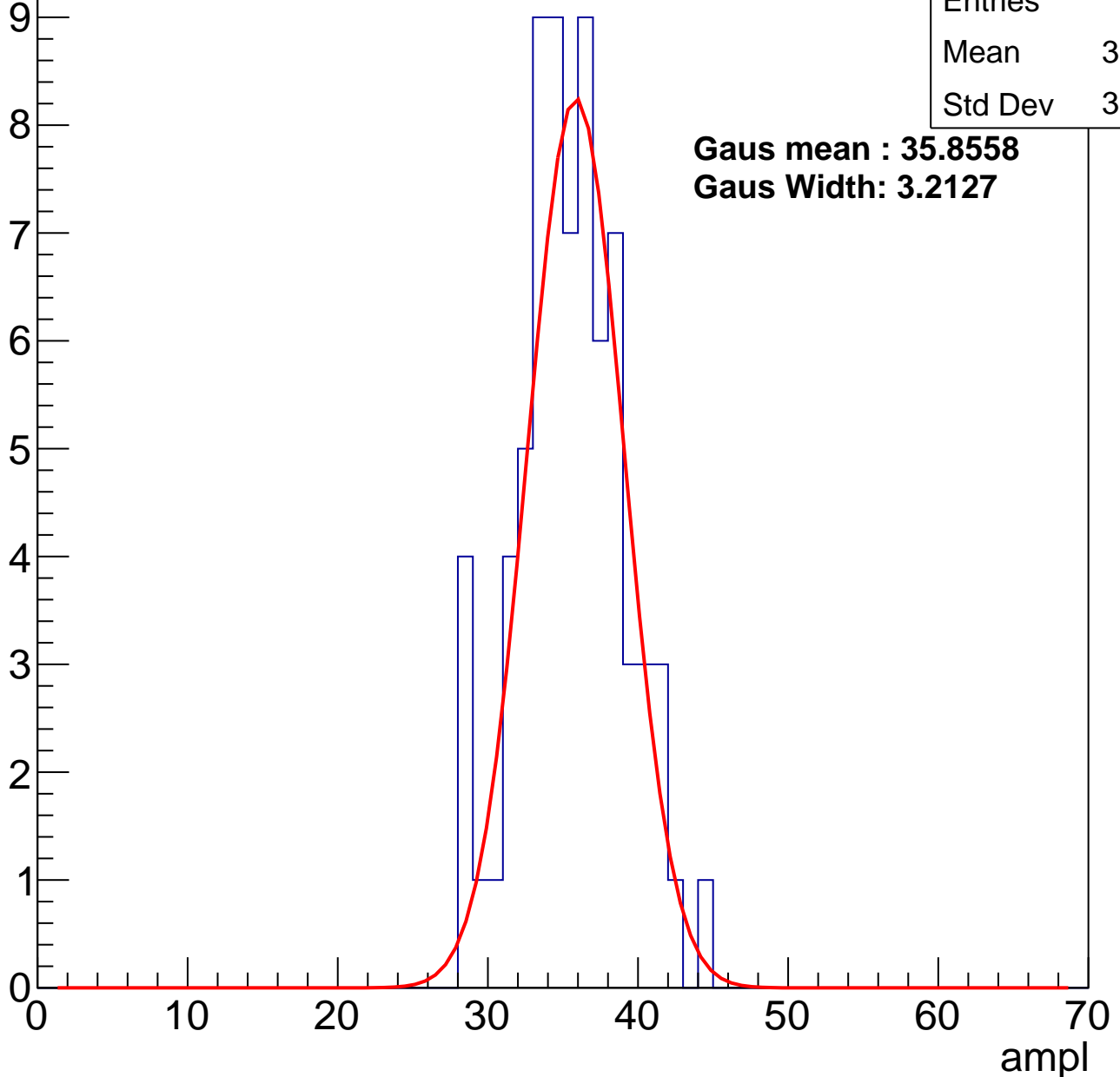
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	35.08
Std Dev	3.447

**Gaus mean : 35.8558**

**Gaus Width: 3.2127**



# B1L002S, U18-ch51, adc2

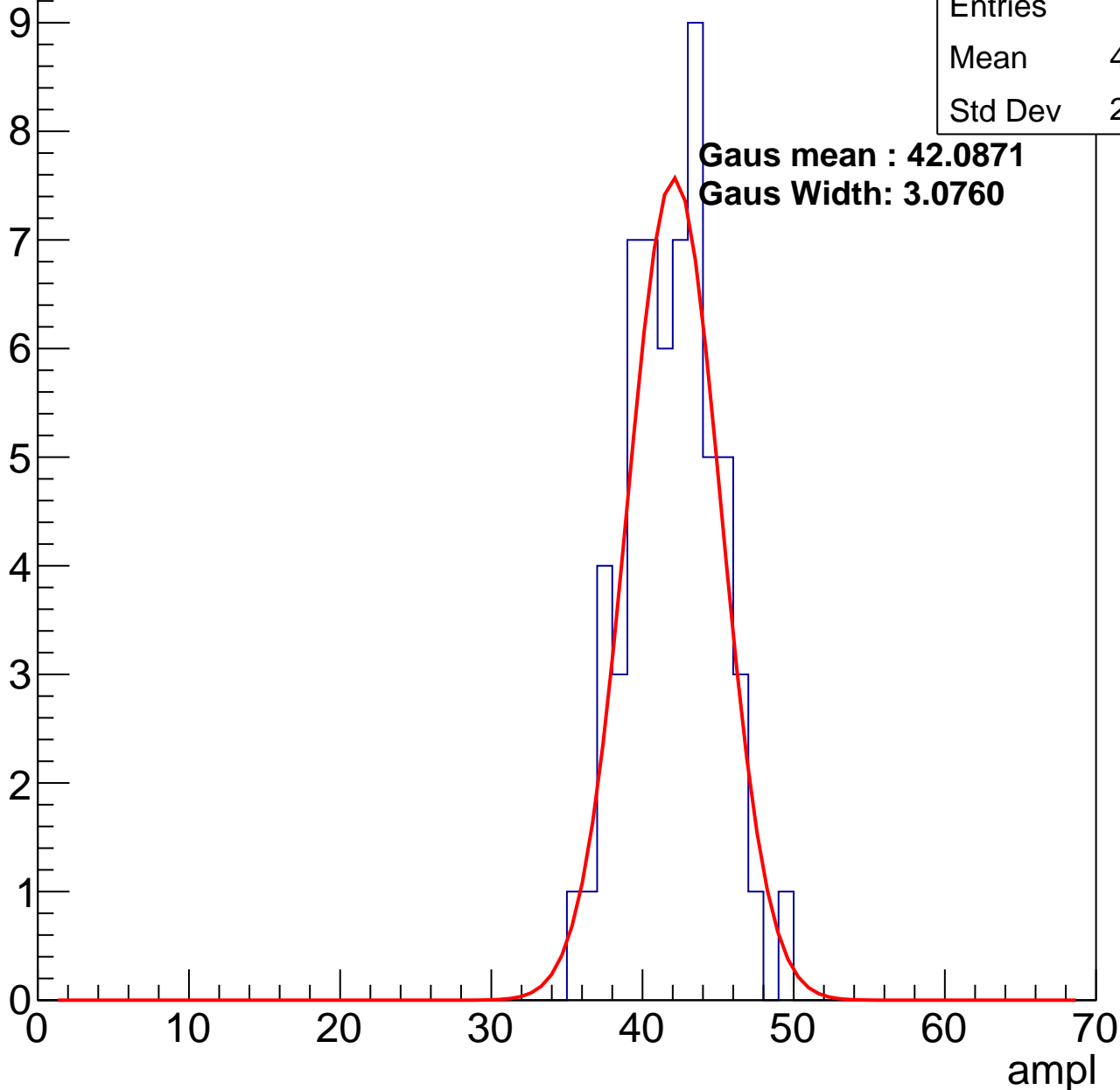
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.53
Std Dev	2.924

**Gaus mean : 42.0871**

**Gaus Width: 3.0760**

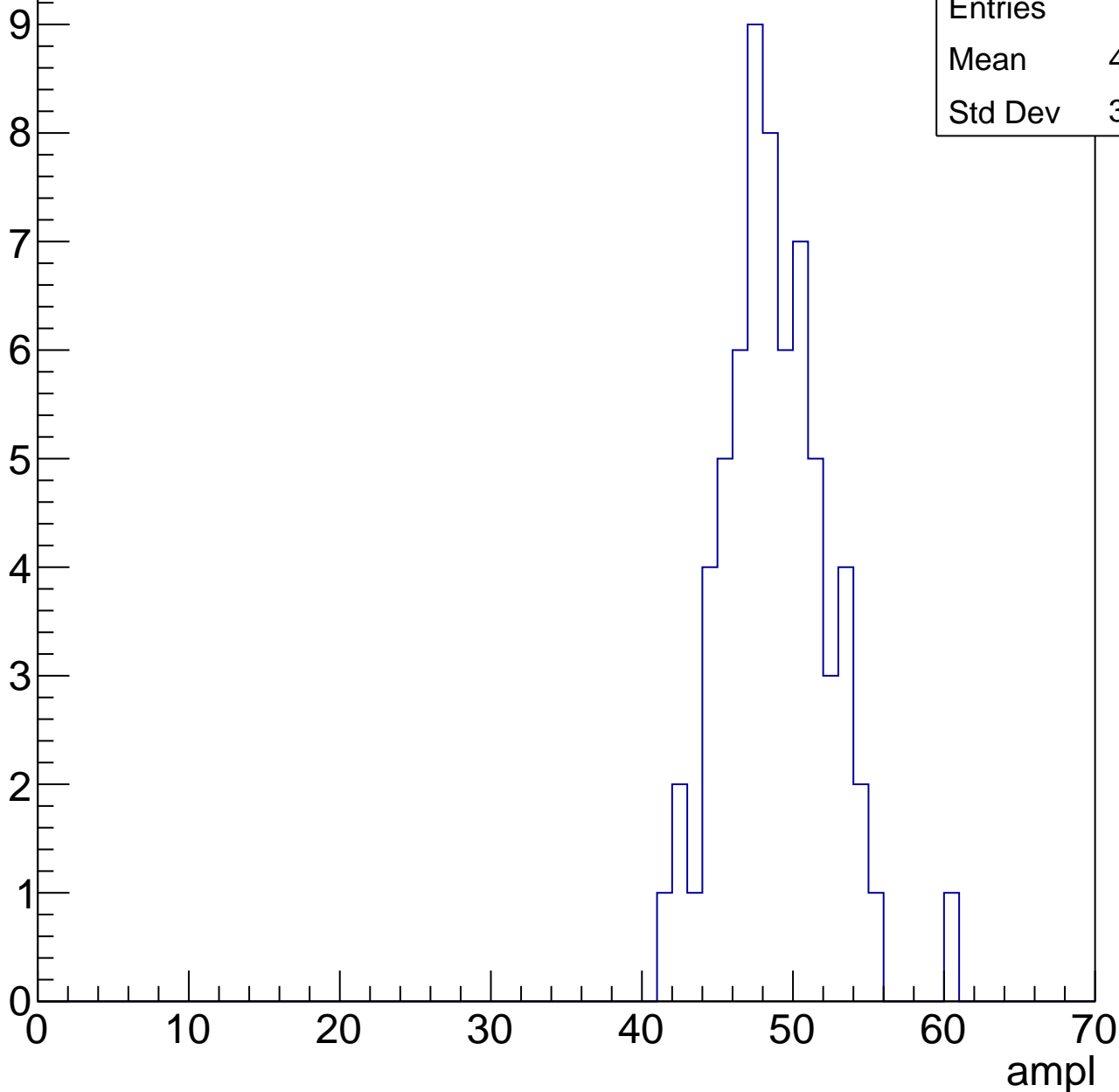


# B1L002S, U18-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	48.34
Std Dev	3.448

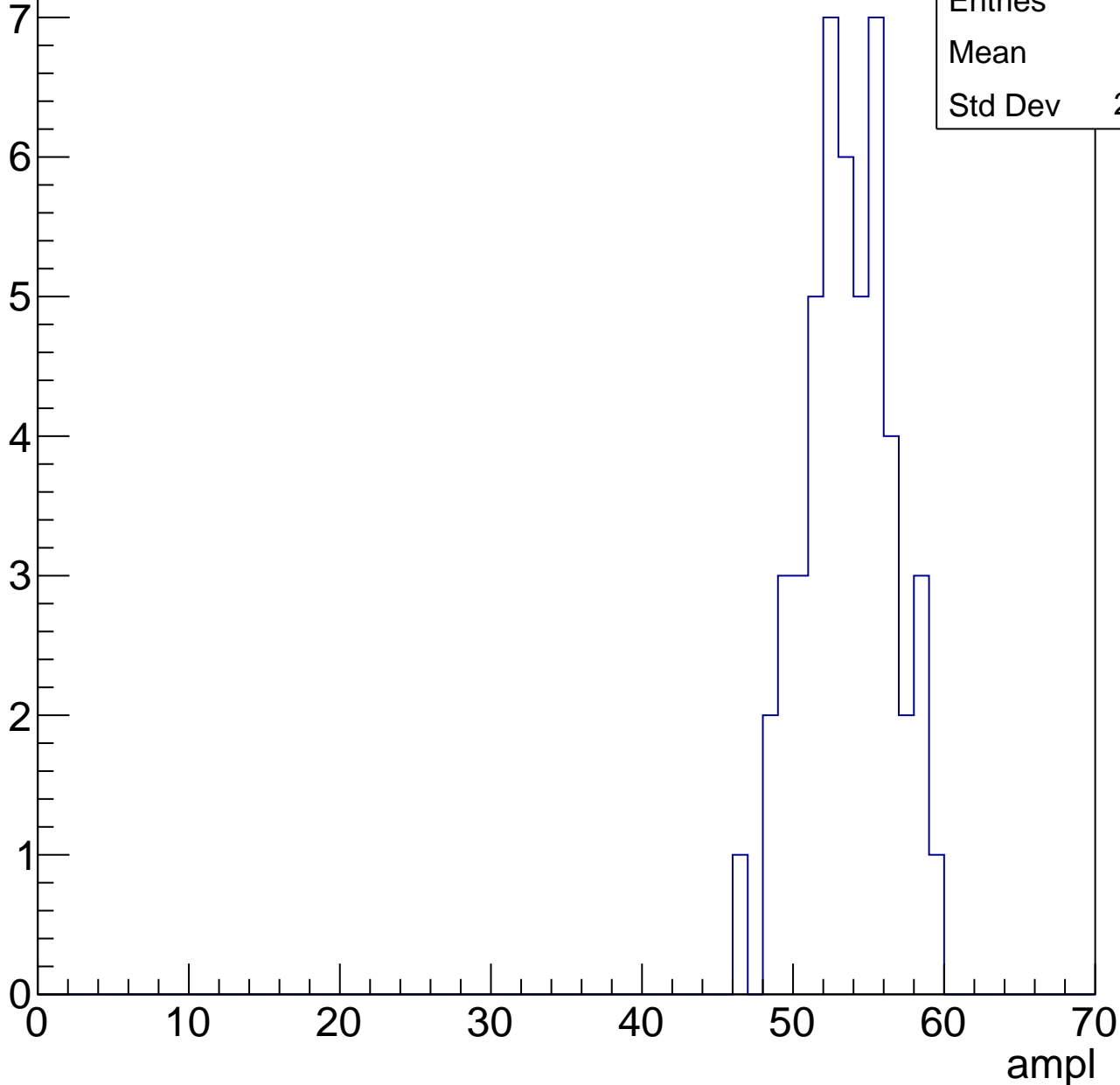


# B1L002S, U18-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	53.1
Std Dev	2.901



# B1L002S, U18-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

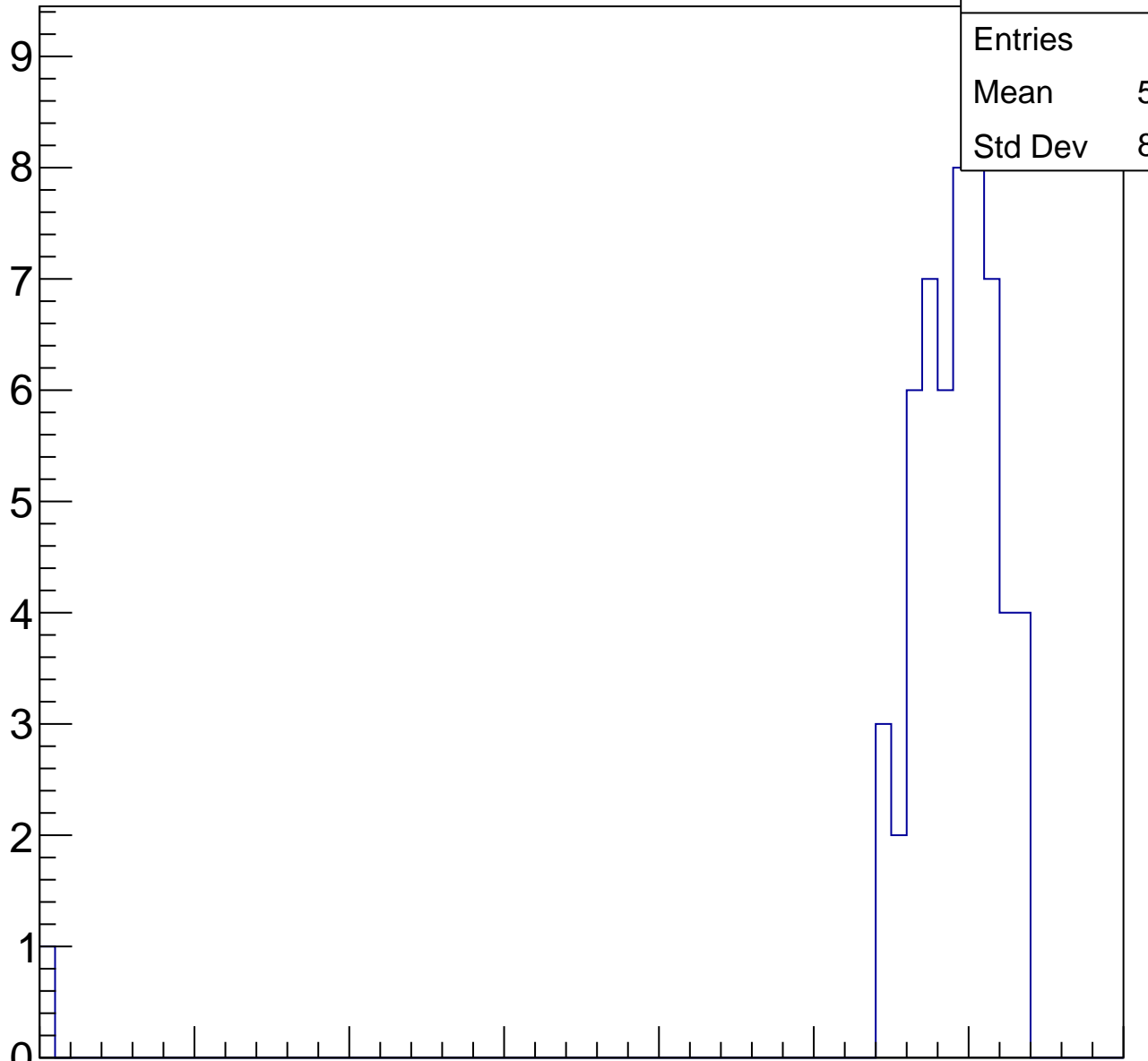
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	57.79
Std Dev	8.089

ampl

0 10 20 30 40 50 60 70

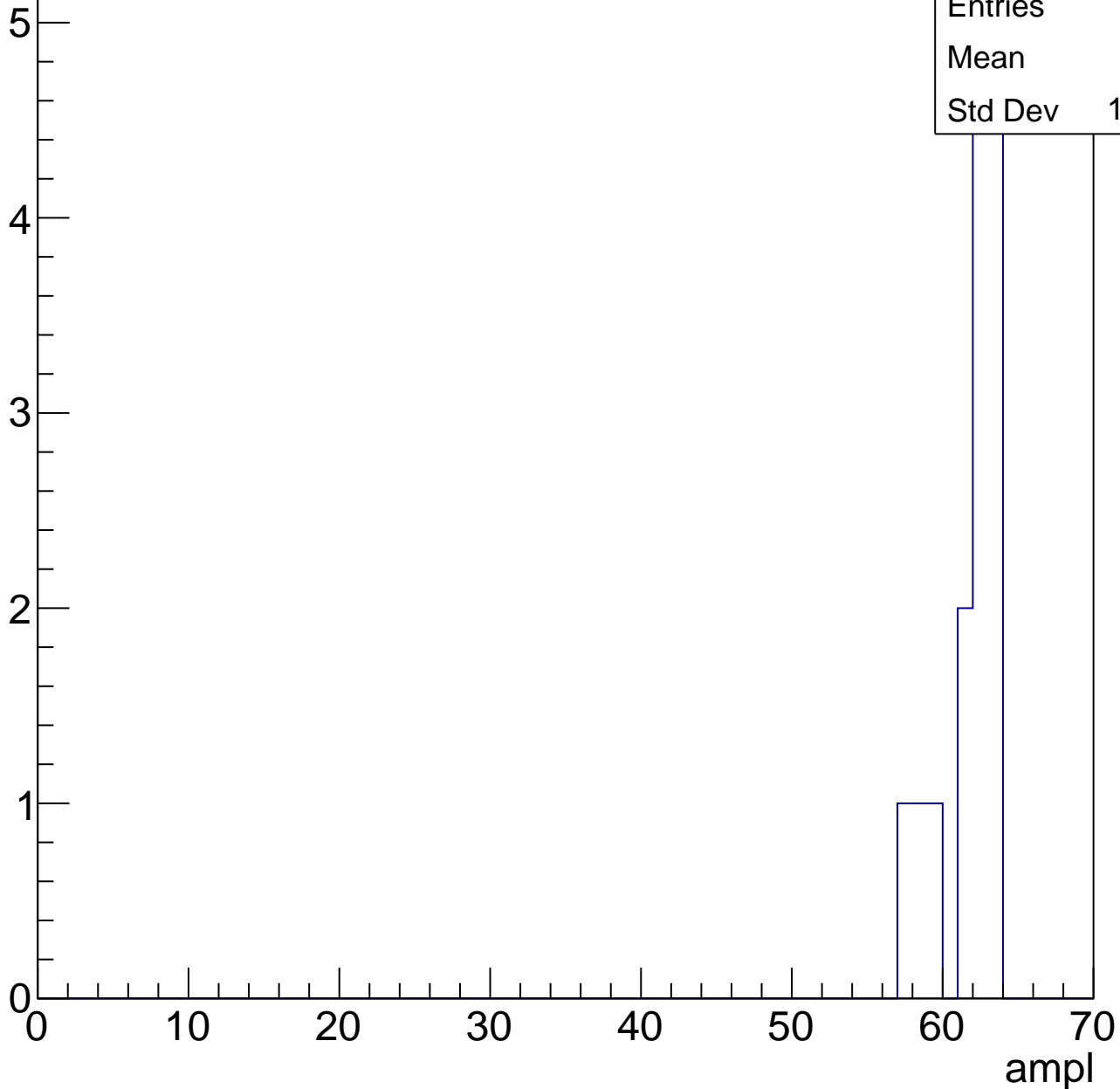


# B1L002S, U18-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	61.4
Std Dev	1.855

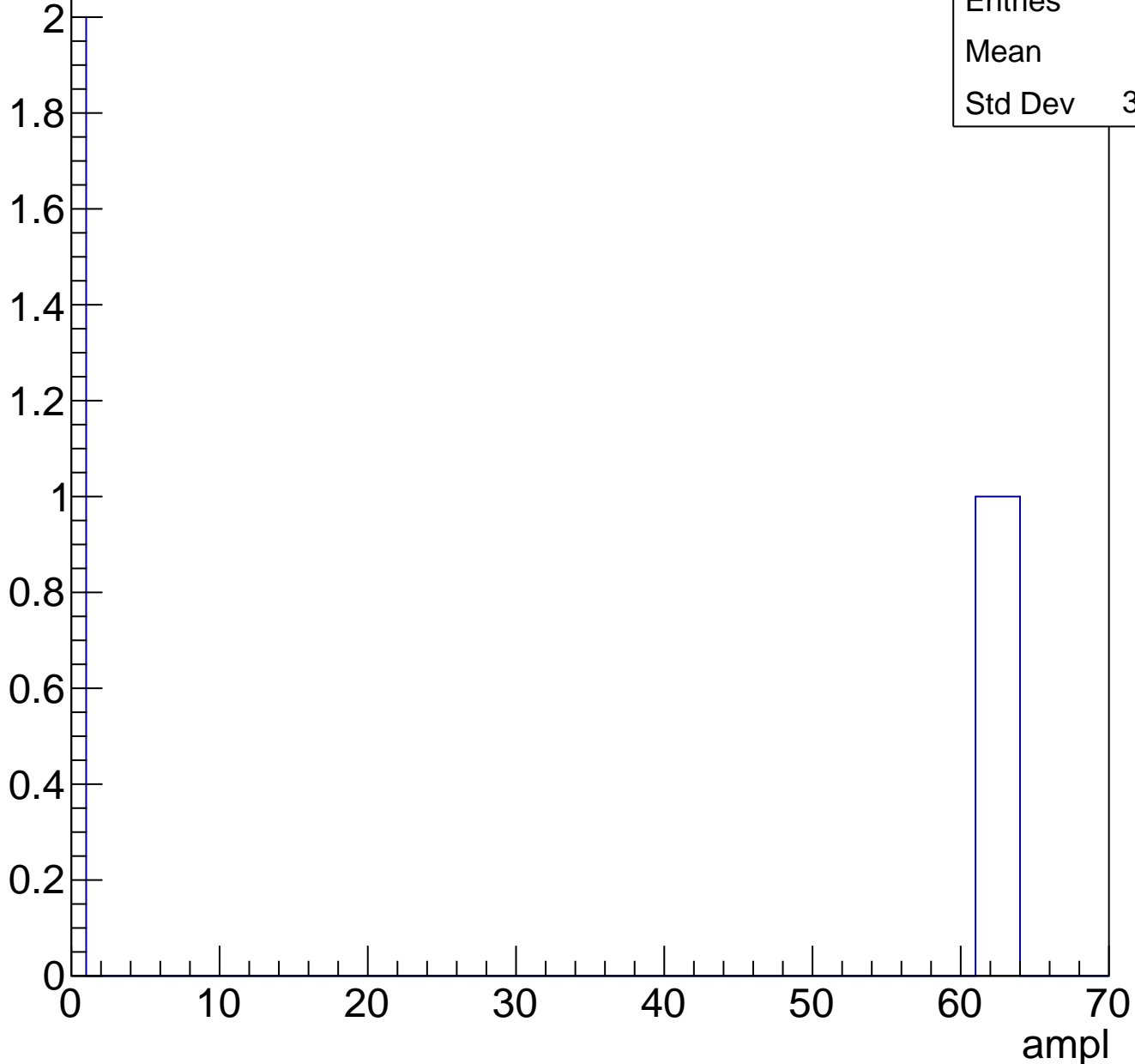




# B1L002S, U18-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	37.2
Std Dev	30.38

# B1L002S, U18-ch52, adc0

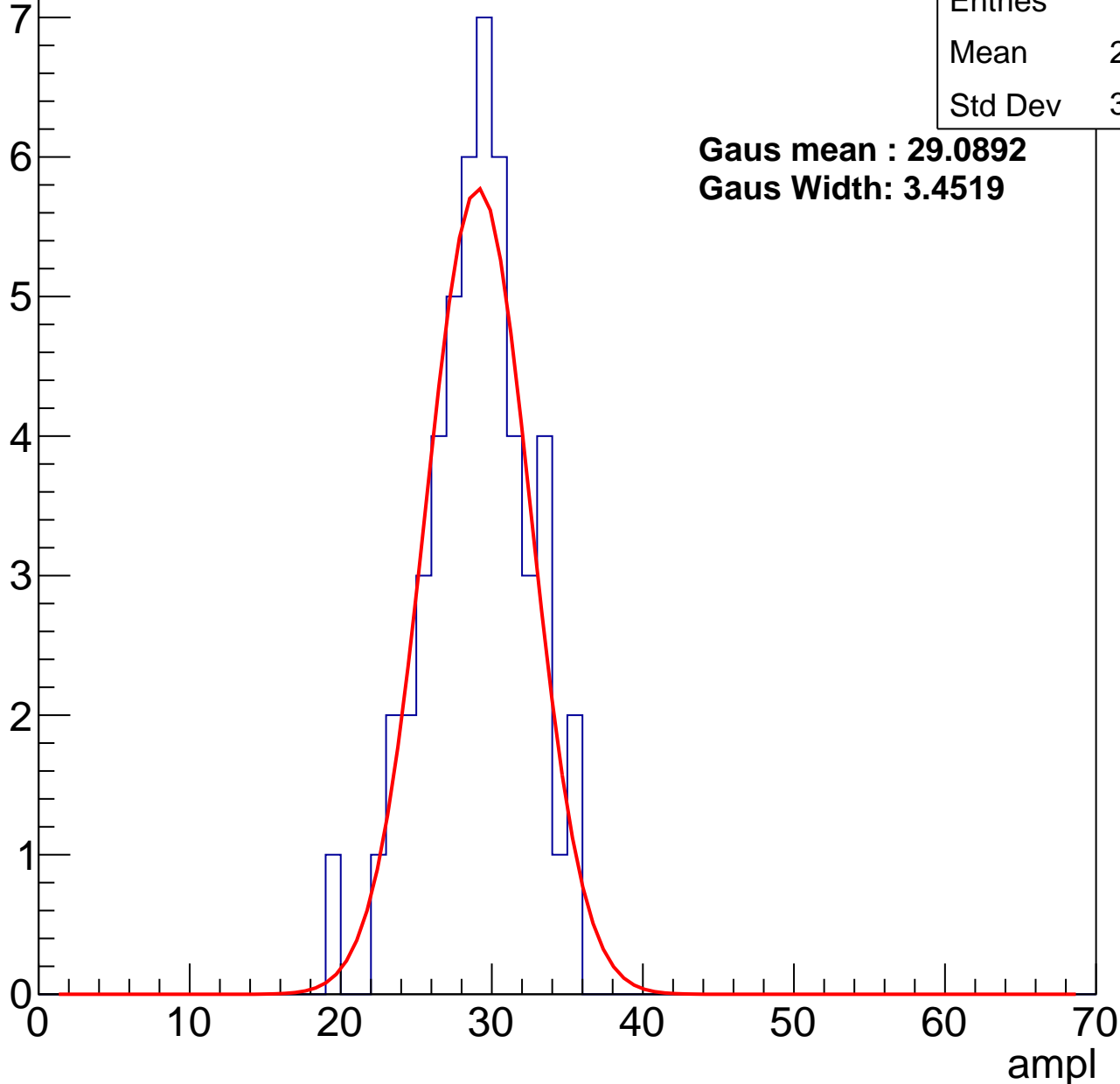
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	28.55
Std Dev	3.374

**Gaus mean : 29.0892**

**Gaus Width: 3.4519**



# B1L002S, U18-ch52, adc1

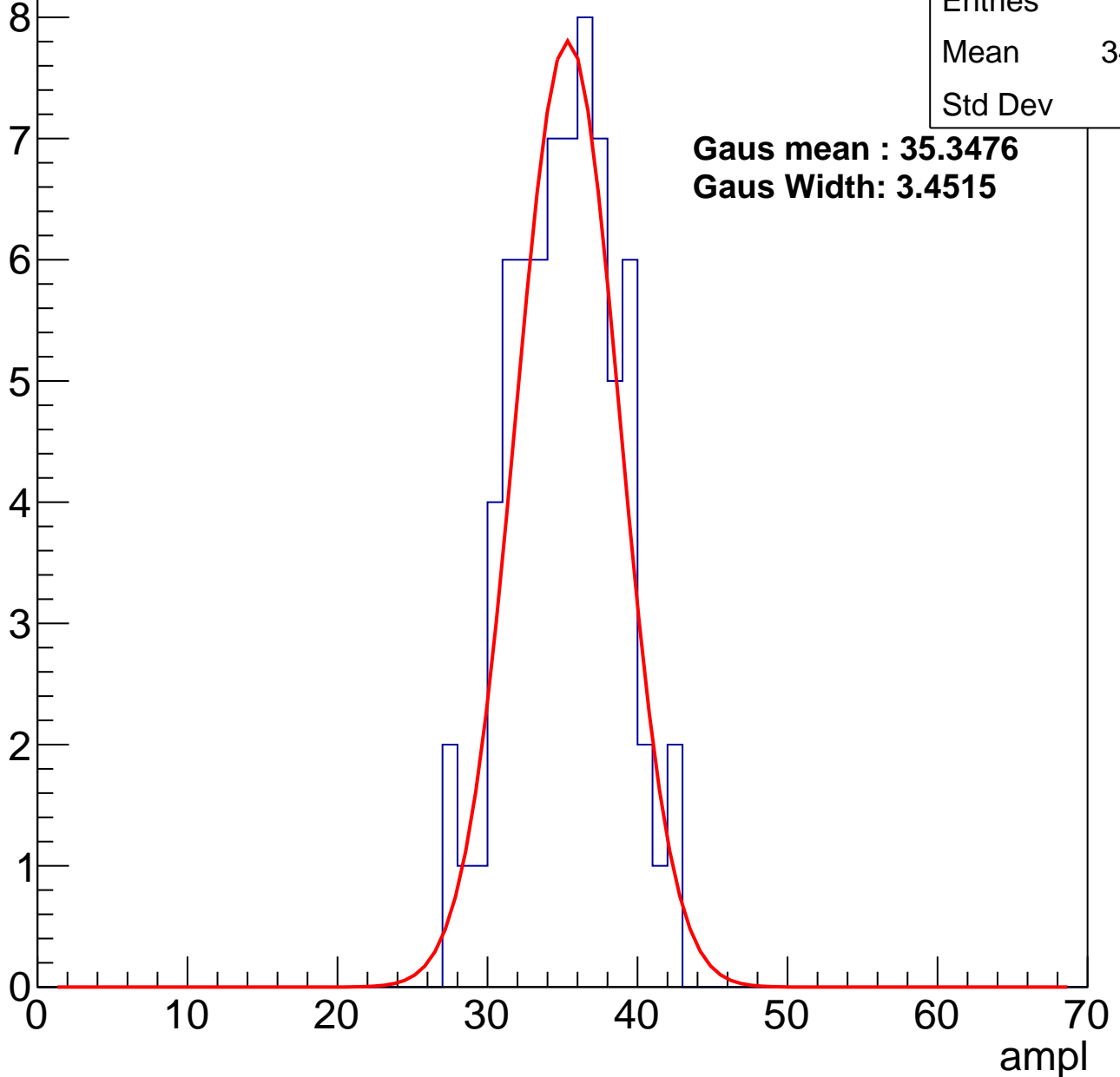
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.73
Std Dev	3.46

**Gaus mean : 35.3476**

**Gaus Width: 3.4515**



# B1L002S, U18-ch52, adc2

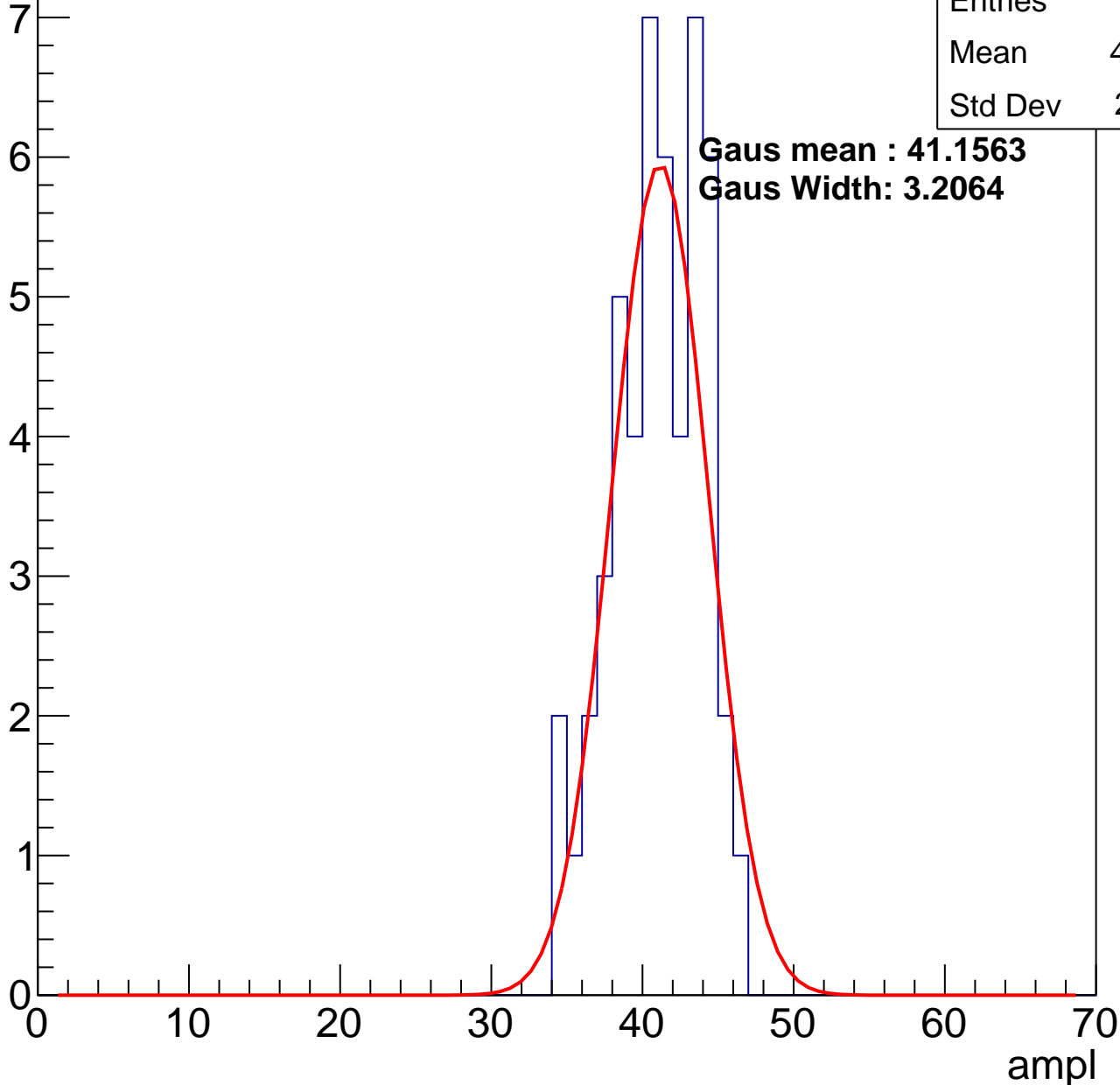
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.54
Std Dev	2.941

**Gaus mean : 41.1563**

**Gaus Width: 3.2064**

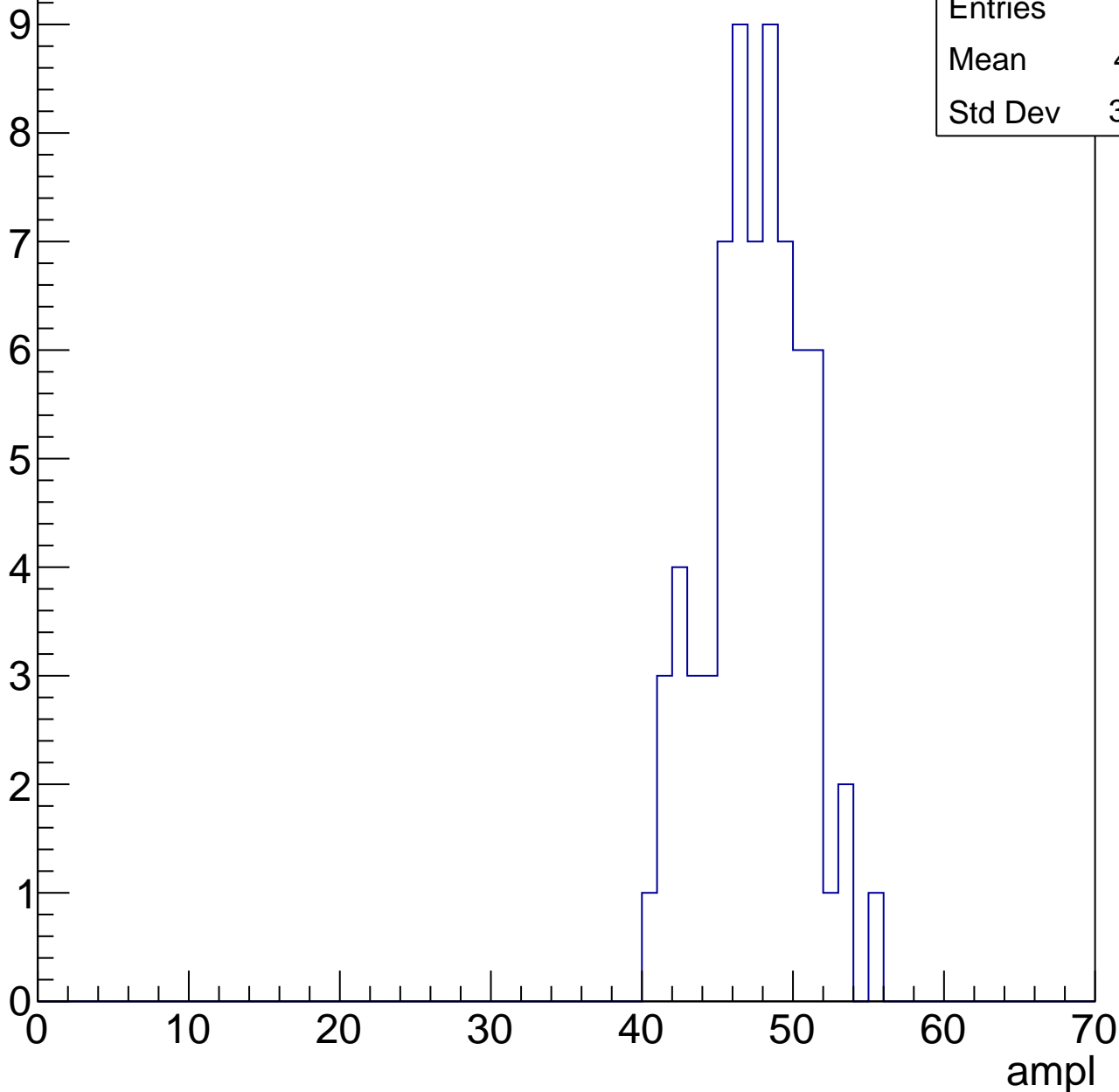


# B1L002S, U18-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

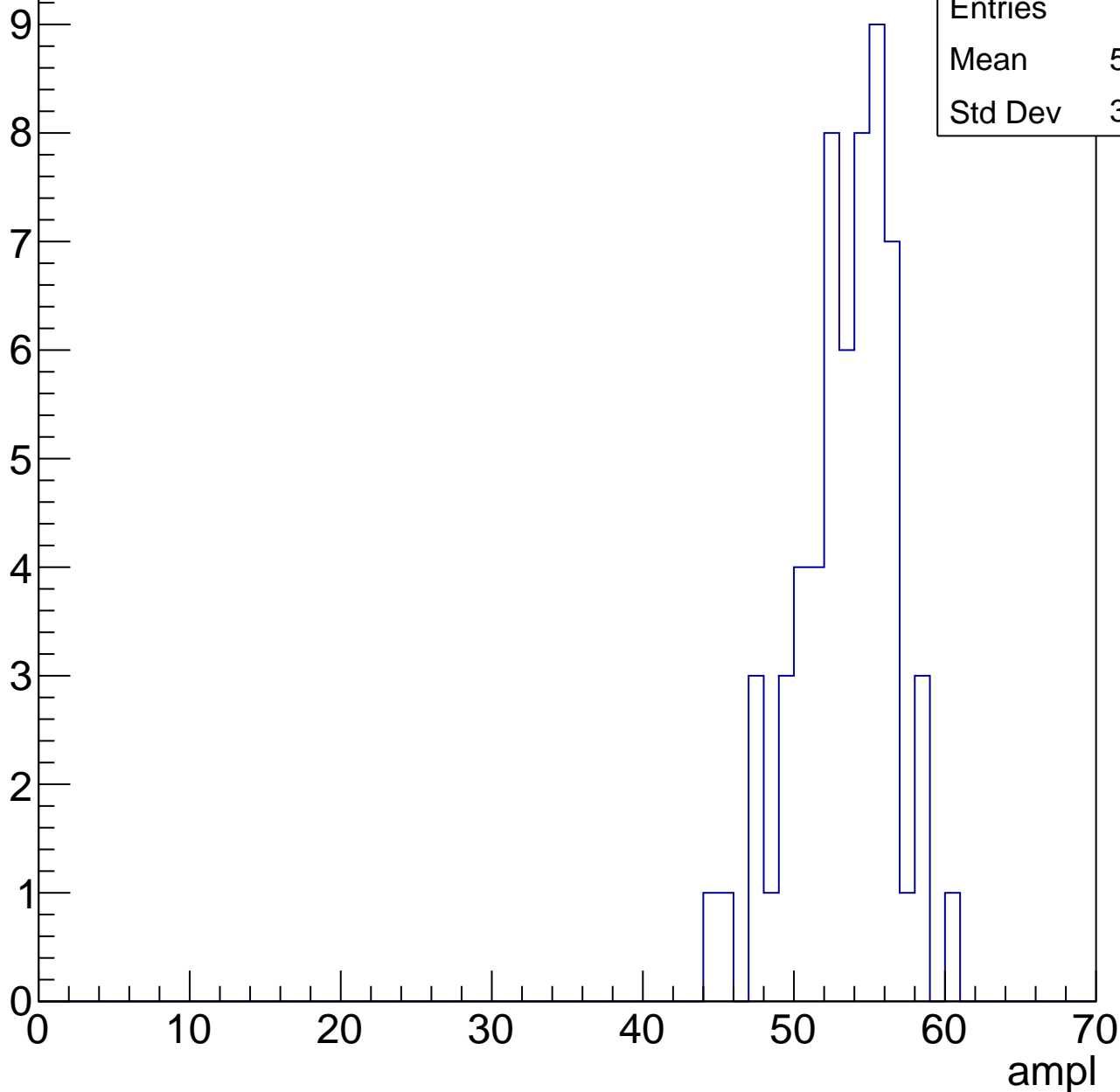
Entries	69
Mean	47.01
Std Dev	3.224



# B1L002S, U18-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

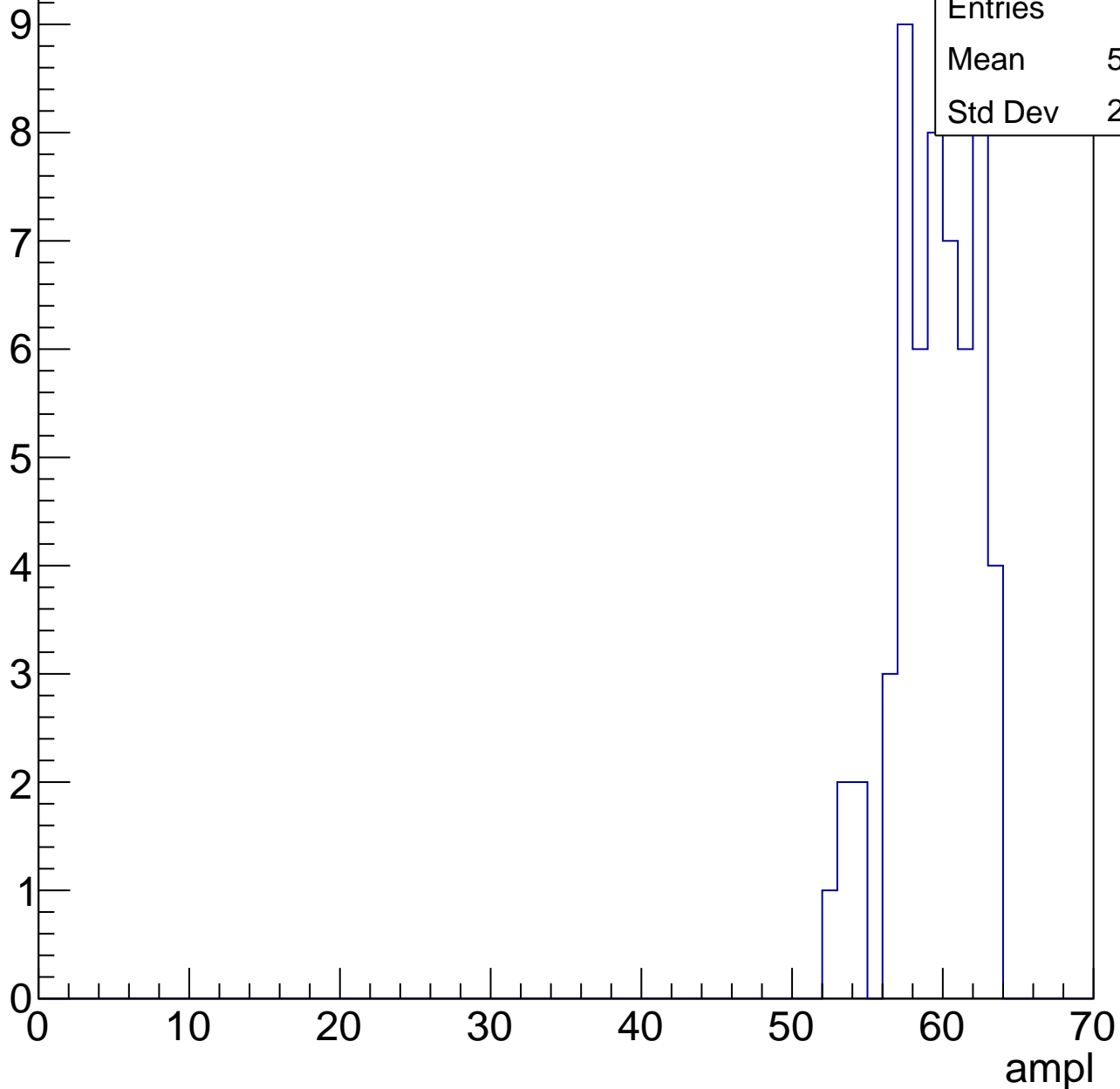
Entry



# B1L002S, U18-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

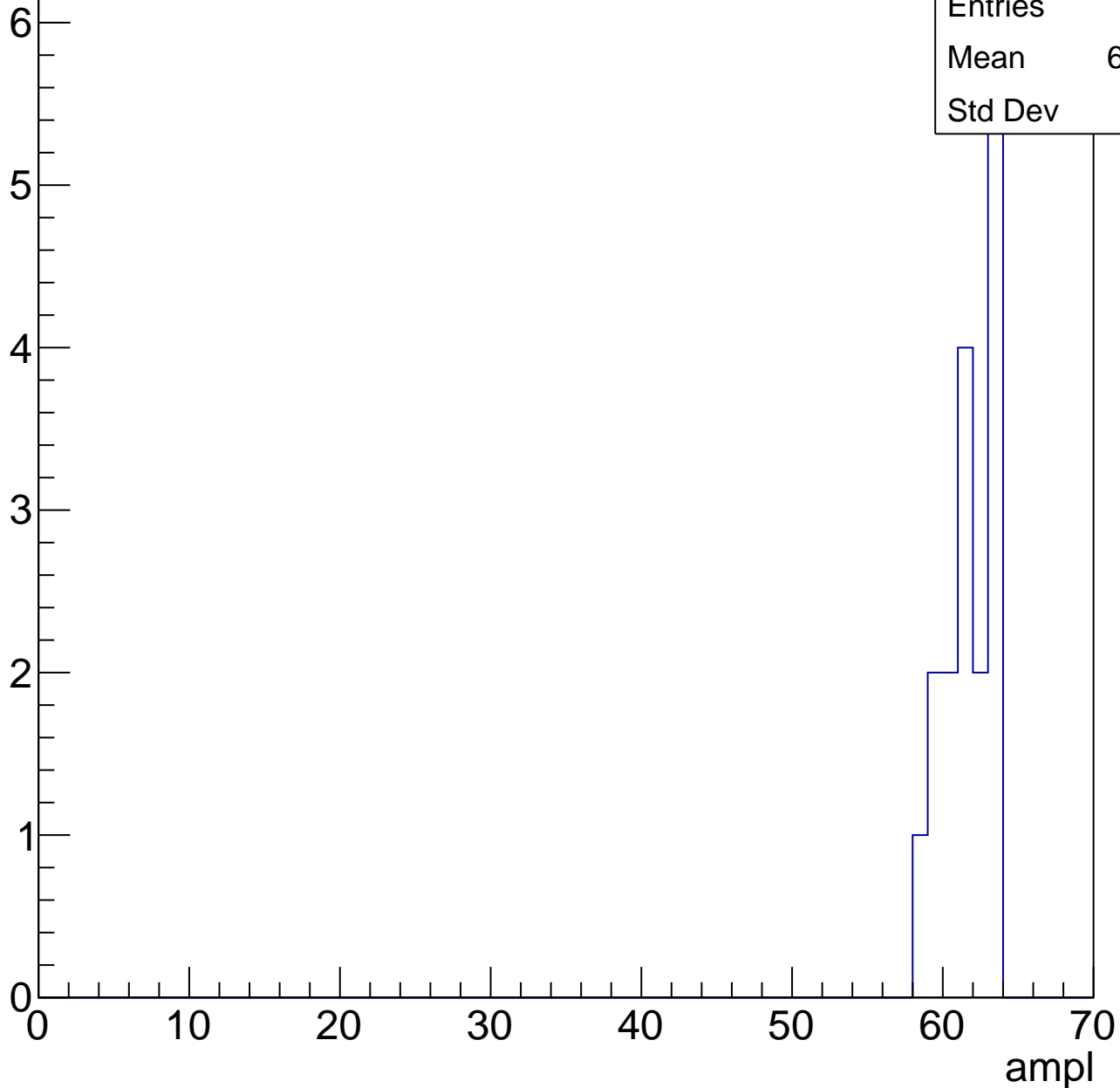
Entry



# B1L002S, U18-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U18-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	9
Std Dev	9

# B1L002S, U18-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	87
Mean	29.08
Std Dev	3.452

**Gaus mean : 29.7804**

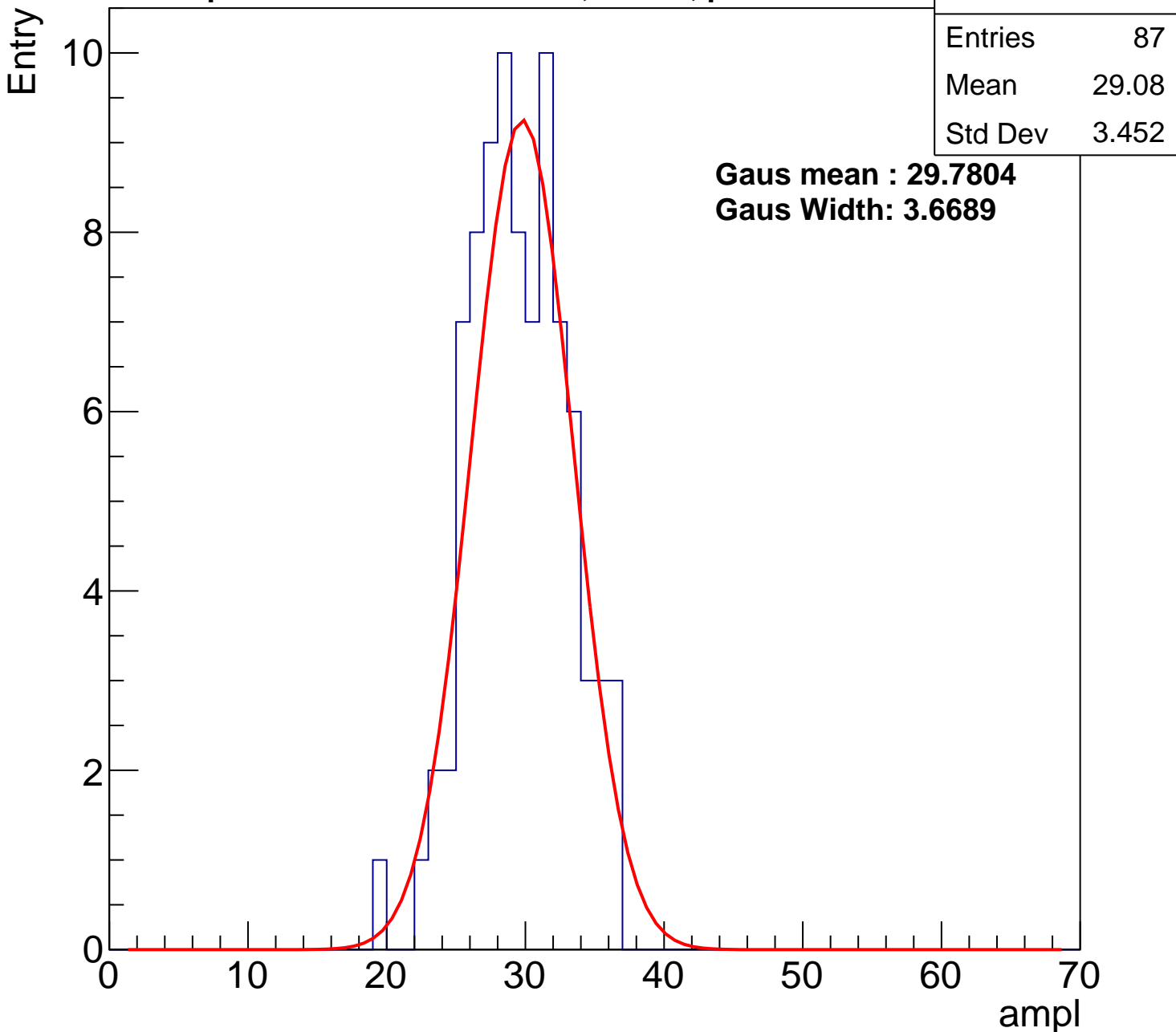
**Gaus Width: 3.6689**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch53, adc1

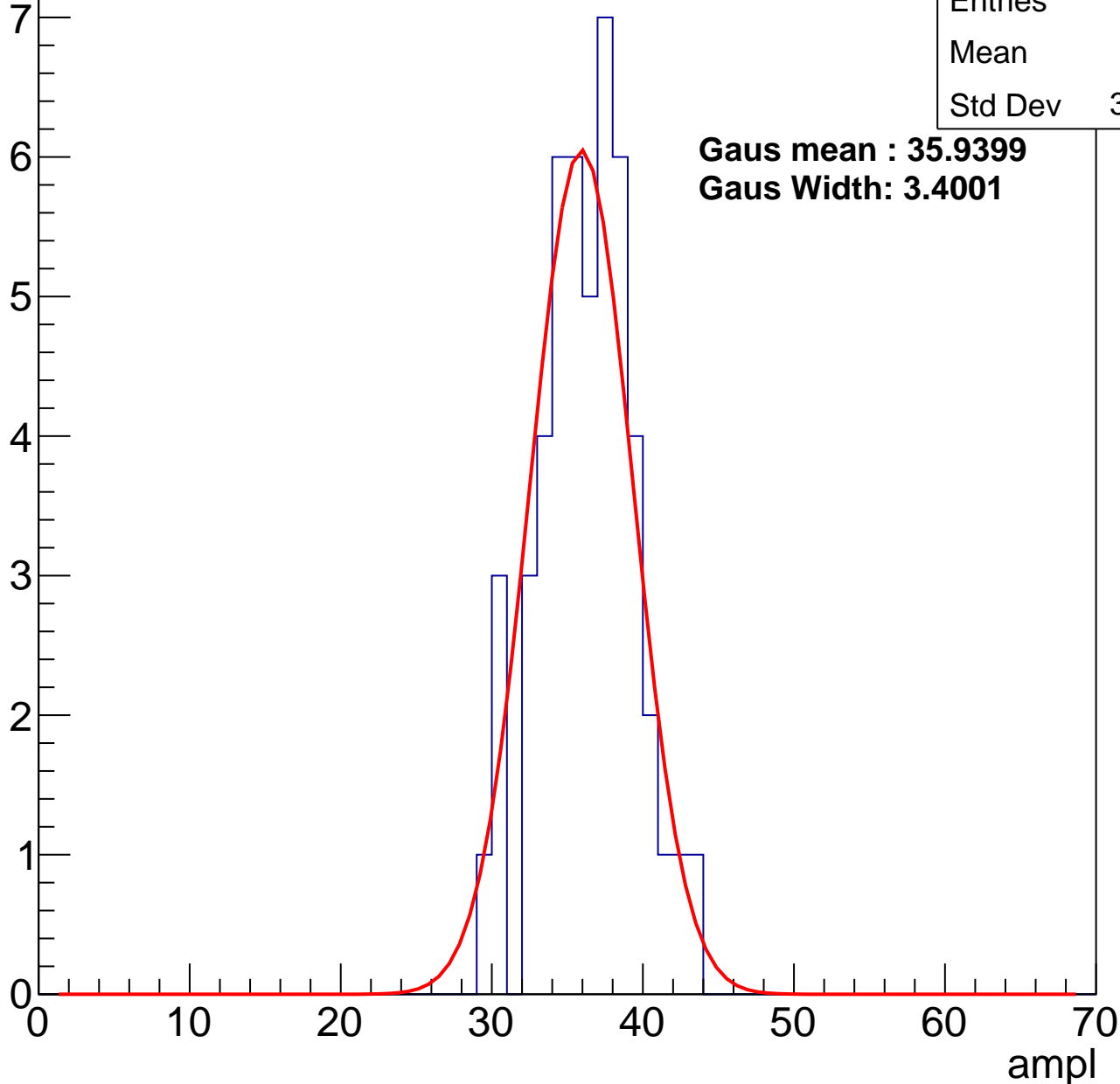
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	35.8
Std Dev	3.092

**Gaus mean : 35.9399**

**Gaus Width: 3.4001**



# B1L002S, U18-ch53, adc2

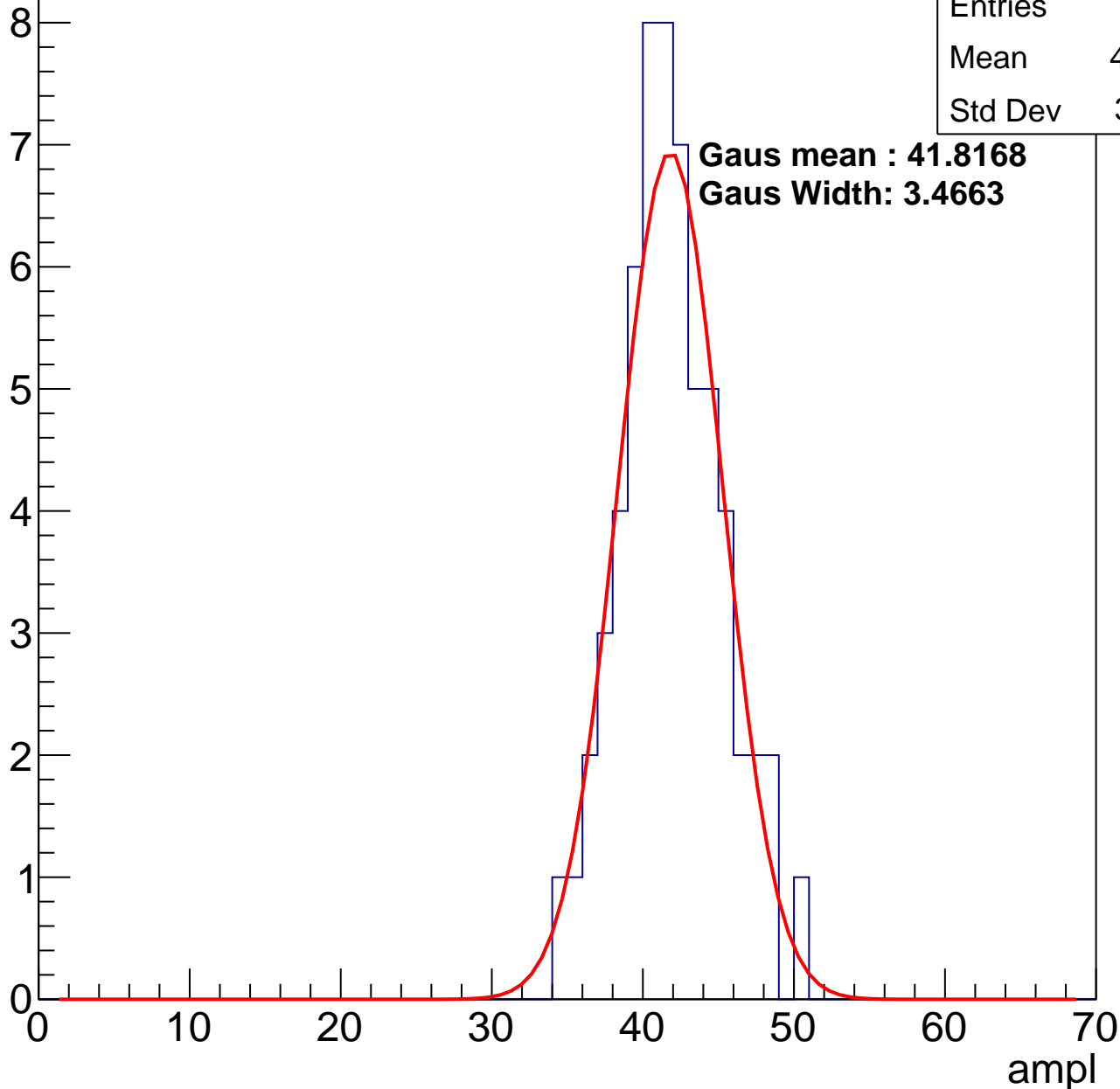
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.43
Std Dev	3.331

**Gaus mean : 41.8168**

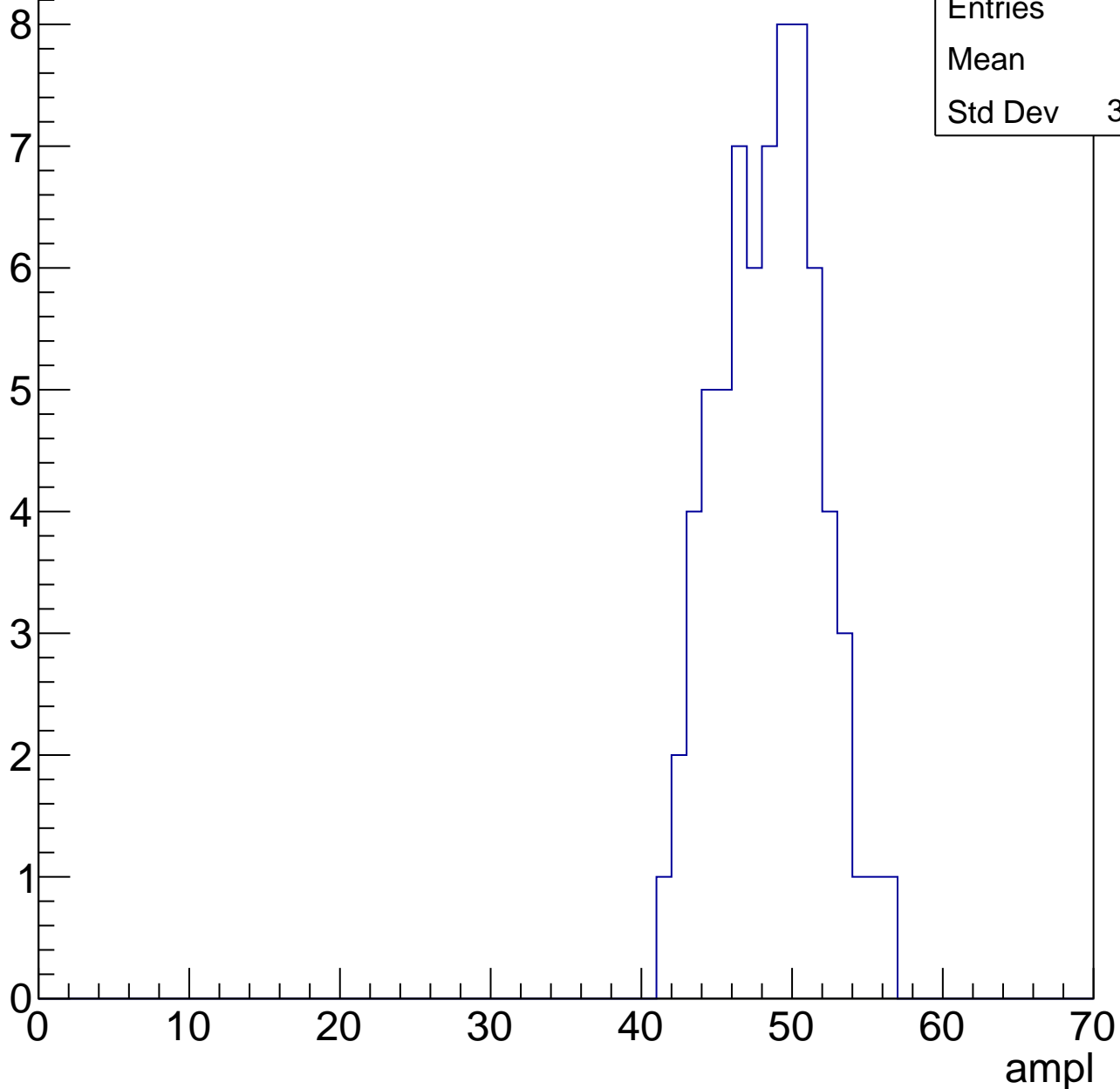
**Gaus Width: 3.4663**



# B1L002S, U18-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



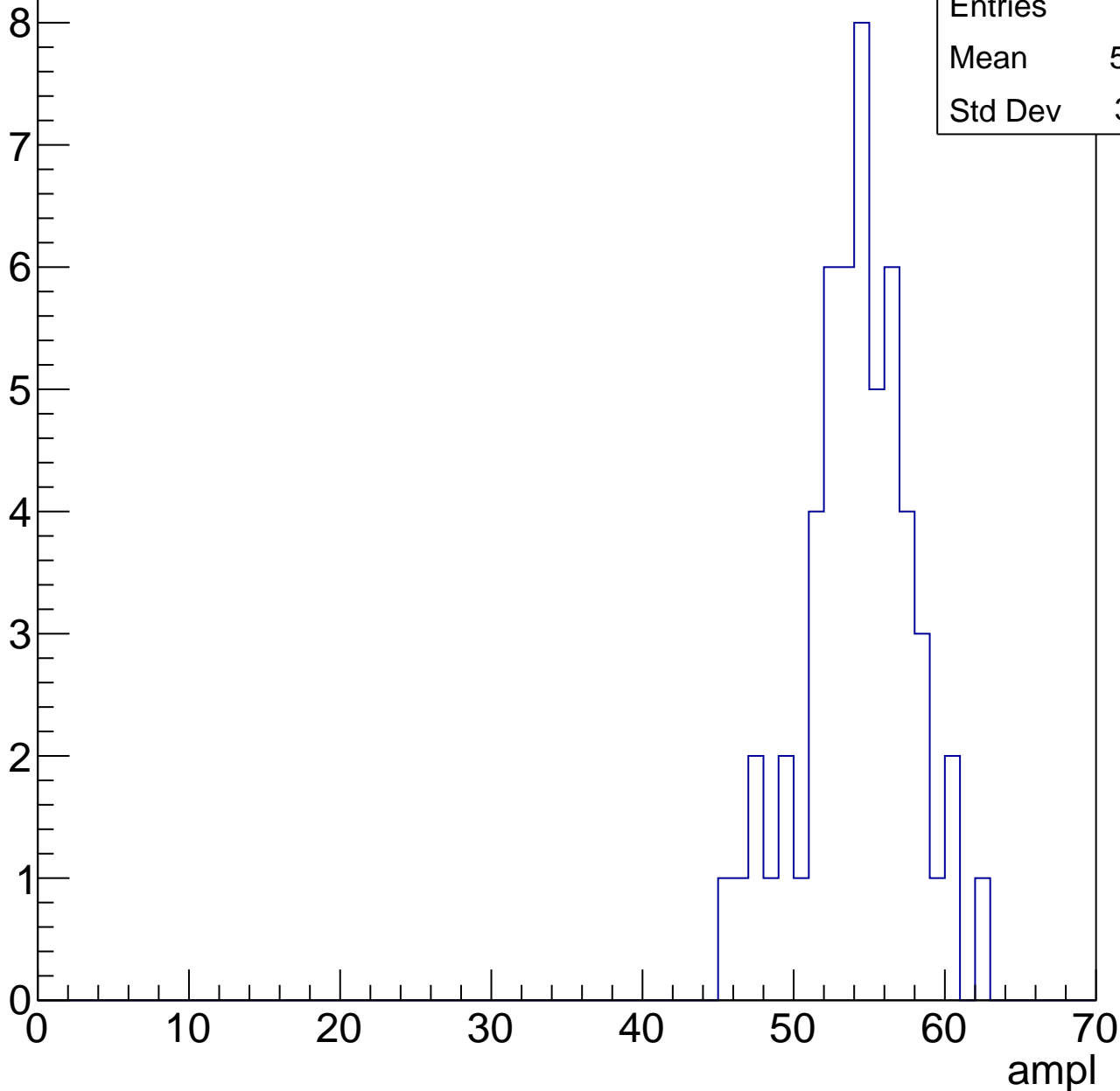
Entries	69
Mean	48
Std Dev	3.323

# B1L002S, U18-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	53.72
Std Dev	3.551

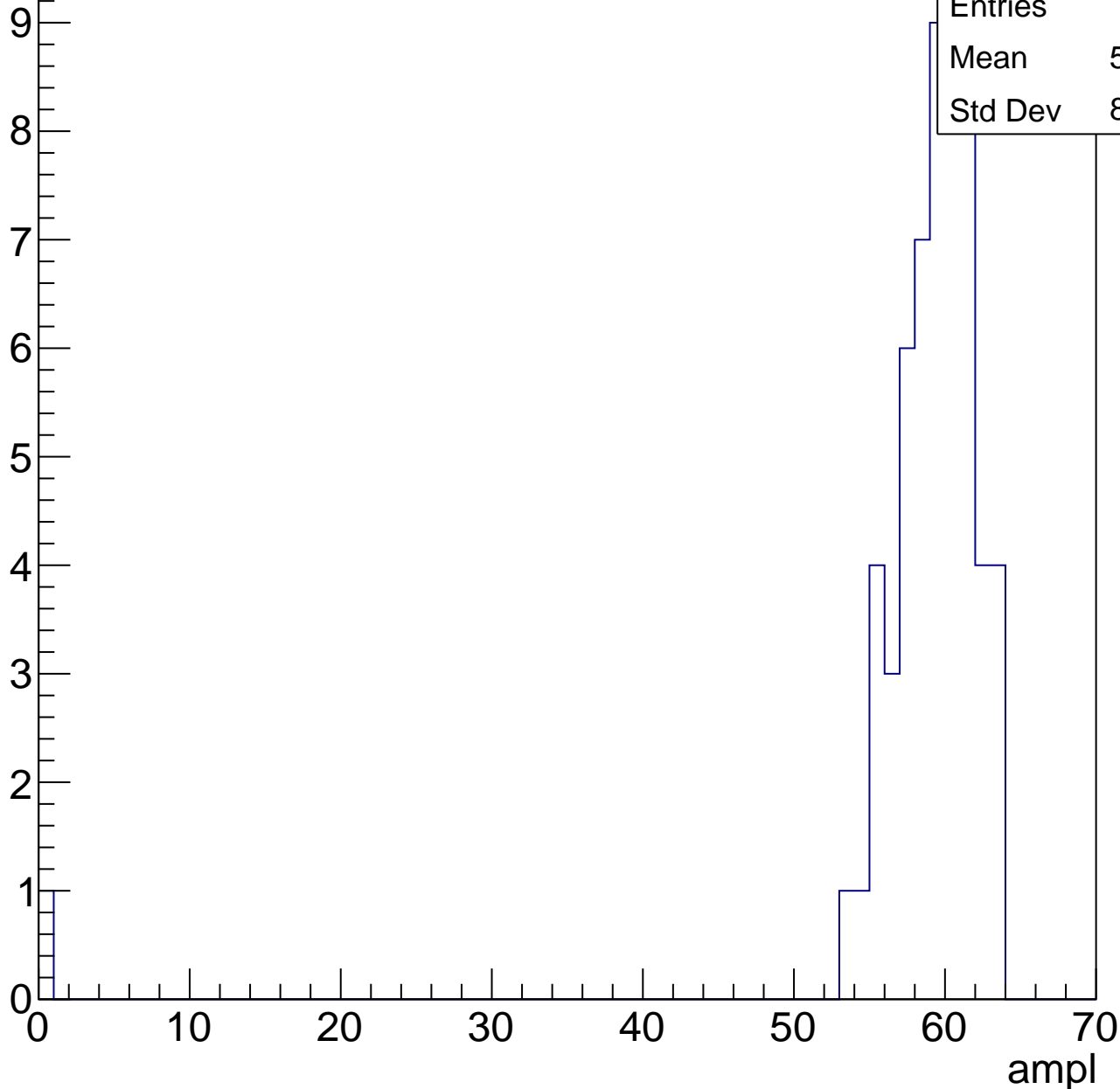


# B1L002S, U18-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.89
Std Dev	8.167

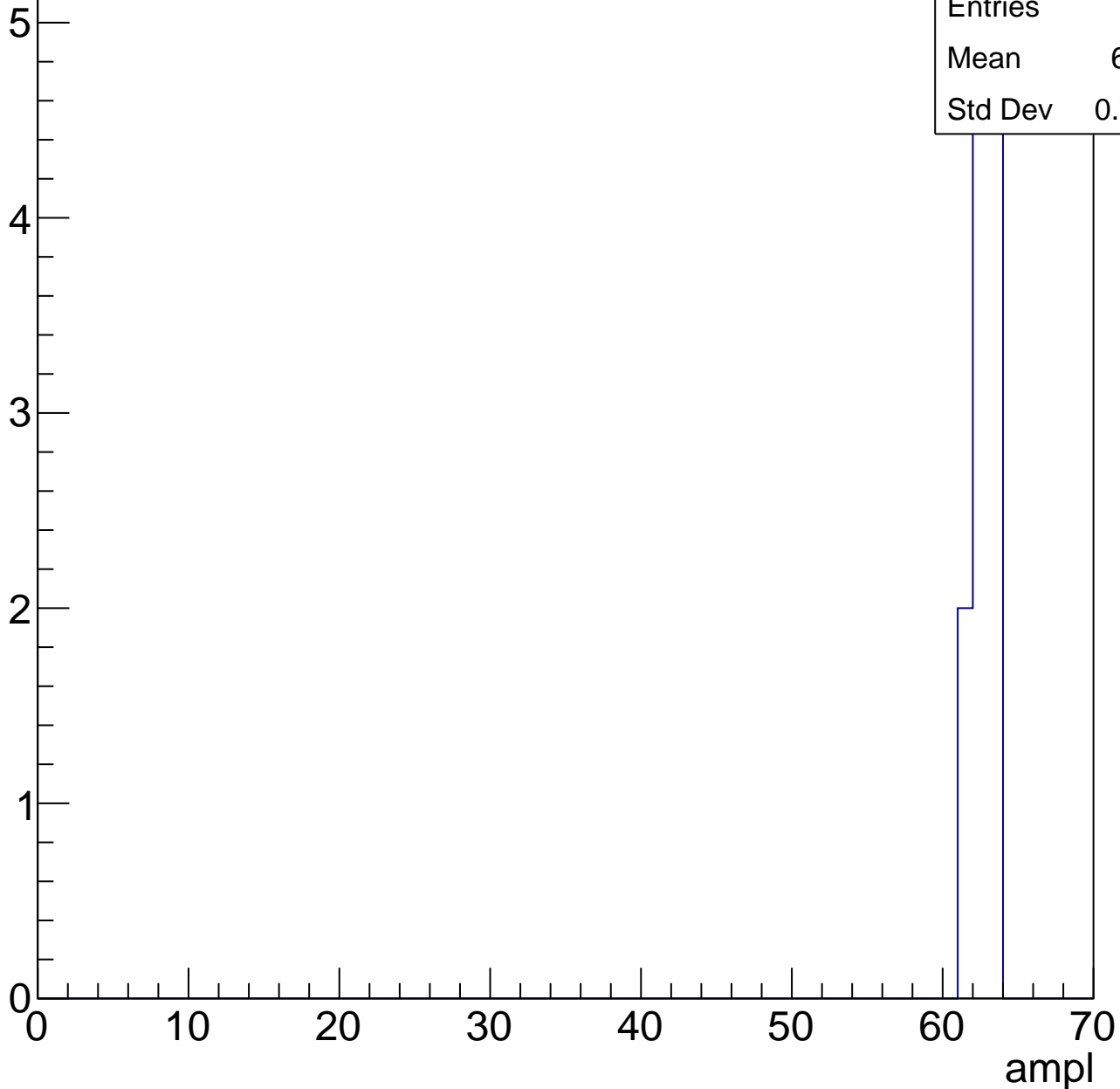


# B1L002S, U18-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	12
Mean	62.25
Std Dev	0.7217





# B1L002S, U18-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch54, adc0

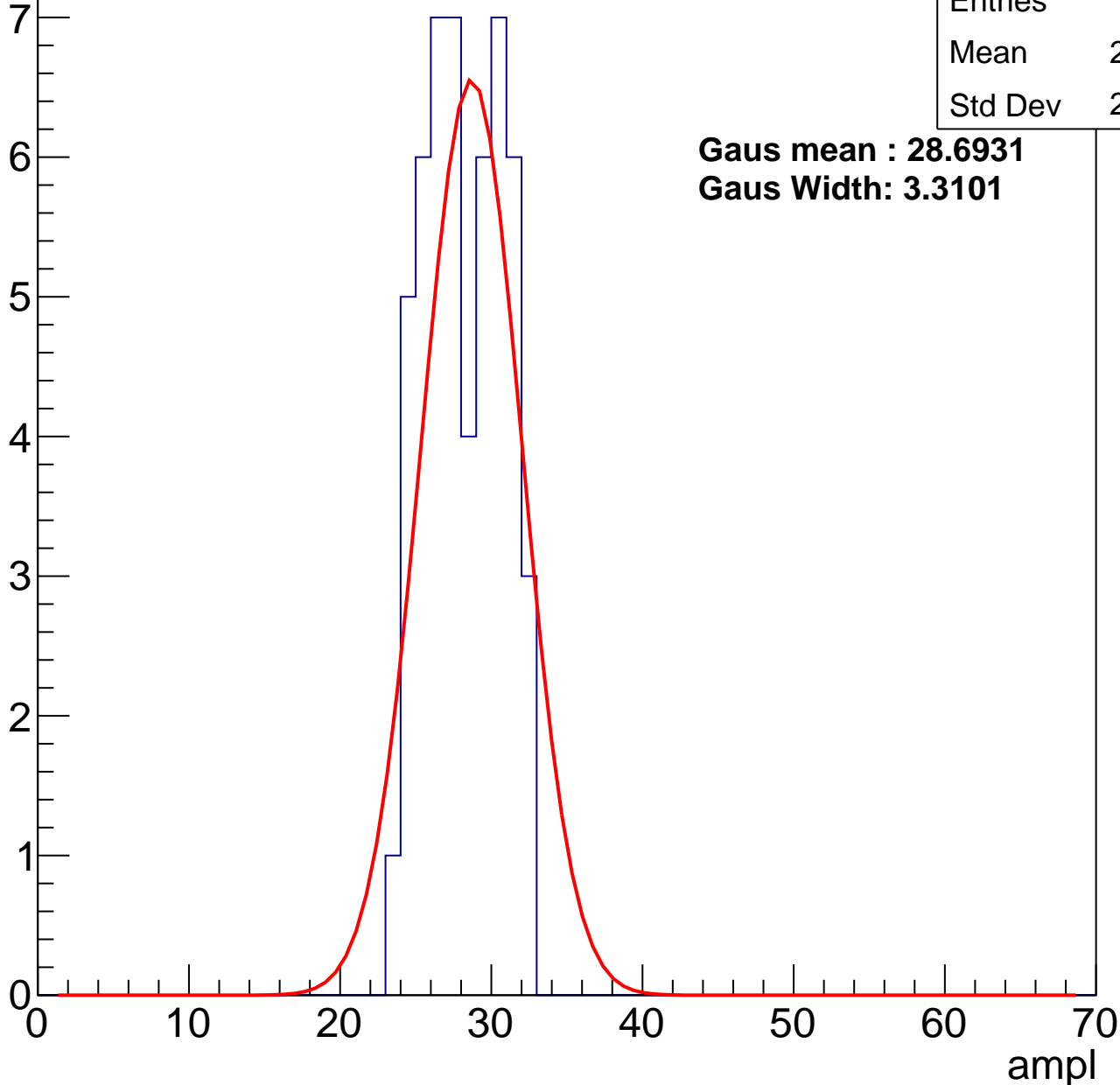
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.73
Std Dev	2.505

**Gaus mean : 28.6931**

**Gaus Width: 3.3101**



# B1L002S, U18-ch54, adc1

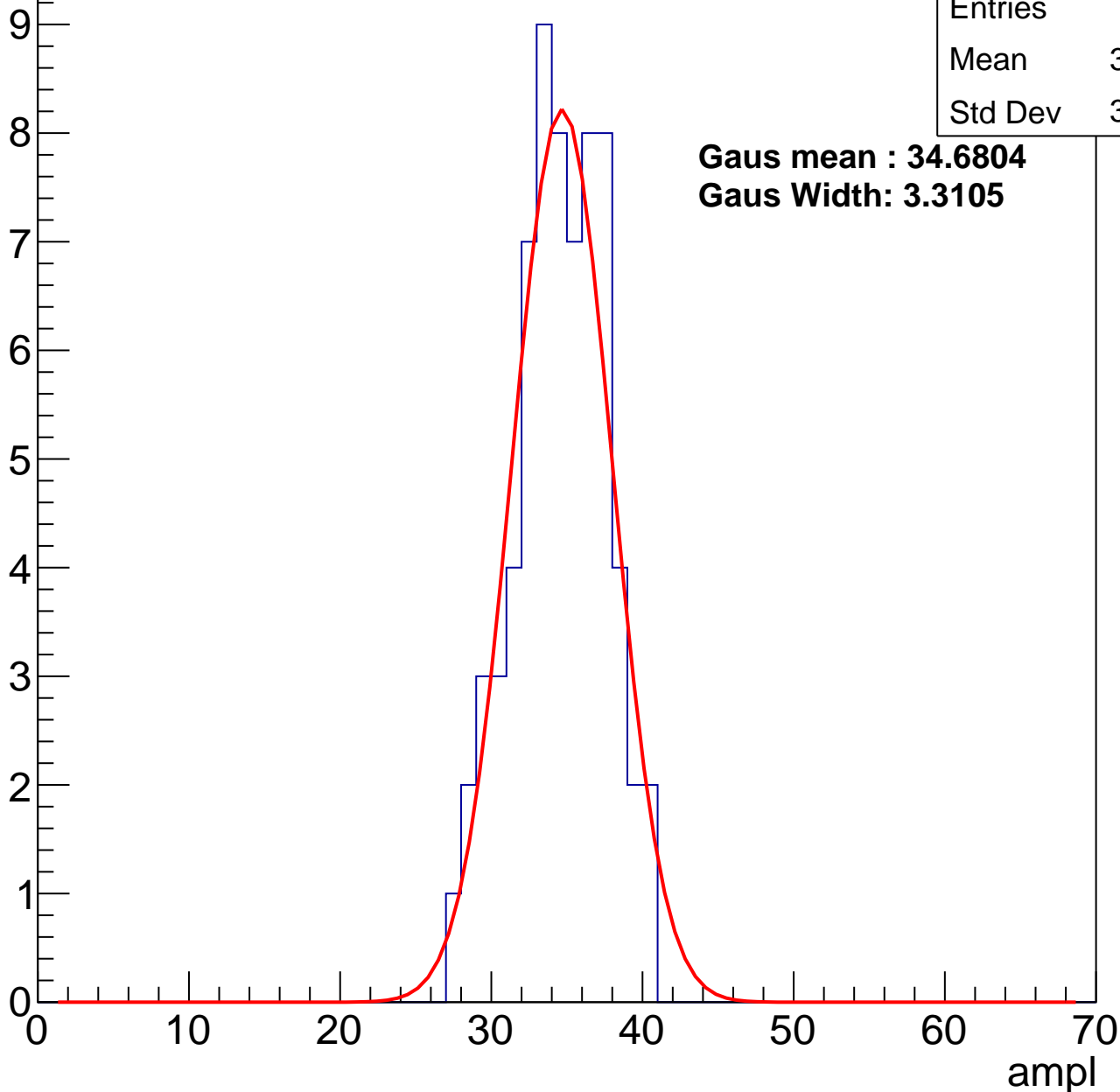
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.06
Std Dev	3.004

**Gaus mean : 34.6804**

**Gaus Width: 3.3105**



# B1L002S, U18-ch54, adc2

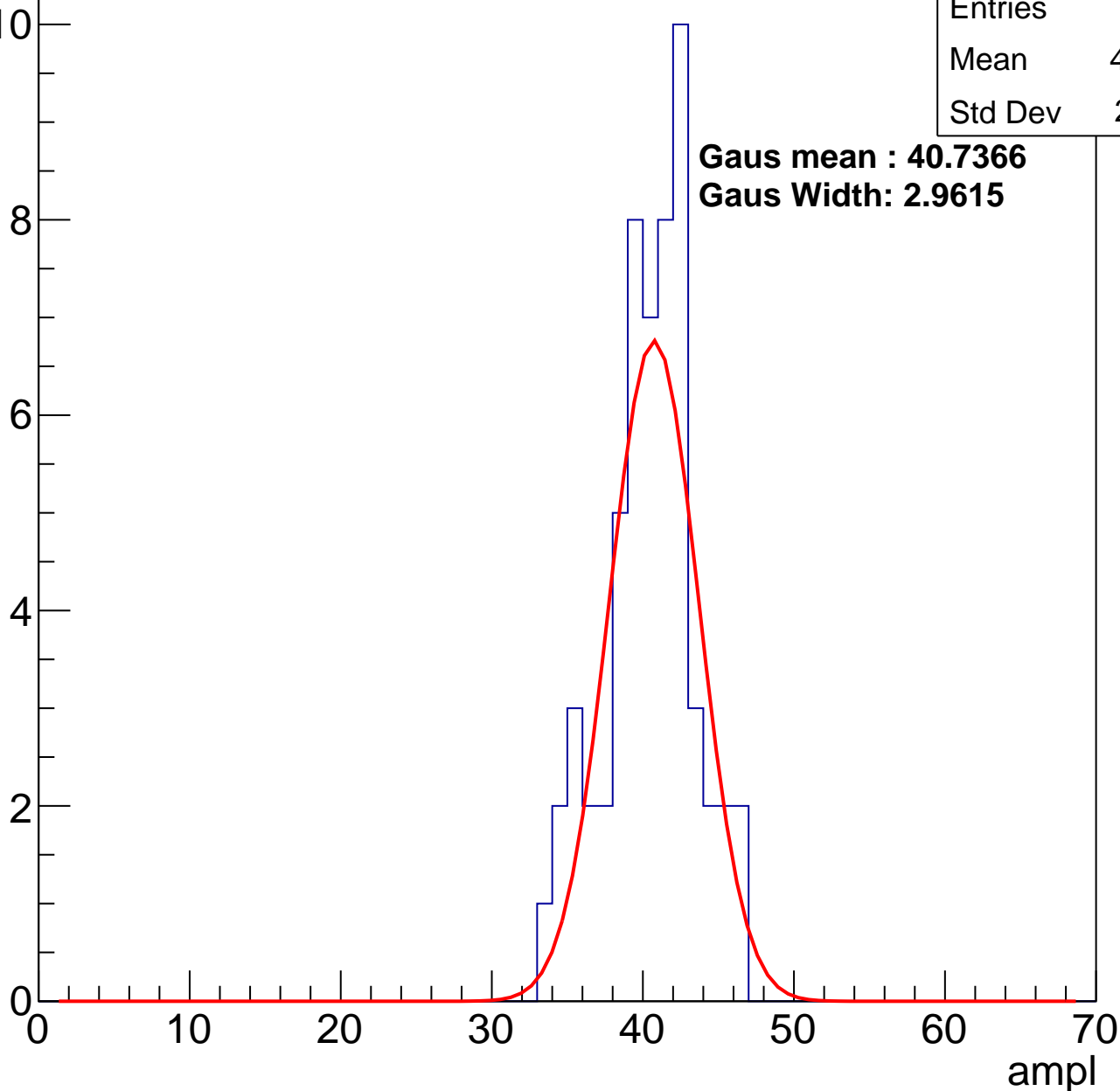
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.02
Std Dev	2.971

**Gaus mean : 40.7366**

**Gaus Width: 2.9615**

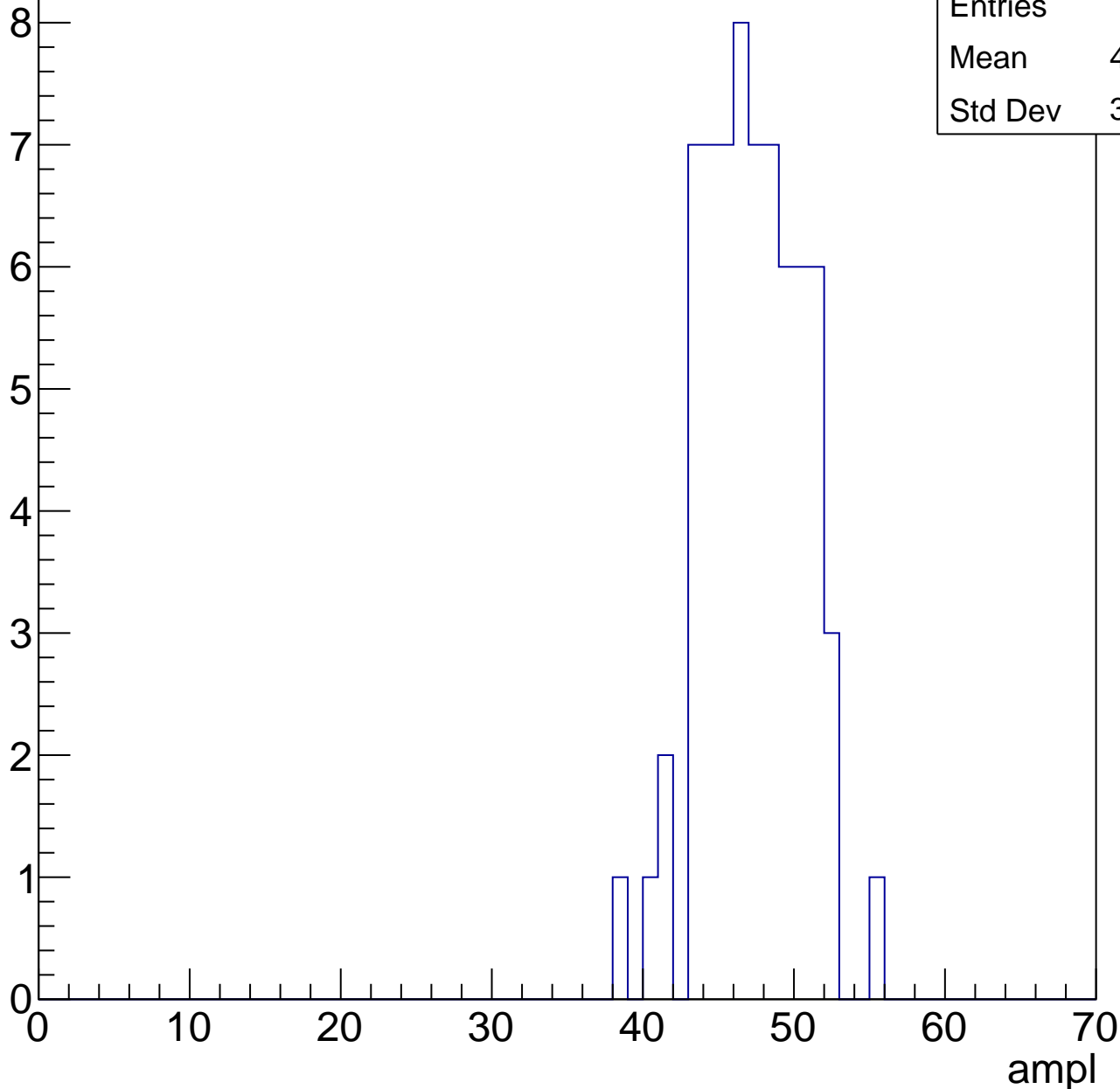


# B1L002S, U18-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.78
Std Dev	3.252

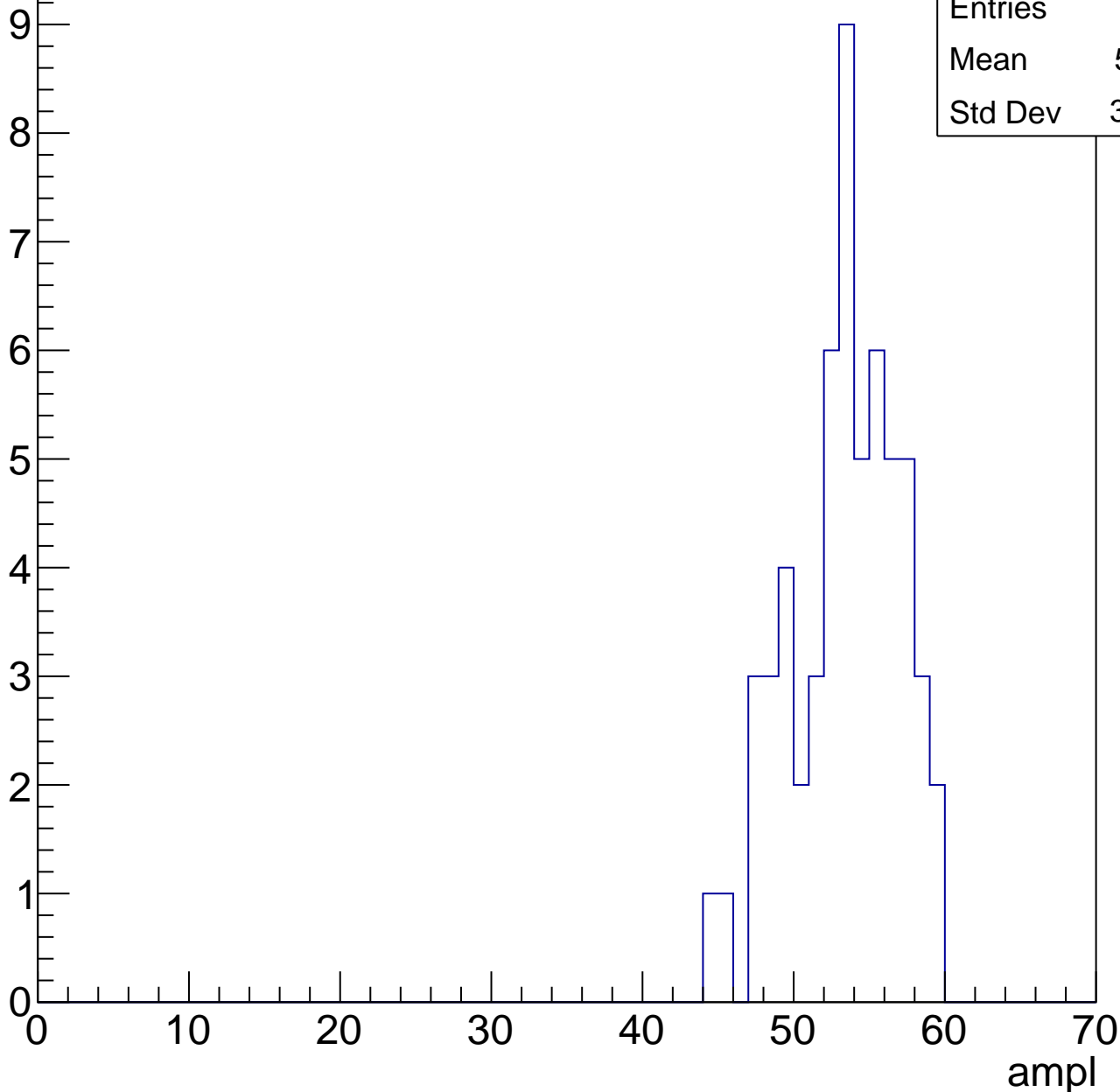


# B1L002S, U18-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

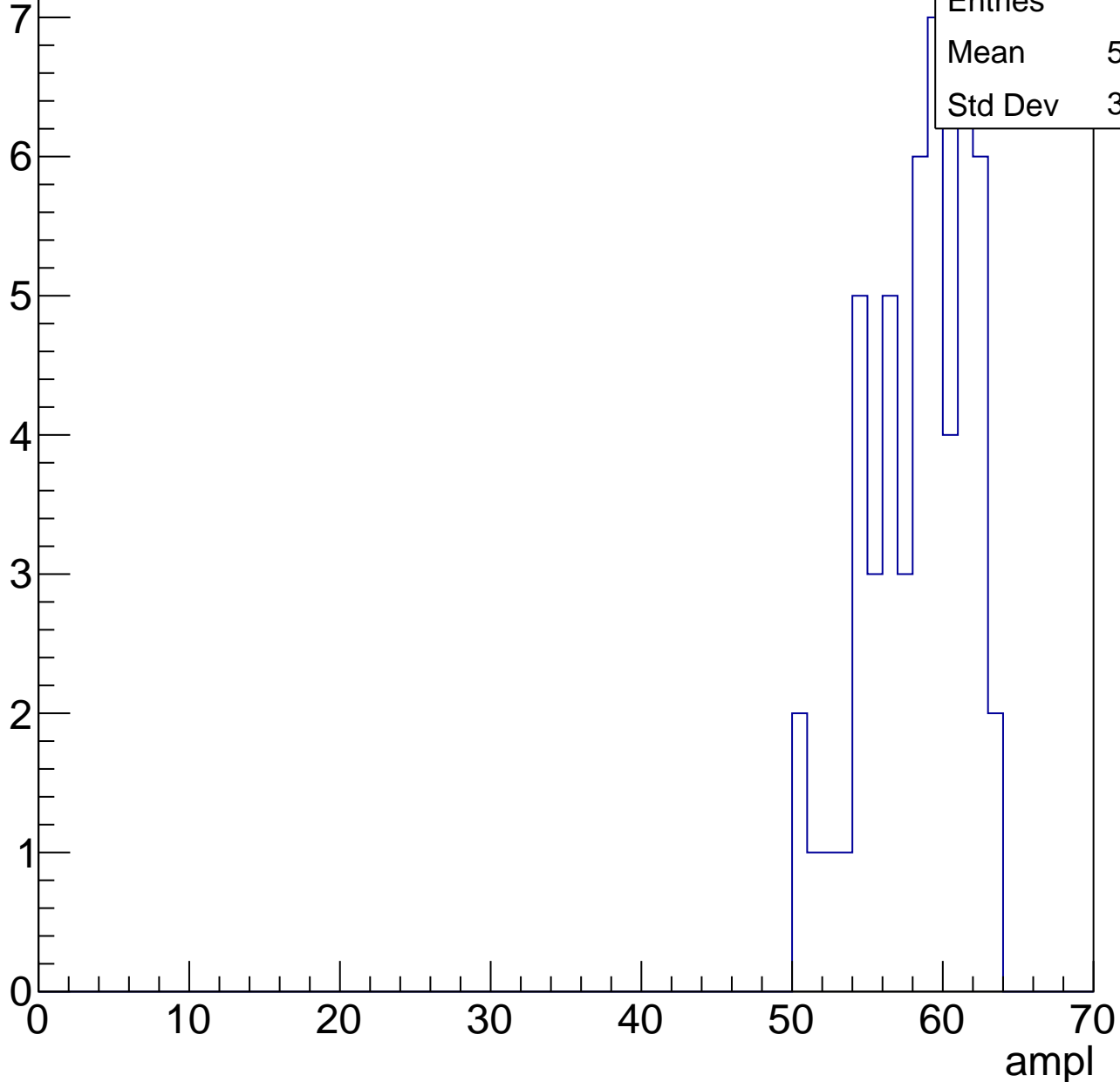
Entries	58
Mean	52.91
Std Dev	3.544



# B1L002S, U18-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch54, adc6

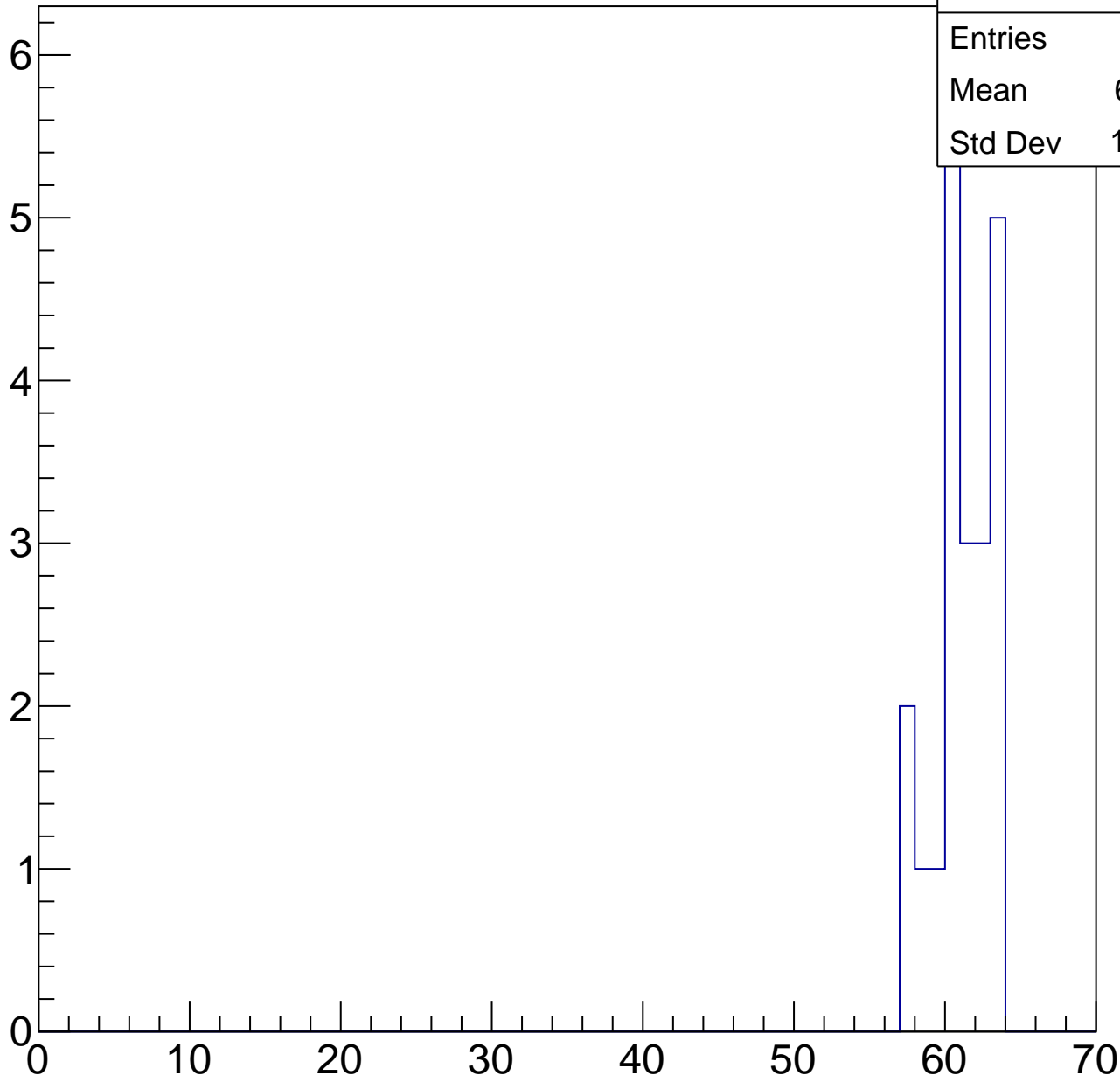
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

6  
5  
4  
3  
2  
1  
0

Entries	21
Mean	60.71
Std Dev	1.855

ampl

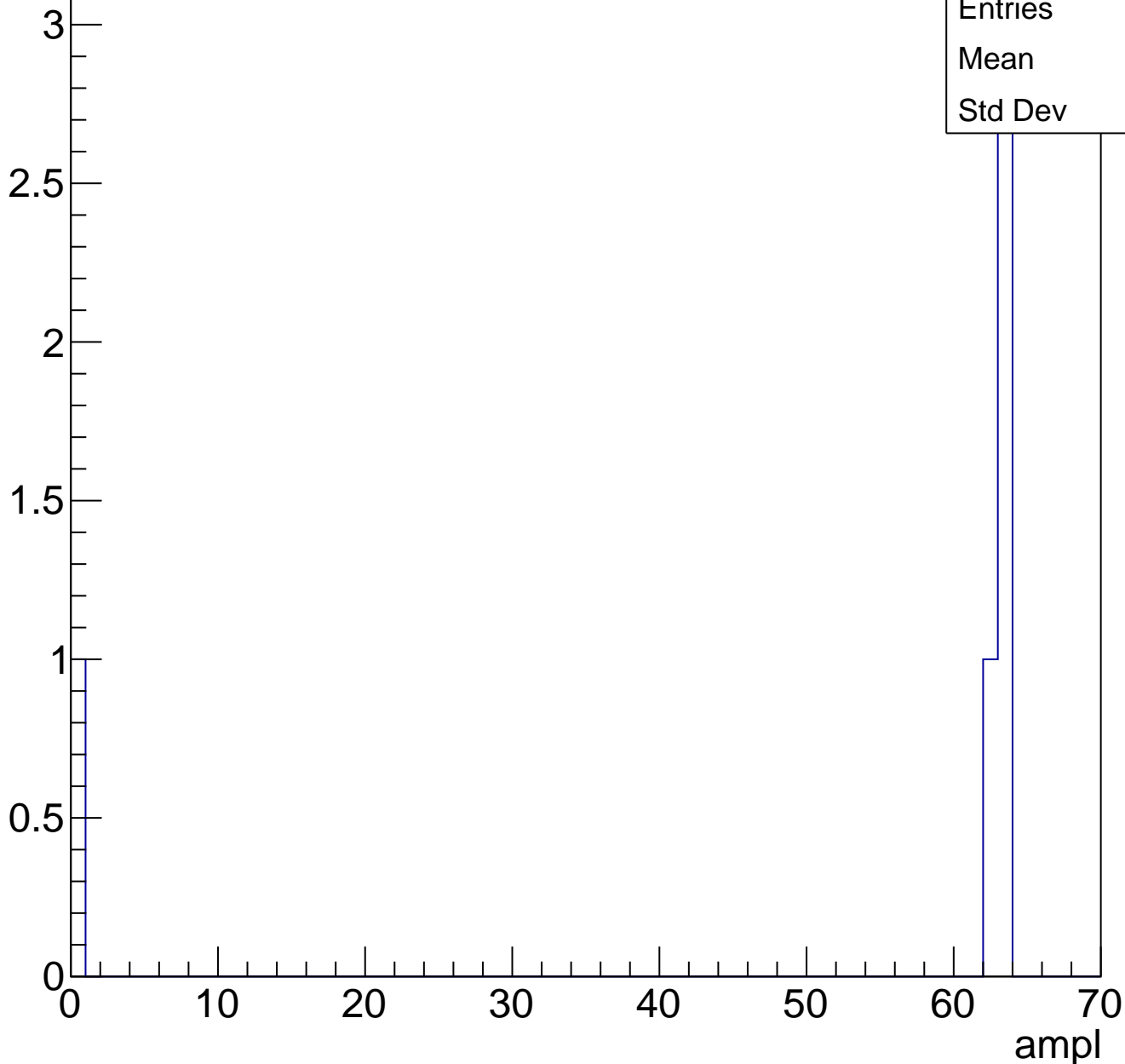




# B1L002S, U18-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27.52
Std Dev	3.096

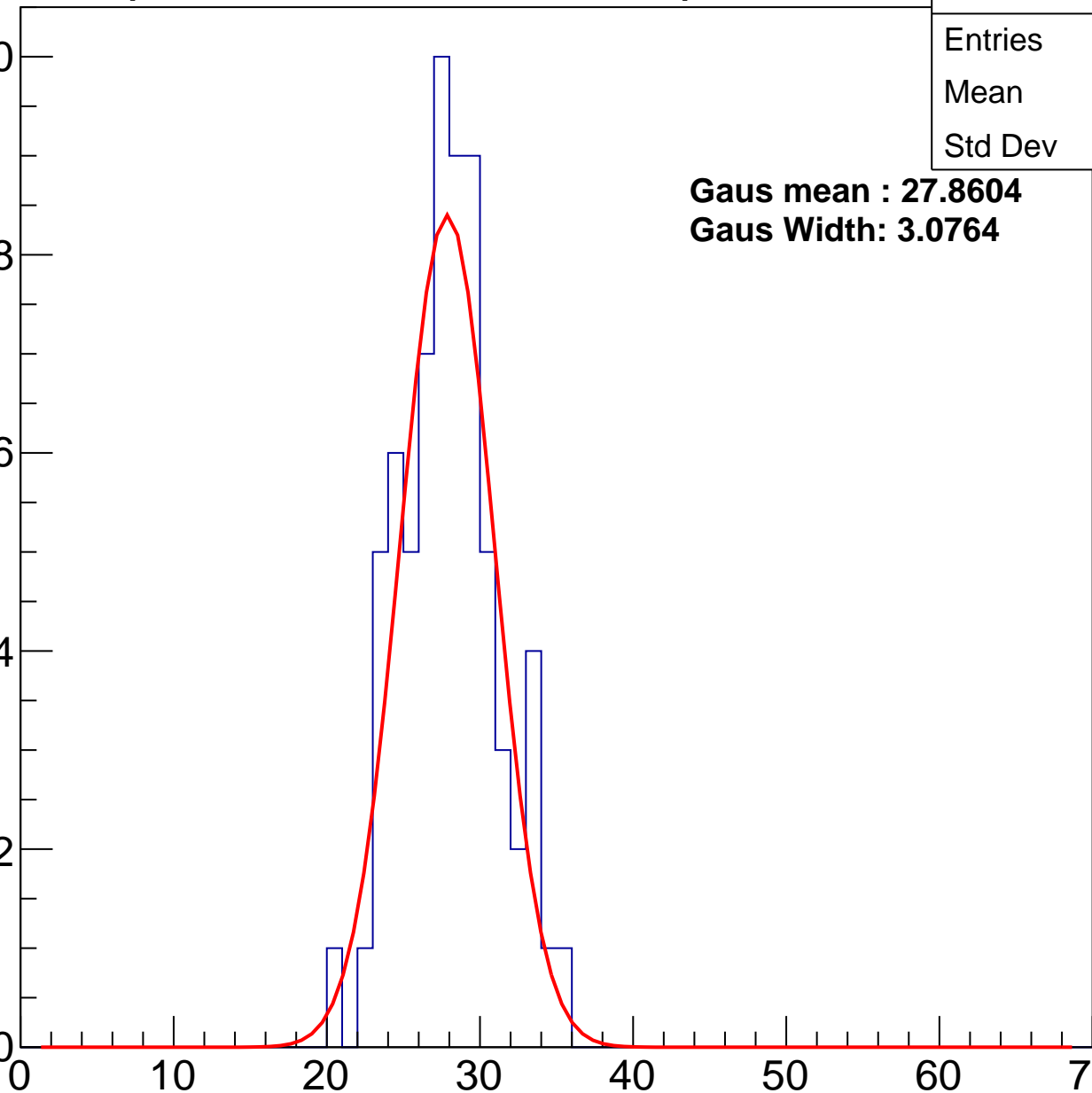
**Gaus mean : 27.8604**

**Gaus Width: 3.0764**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U18-ch55, adc1

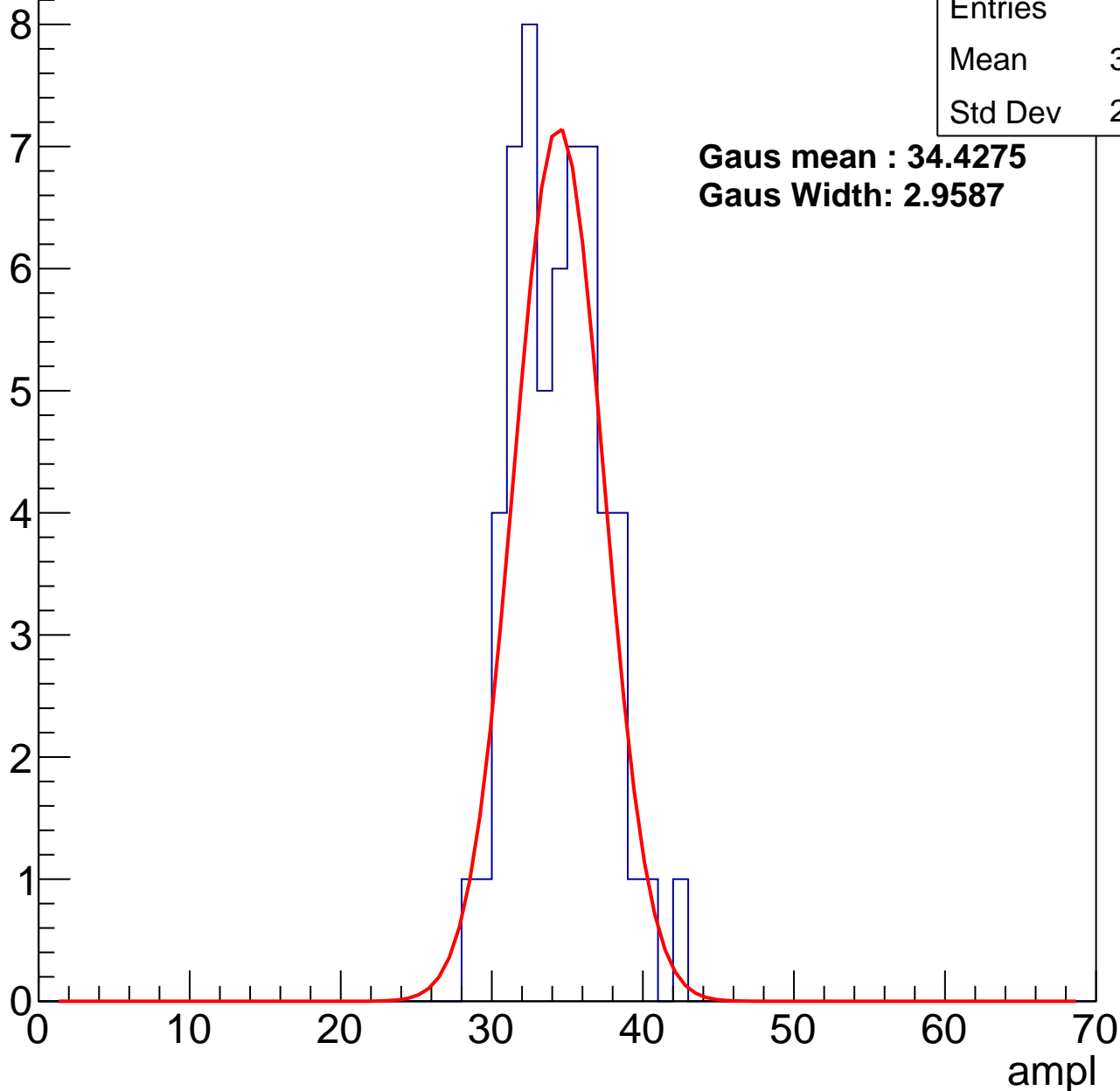
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.98
Std Dev	2.917

**Gaus mean : 34.4275**

**Gaus Width: 2.9587**



# B1L002S, U18-ch55, adc2

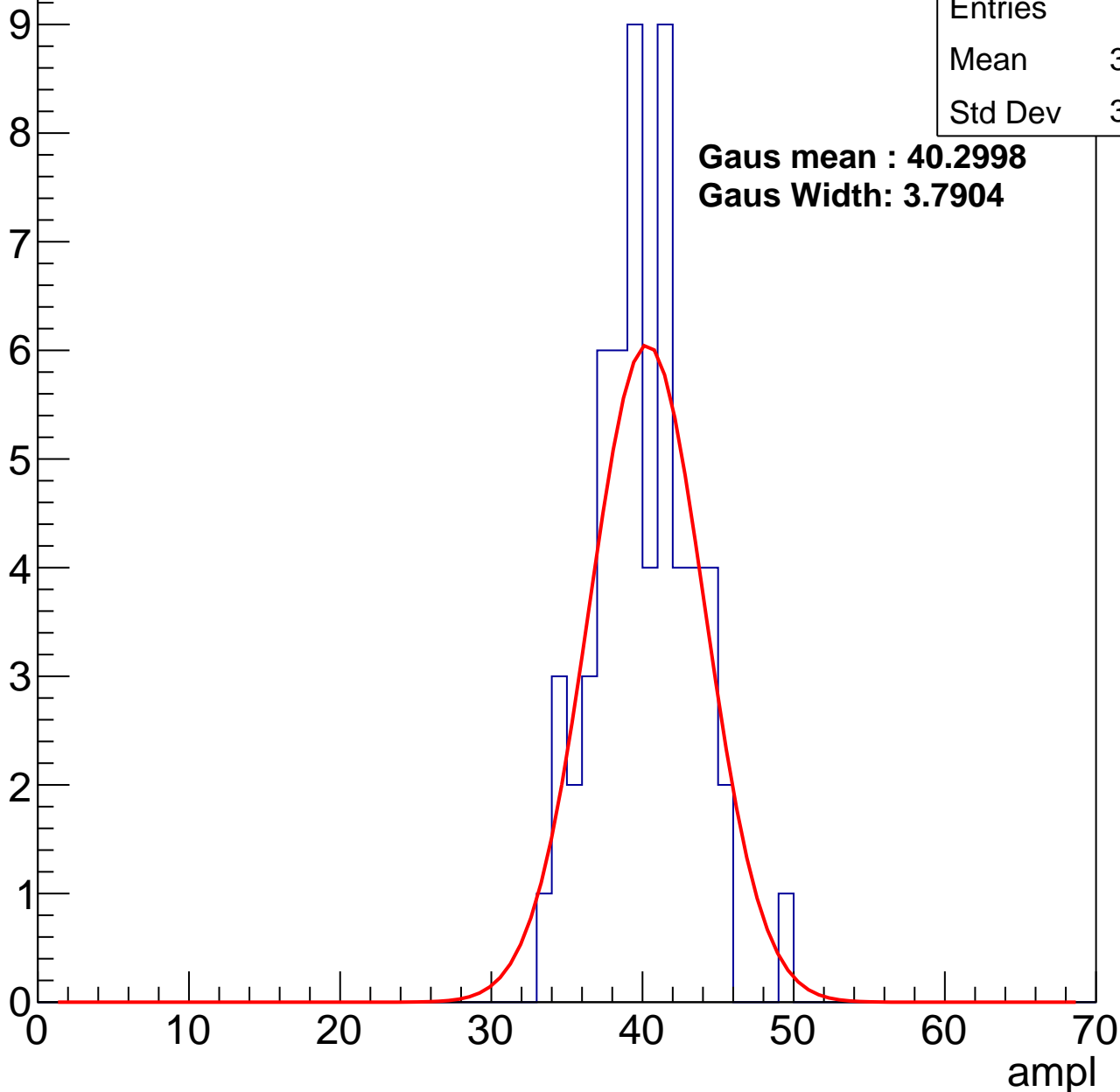
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.62
Std Dev	3.188

**Gaus mean : 40.2998**

**Gaus Width: 3.7904**



# B1L002S, U18-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

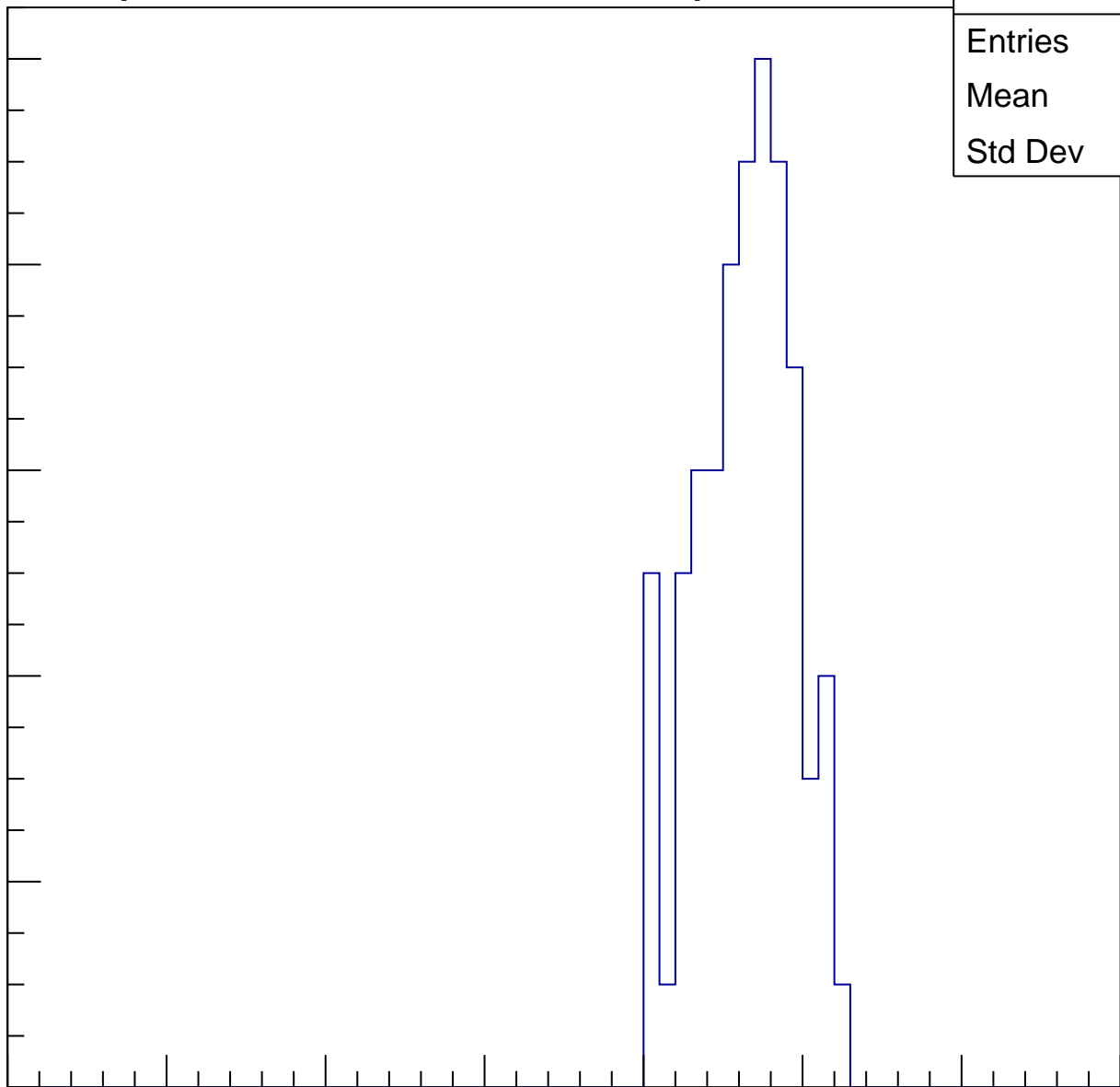
Entries	74
Mean	45.92
Std Dev	2.994

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

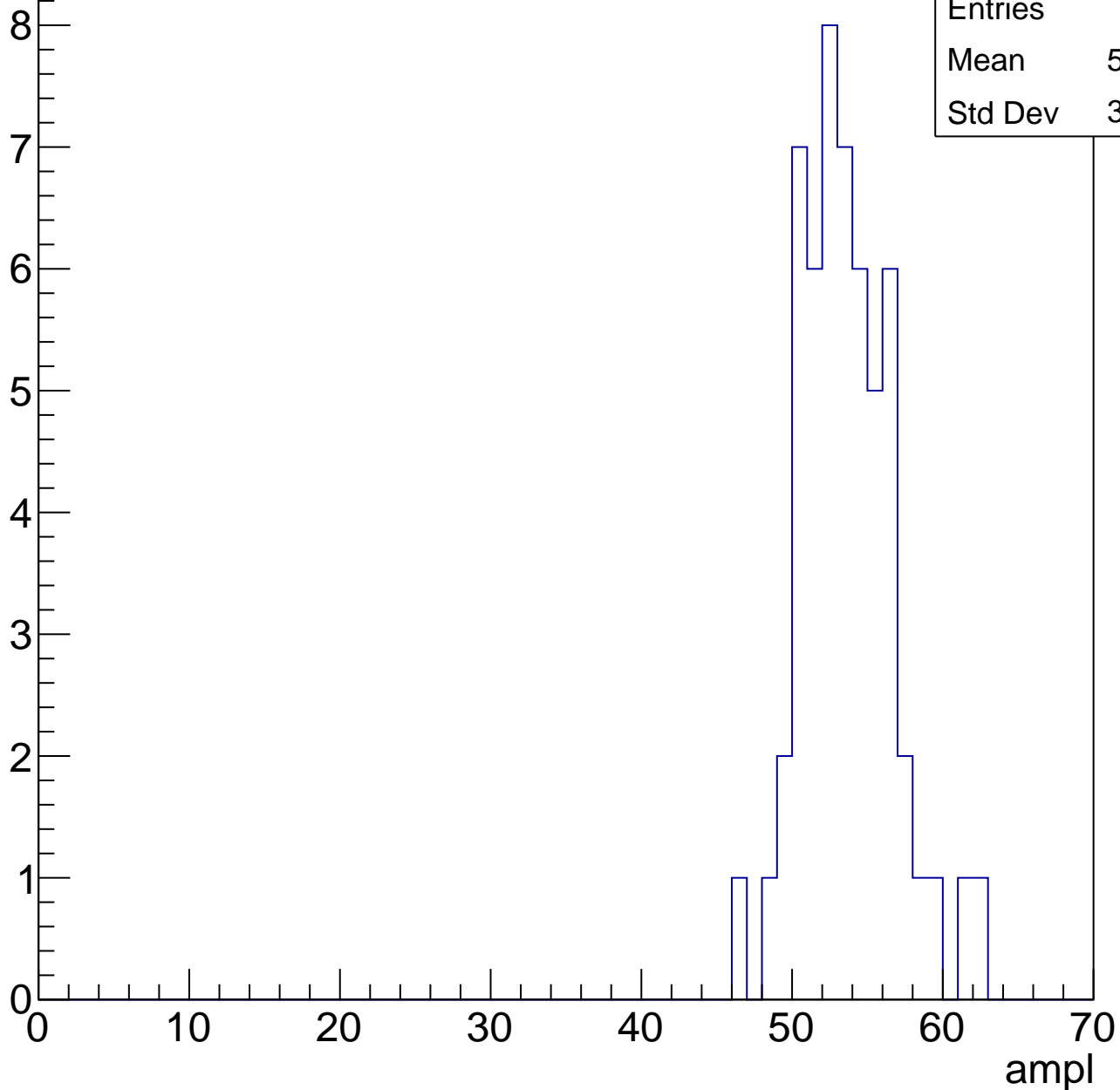


# B1L002S, U18-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.16
Std Dev	3.068



# B1L002S, U18-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	58.08
Std Dev	2.762

Entry

10

8

6

4

2

0

0

10

20

30

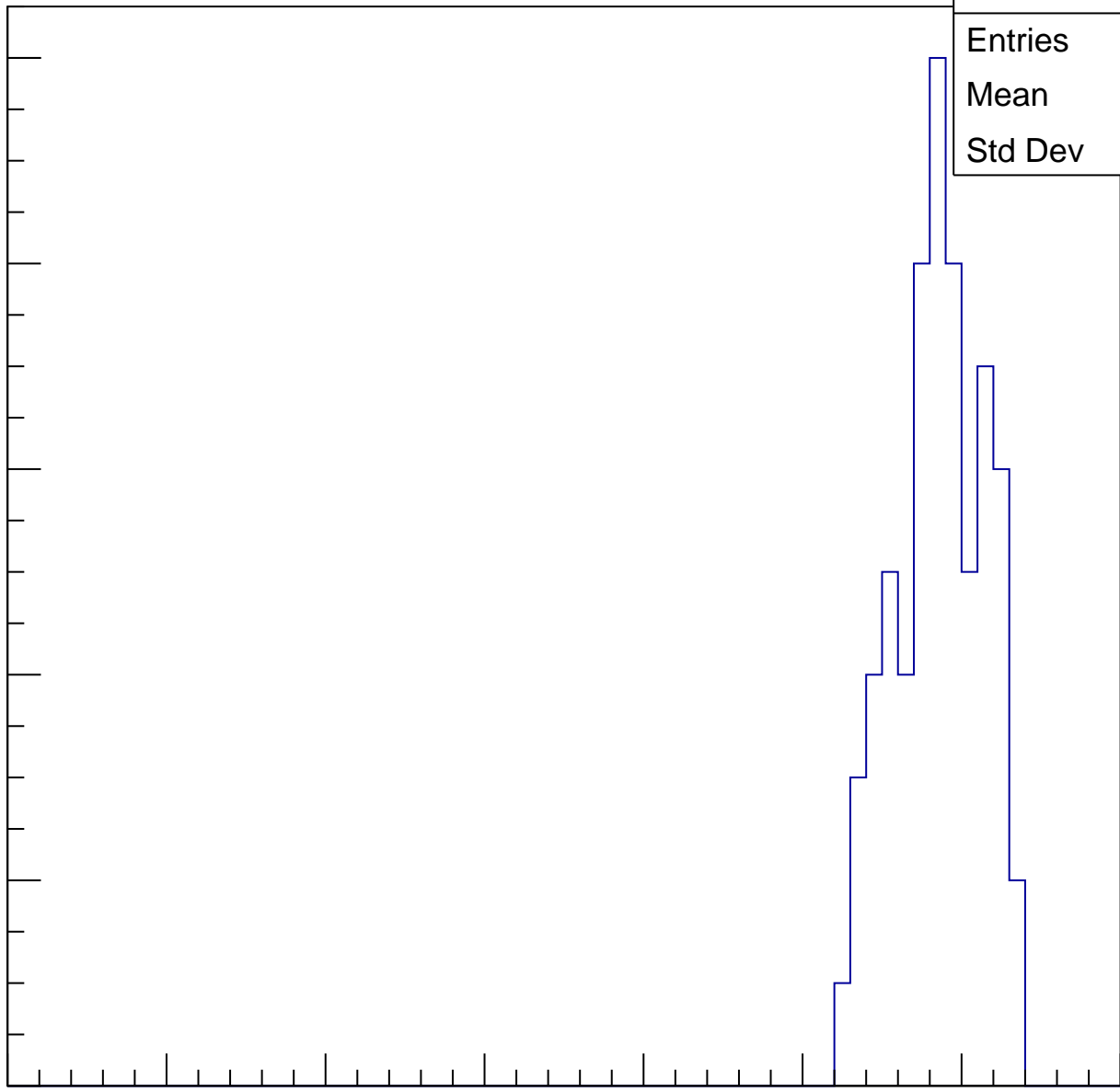
40

50

60

70

ampl

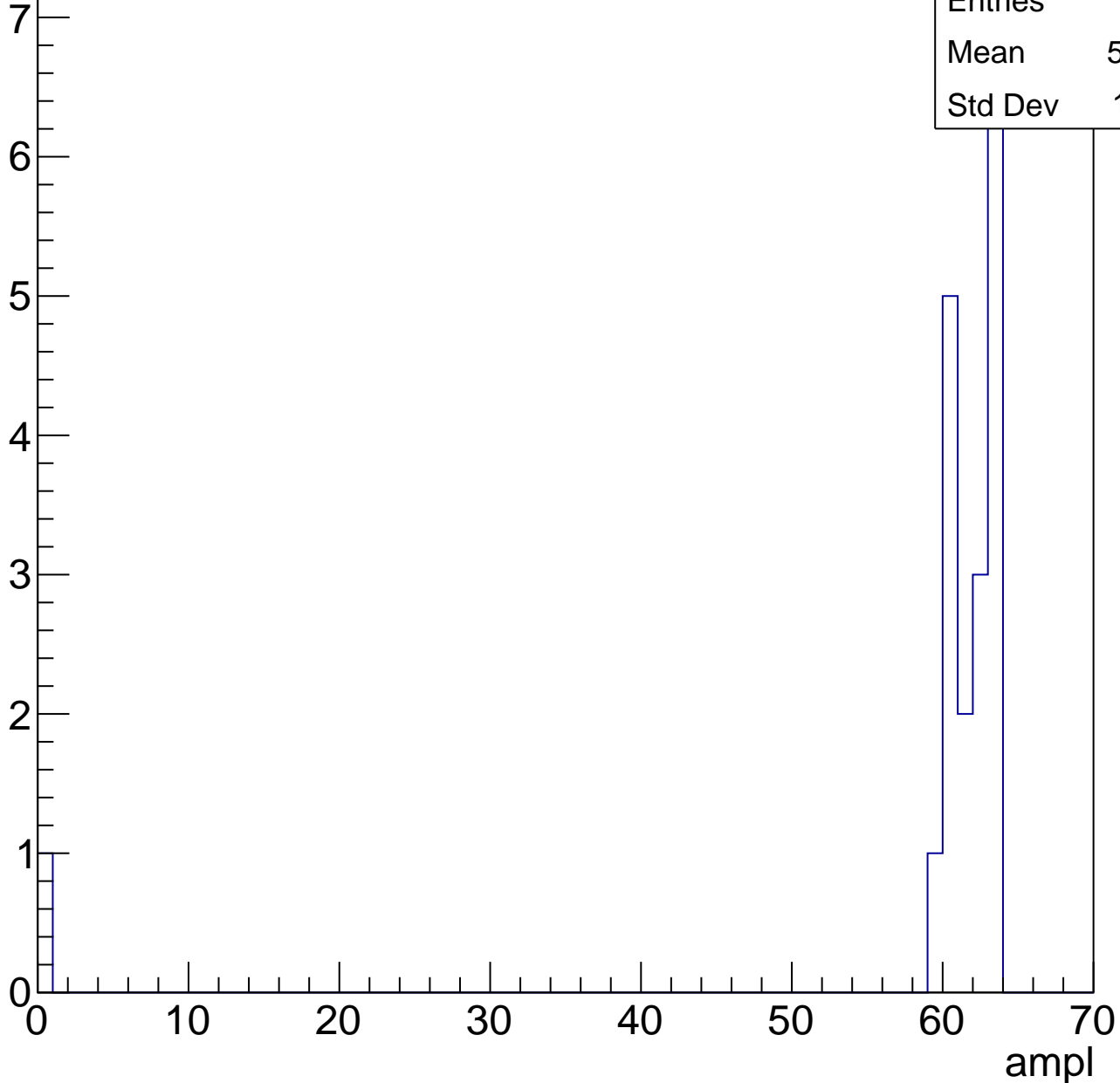


# B1L002S, U18-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.32
Std Dev	13.81





# B1L002S, U18-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch56, adc0

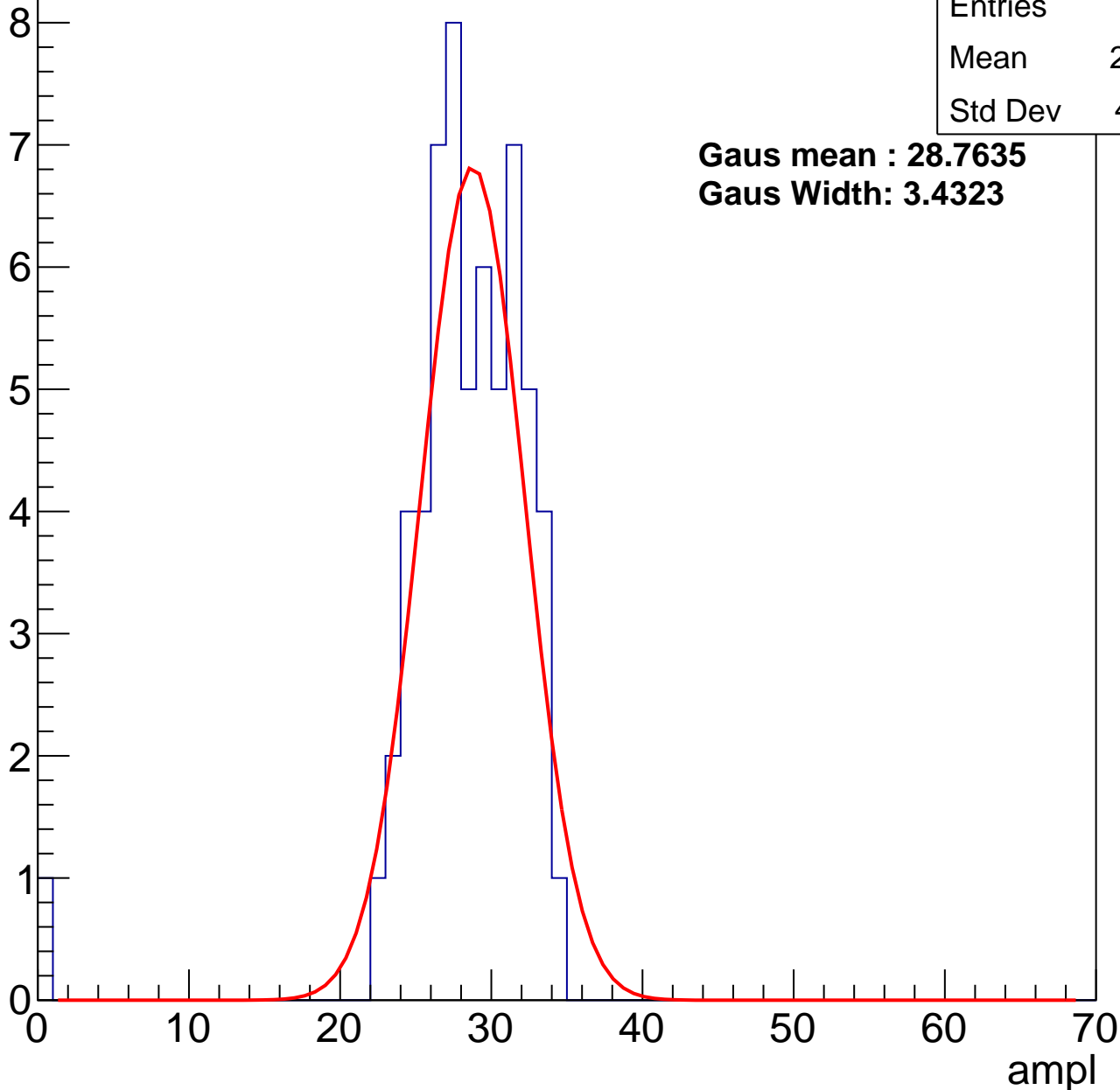
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.82
Std Dev	4.671

**Gaus mean : 28.7635**

**Gaus Width: 3.4323**



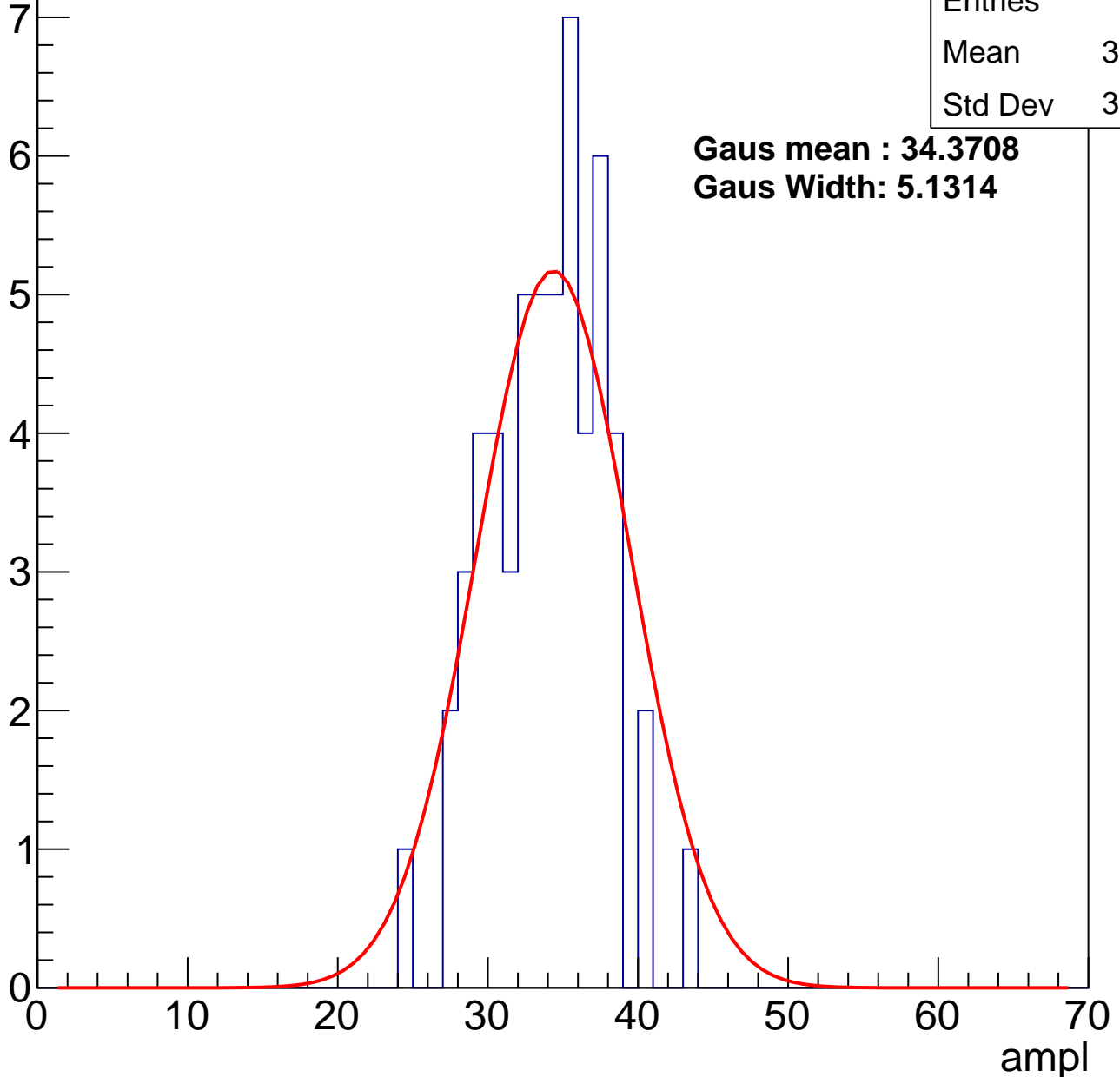
# B1L002S, U18-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.43
Std Dev	3.774

**Gaus mean : 34.3708**  
**Gaus Width: 5.1314**



# B1L002S, U18-ch56, adc2

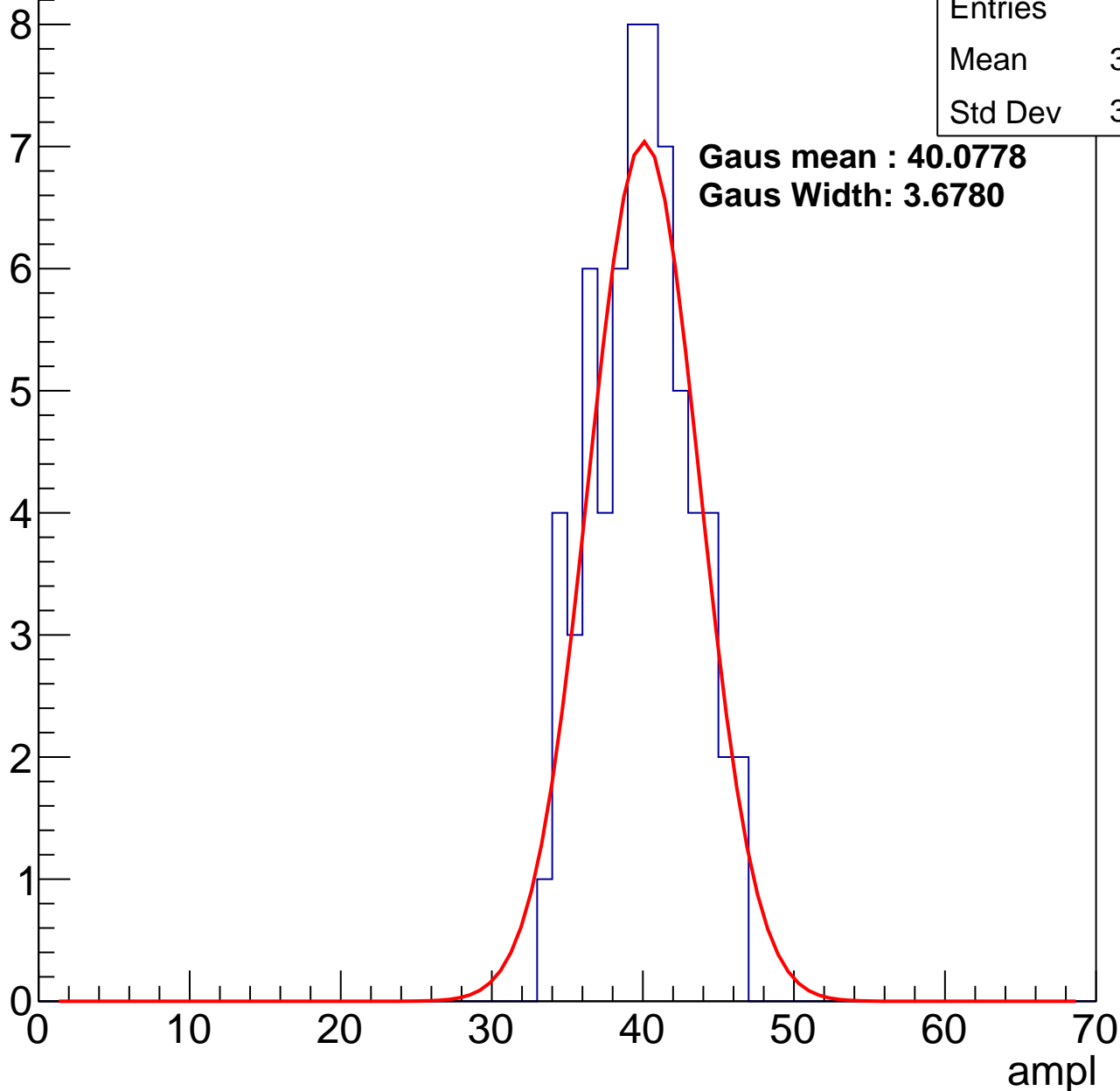
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	39.45
Std Dev	3.216

**Gaus mean : 40.0778**

**Gaus Width: 3.6780**

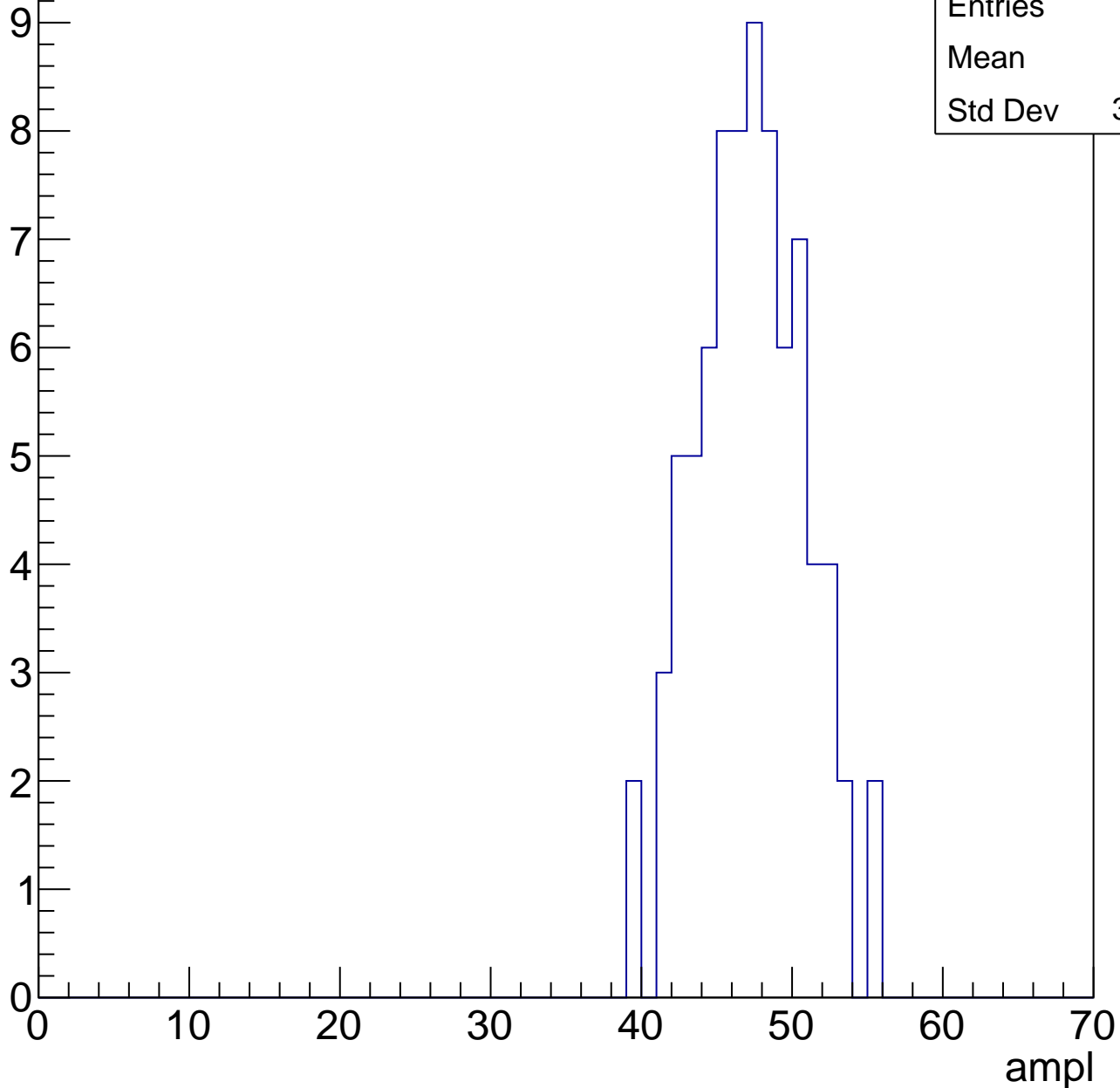


# B1L002S, U18-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	46.8
Std Dev	3.541

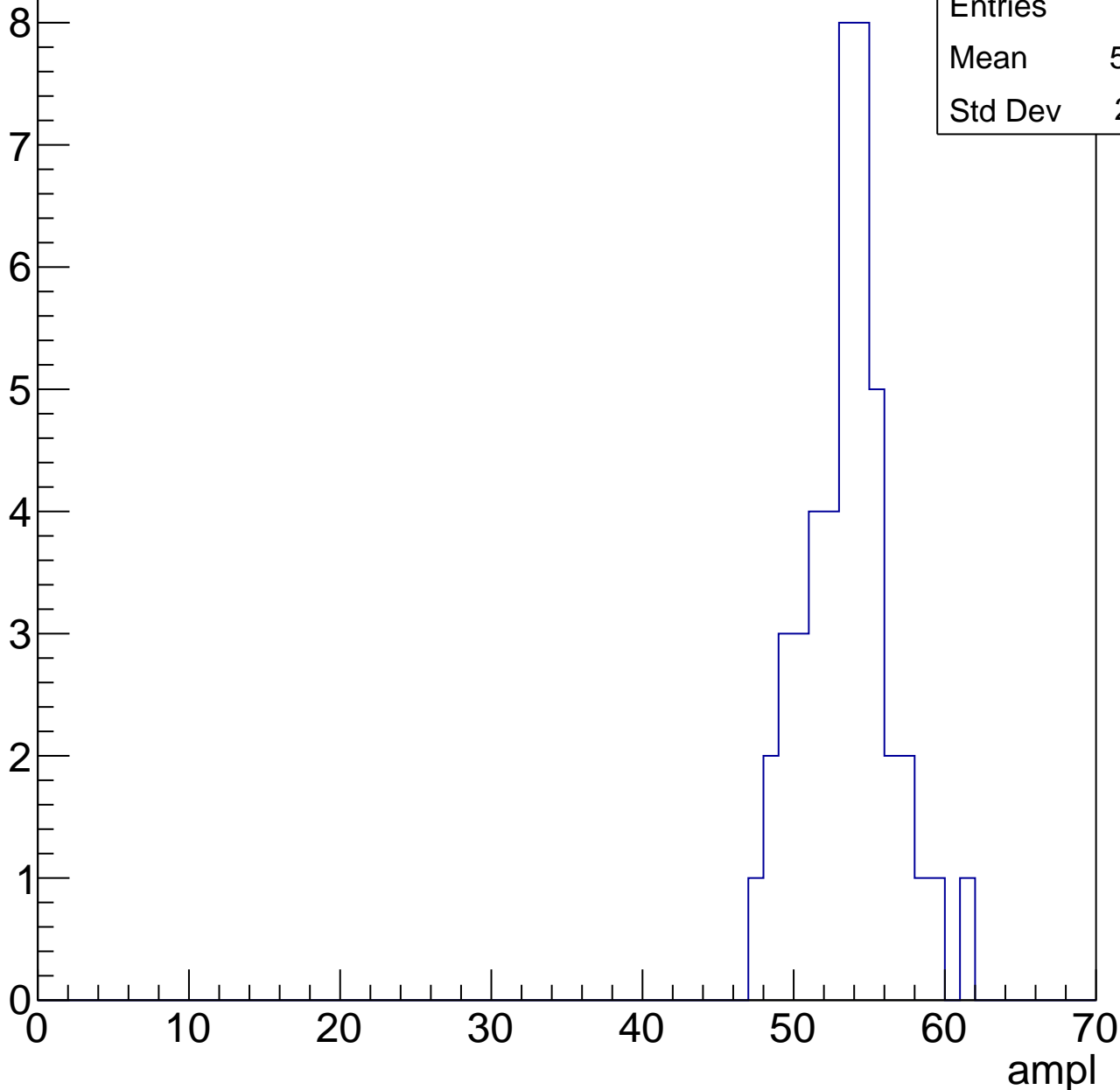


# B1L002S, U18-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	53.04
Std Dev	2.921

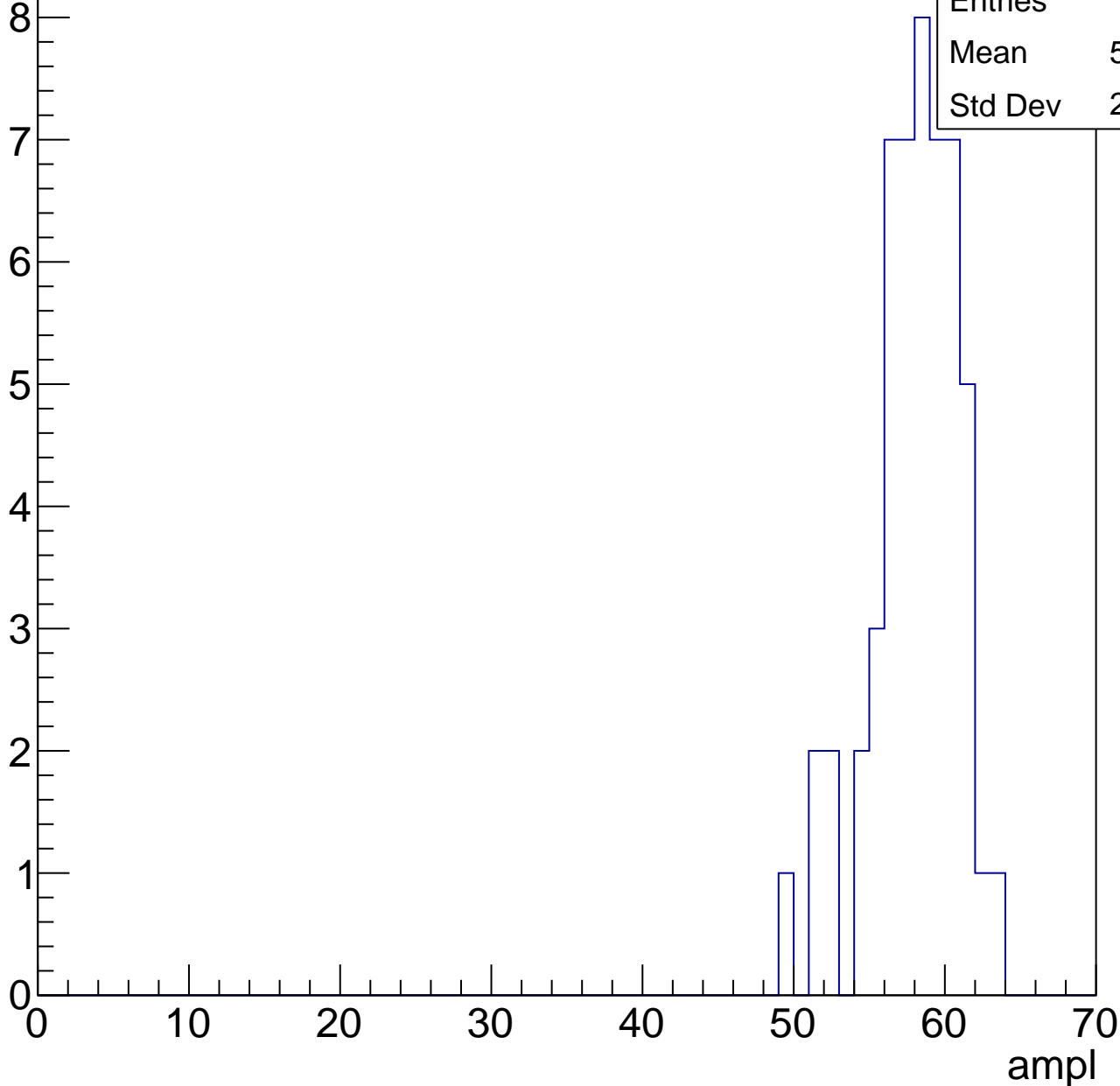


# B1L002S, U18-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.47
Std Dev	2.918

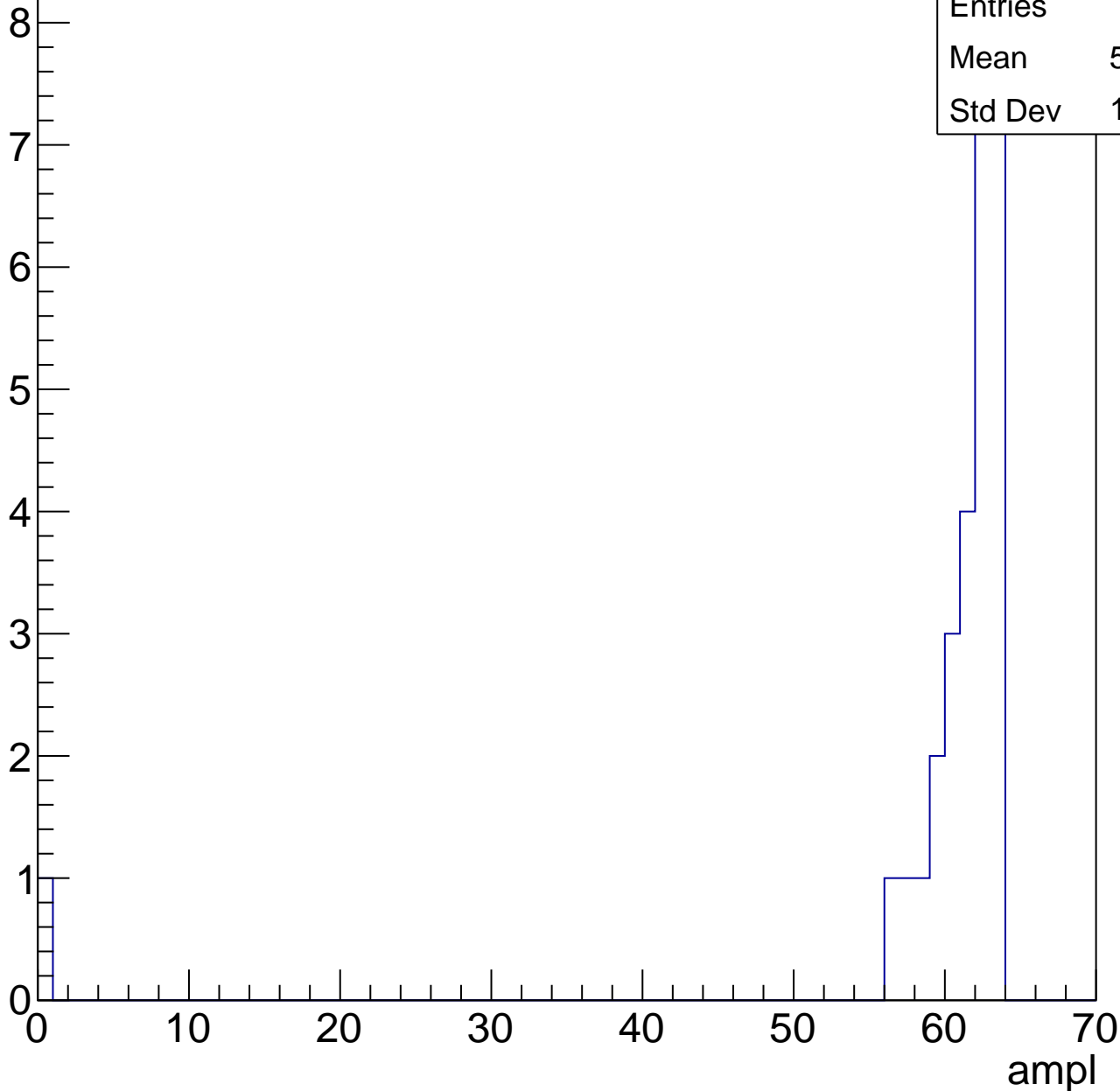


# B1L002S, U18-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	59.07
Std Dev	11.32

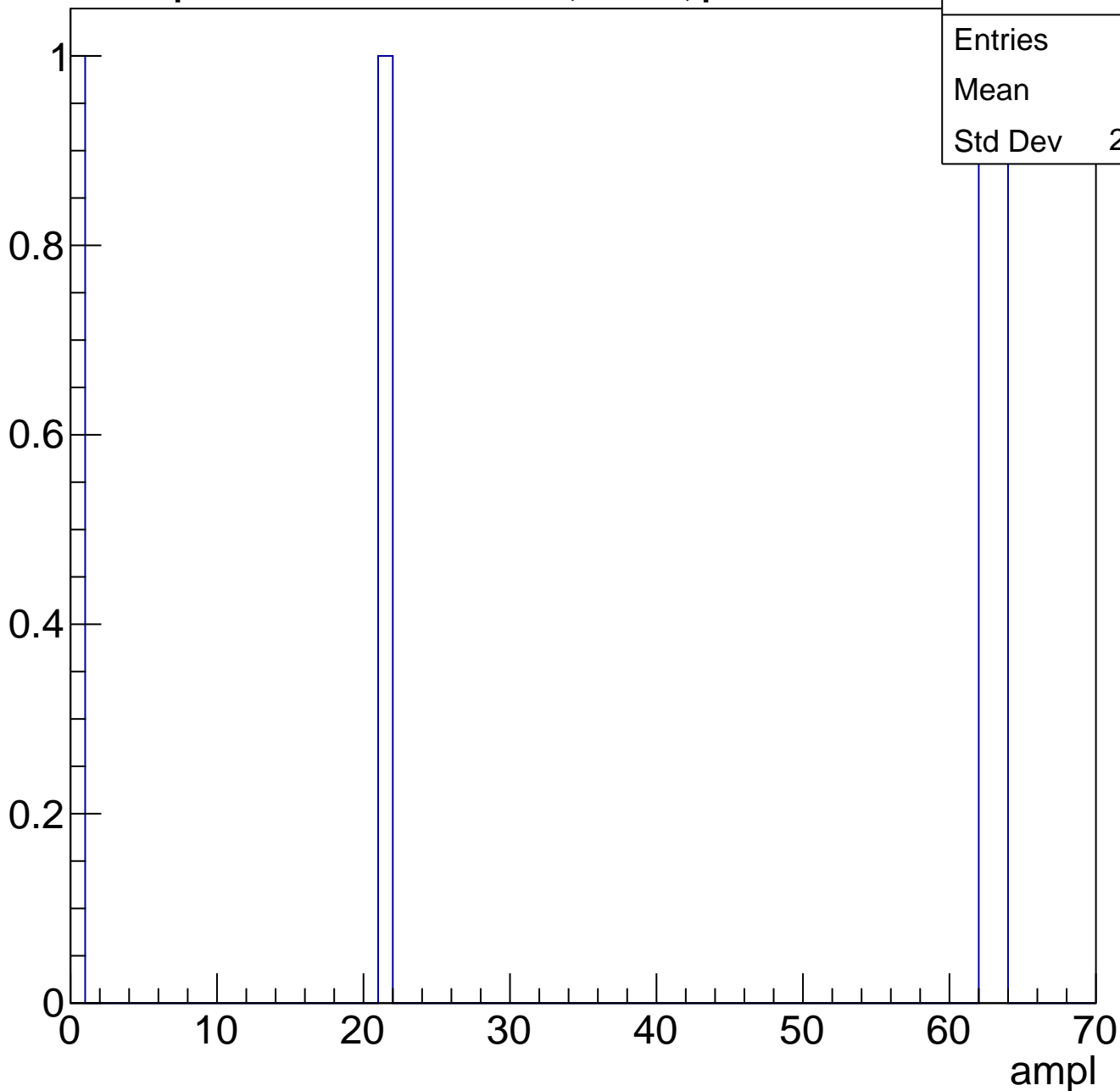




# B1L002S, U18-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch57, adc0

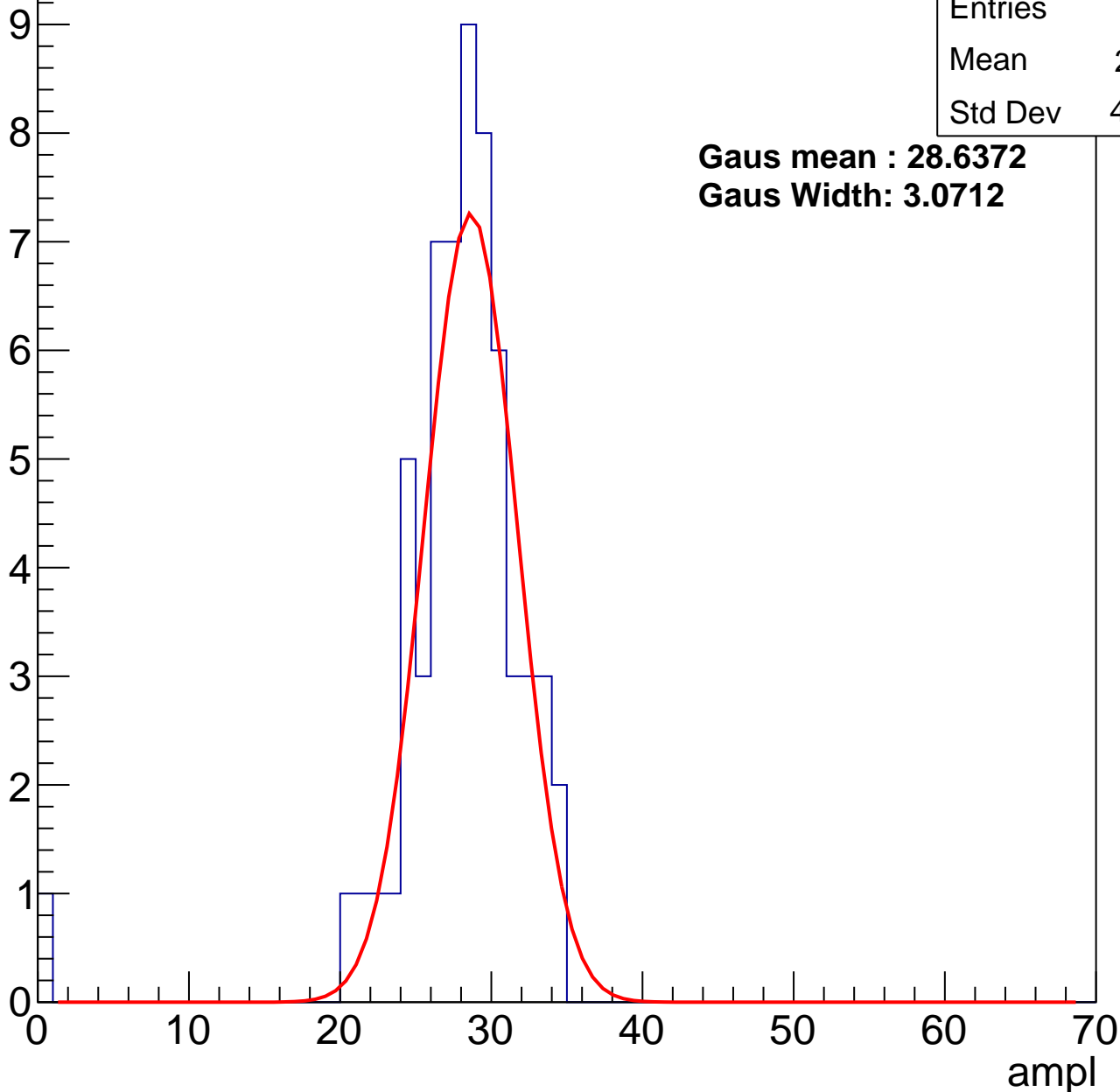
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.41
Std Dev	4.674

**Gaus mean : 28.6372**

**Gaus Width: 3.0712**



# B1L002S, U18-ch57, adc1

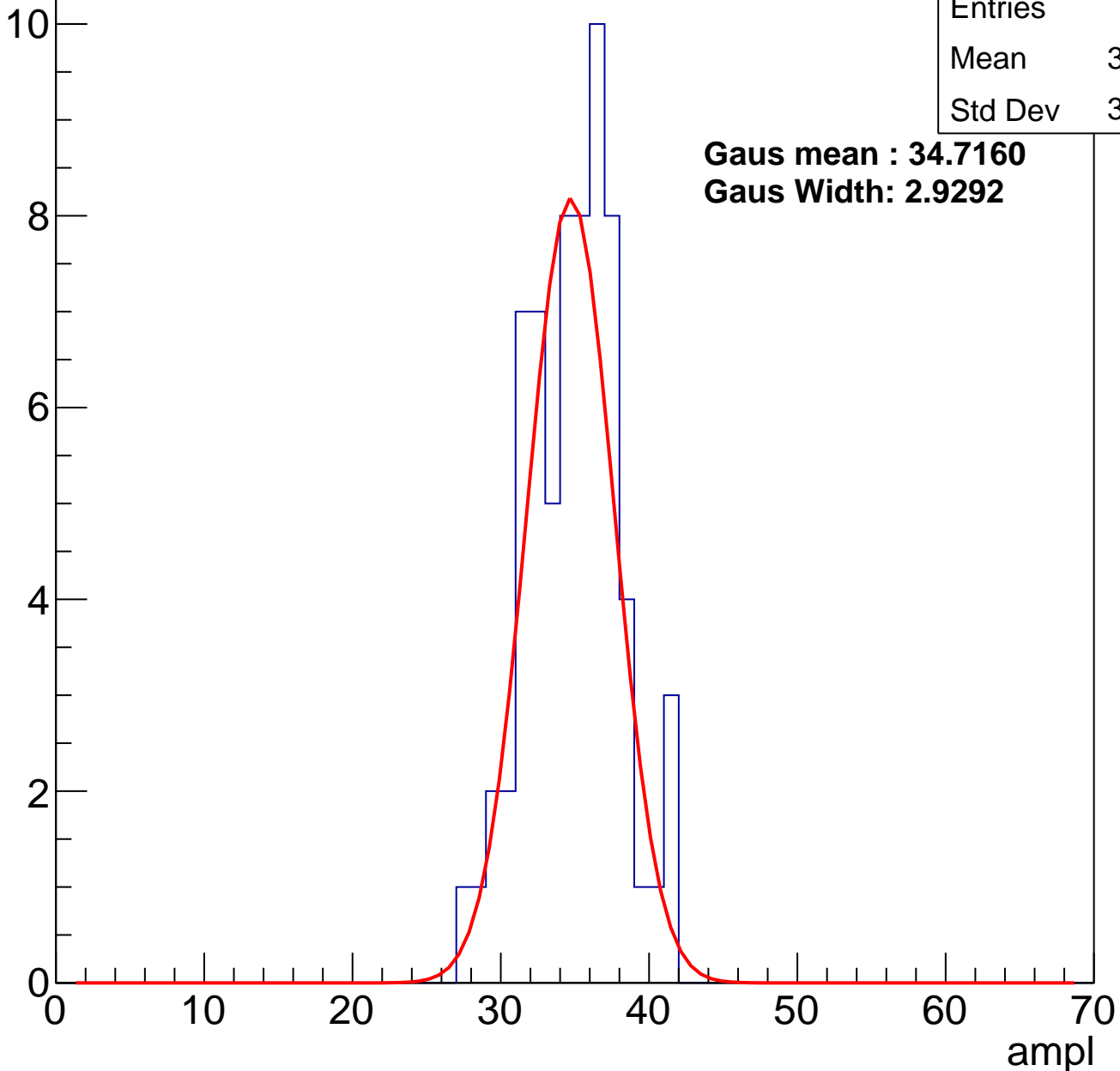
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	34.43
Std Dev	3.074

**Gaus mean : 34.7160**

**Gaus Width: 2.9292**

Entry



# B1L002S, U18-ch57, adc2

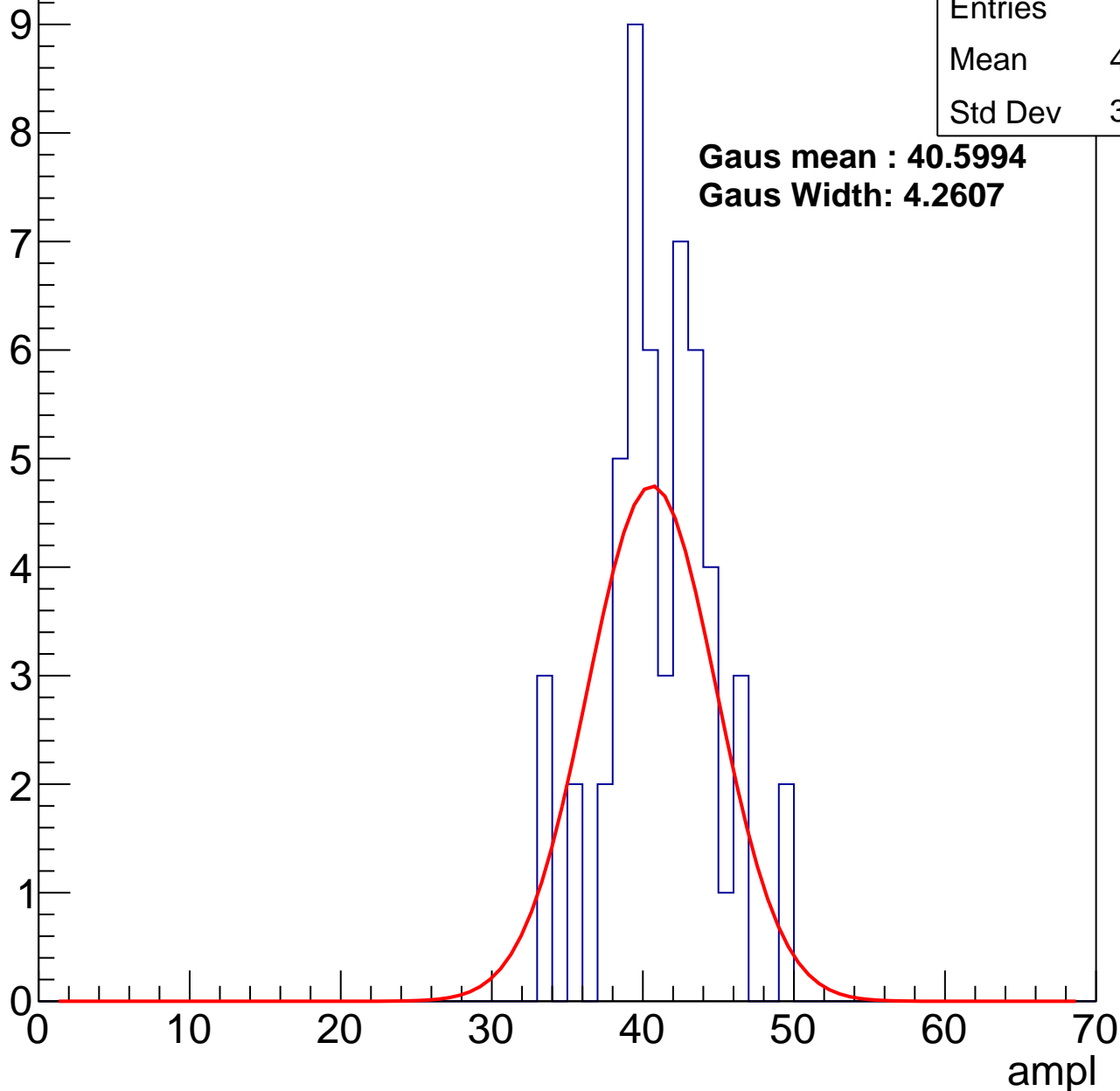
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.68
Std Dev	3.538

**Gaus mean : 40.5994**

**Gaus Width: 4.2607**



# B1L002S, U18-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	46.46
Std Dev	3.511

Entry

10

8

6

4

2

0

0

10

20

30

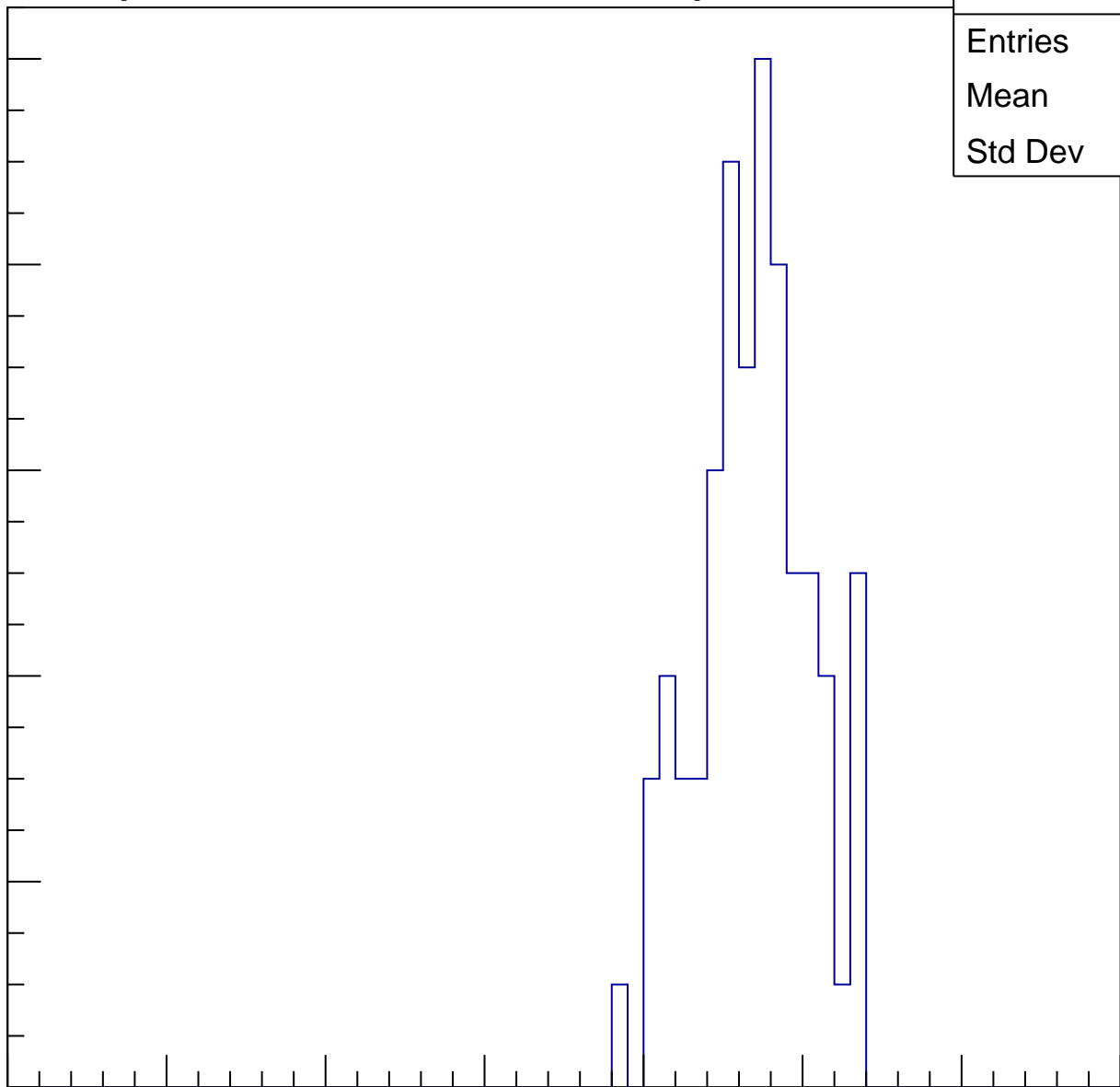
40

50

60

70

ampl

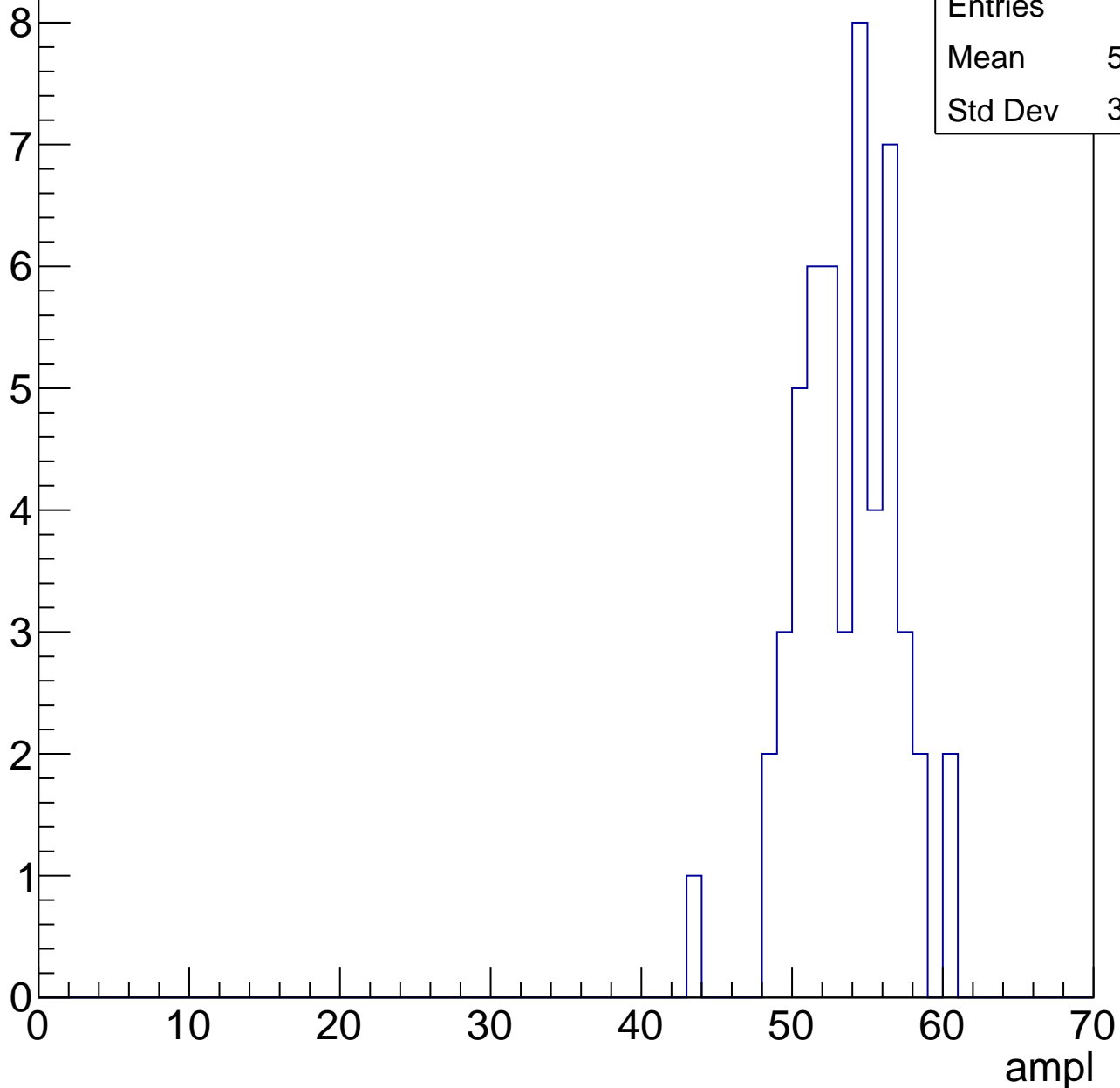


# B1L002S, U18-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.15
Std Dev	3.266

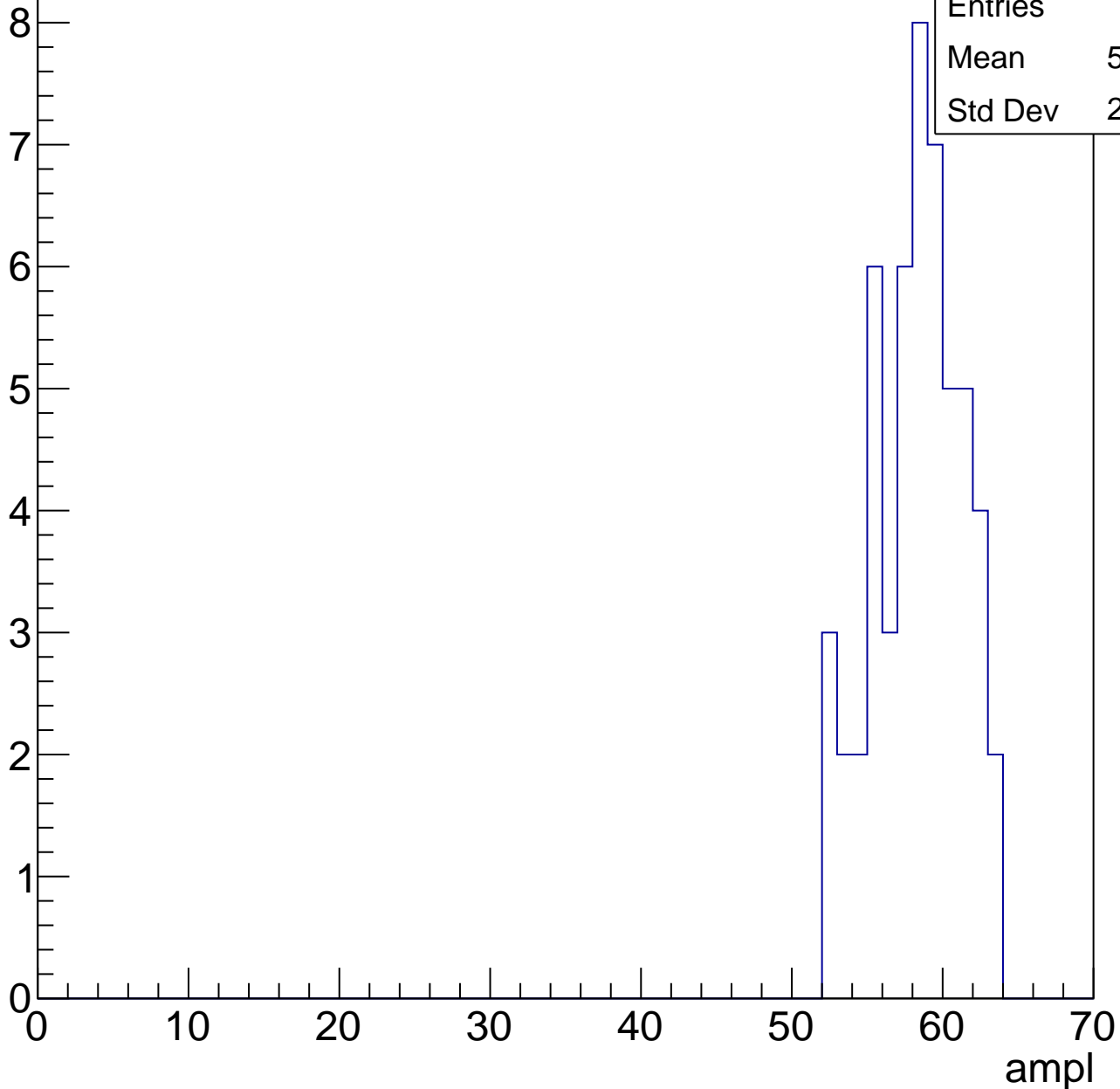


# B1L002S, U18-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.85
Std Dev	2.903

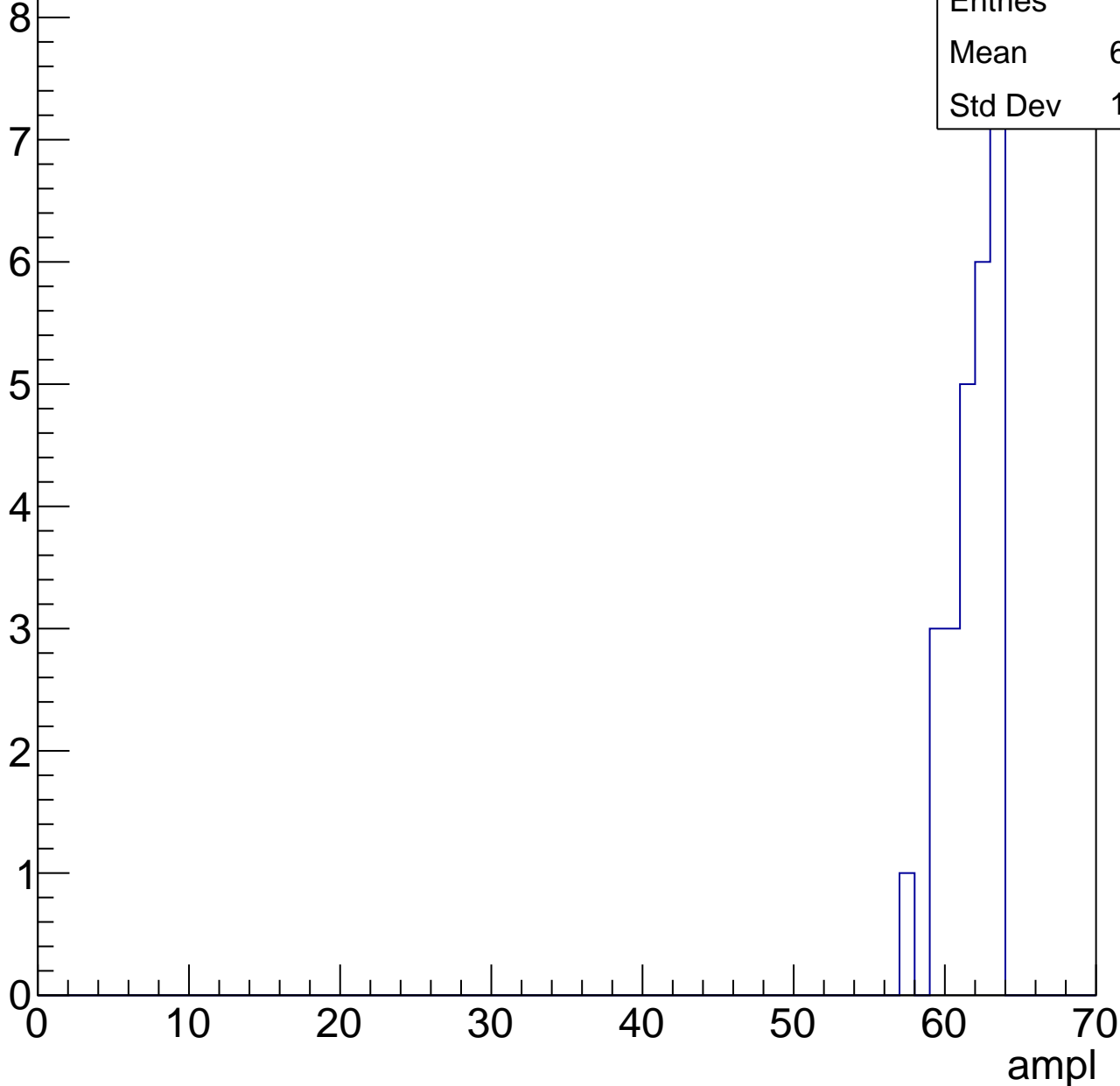


# B1L002S, U18-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.35
Std Dev	1.592





# B1L002S, U18-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch58, adc0

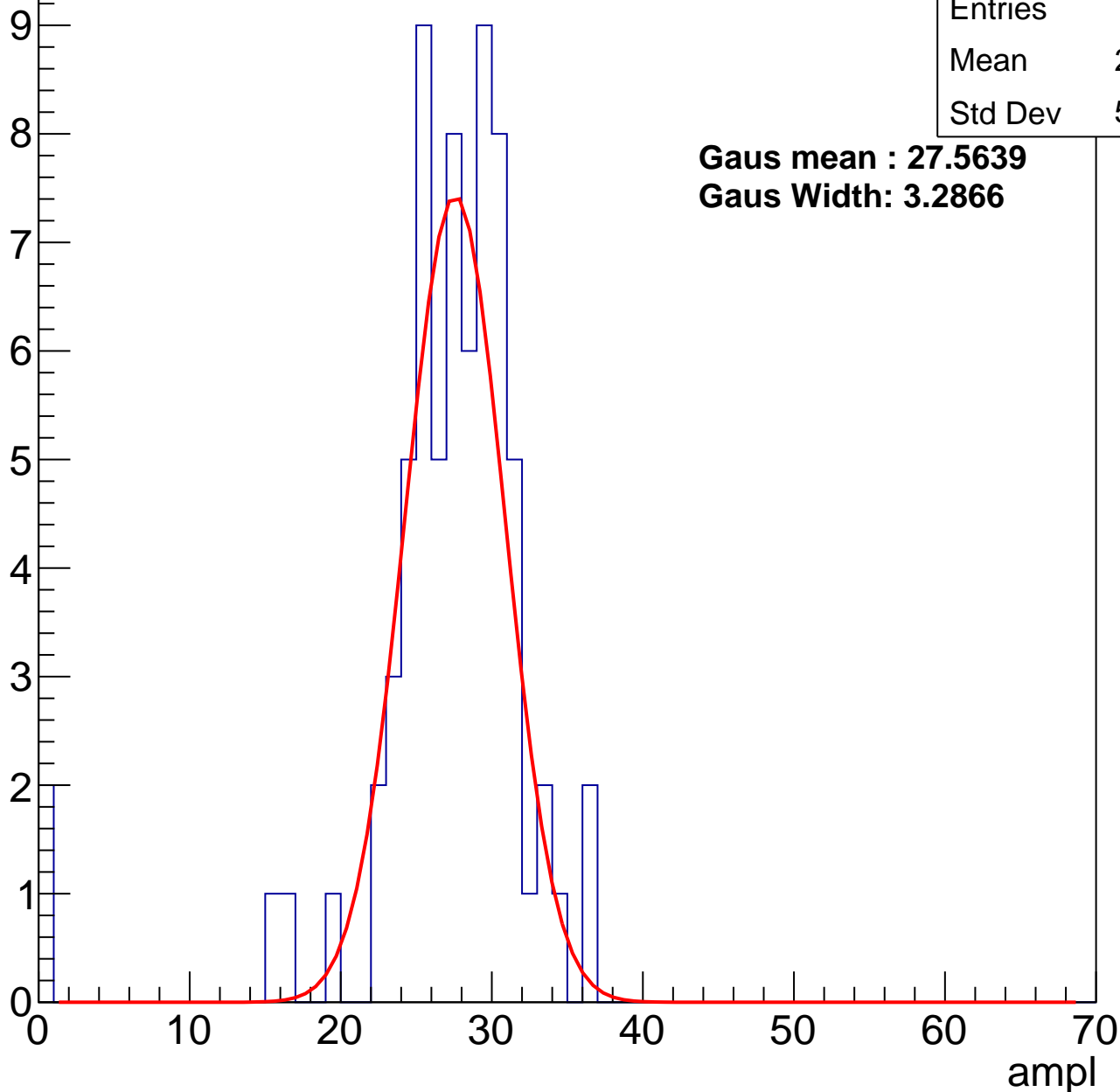
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	26.51
Std Dev	5.881

**Gaus mean : 27.5639**

**Gaus Width: 3.2866**



# B1L002S, U18-ch58, adc1

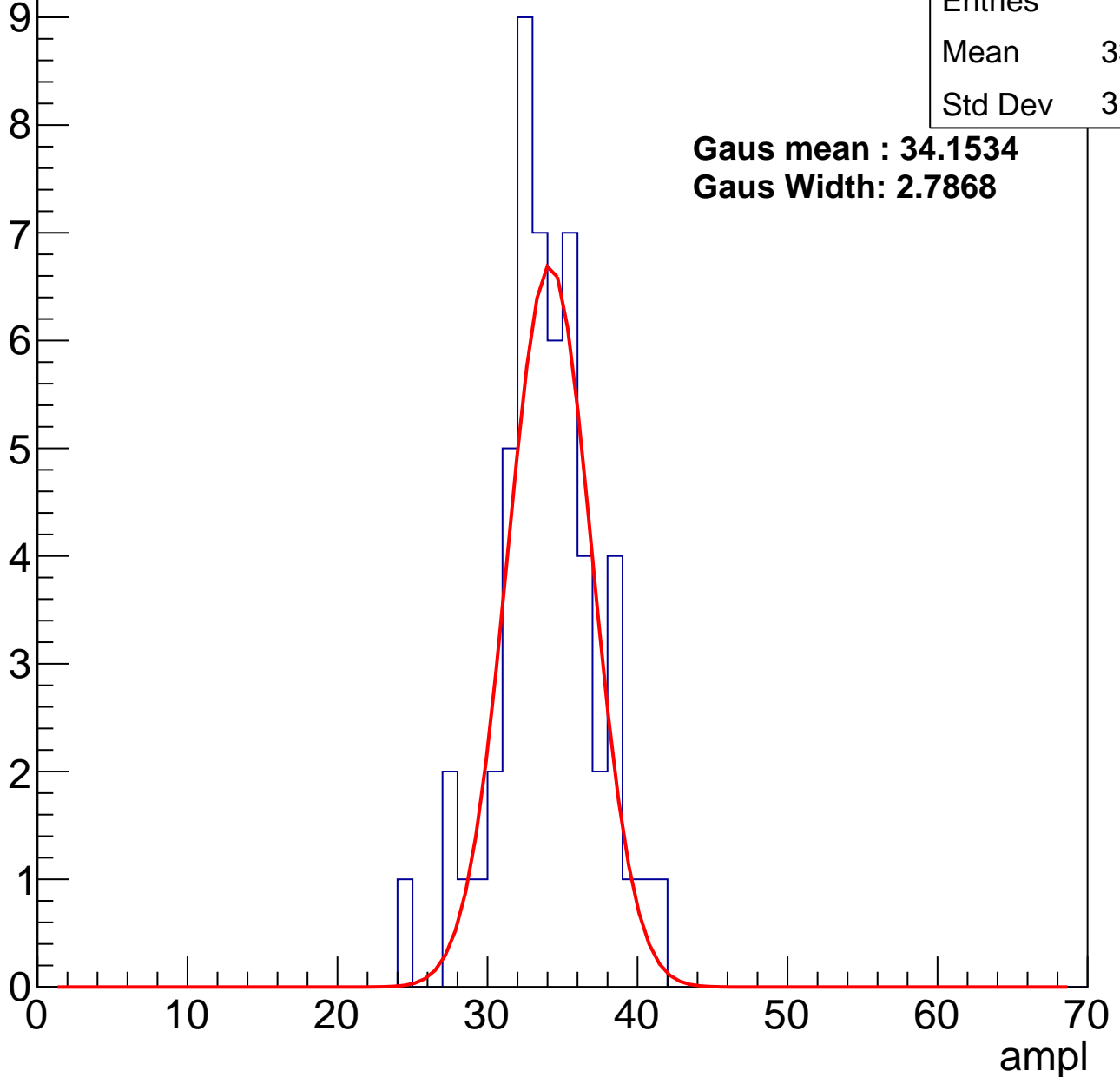
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	33.48
Std Dev	3.276

**Gaus mean : 34.1534**

**Gaus Width: 2.7868**



# B1L002S, U18-ch58, adc2

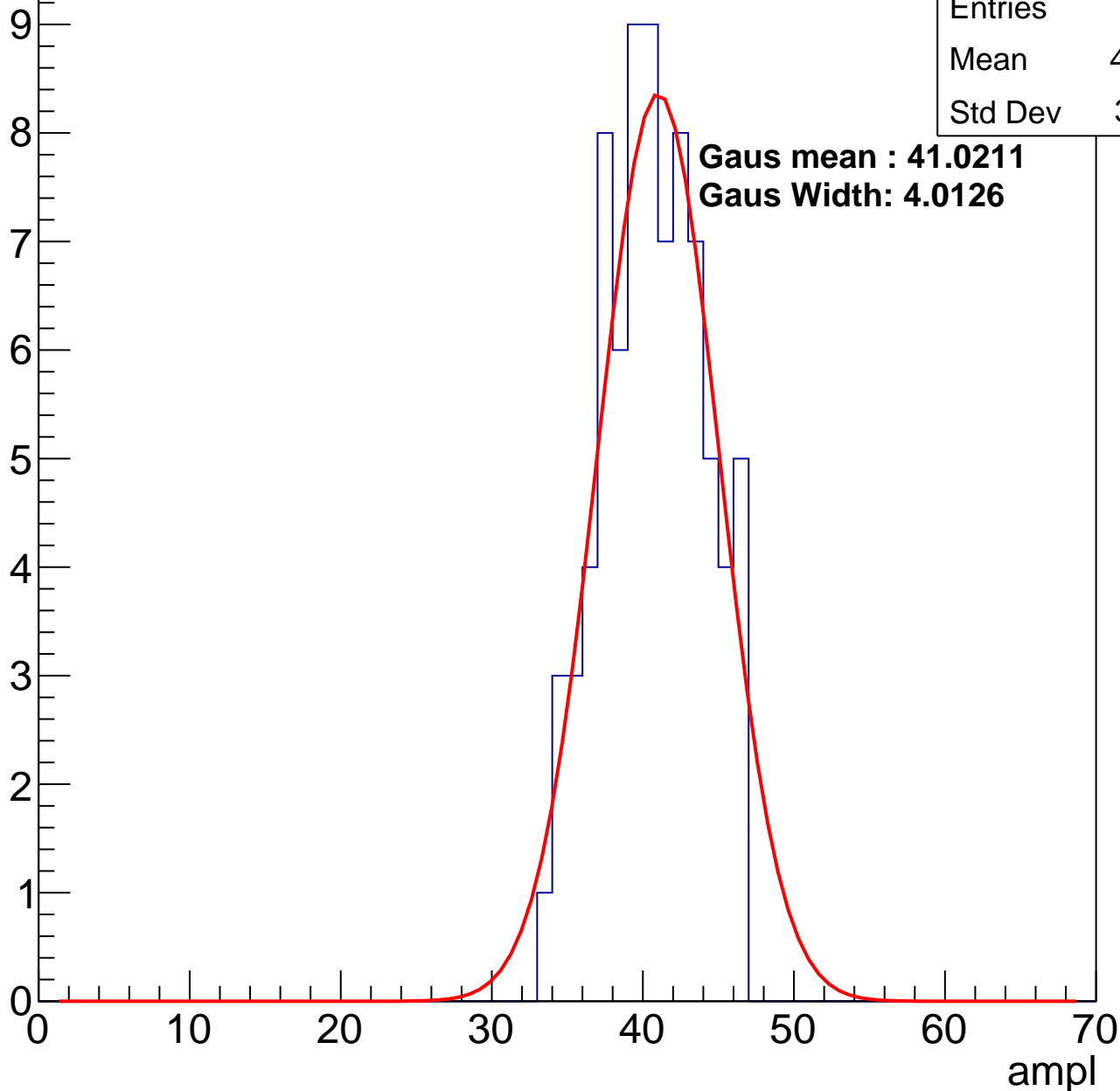
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	40.16
Std Dev	3.301

**Gaus mean : 41.0211**

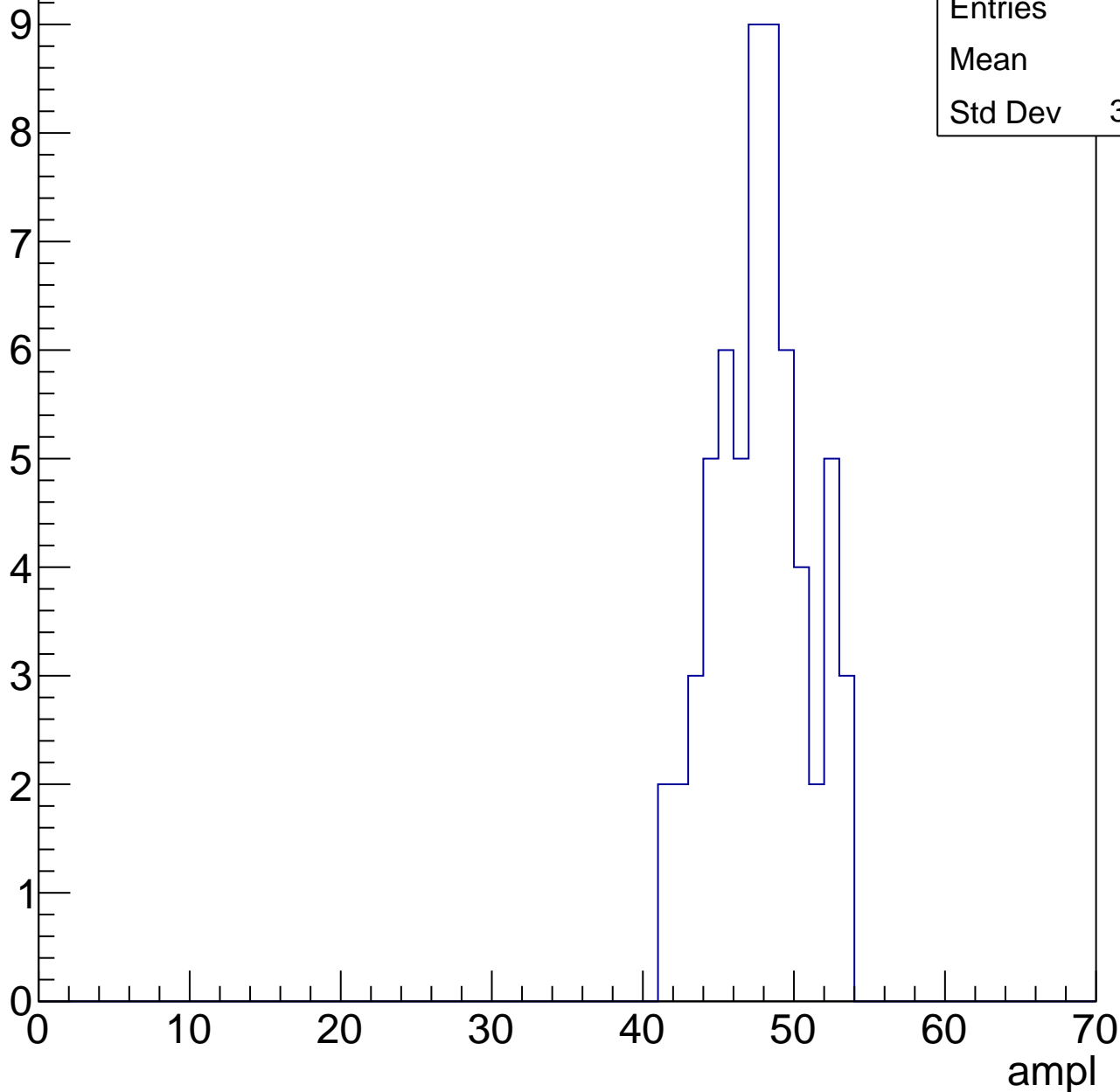
**Gaus Width: 4.0126**



# B1L002S, U18-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

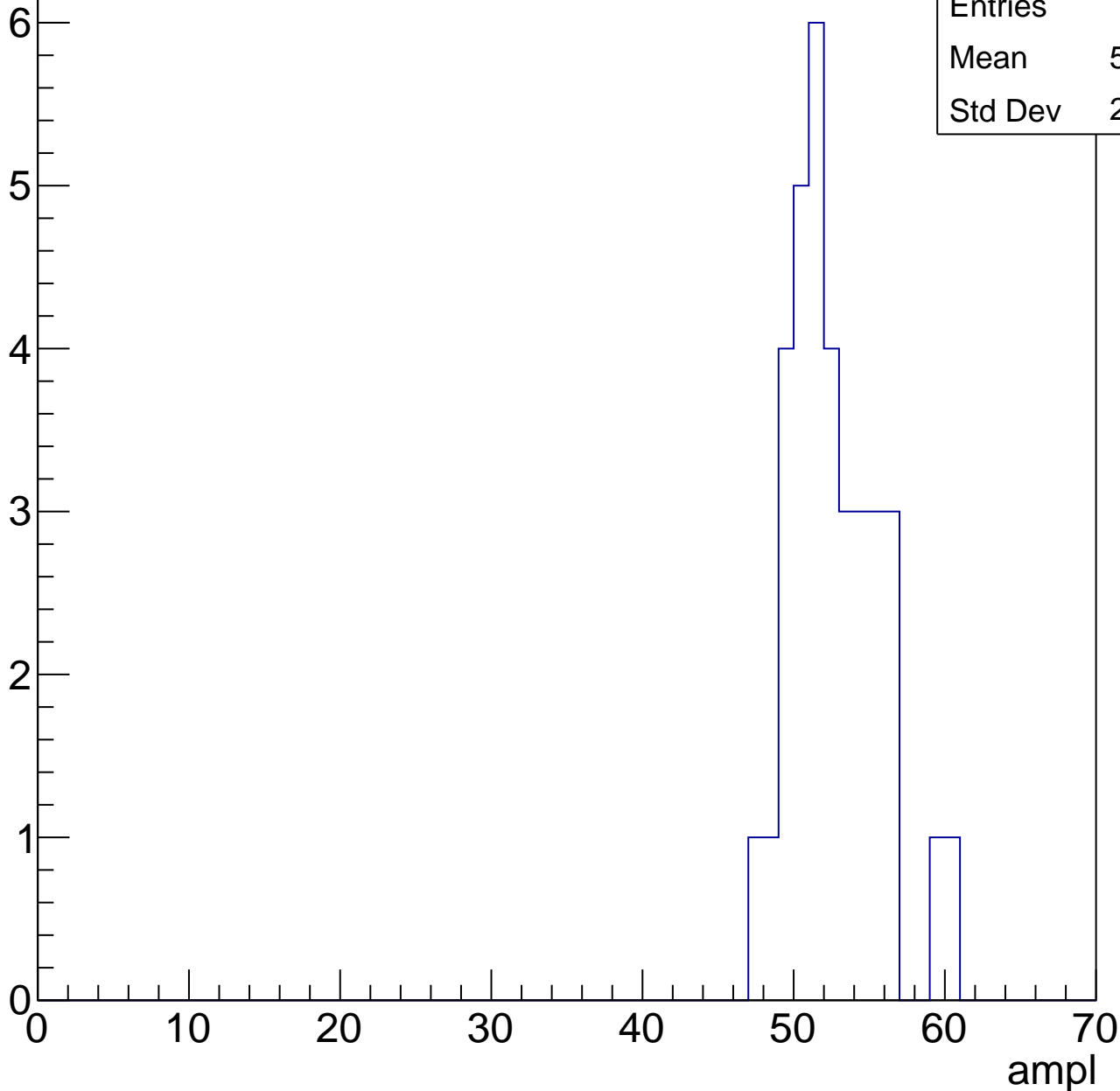


# B1L002S, U18-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	52.23
Std Dev	2.948

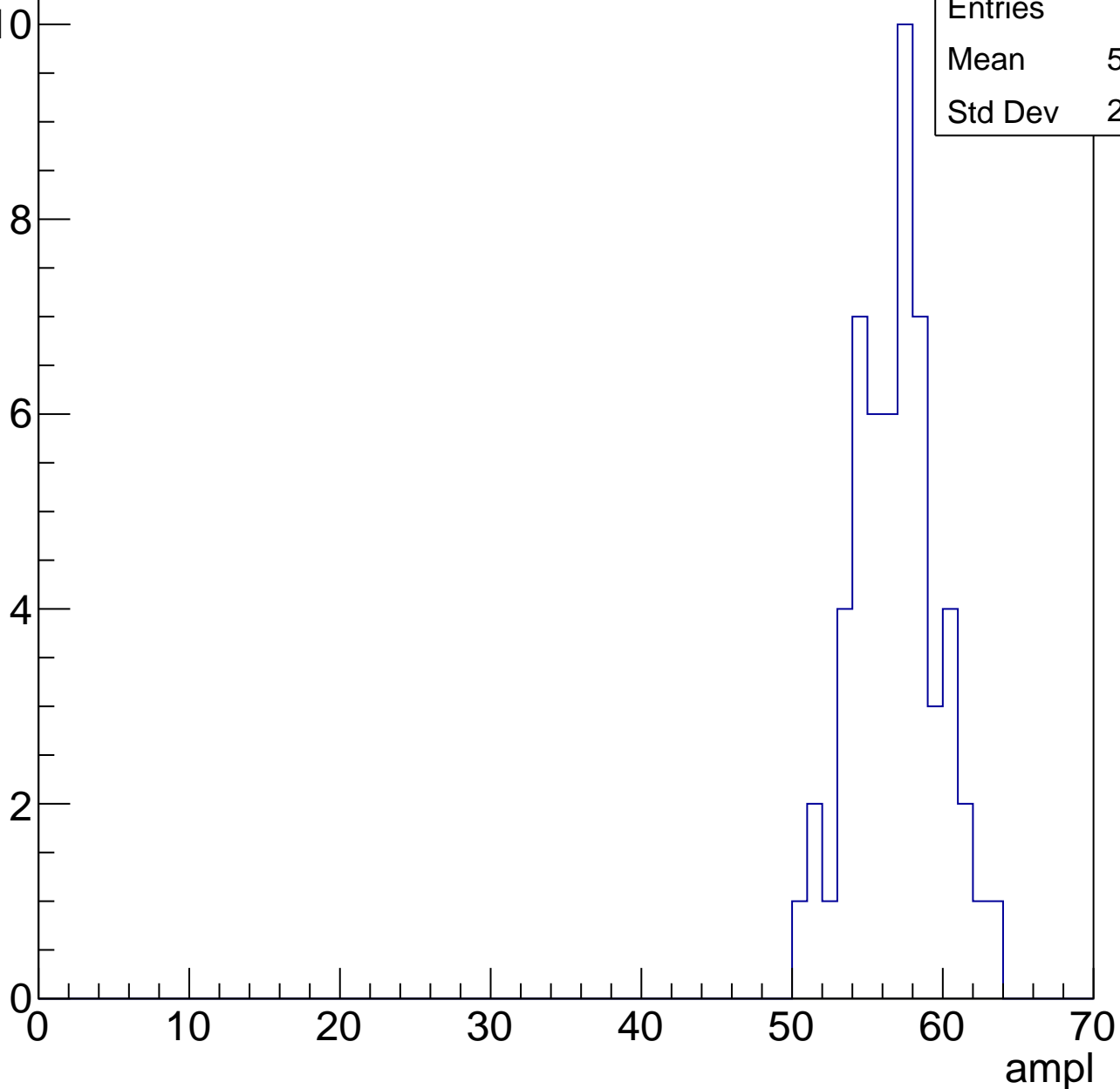


# B1L002S, U18-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	56.36
Std Dev	2.792



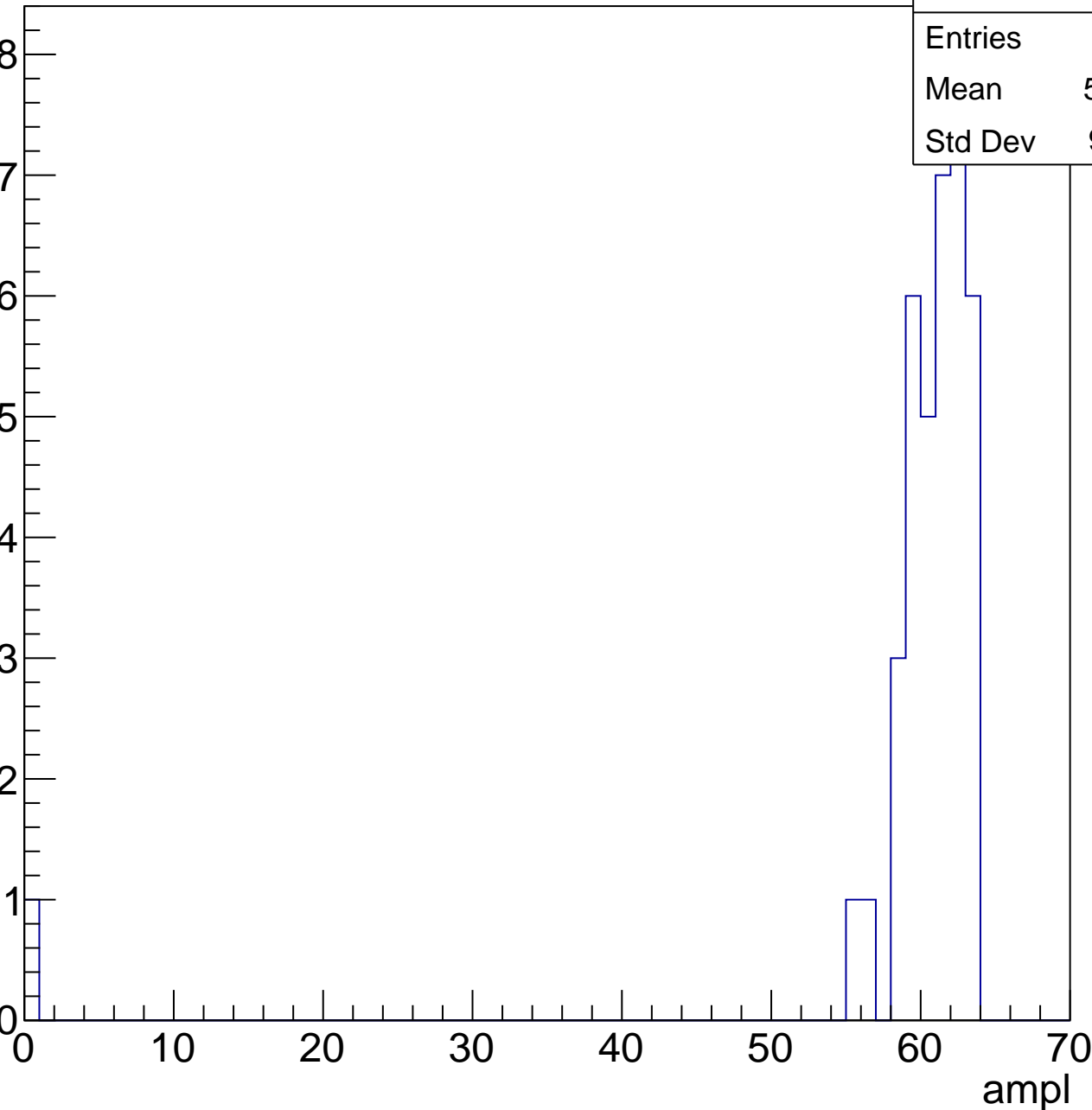
# B1L002S, U18-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	58.95
Std Dev	9.881

8  
7  
6  
5  
4  
3  
2  
1  
0

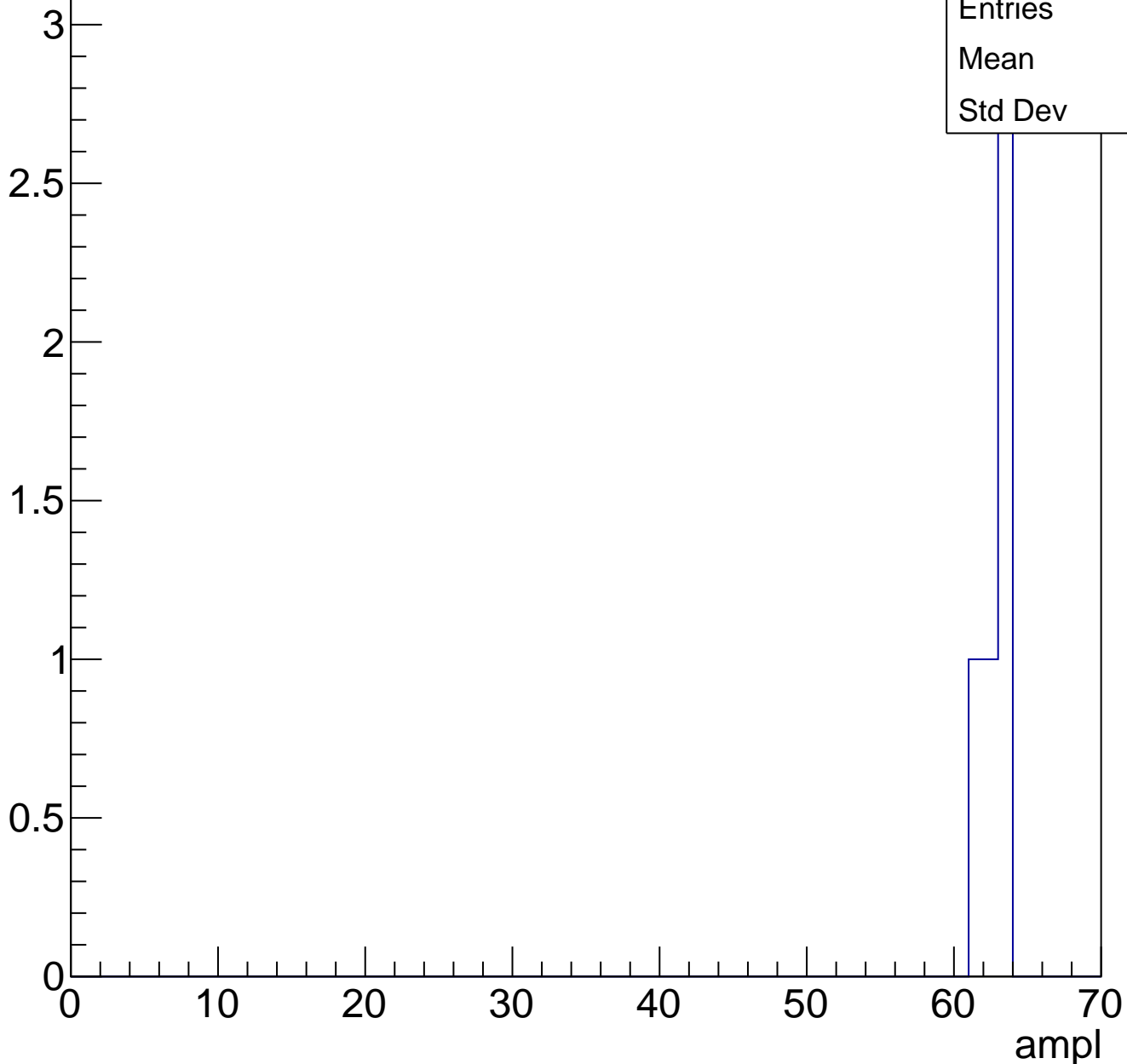




# B1L002S, U18-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	62.4
Std Dev	0.8

# B1L002S, U18-ch59, adc0

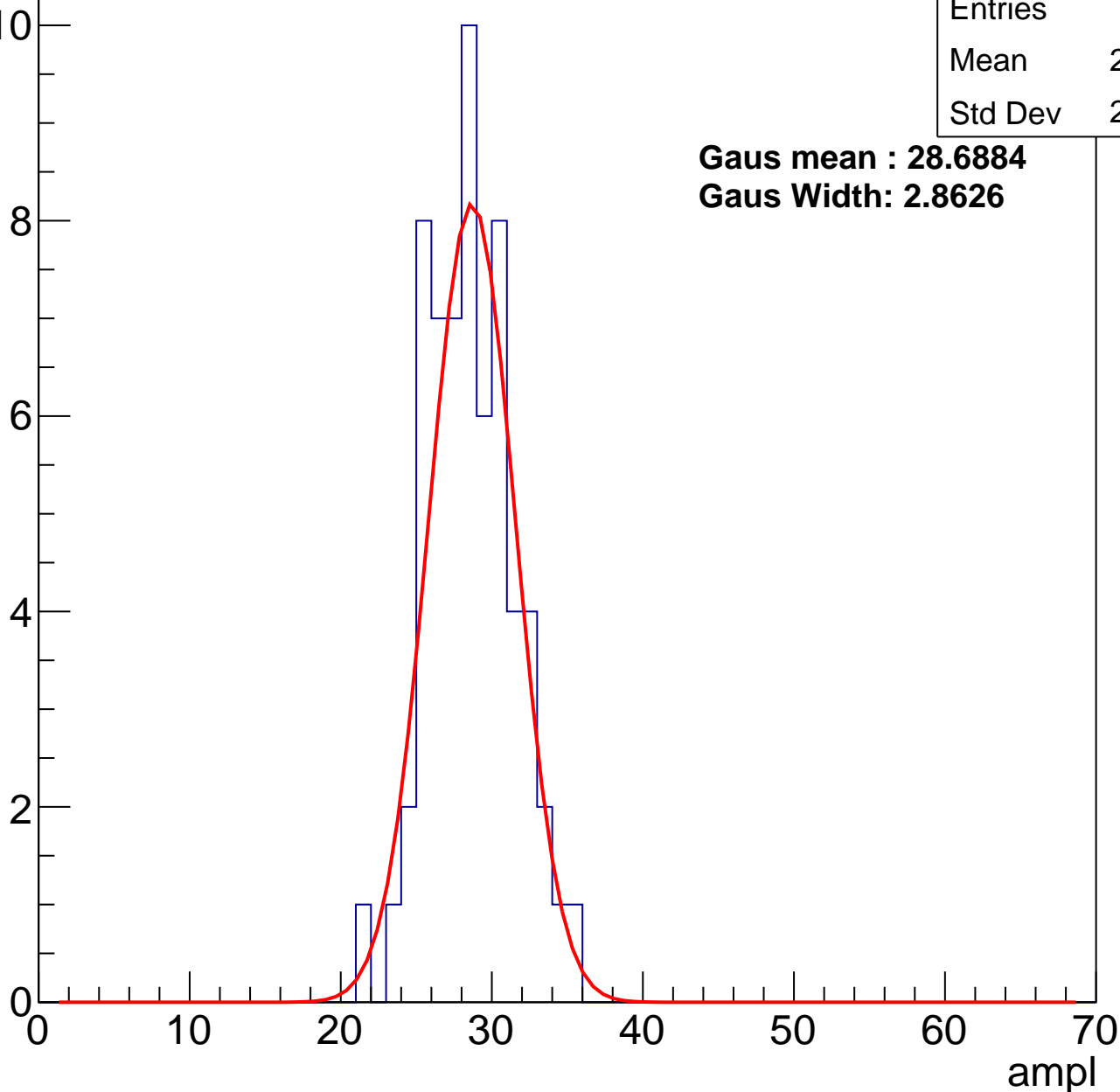
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.13
Std Dev	2.797

**Gaus mean : 28.6884**

**Gaus Width: 2.8626**



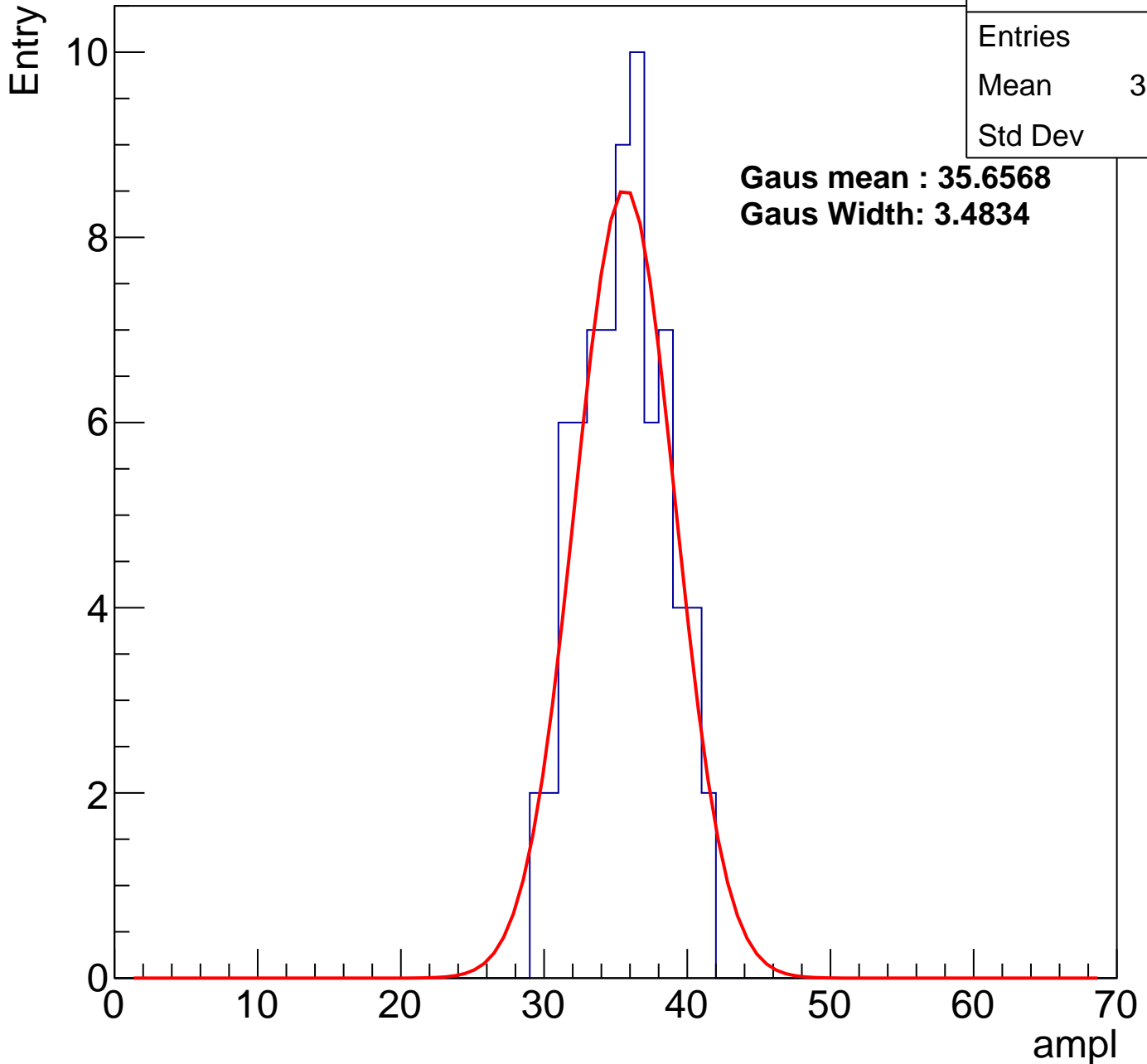
# B1L002S, U18-ch59, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	35.08
Std Dev	2.98

**Gaus mean : 35.6568**

**Gaus Width: 3.4834**



# B1L002S, U18-ch59, adc2

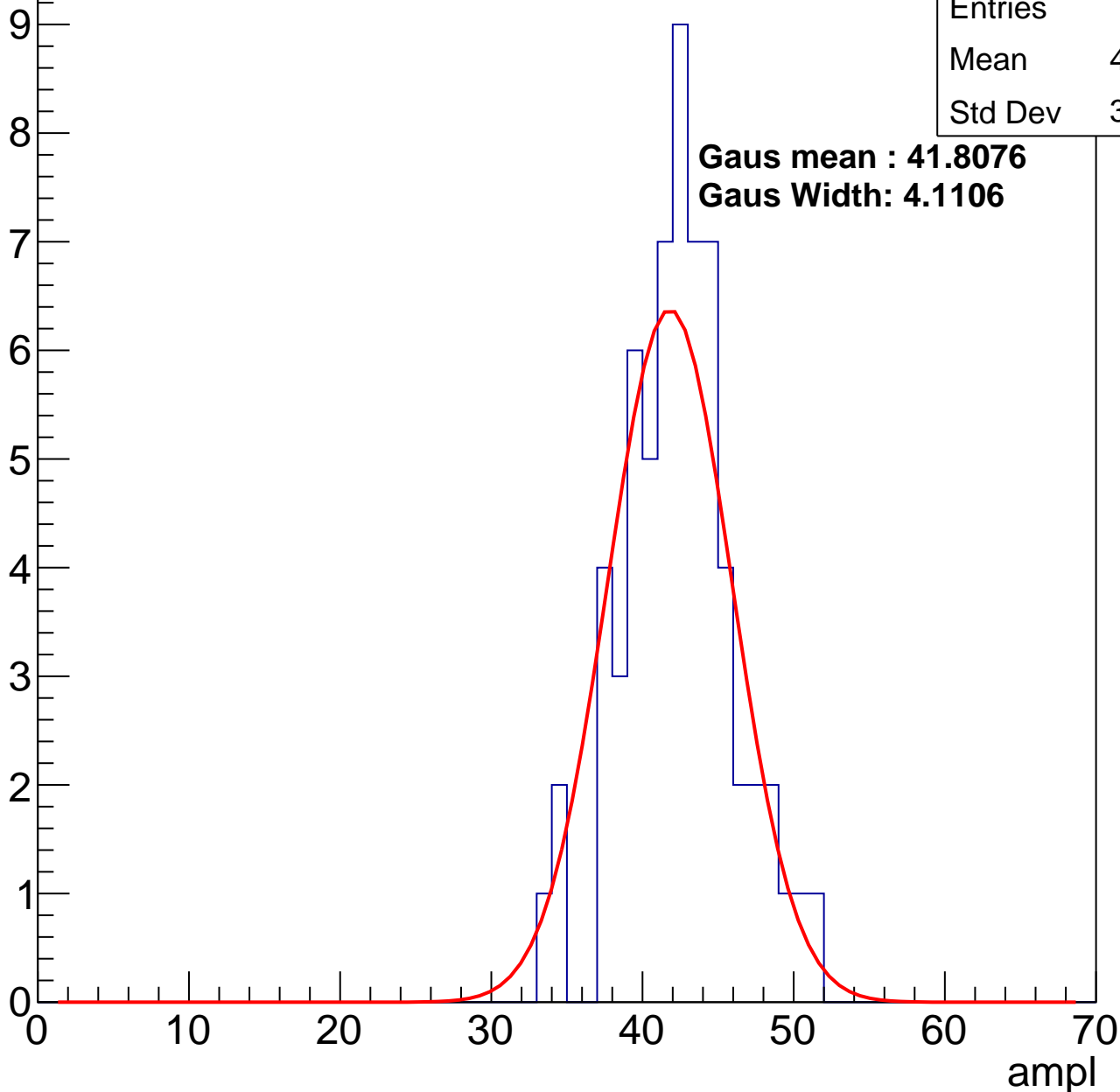
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.92
Std Dev	3.658

**Gaus mean : 41.8076**

**Gaus Width: 4.1106**

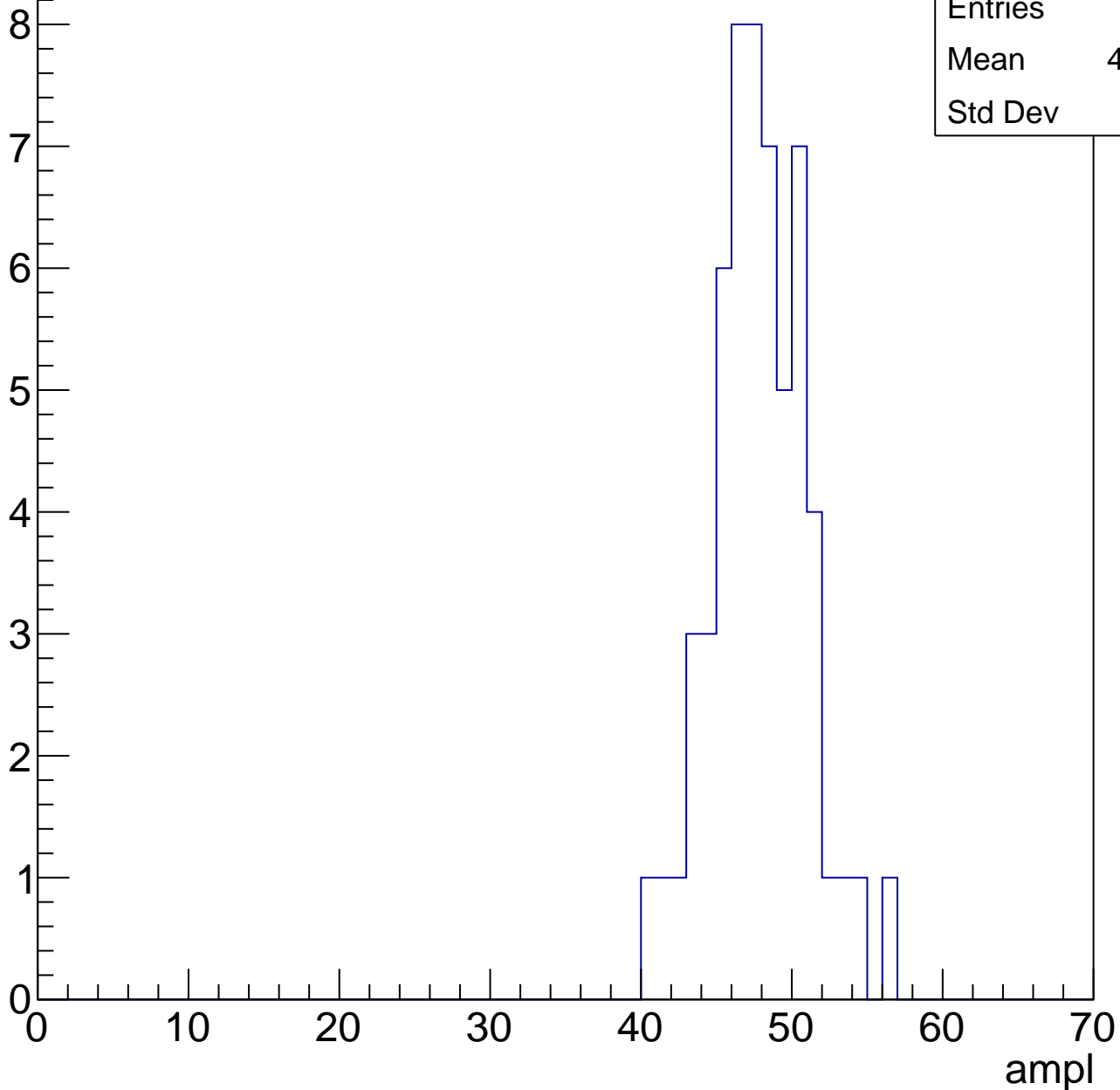


# B1L002S, U18-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	47.38
Std Dev	3.09

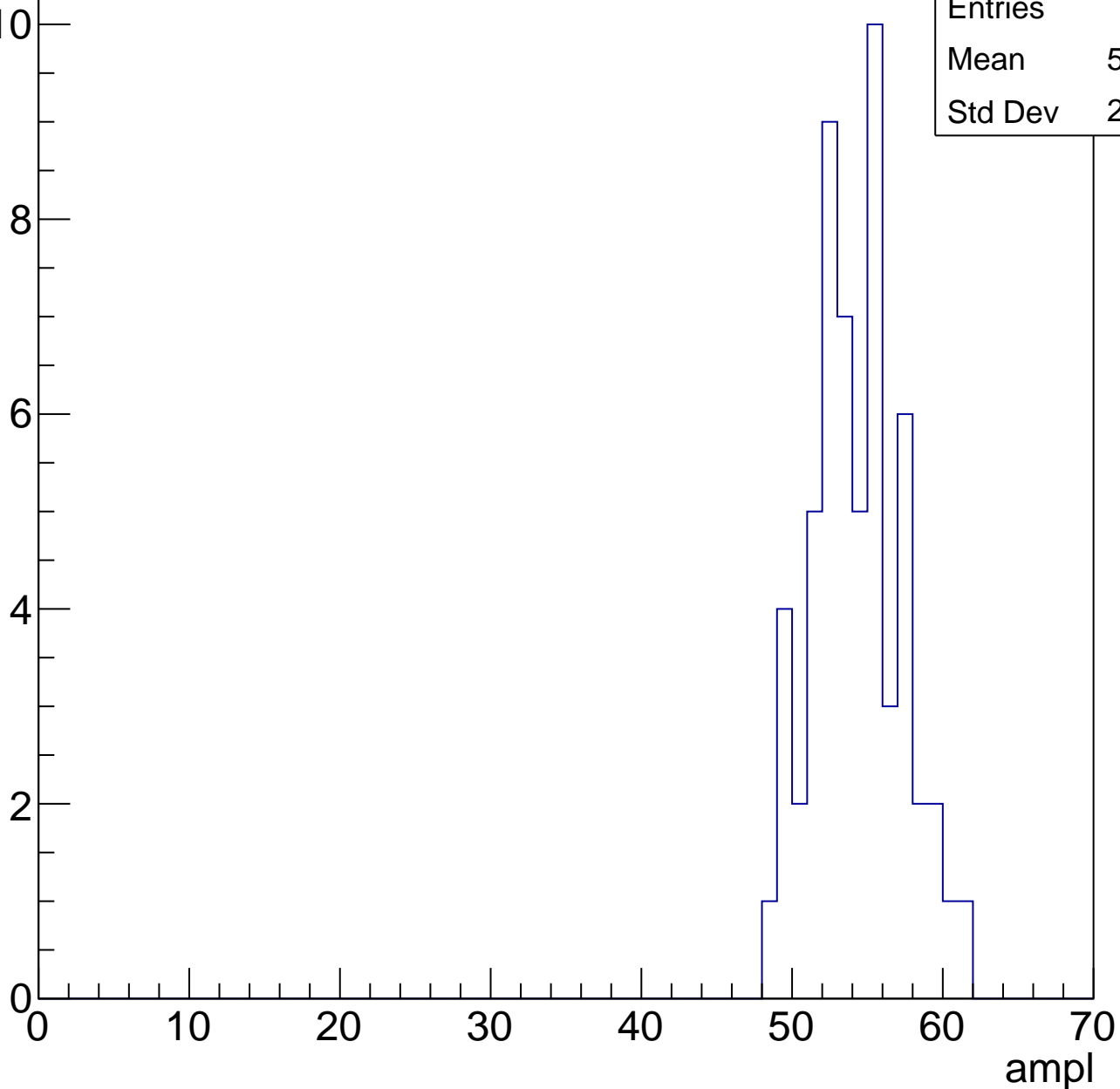


# B1L002S, U18-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.84
Std Dev	2.929



# B1L002S, U18-ch59, adc5

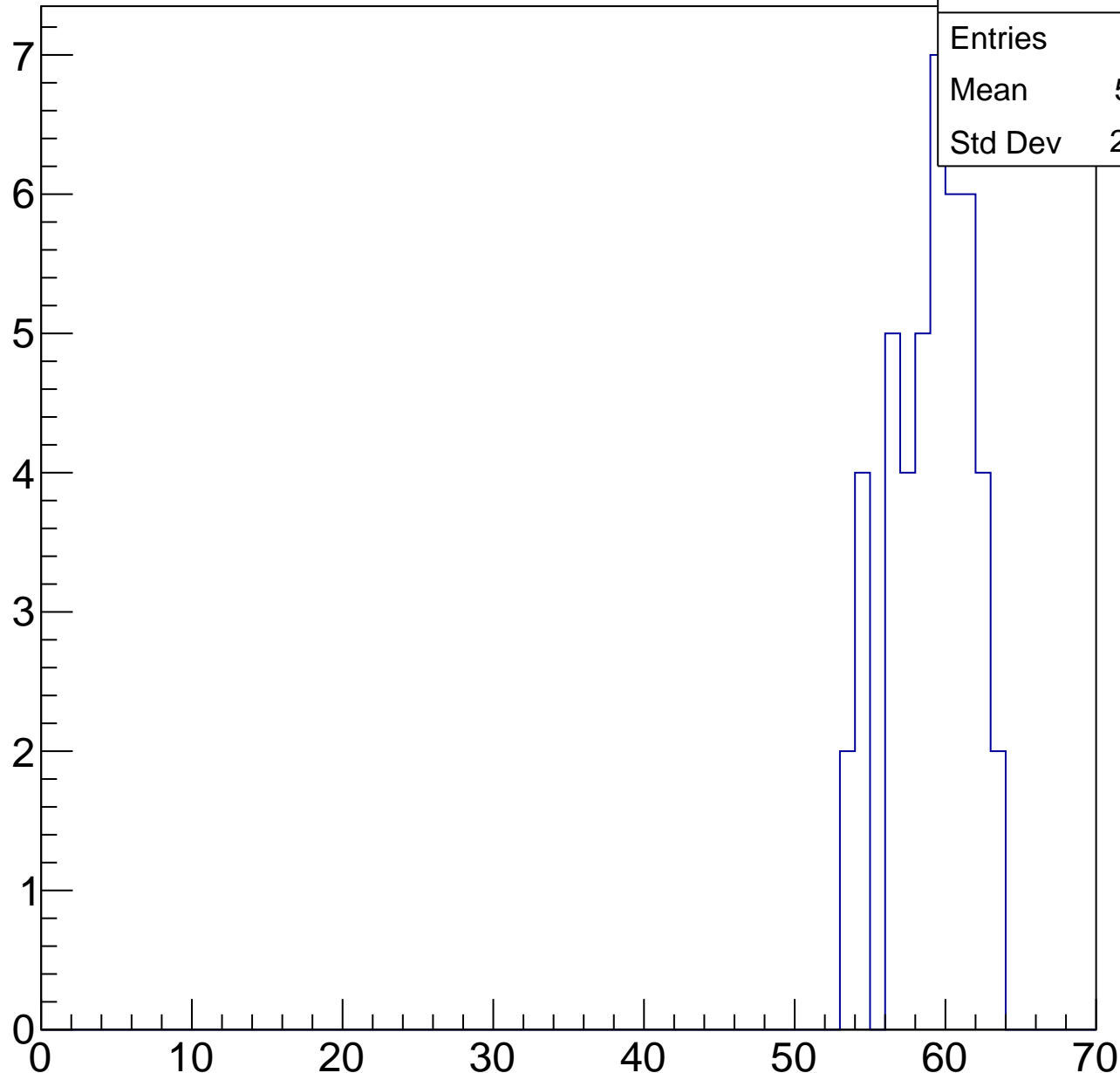
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.51
Std Dev	2.688

ampl

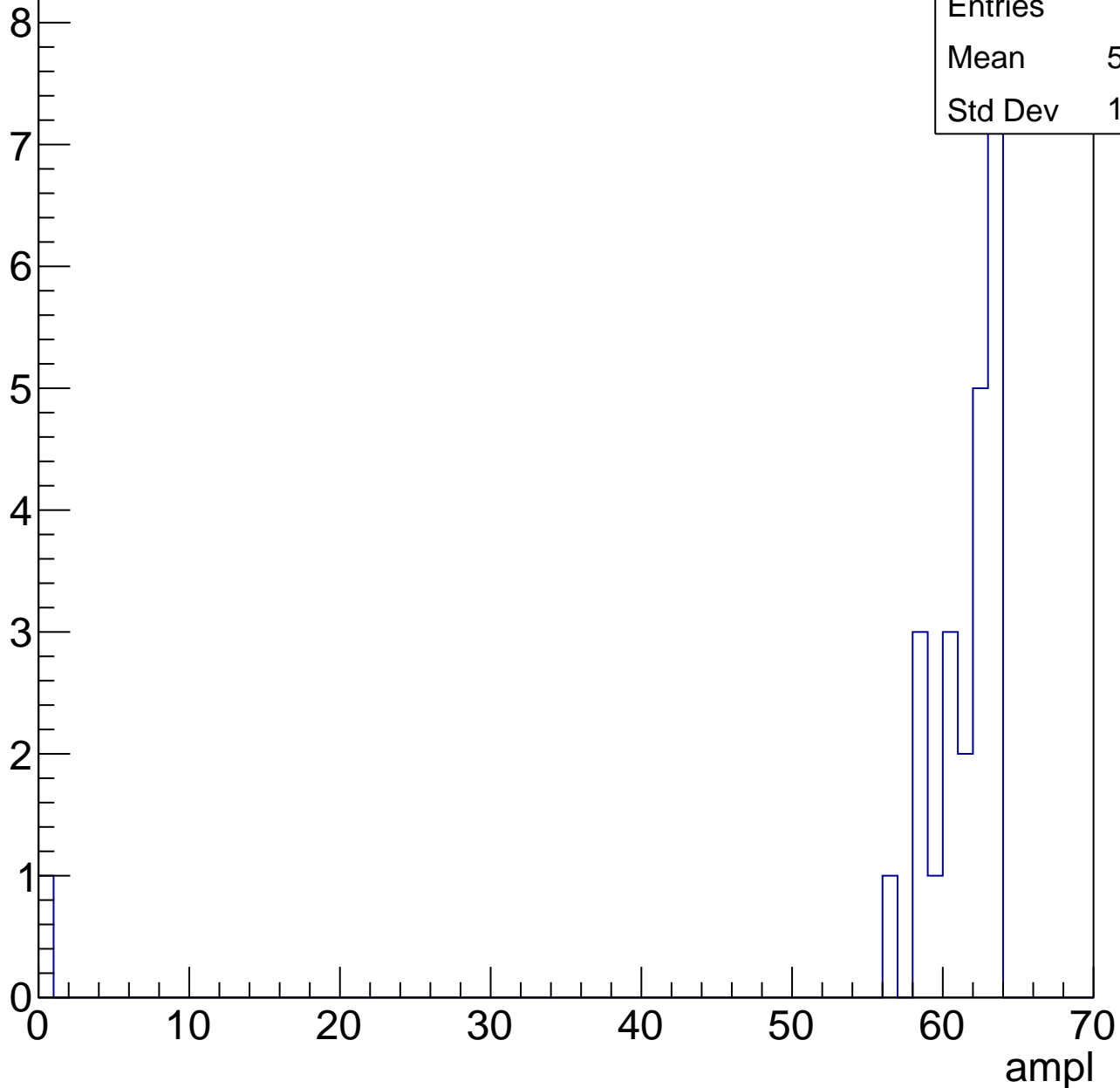


# B1L002S, U18-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.54
Std Dev	12.37

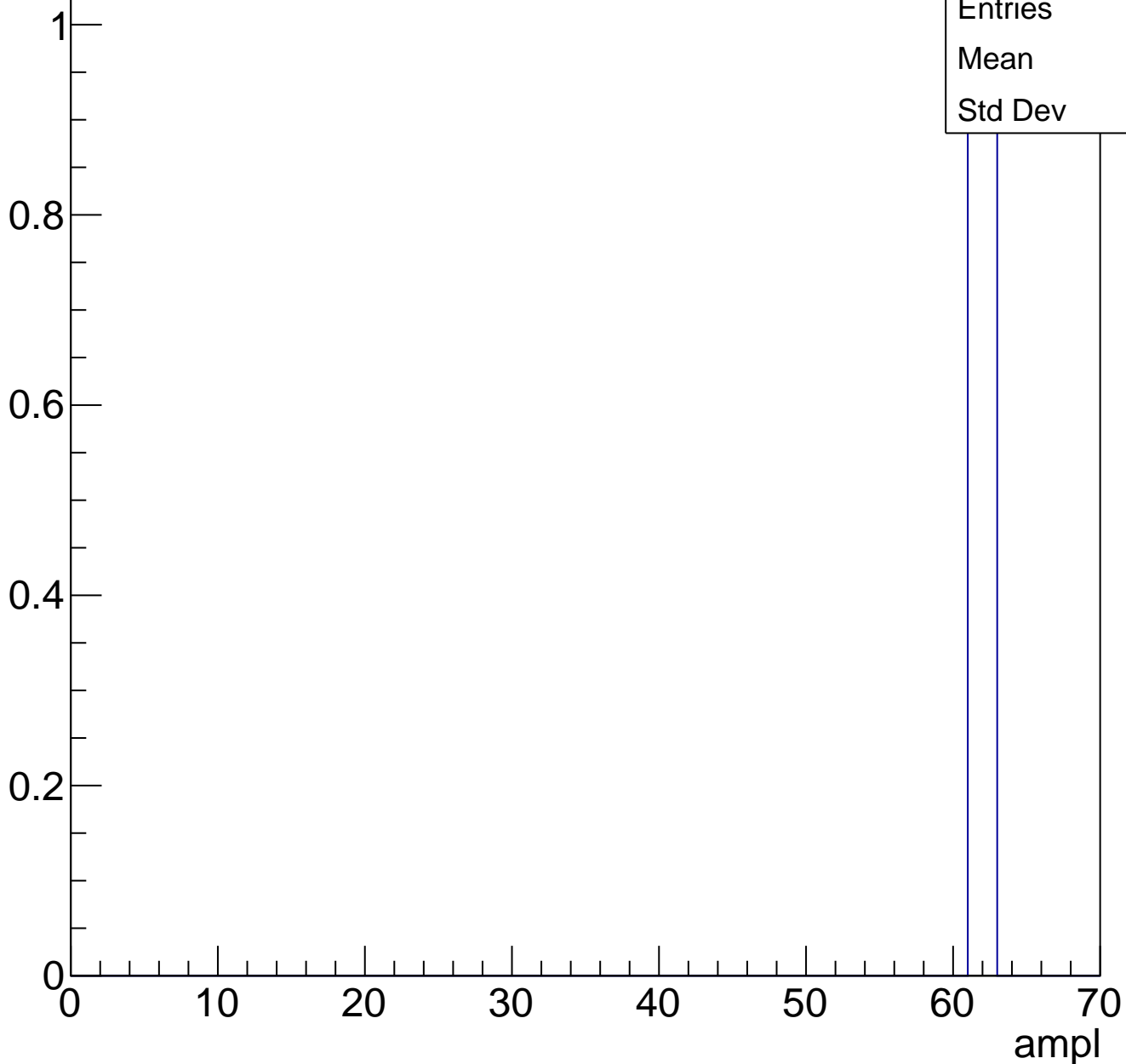




# B1L002S, U18-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch60, adc0

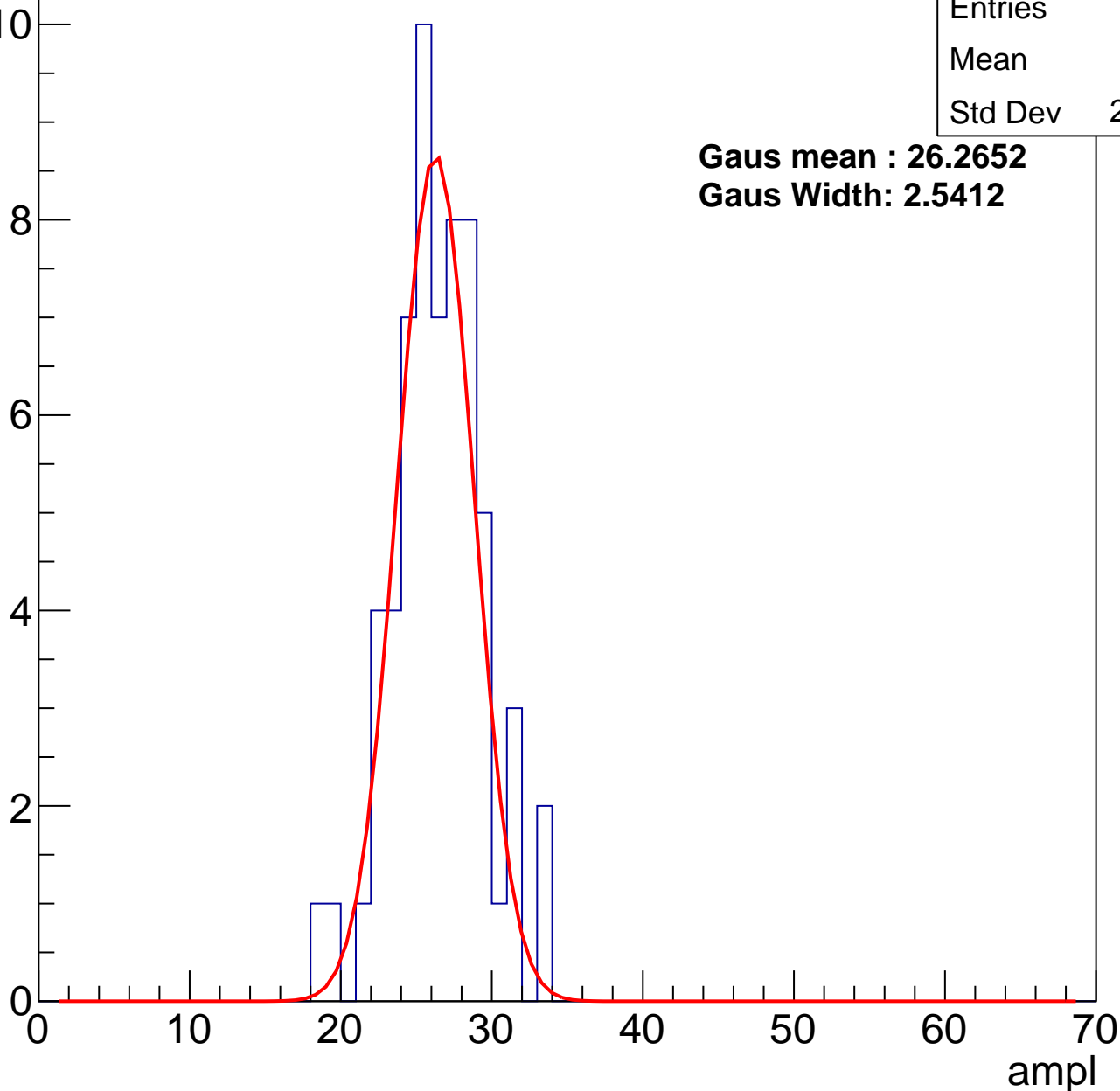
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26
Std Dev	2.978

**Gaus mean : 26.2652**

**Gaus Width: 2.5412**



# B1L002S, U18-ch60, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	33.39
Std Dev	3.37

**Gaus mean : 33.9192**

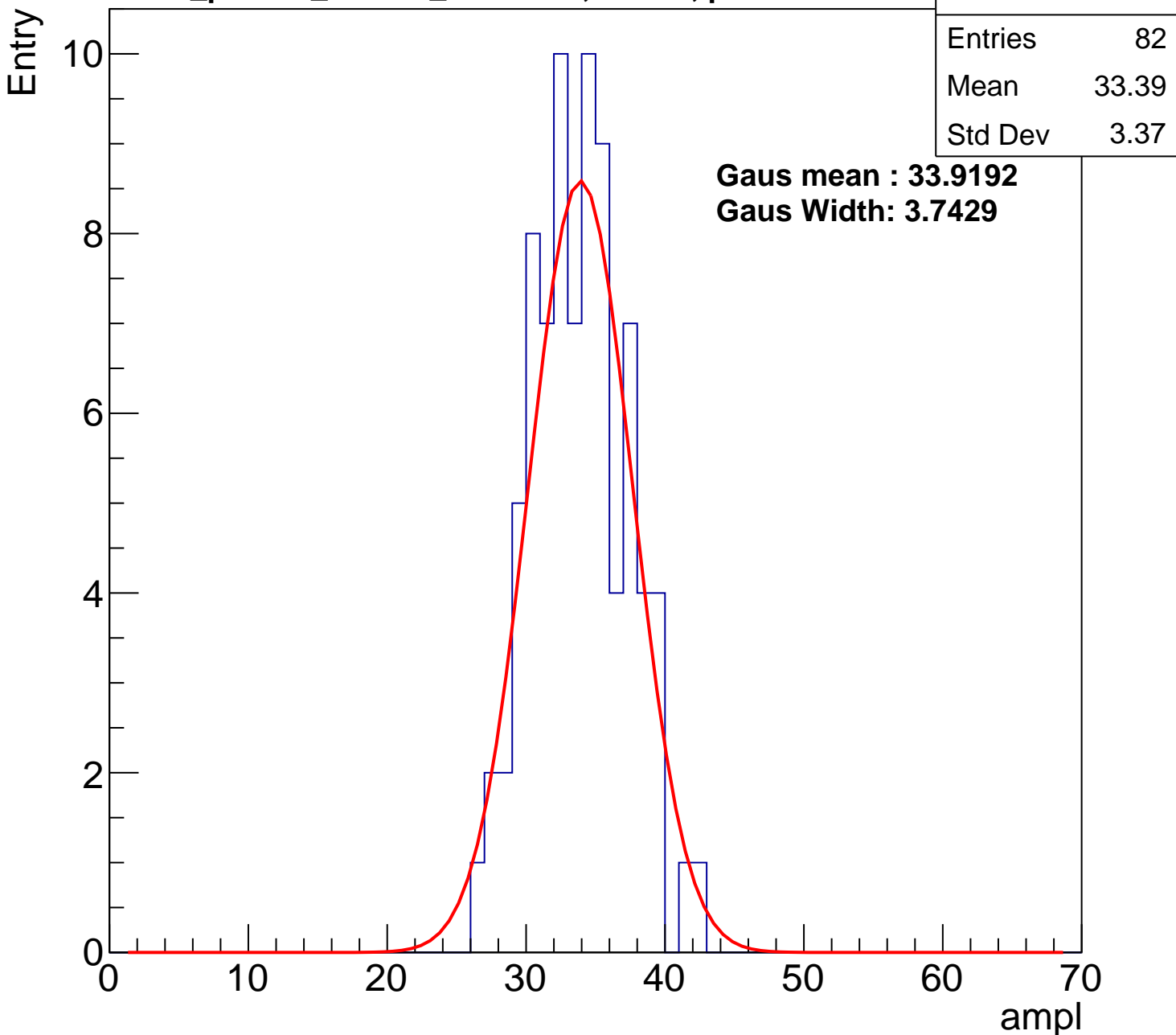
**Gaus Width: 3.7429**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch60, adc2

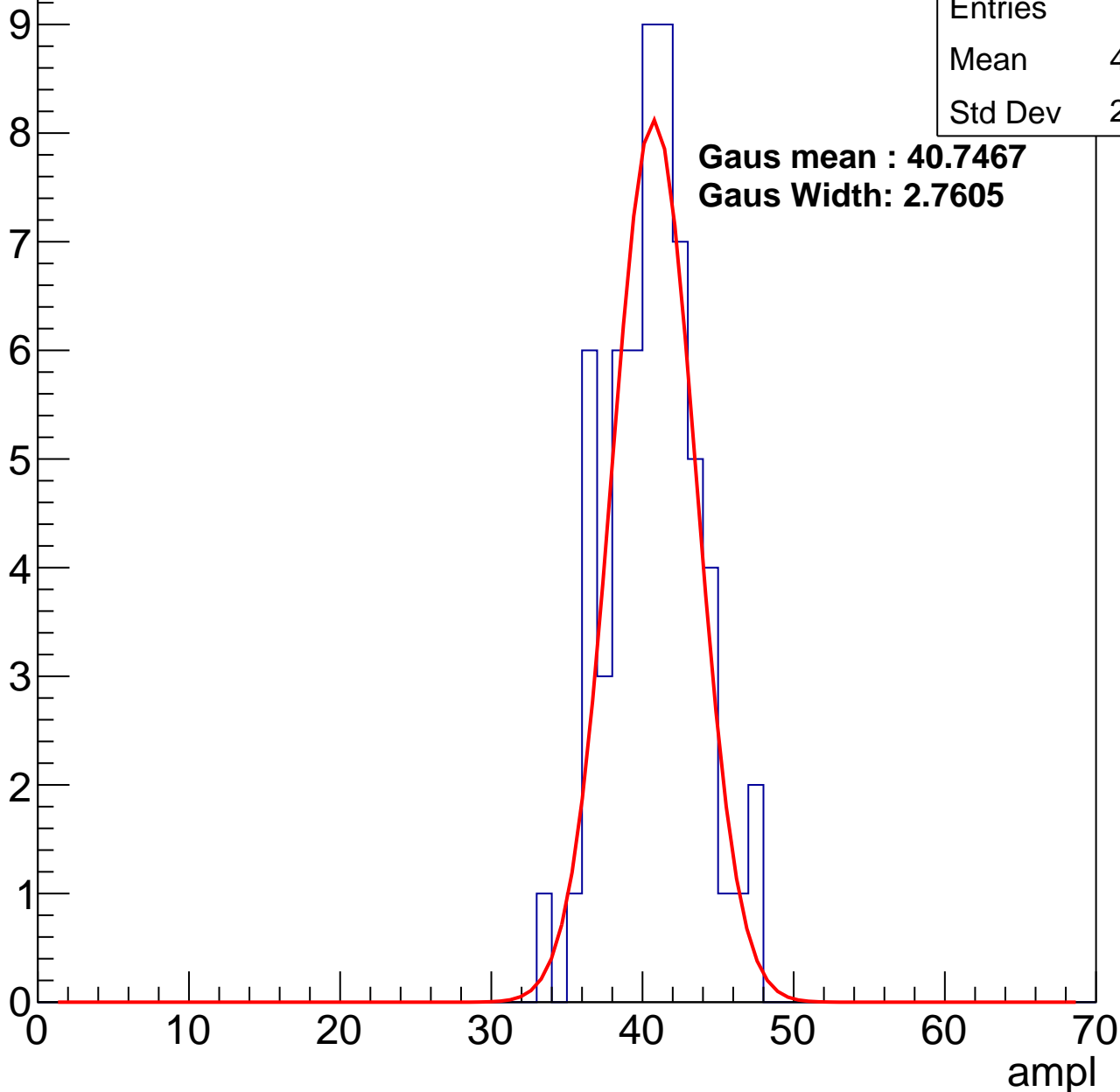
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.26
Std Dev	2.942

**Gaus mean : 40.7467**

**Gaus Width: 2.7605**

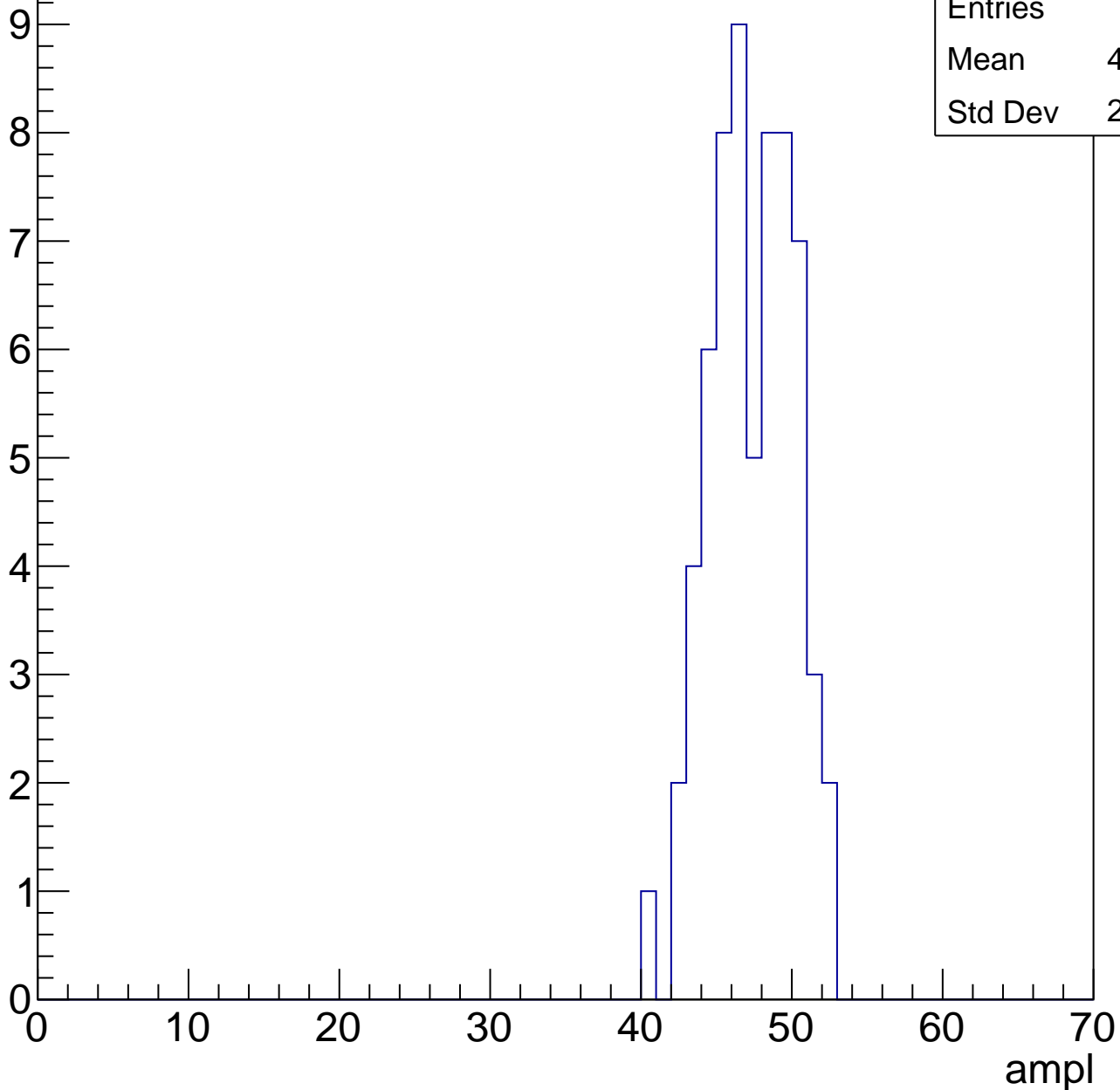


# B1L002S, U18-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

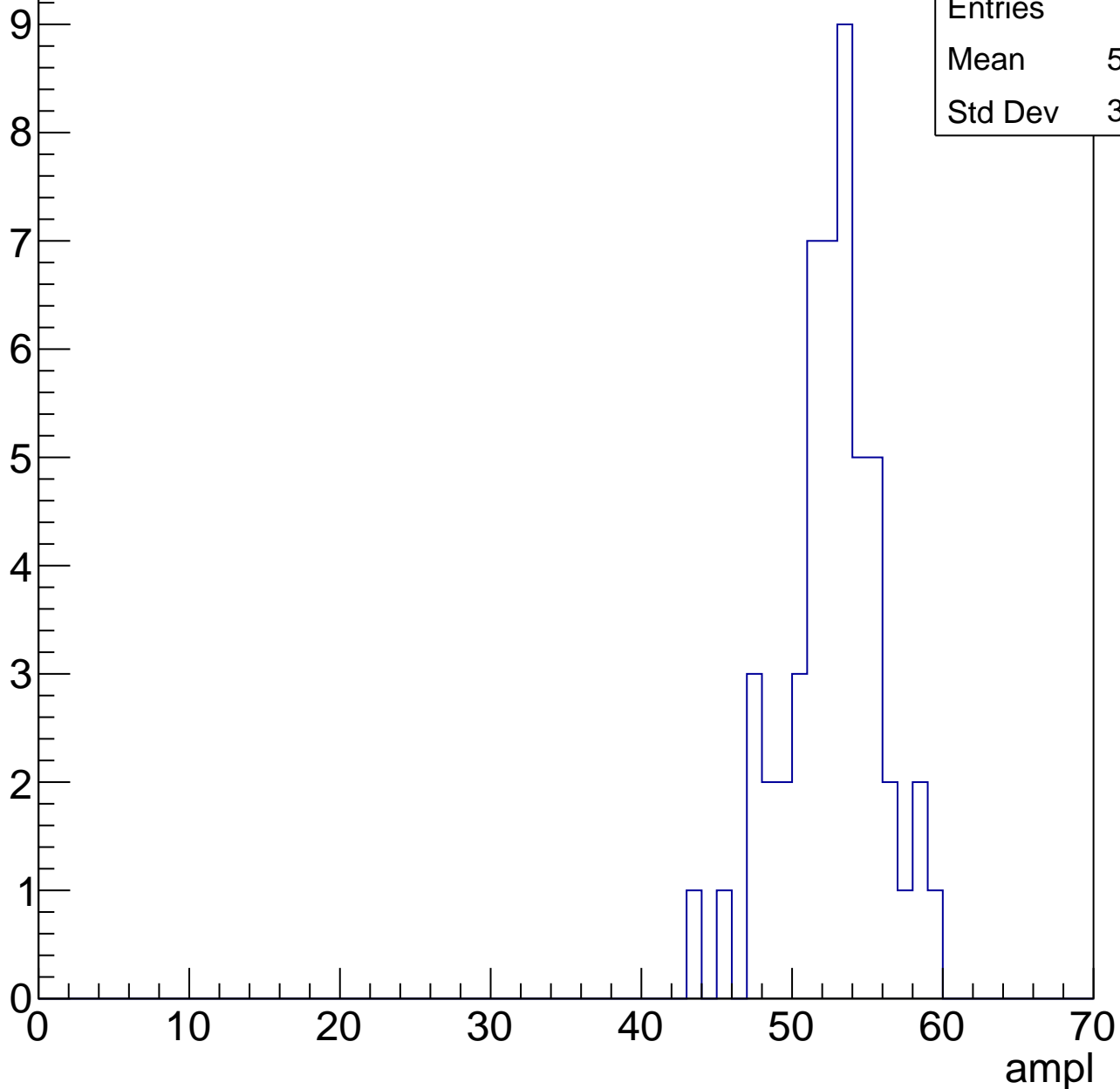
Entries	63
Mean	46.86
Std Dev	2.695



# B1L002S, U18-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	51
Mean	52.16
Std Dev	3.208

# B1L002S, U18-ch60, adc5

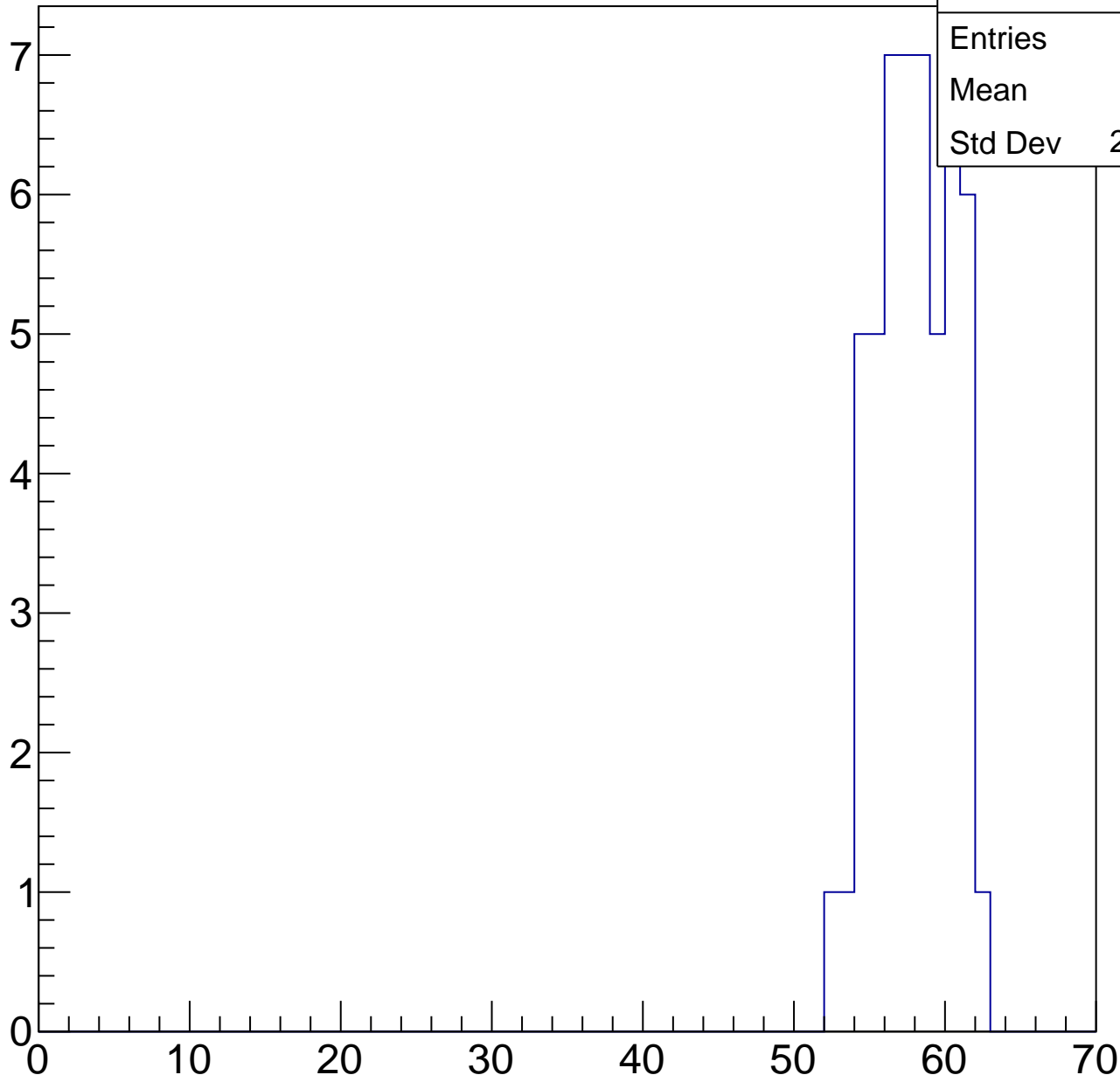
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	52
Mean	57.5
Std Dev	2.446

ampl

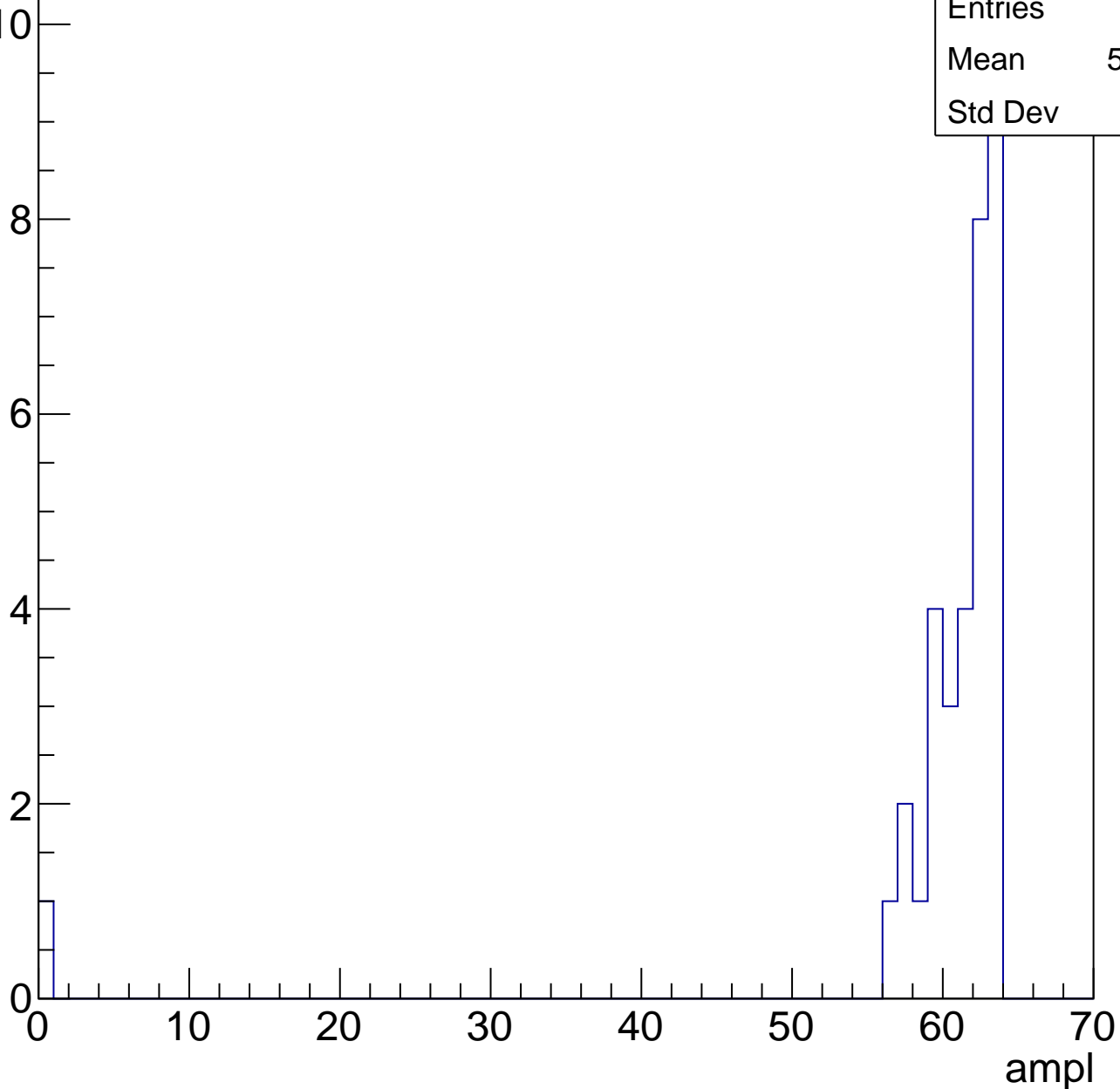


# B1L002S, U18-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	59.24
Std Dev	10.5

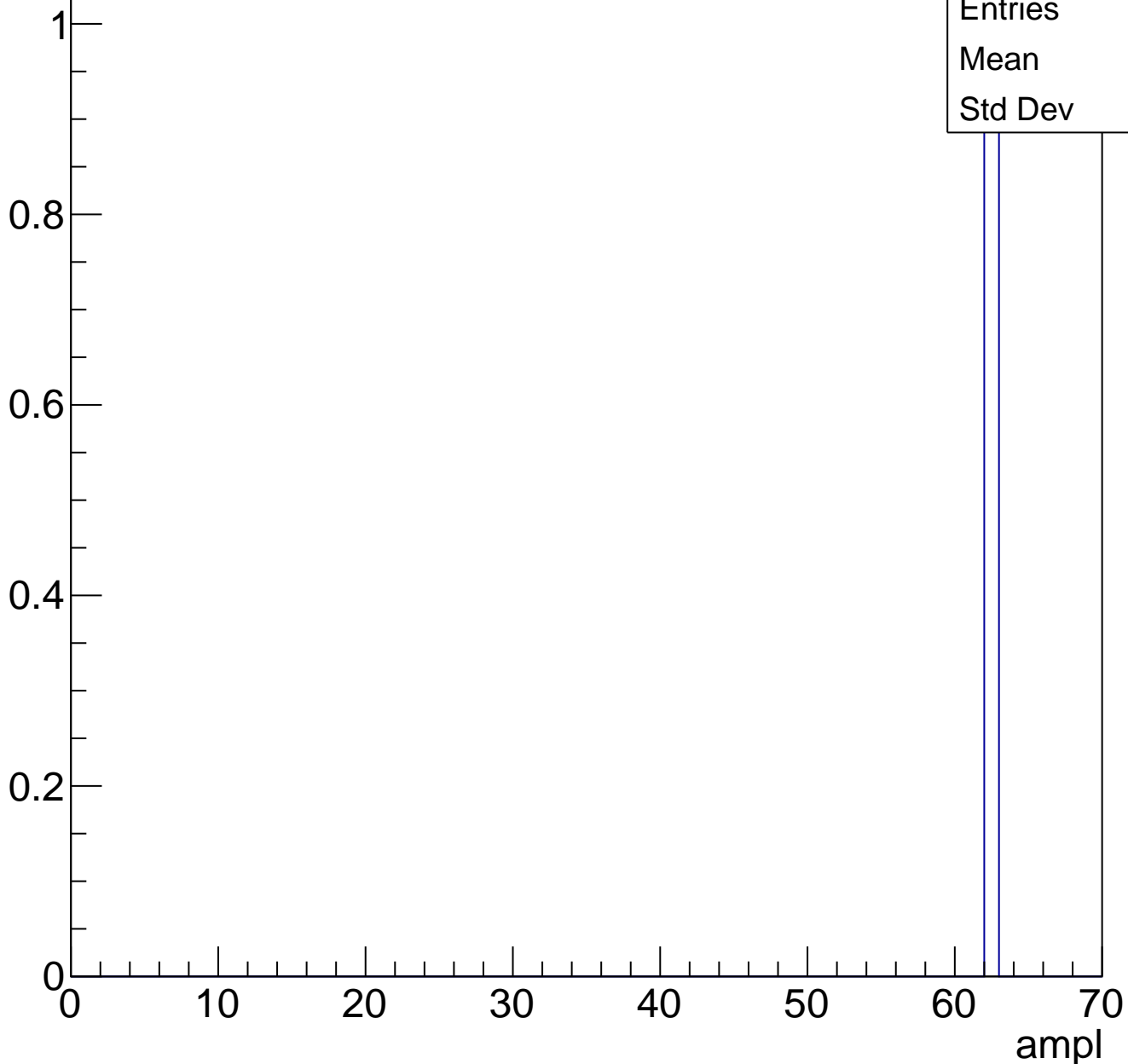




# B1L002S, U18-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	62
Std Dev	0

# B1L002S, U18-ch61, adc0

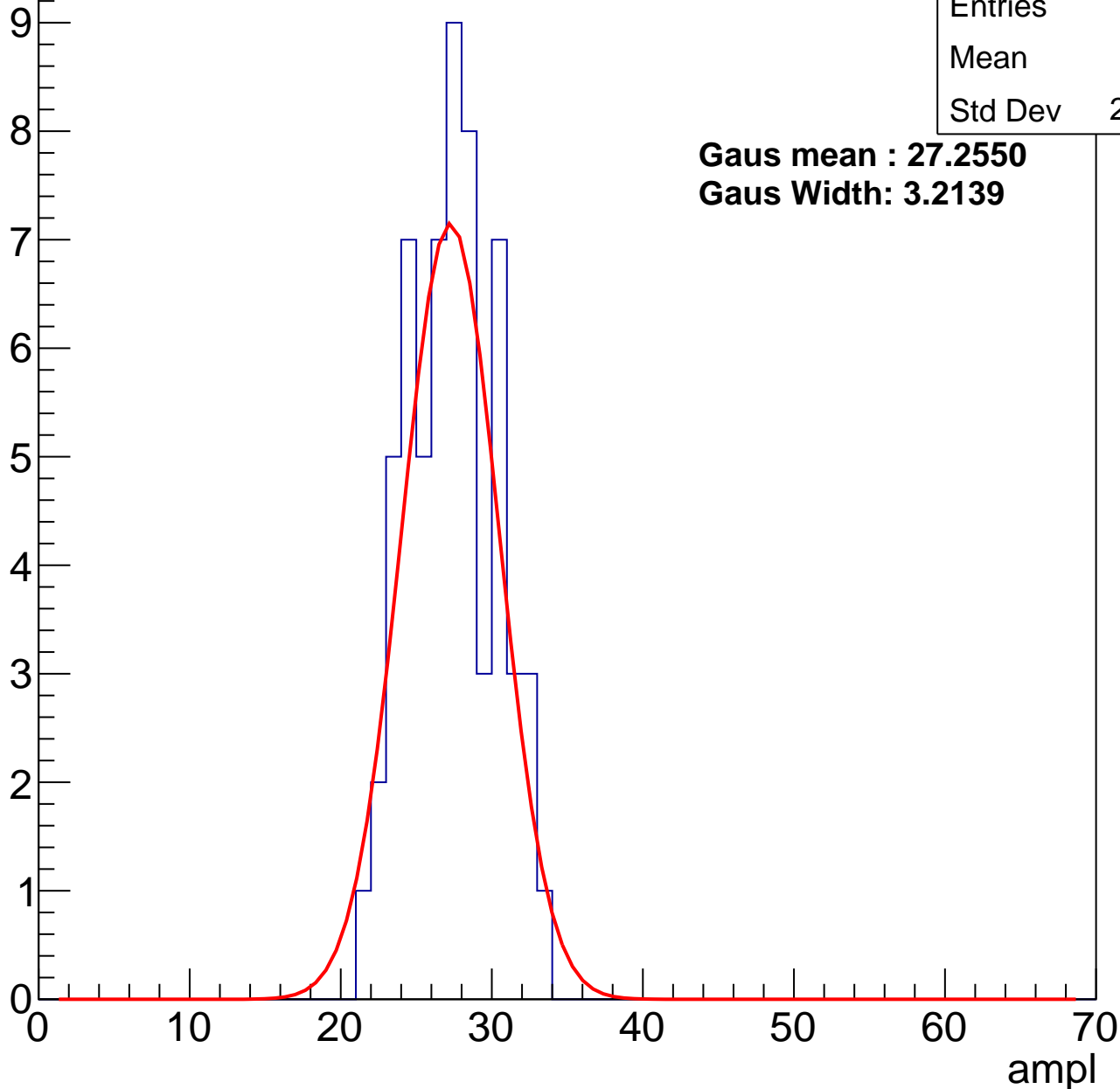
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	26.9
Std Dev	2.856

**Gaus mean : 27.2550**

**Gaus Width: 3.2139**



# B1L002S, U18-ch61, adc1

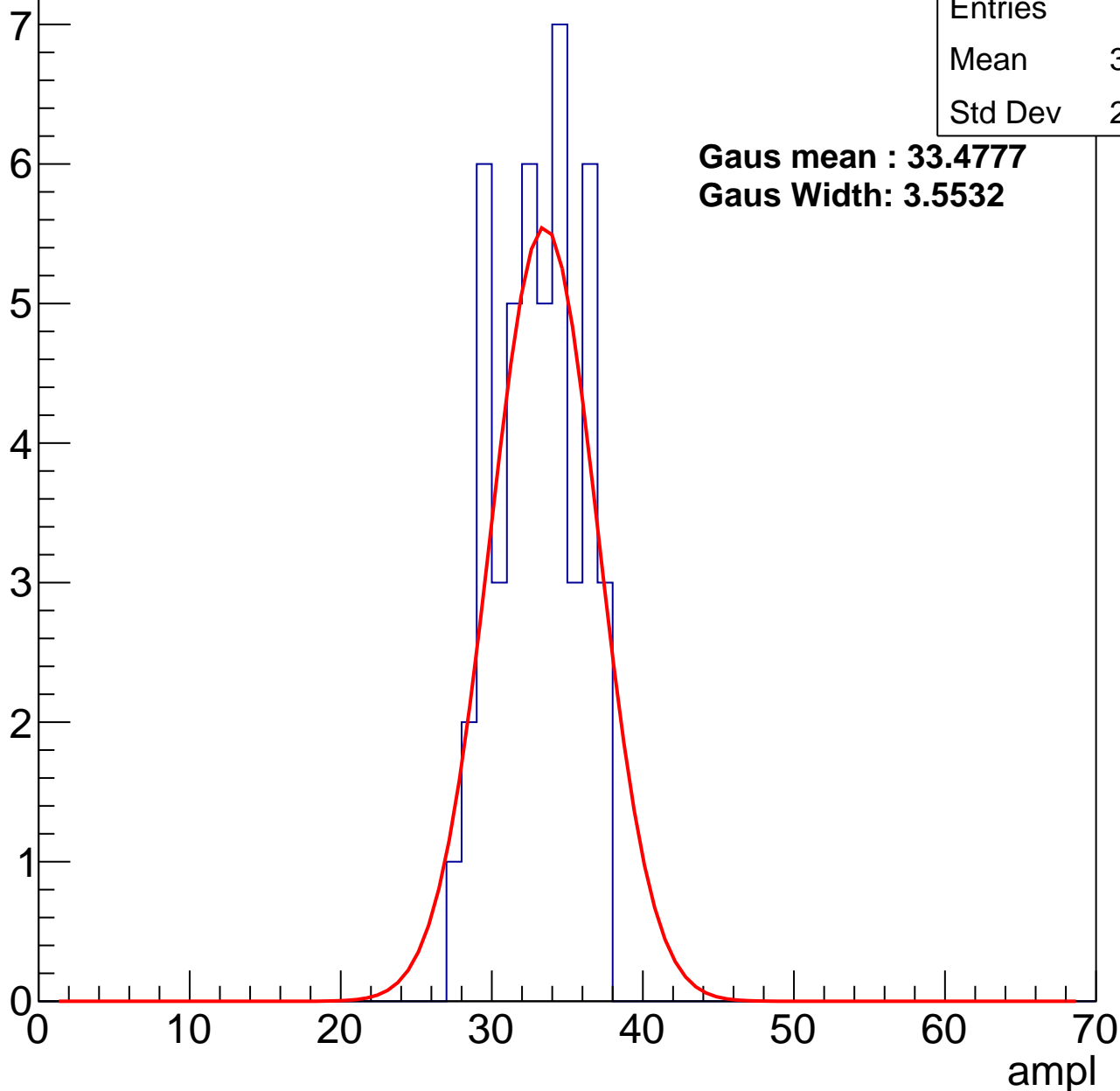
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	32.53
Std Dev	2.712

**Gaus mean : 33.4777**

**Gaus Width: 3.5532**



# B1L002S, U18-ch61, adc2

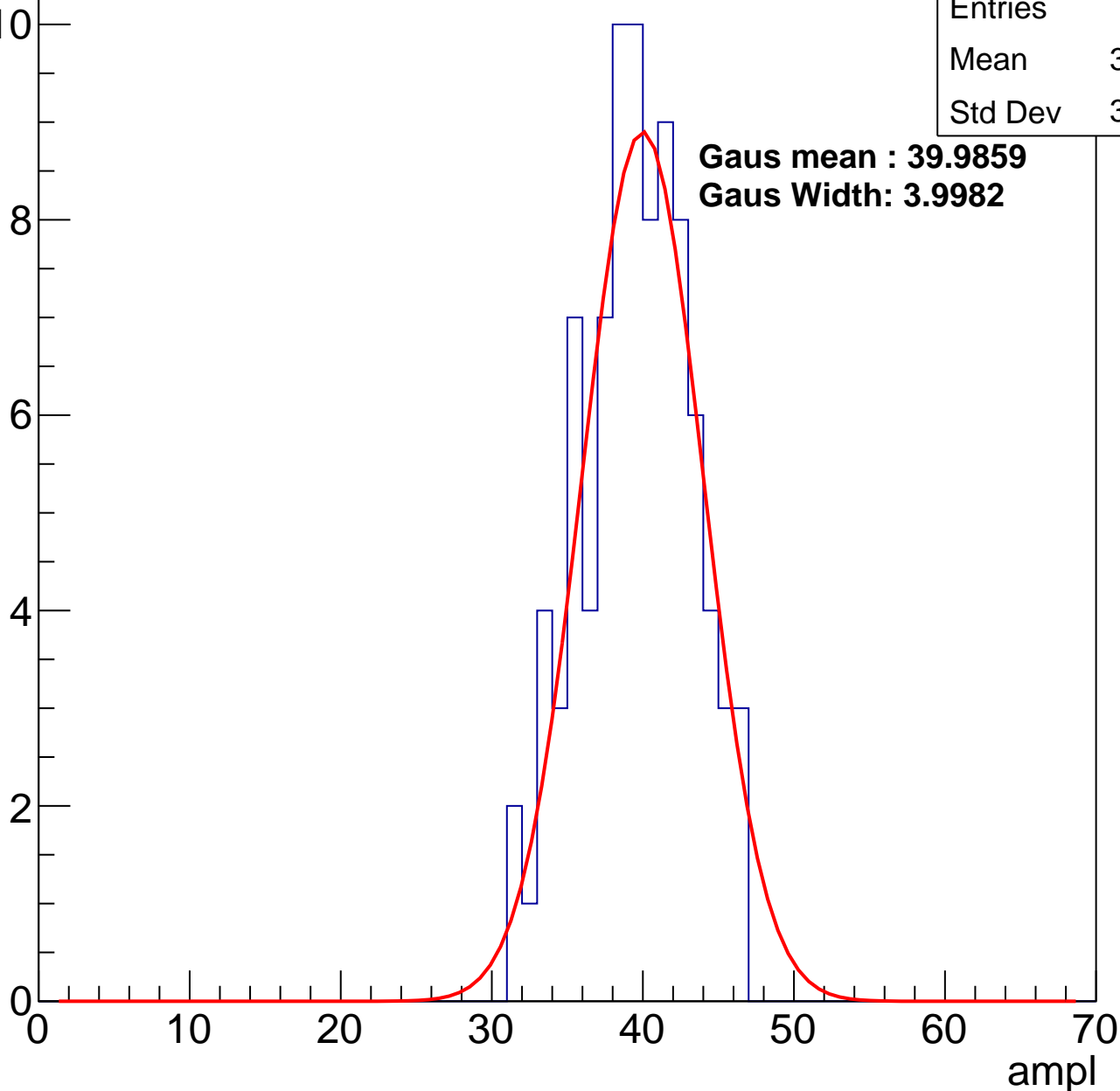
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	89
Mean	39.08
Std Dev	3.592

**Gaus mean : 39.9859**

**Gaus Width: 3.9982**

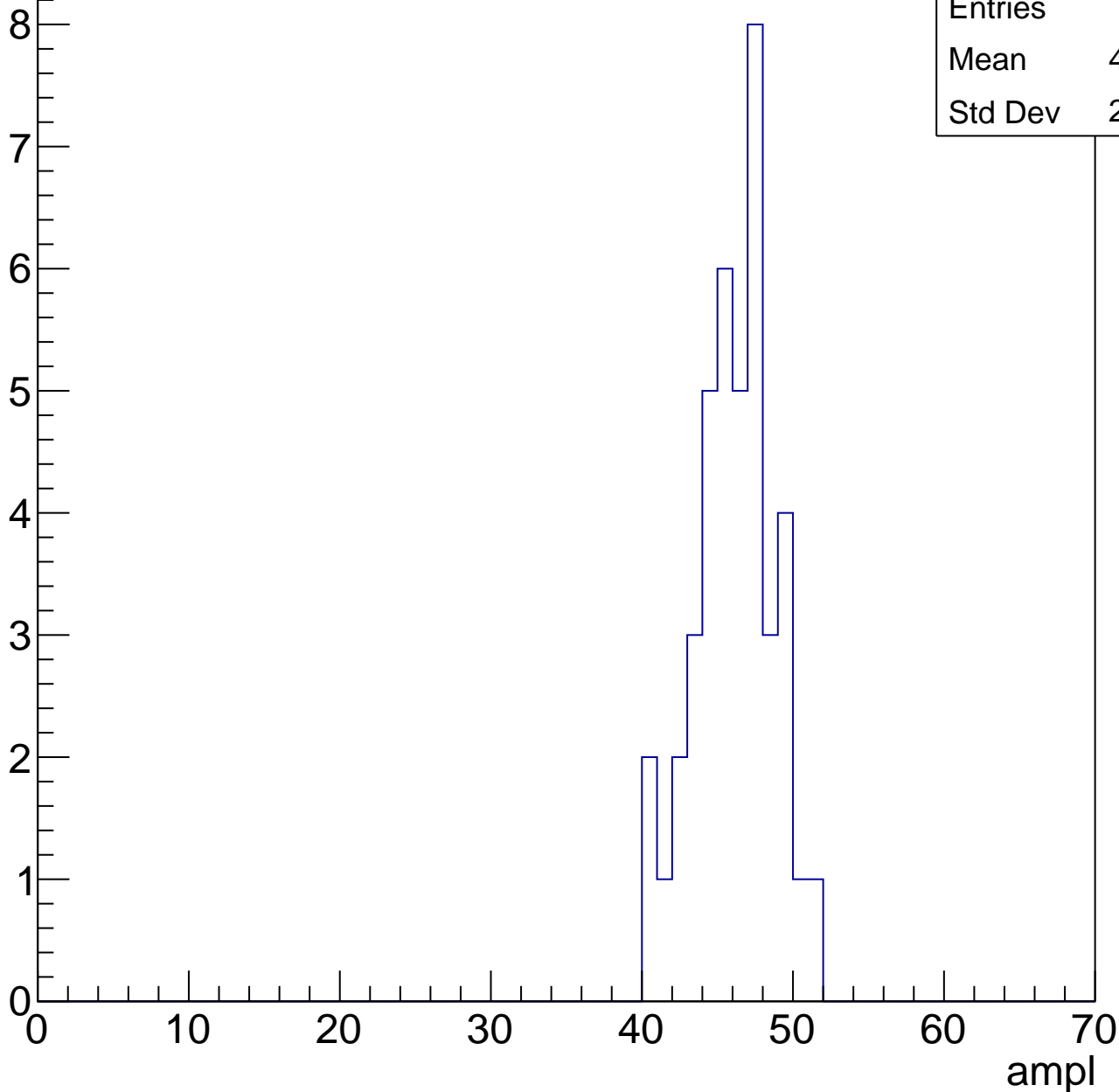


# B1L002S, U18-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	45.63
Std Dev	2.583

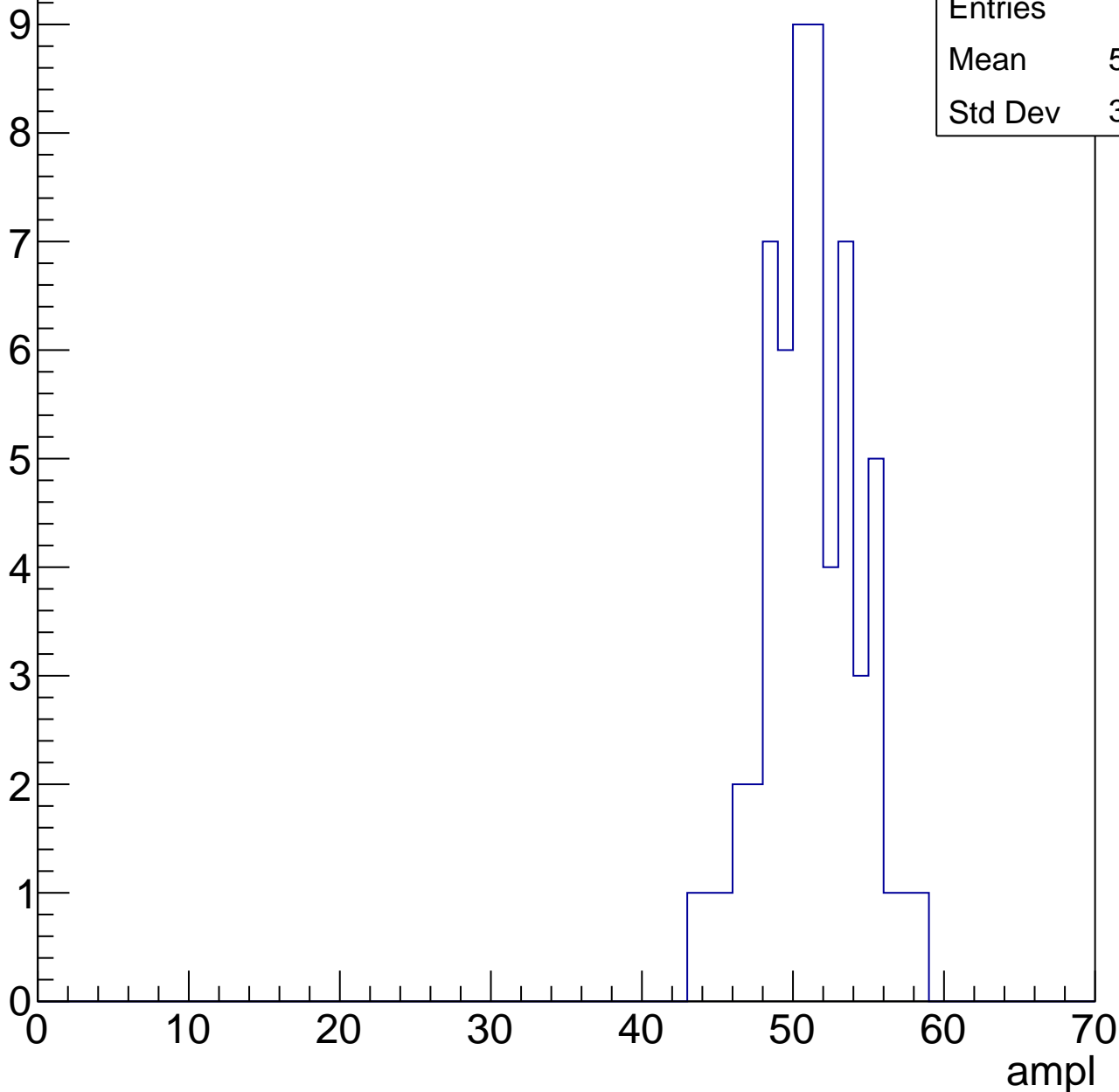


# B1L002S, U18-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

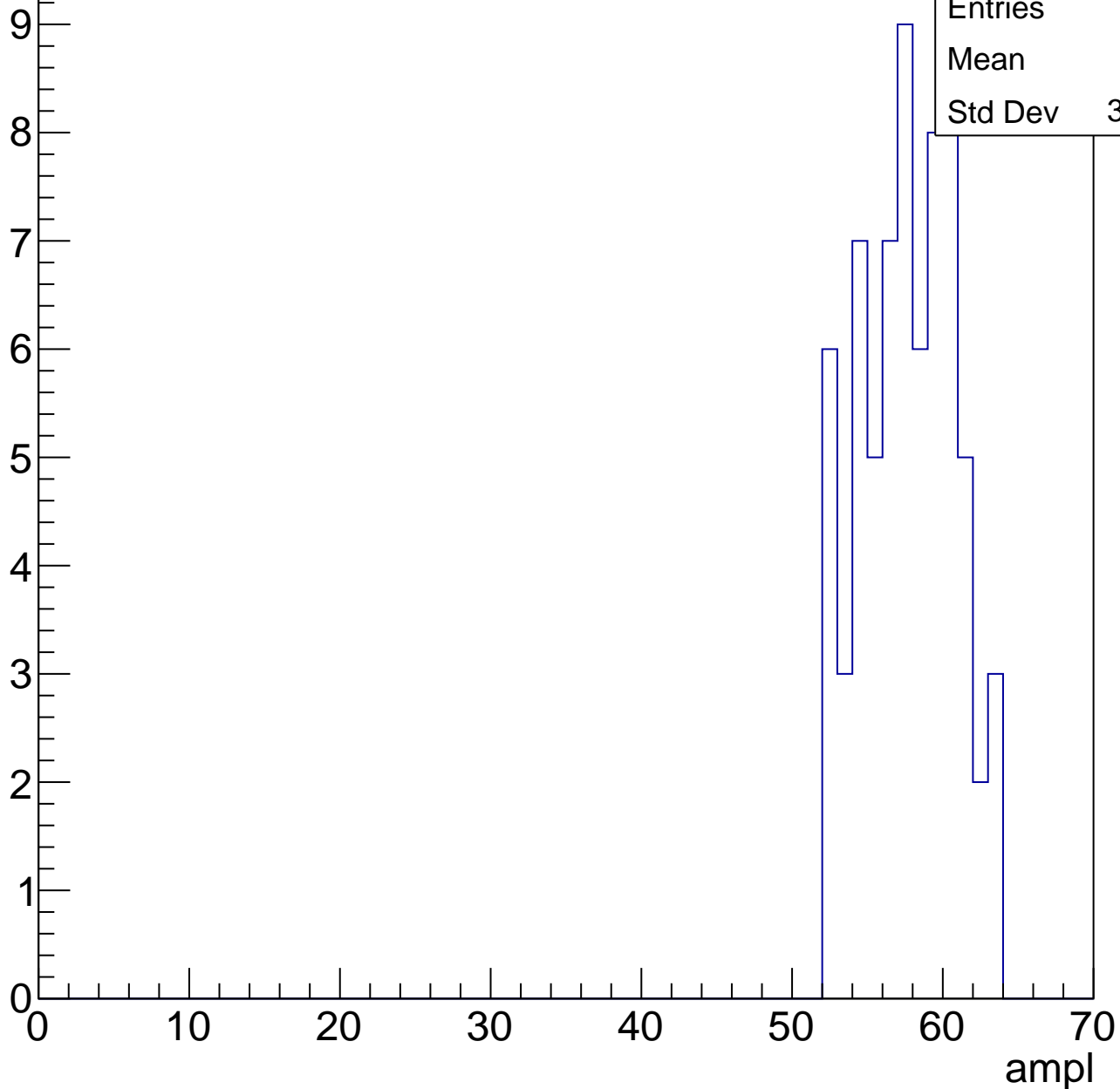
Entries	60
Mean	50.73
Std Dev	3.087



# B1L002S, U18-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



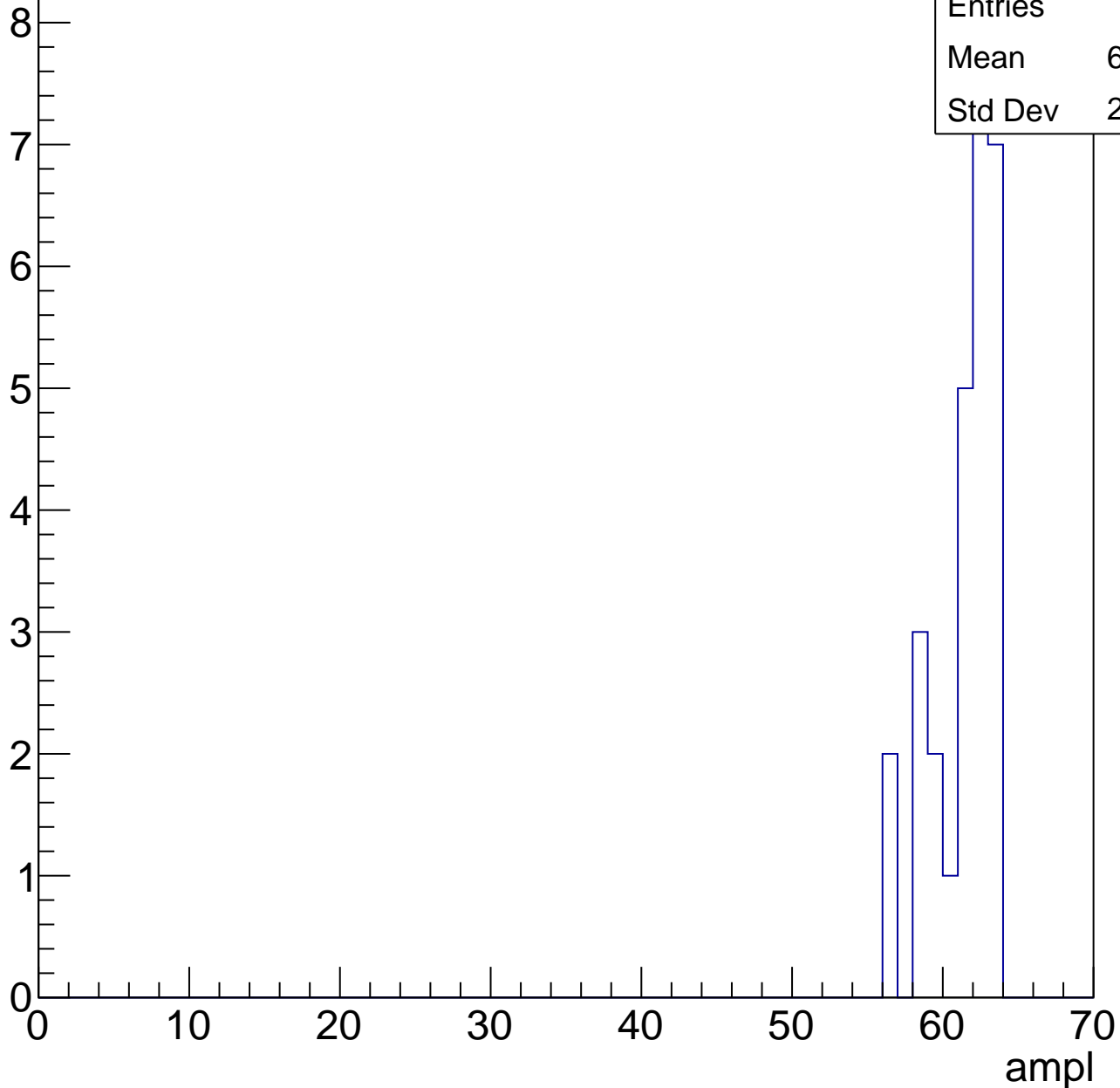
Entries	69
Mean	57.2
Std Dev	3.029

# B1L002S, U18-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	60.93
Std Dev	2.086

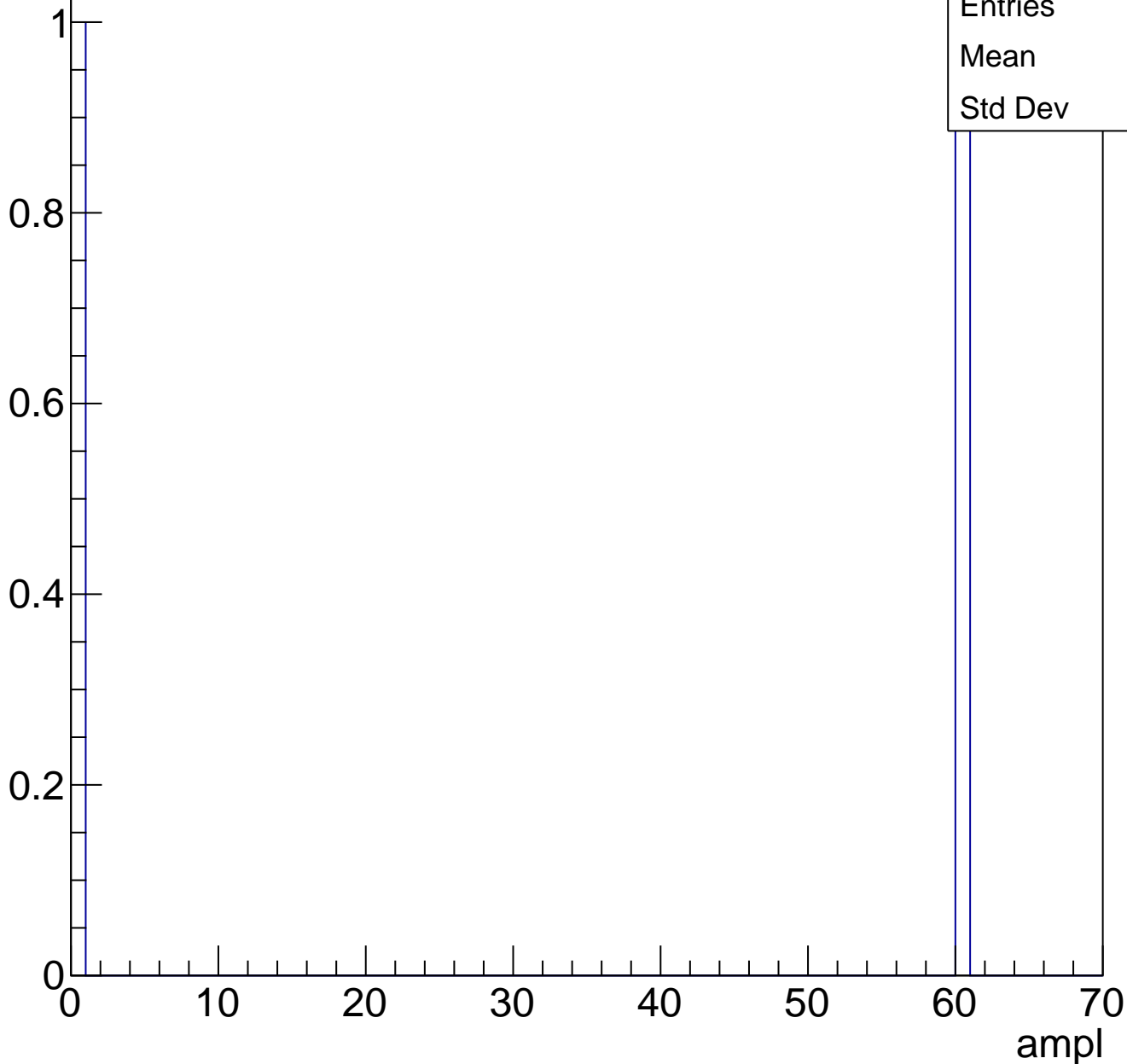




# B1L002S, U18-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch62, adc0

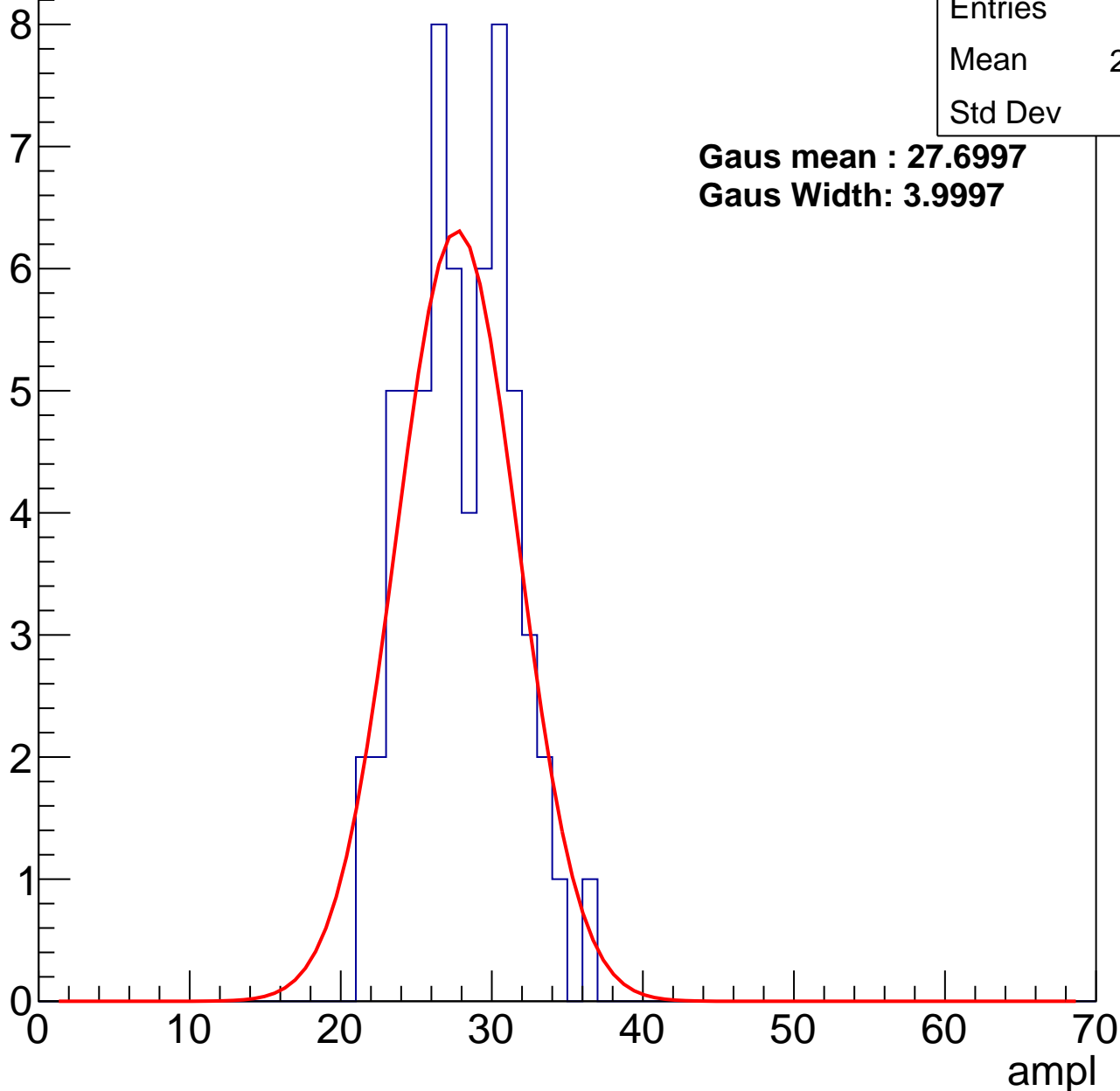
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.44
Std Dev	3.38

**Gaus mean : 27.6997**

**Gaus Width: 3.9997**



# B1L002S, U18-ch62, adc1

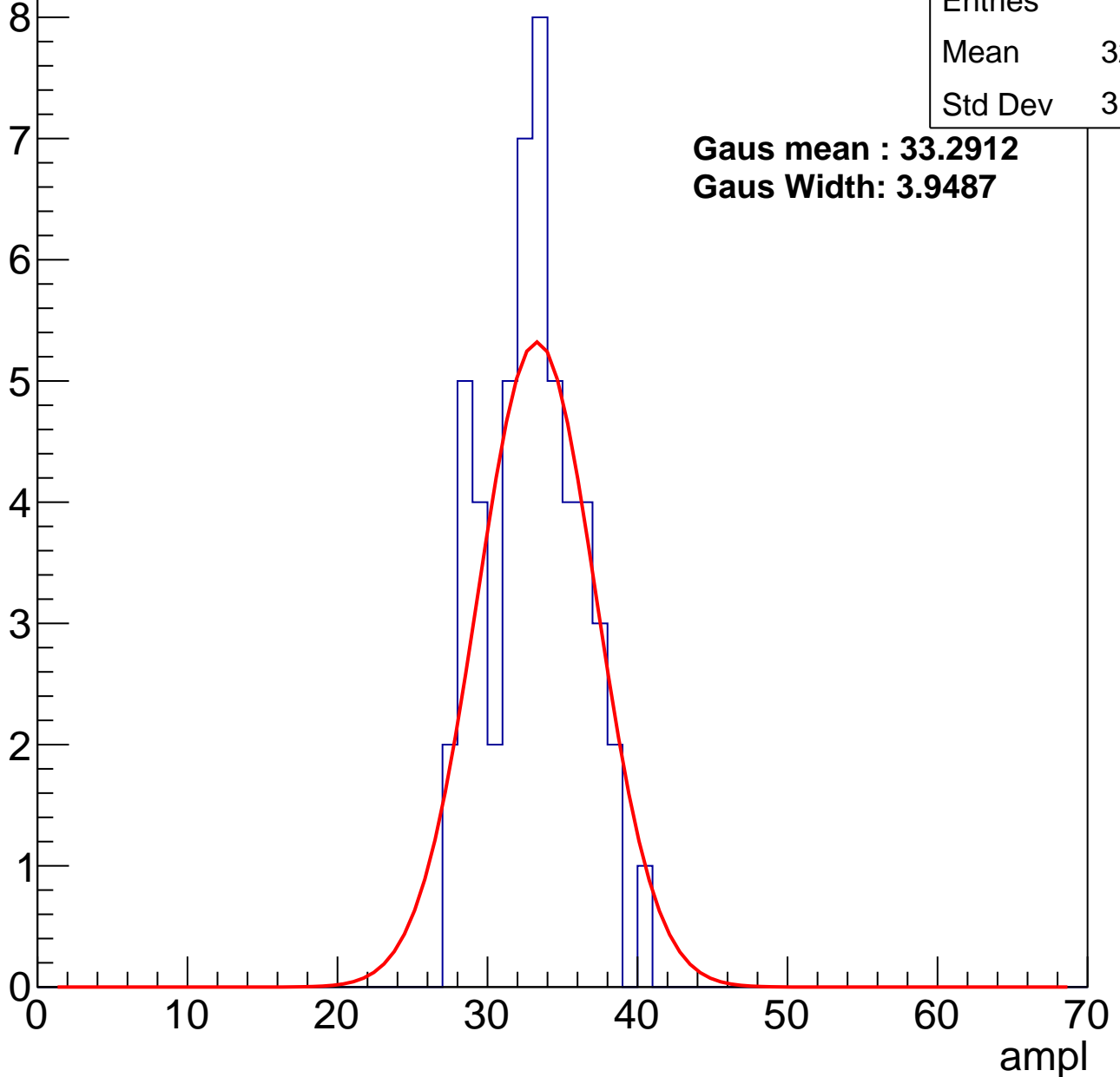
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	32.58
Std Dev	3.103

**Gaus mean : 33.2912**

**Gaus Width: 3.9487**



# B1L002S, U18-ch62, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	39.83
Std Dev	3.264

**Gaus mean : 40.1145**

**Gaus Width: 4.5318**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

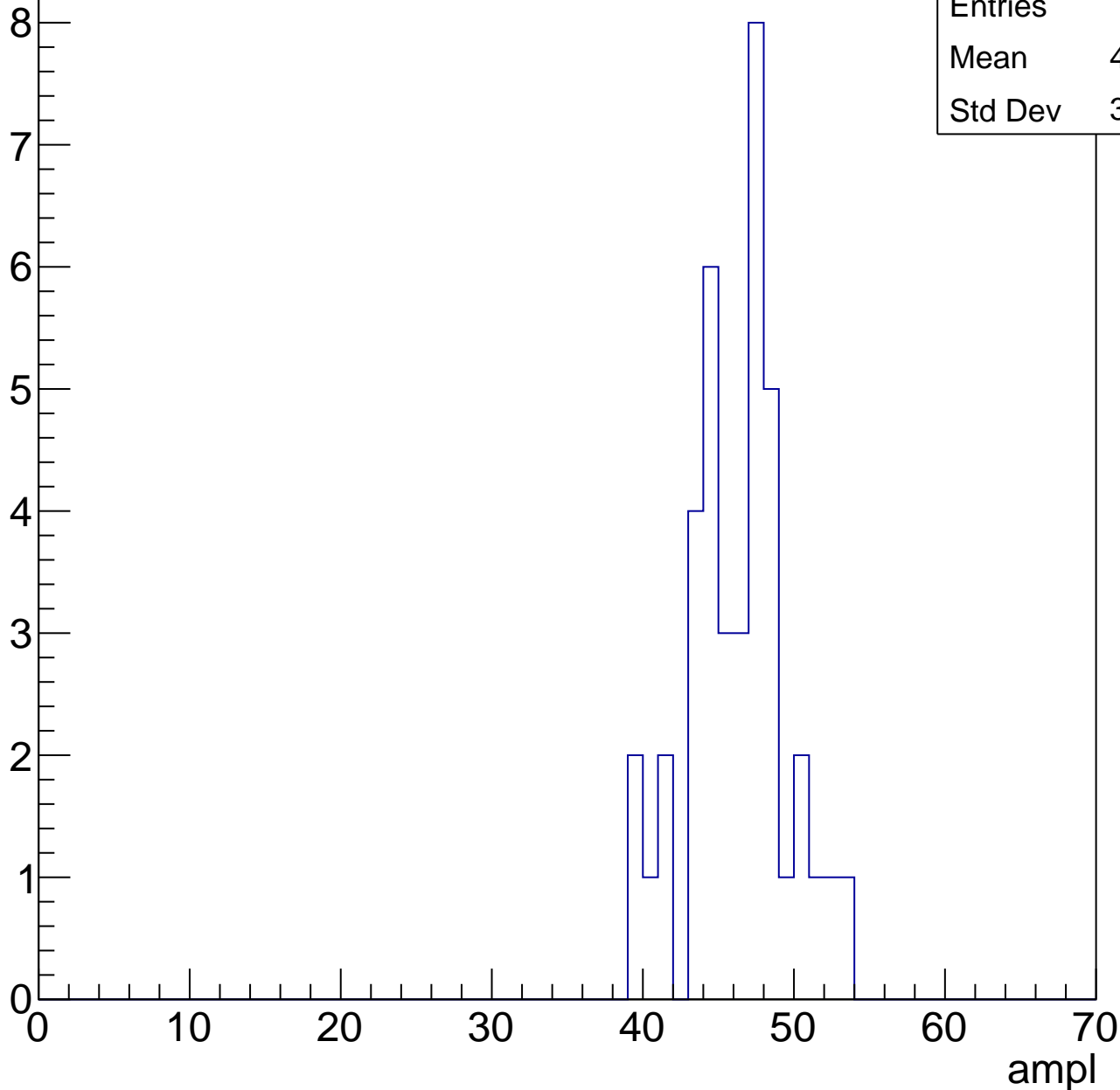


# B1L002S, U18-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	45.75
Std Dev	3.254

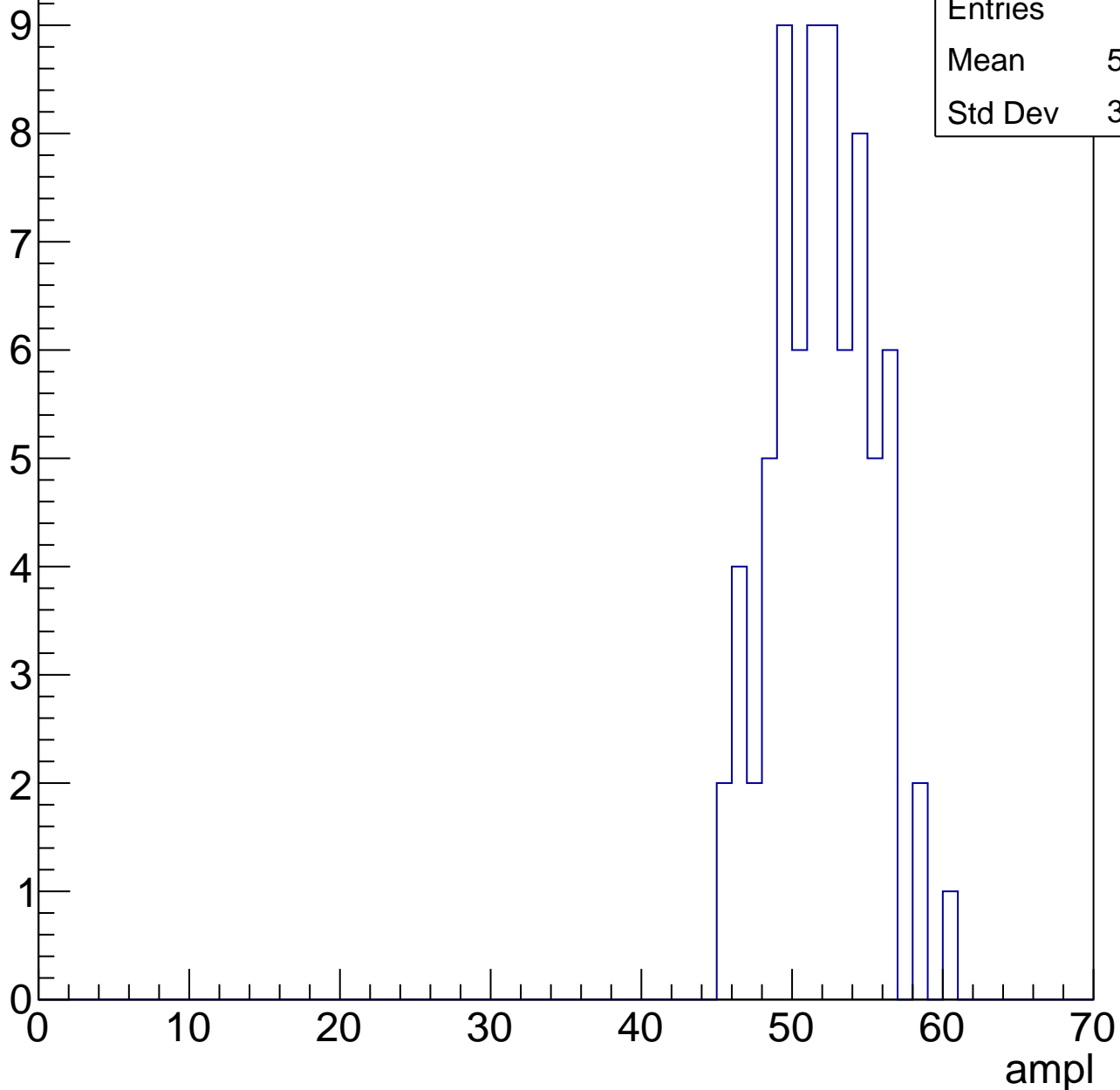


# B1L002S, U18-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

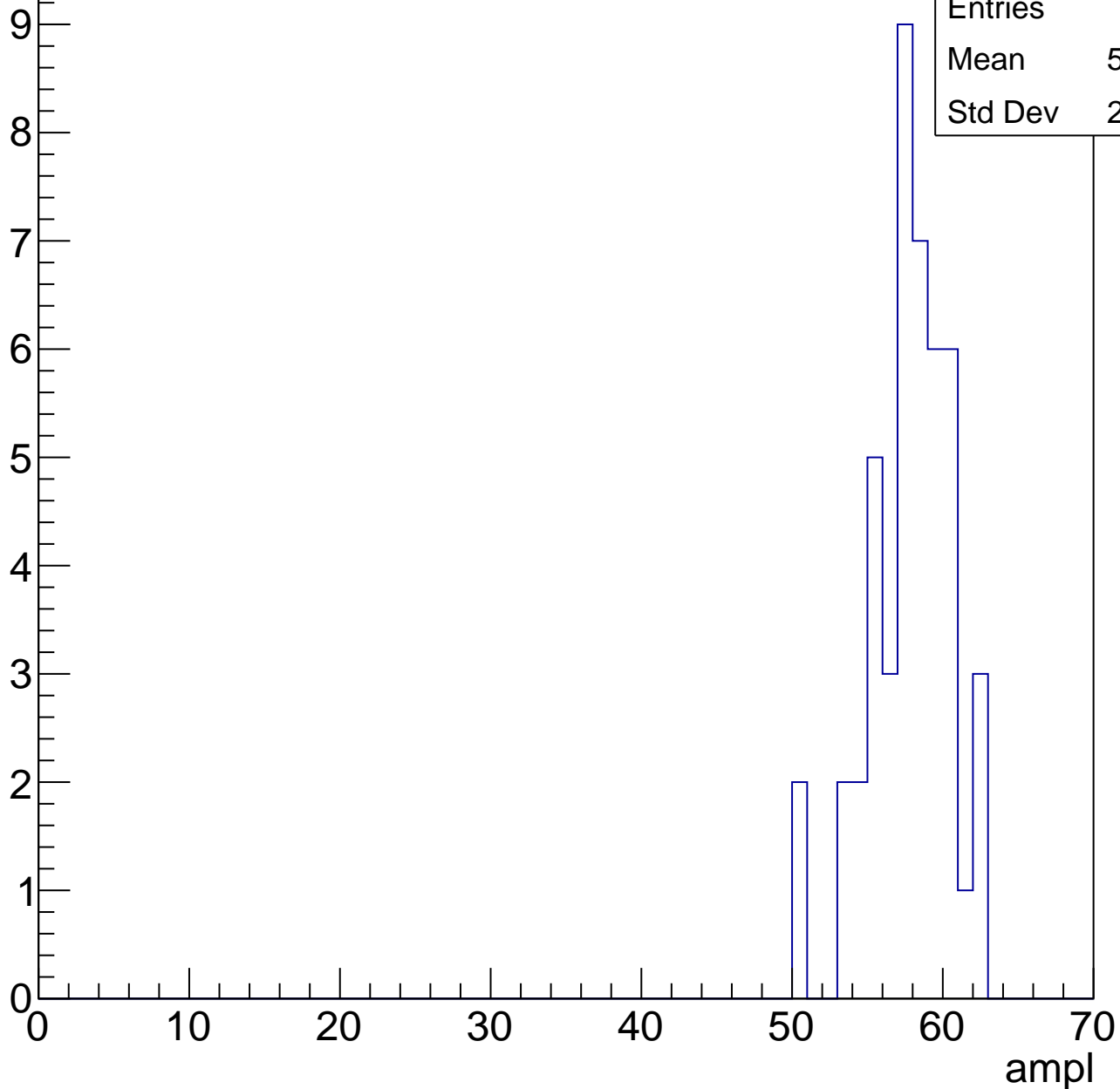
Entries	74
Mean	51.53
Std Dev	3.256



# B1L002S, U18-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

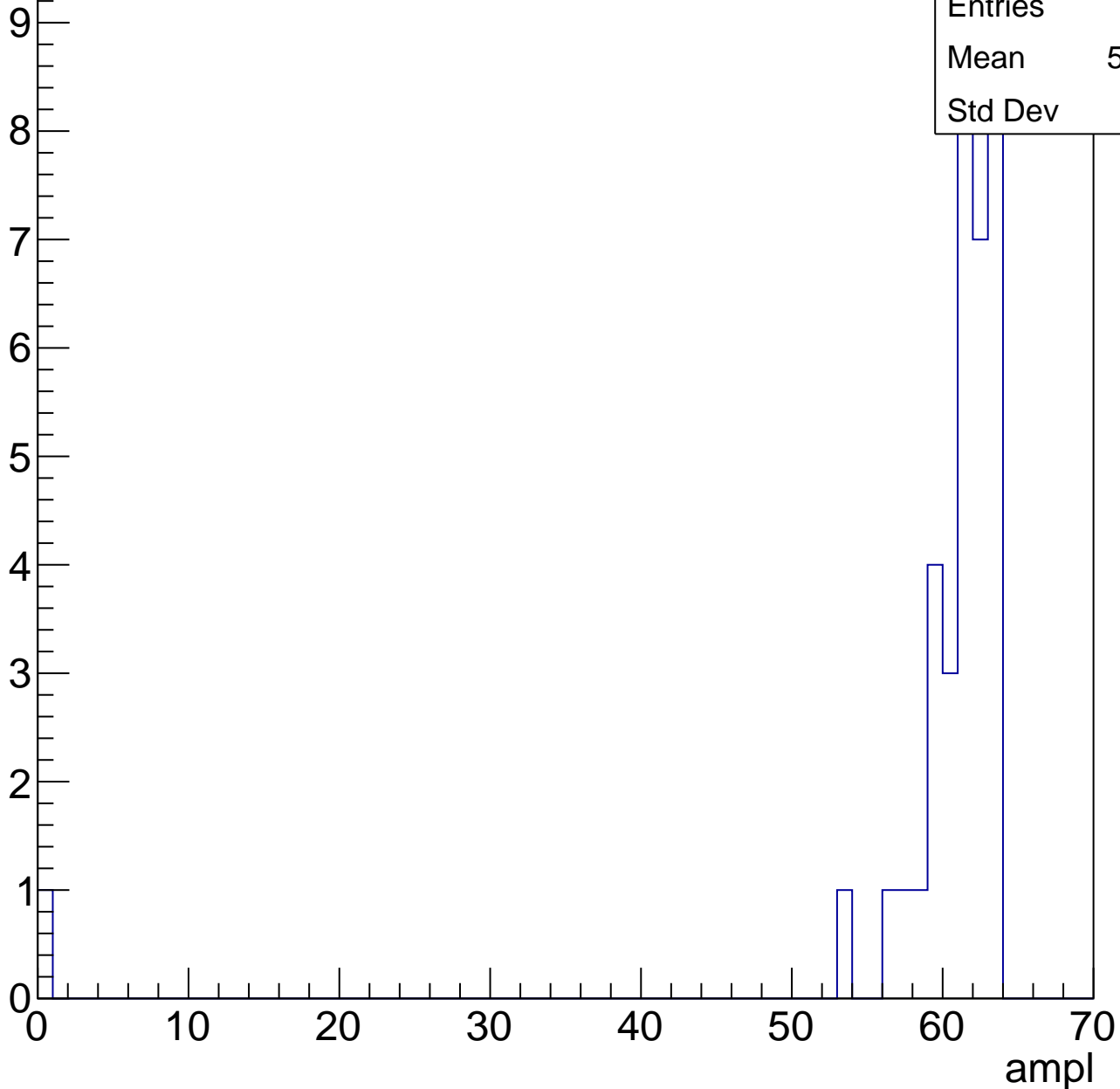


# B1L002S, U18-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	59.19
Std Dev	10.1





# B1L002S, U18-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch63, adc0

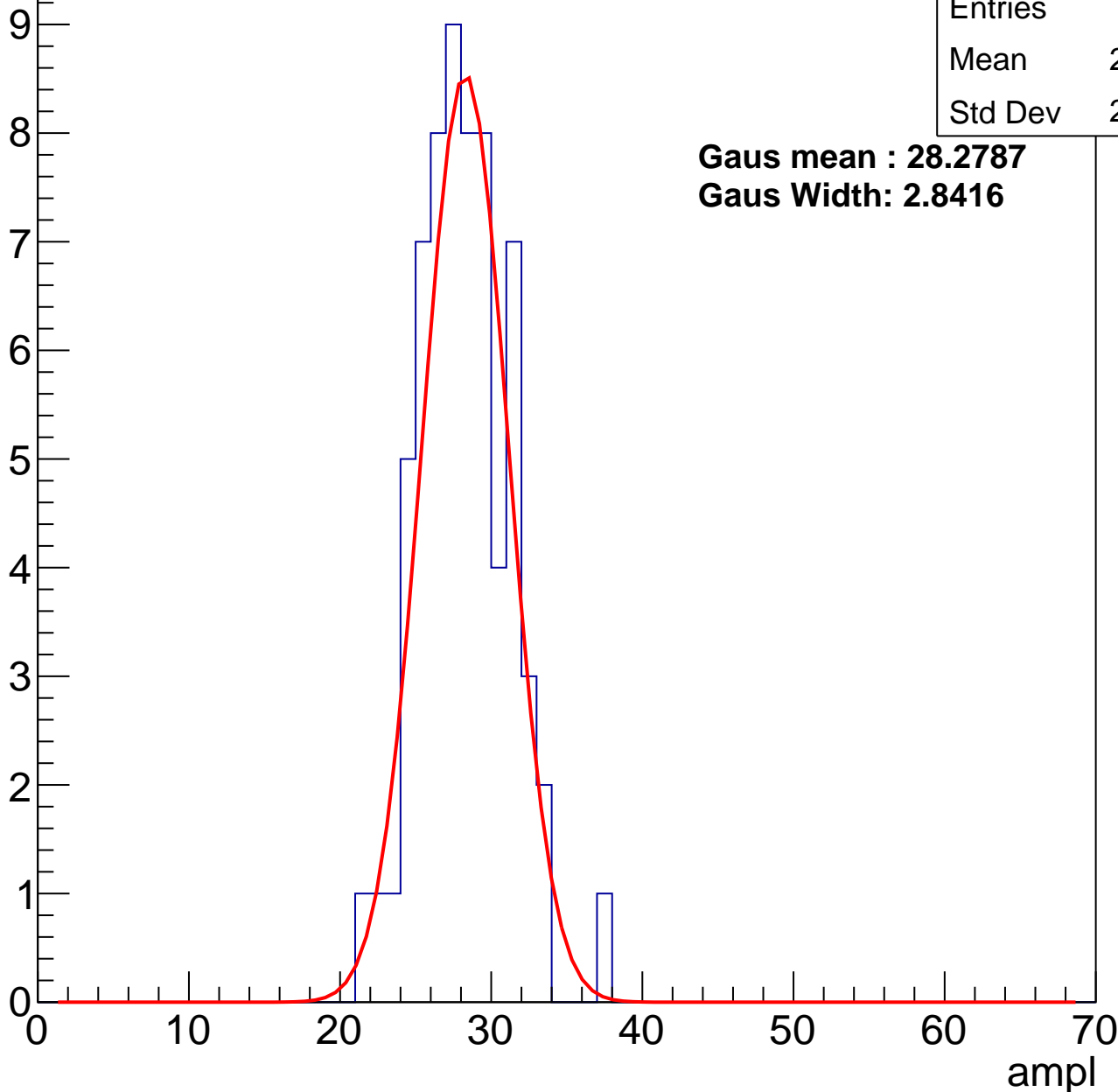
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.75
Std Dev	2.925

**Gaus mean : 28.2787**

**Gaus Width: 2.8416**

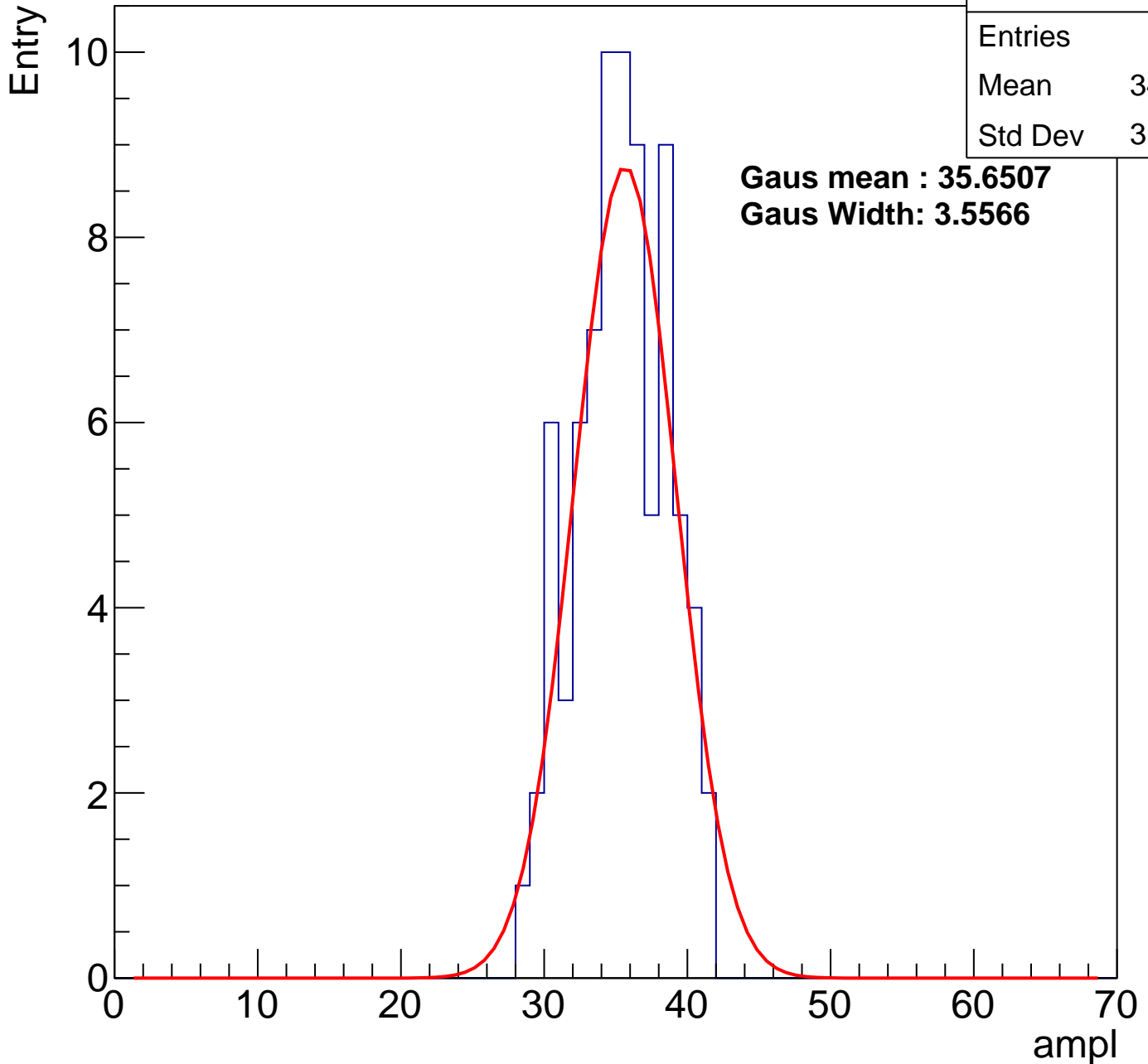


# B1L002S, U18-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	34.94
Std Dev	3.127

**Gaus mean : 35.6507**  
**Gaus Width: 3.5566**



# B1L002S, U18-ch63, adc2

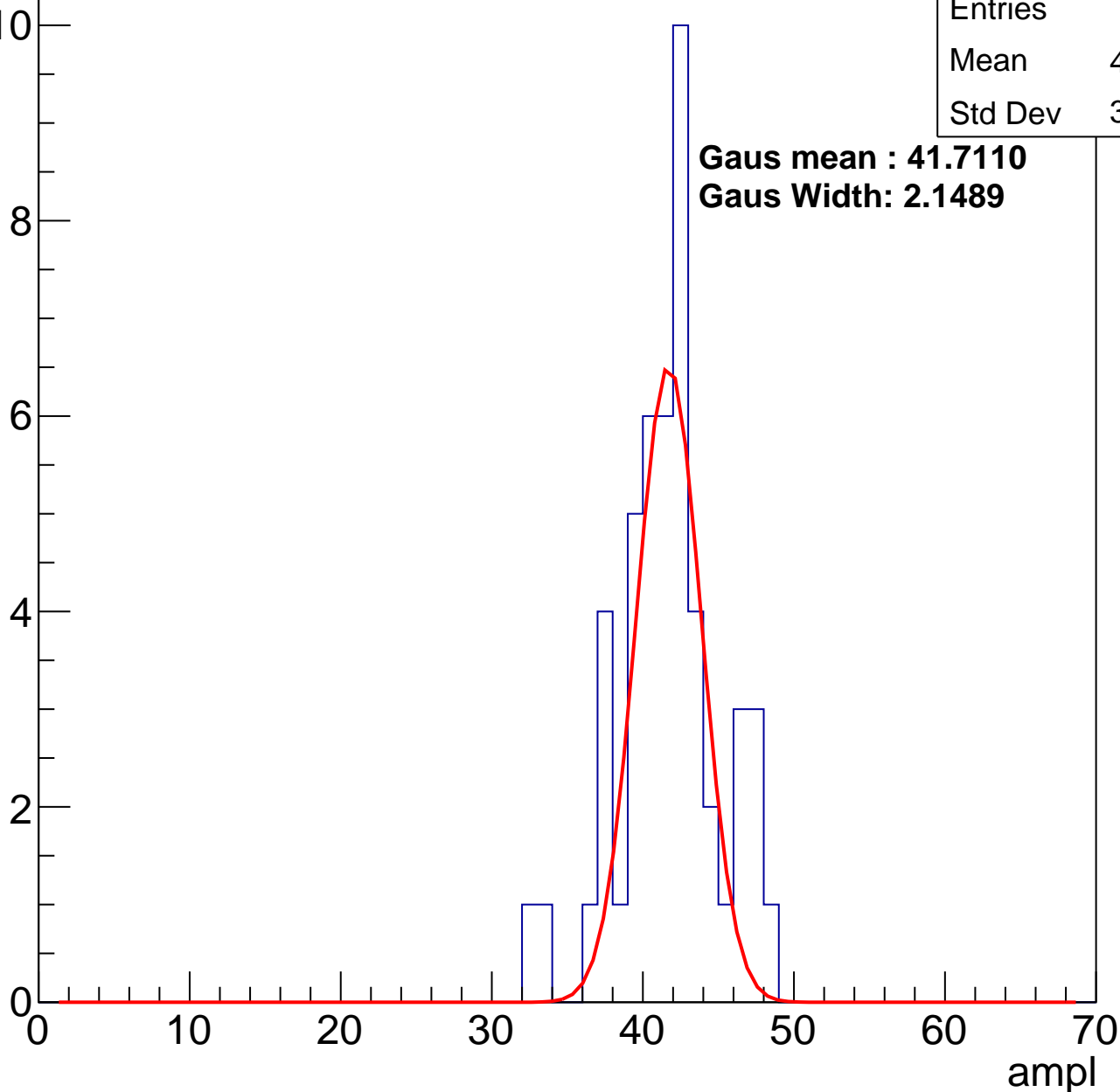
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	41.22
Std Dev	3.376

**Gaus mean : 41.7110**

**Gaus Width: 2.1489**

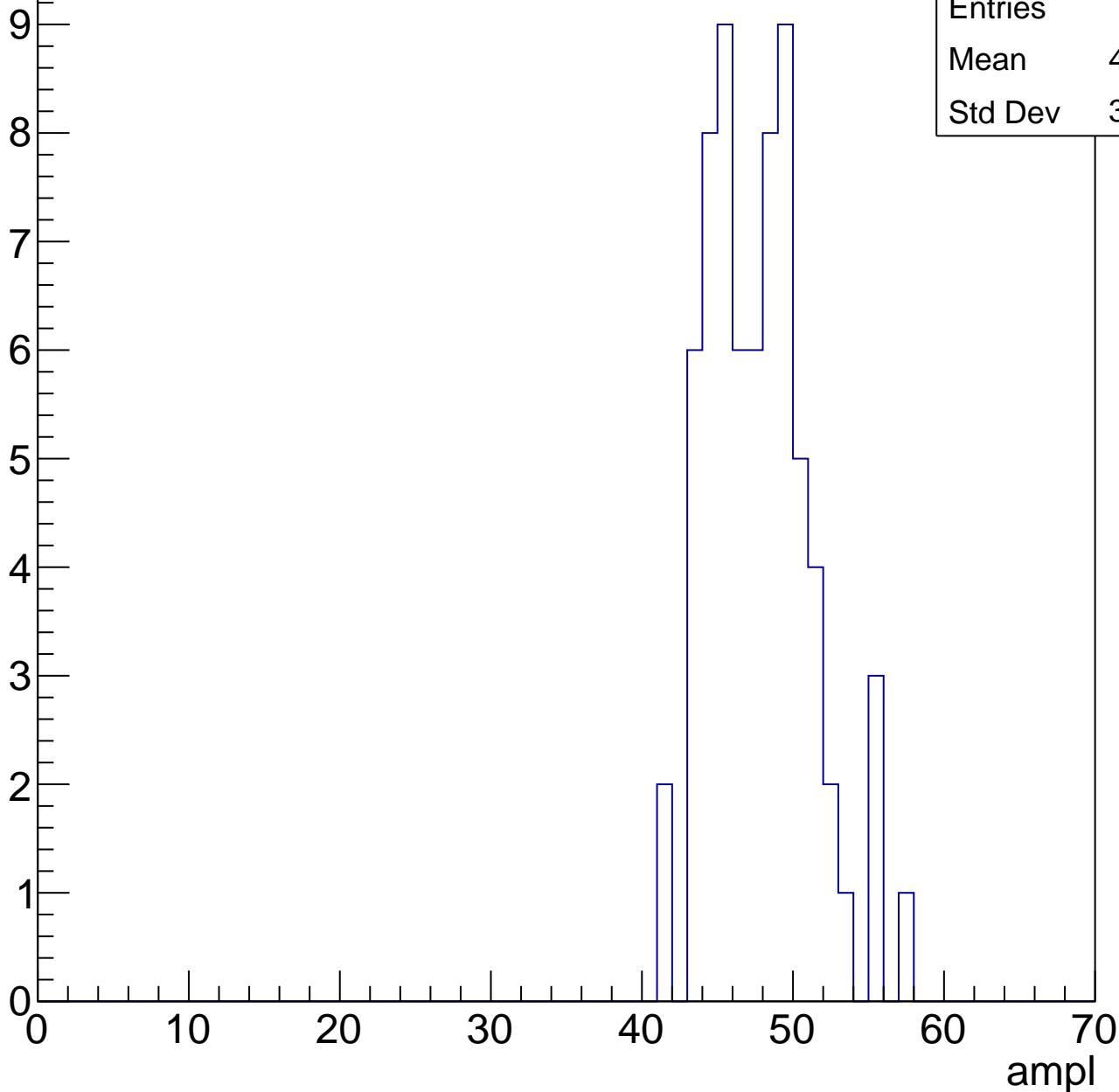


# B1L002S, U18-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	47.33
Std Dev	3.392

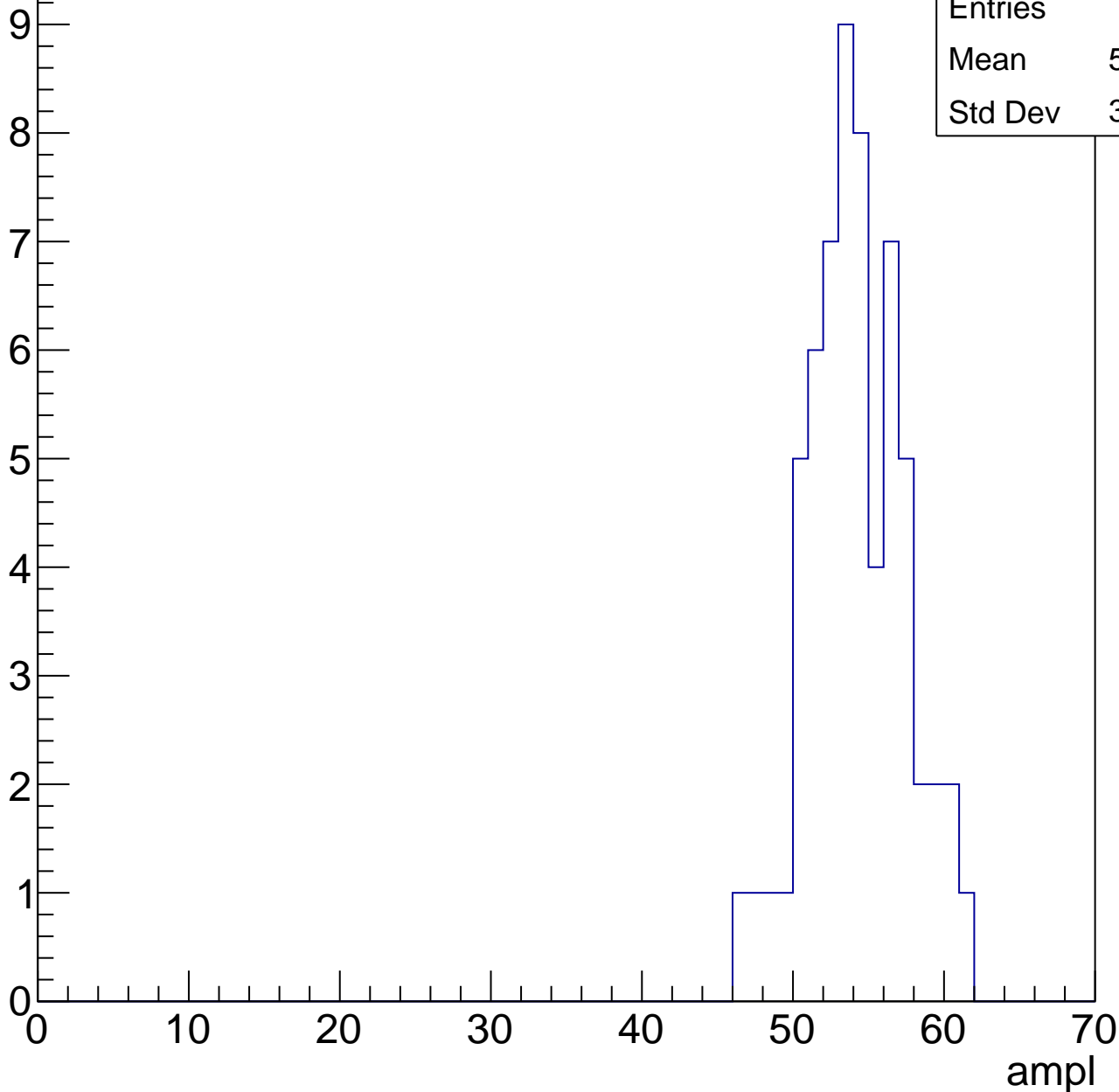


# B1L002S, U18-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	53.73
Std Dev	3.163

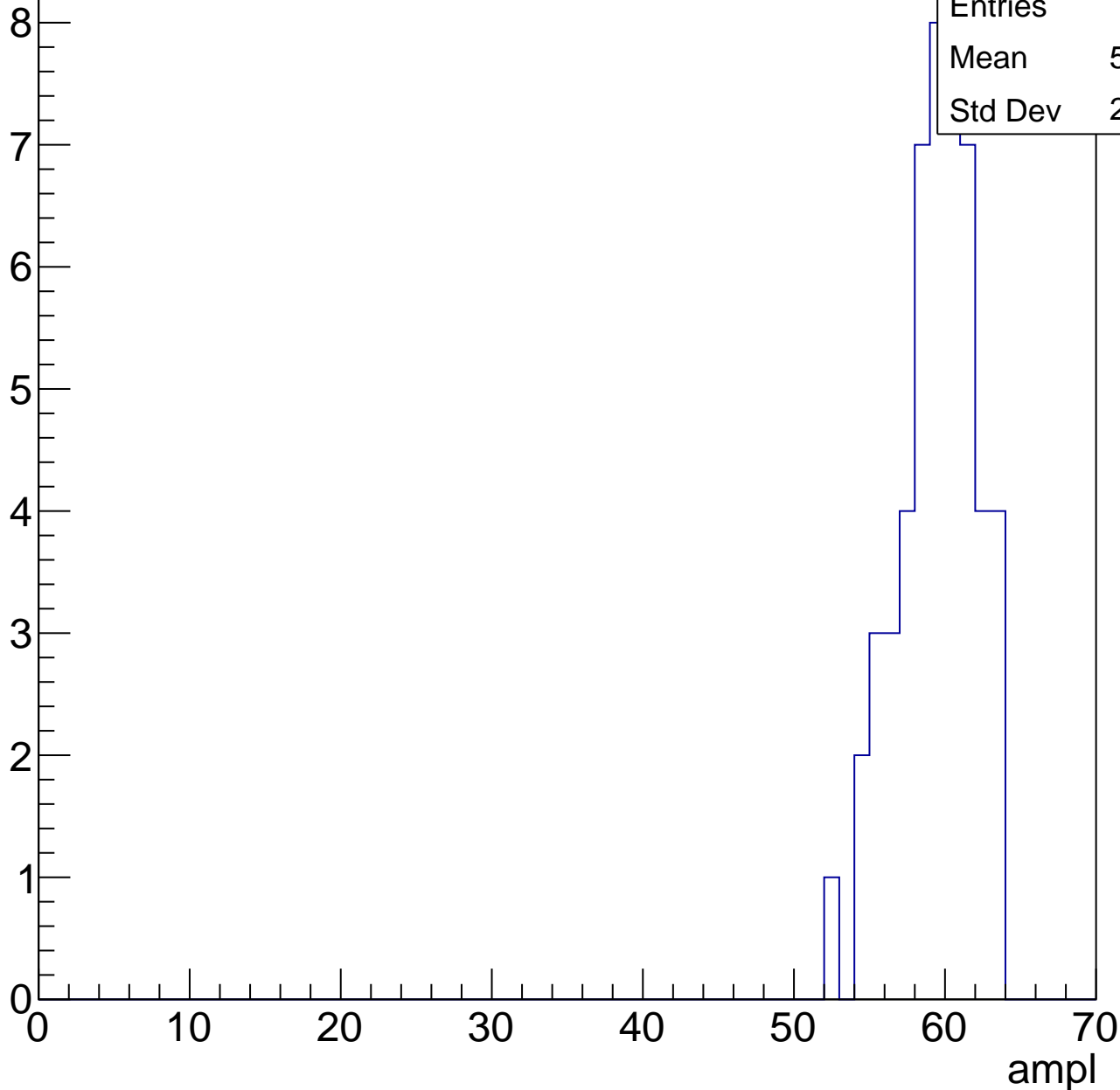


# B1L002S, U18-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.94
Std Dev	2.555

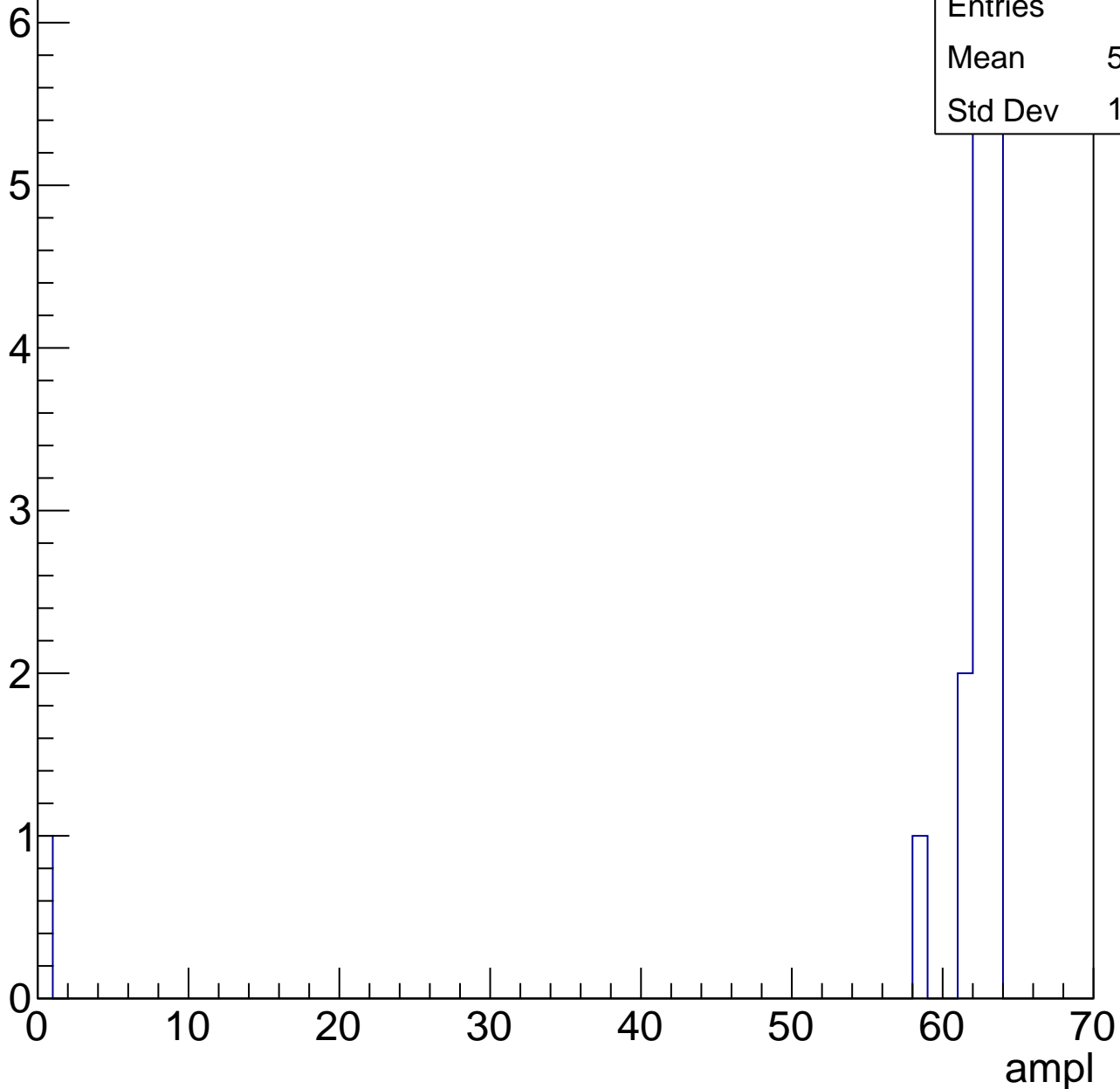


# B1L002S, U18-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	58.12
Std Dev	15.06





# B1L002S, U18-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U18-ch64, adc0

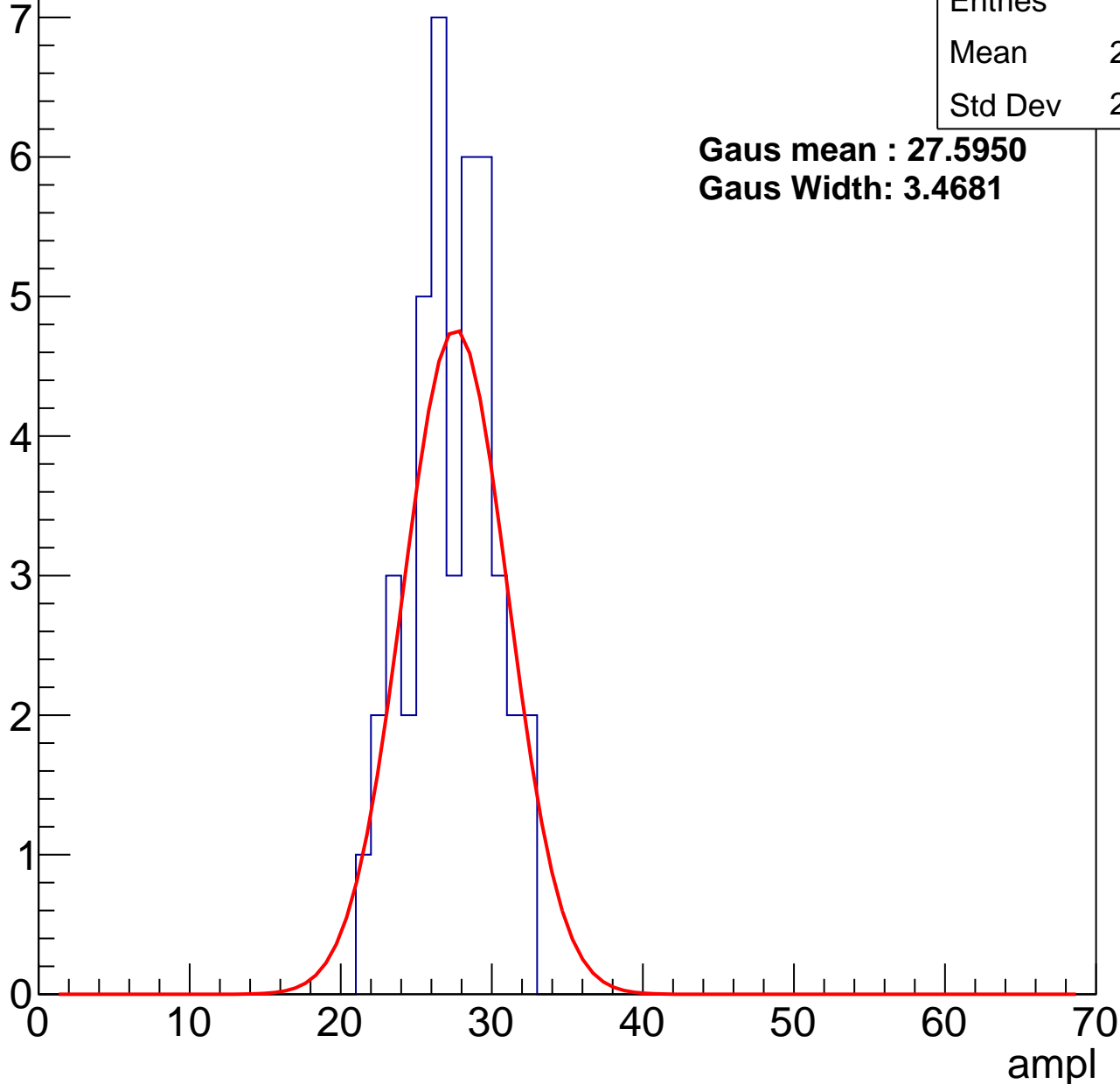
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	26.86
Std Dev	2.748

**Gaus mean : 27.5950**

**Gaus Width: 3.4681**



# B1L002S, U18-ch64, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	32.63
Std Dev	3.154

**Gaus mean : 32.8814**

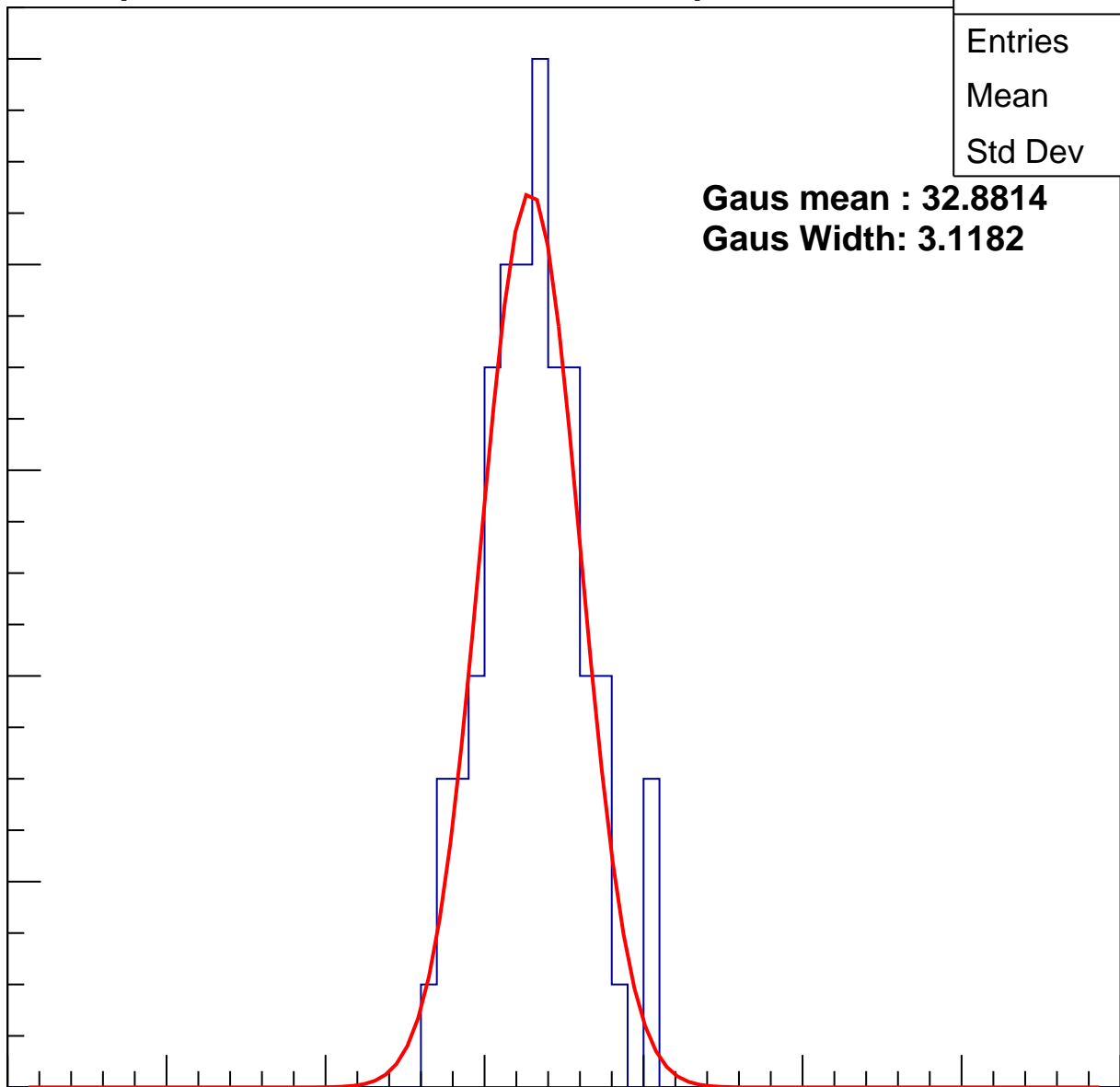
**Gaus Width: 3.1182**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



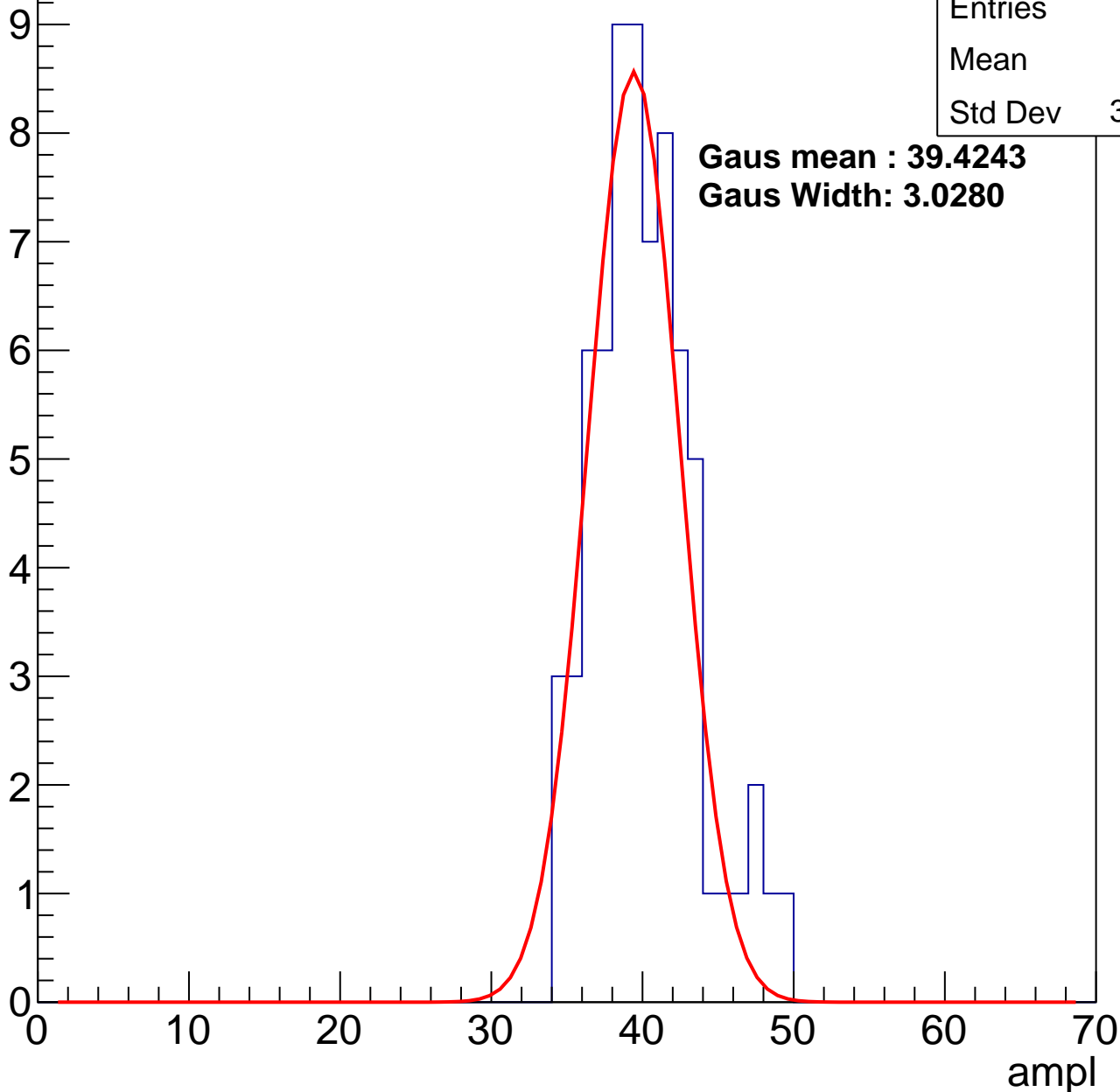
# B1L002S, U18-ch64, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.7
Std Dev	3.338

**Gaus mean : 39.4243**  
**Gaus Width: 3.0280**

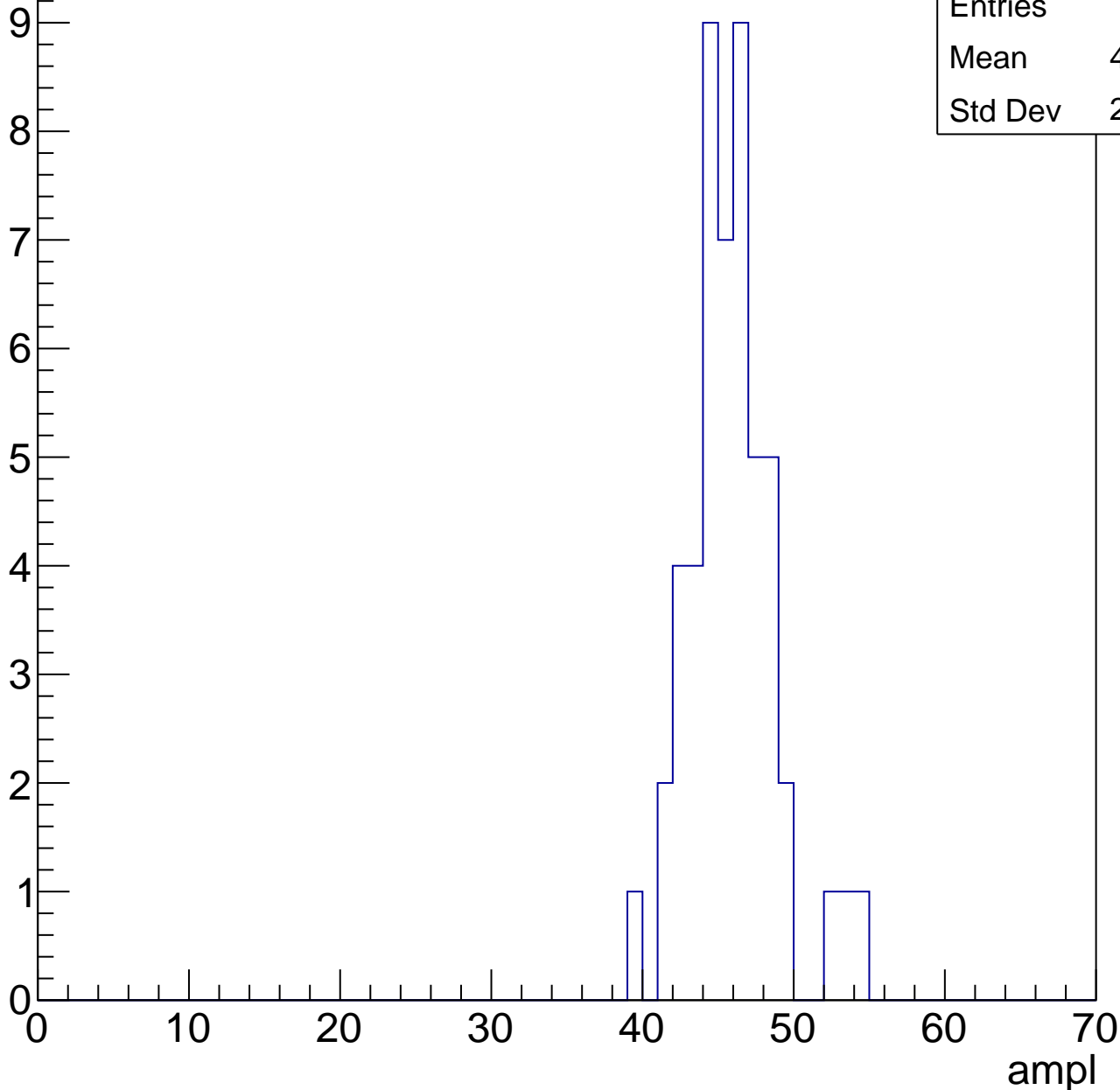


# B1L002S, U18-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	45.45
Std Dev	2.865



# B1L002S, U18-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	51.68
Std Dev	3.15

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

1

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4

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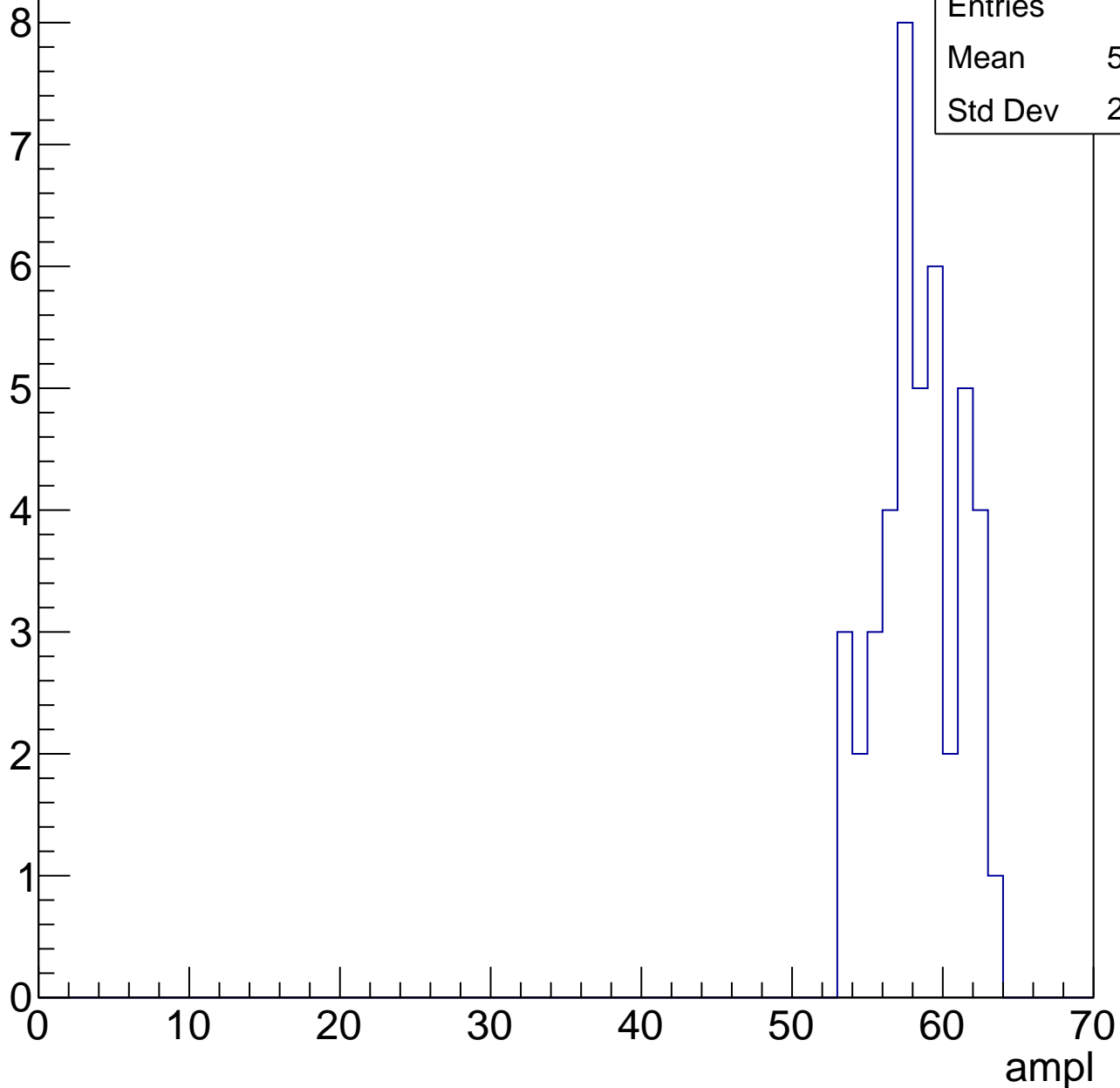
318

319

# B1L002S, U18-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

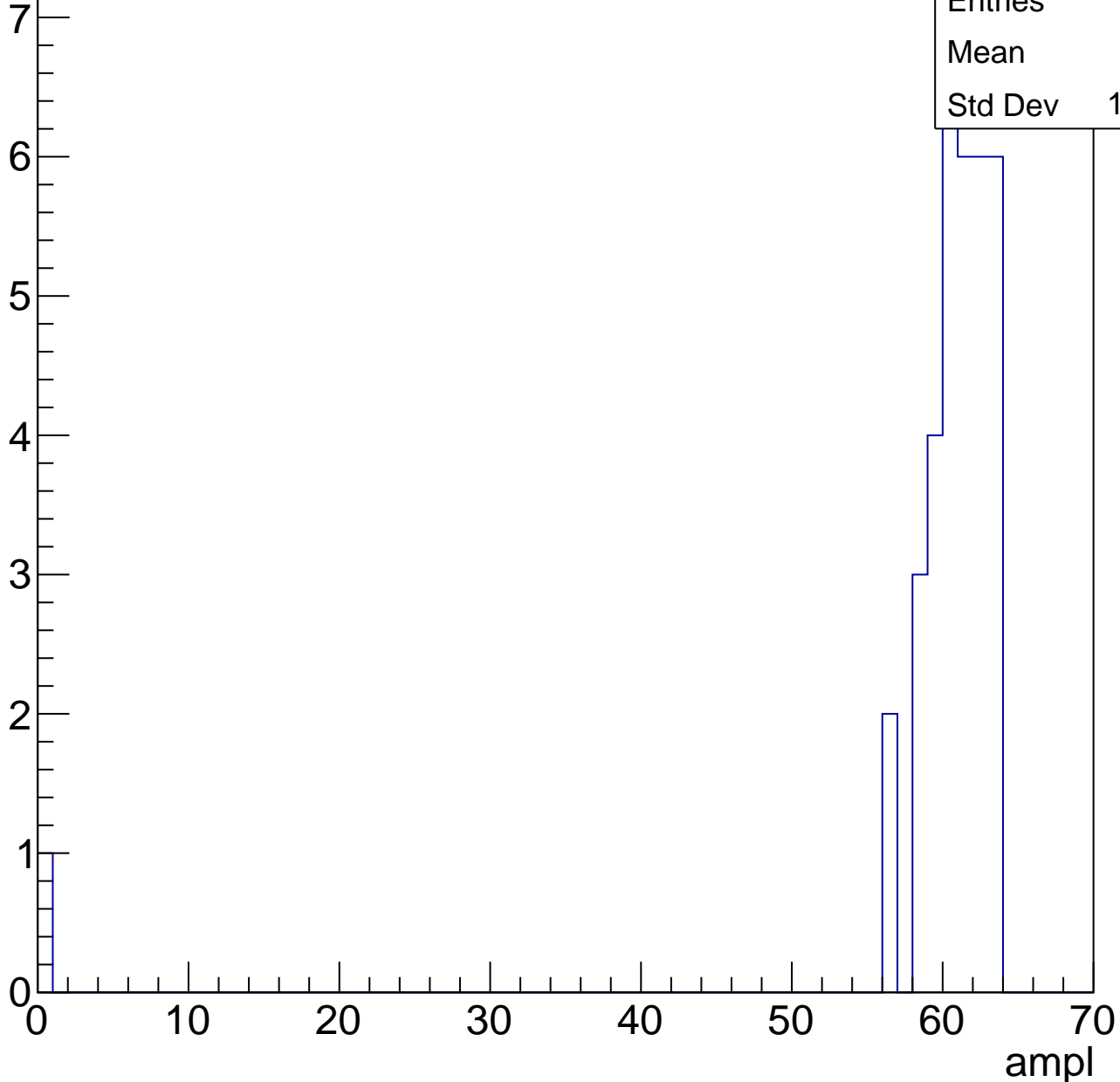


# B1L002S, U18-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.8
Std Dev	10.26

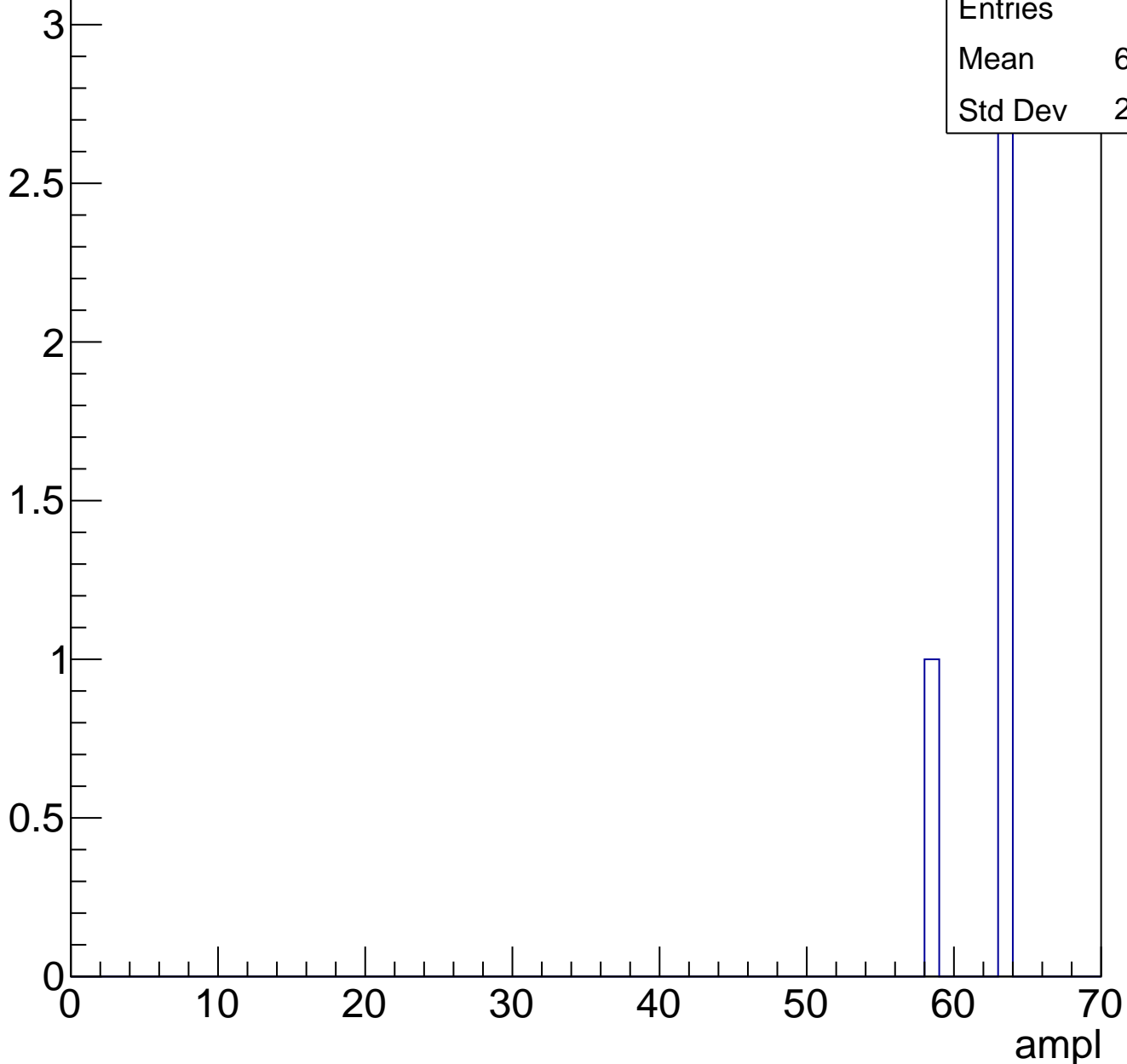




# B1L002S, U18-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch65, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	49
Mean	27.02
Std Dev	4.636

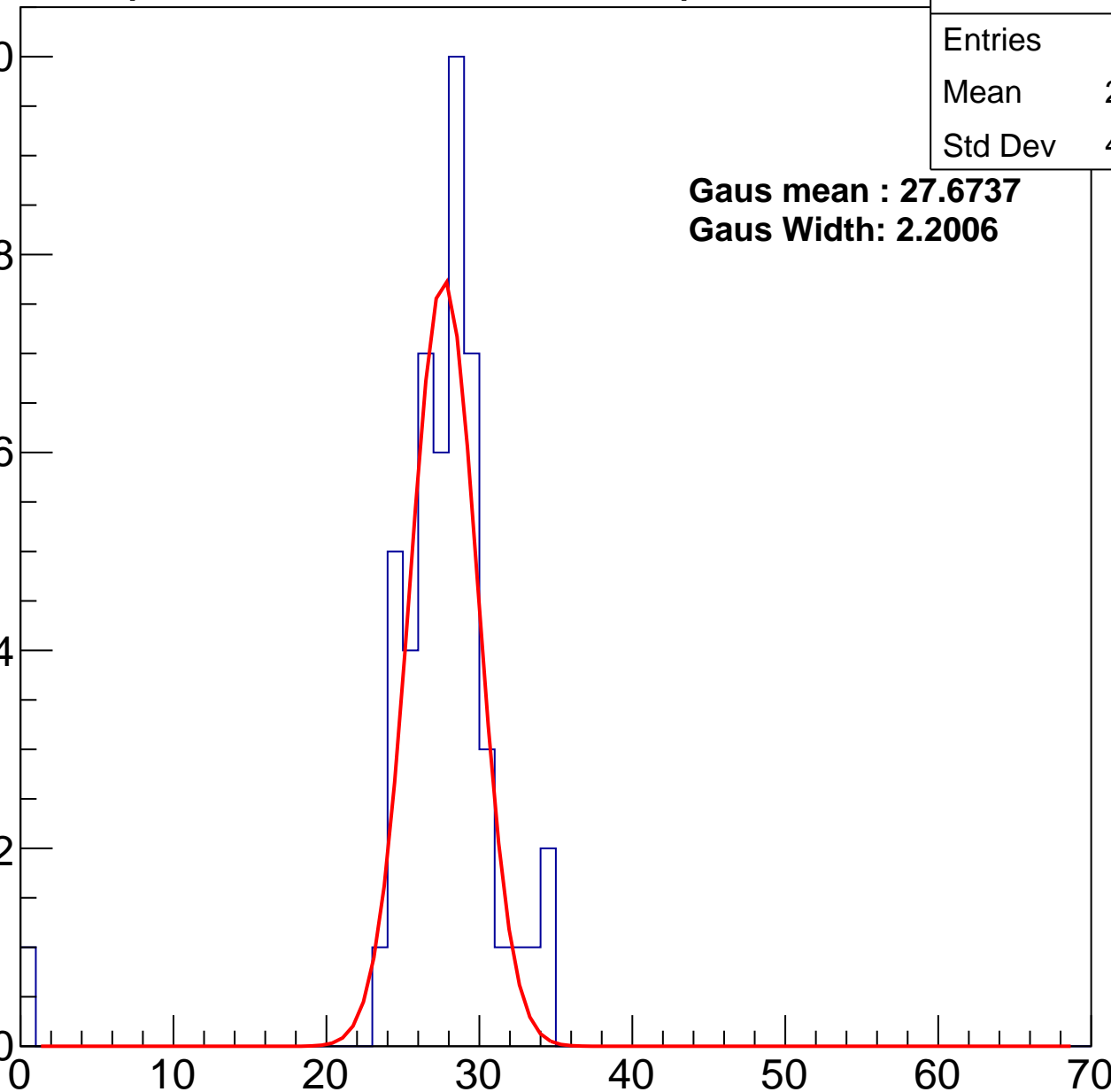
**Gaus mean : 27.6737**

**Gaus Width: 2.2006**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U18-ch65, adc1

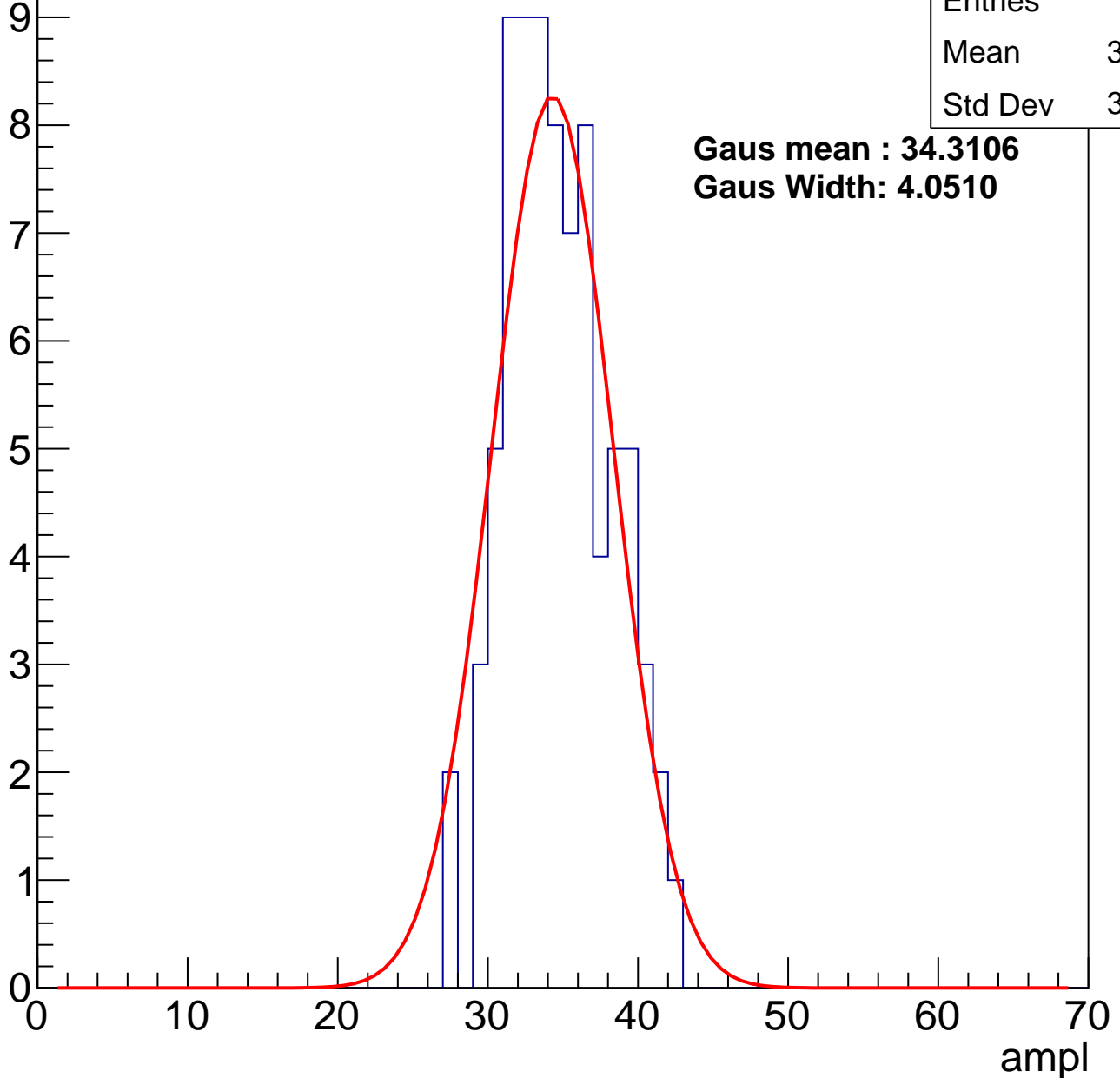
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	34.21
Std Dev	3.401

**Gaus mean : 34.3106**

**Gaus Width: 4.0510**



# B1L002S, U18-ch65, adc2

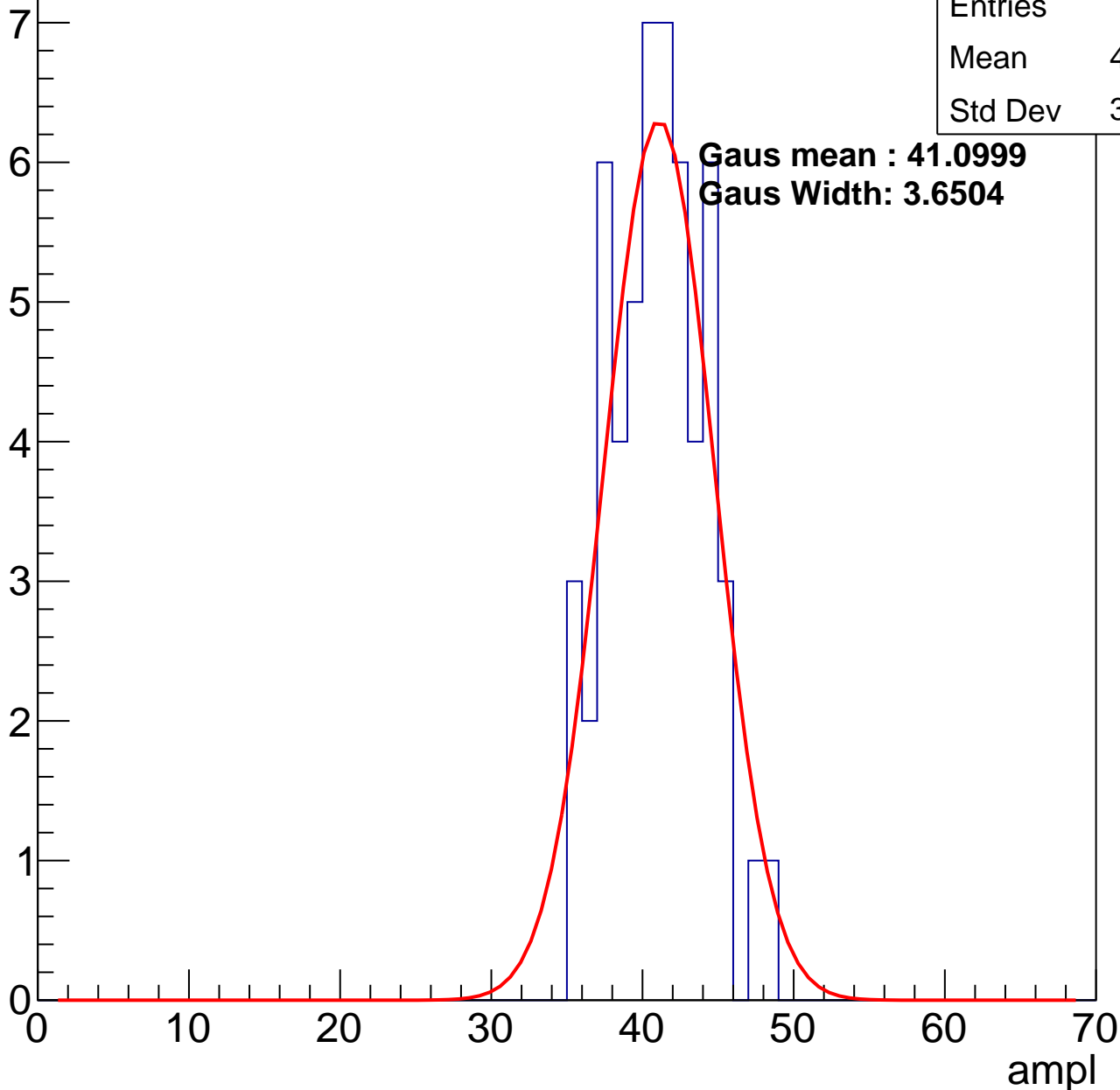
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.56
Std Dev	3.062

**Gaus mean : 41.0999**

**Gaus Width: 3.6504**

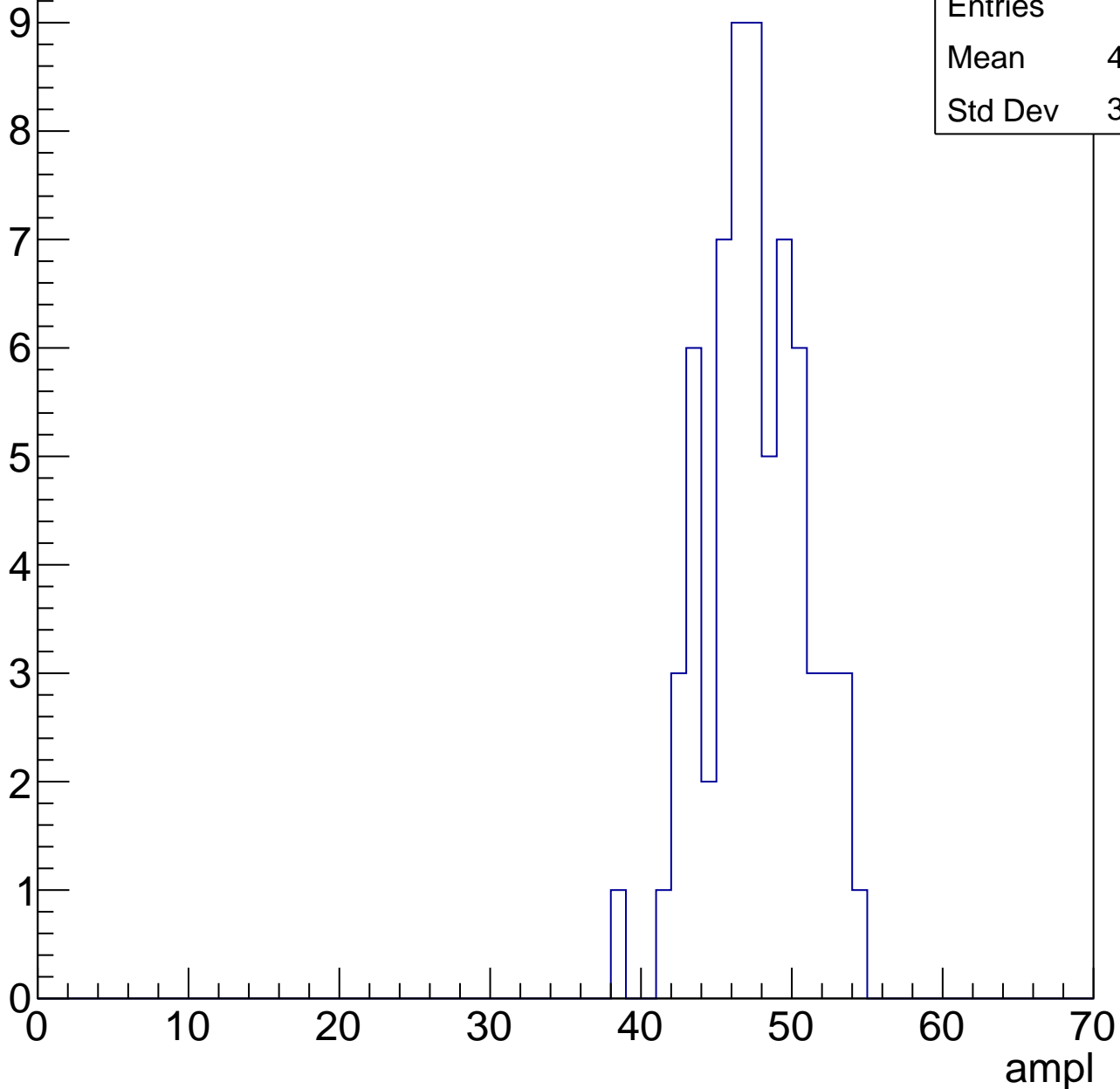


# B1L002S, U18-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	47.09
Std Dev	3.279

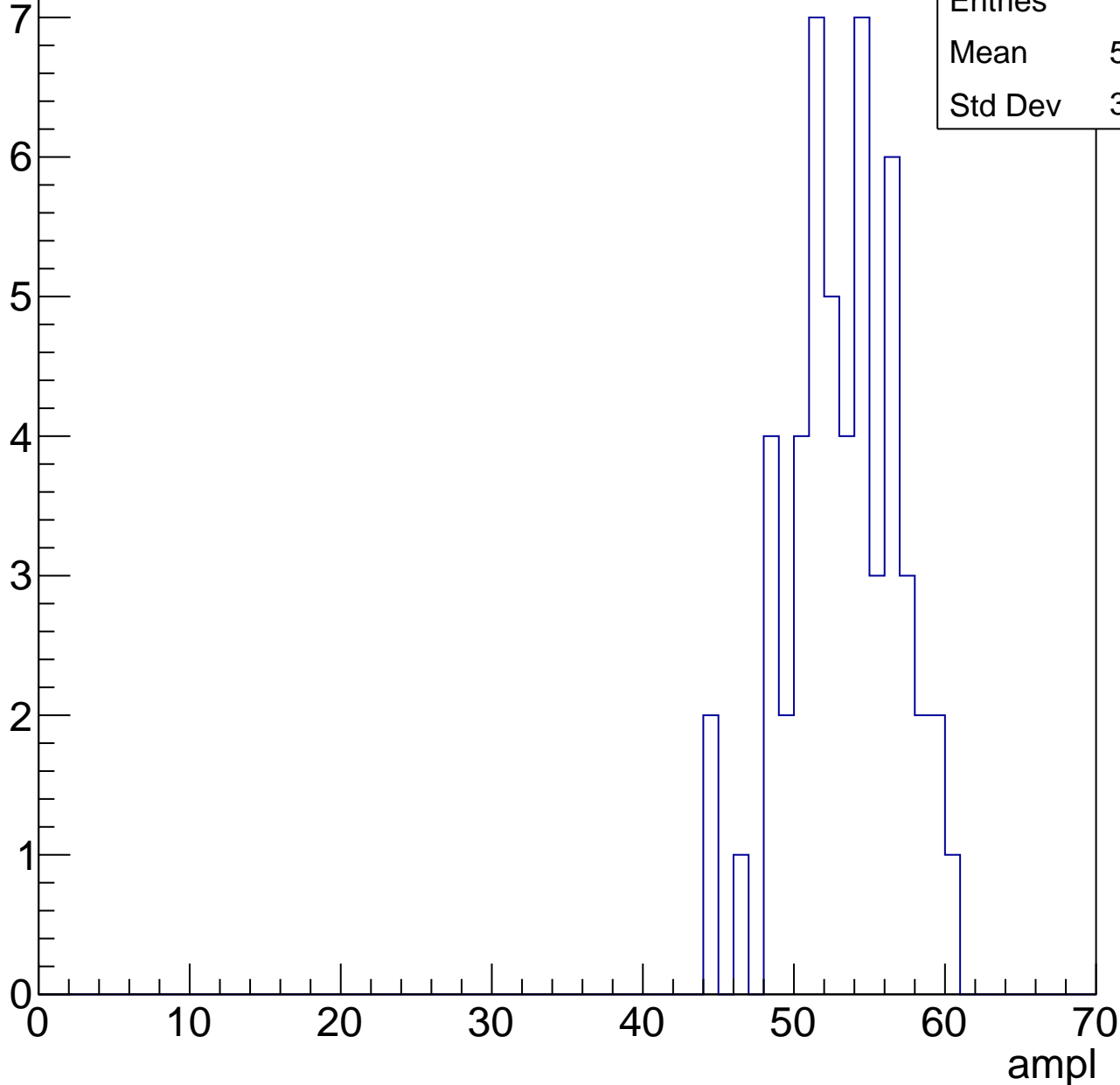


# B1L002S, U18-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.77
Std Dev	3.638

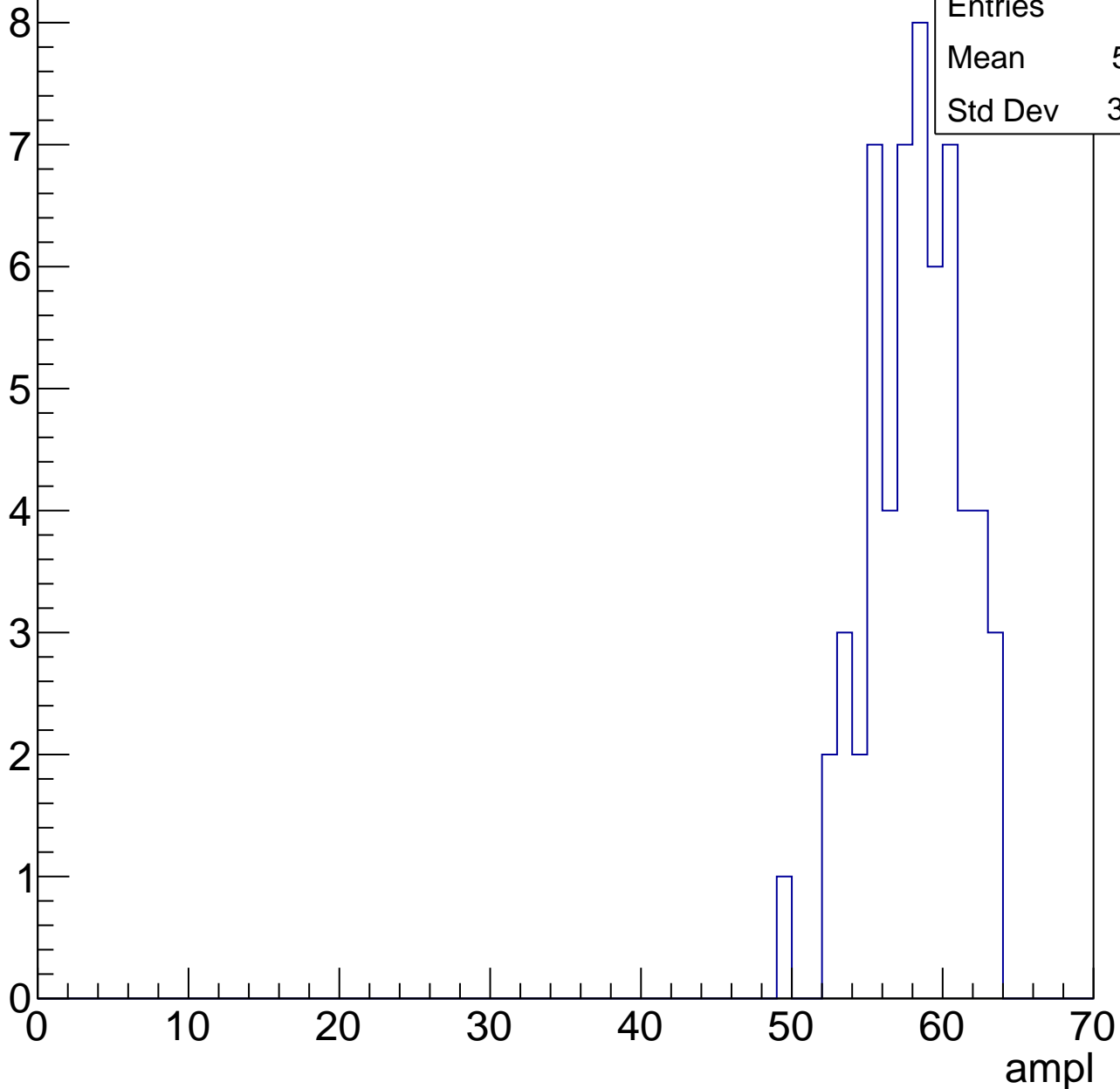


# B1L002S, U18-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.71
Std Dev	3.079

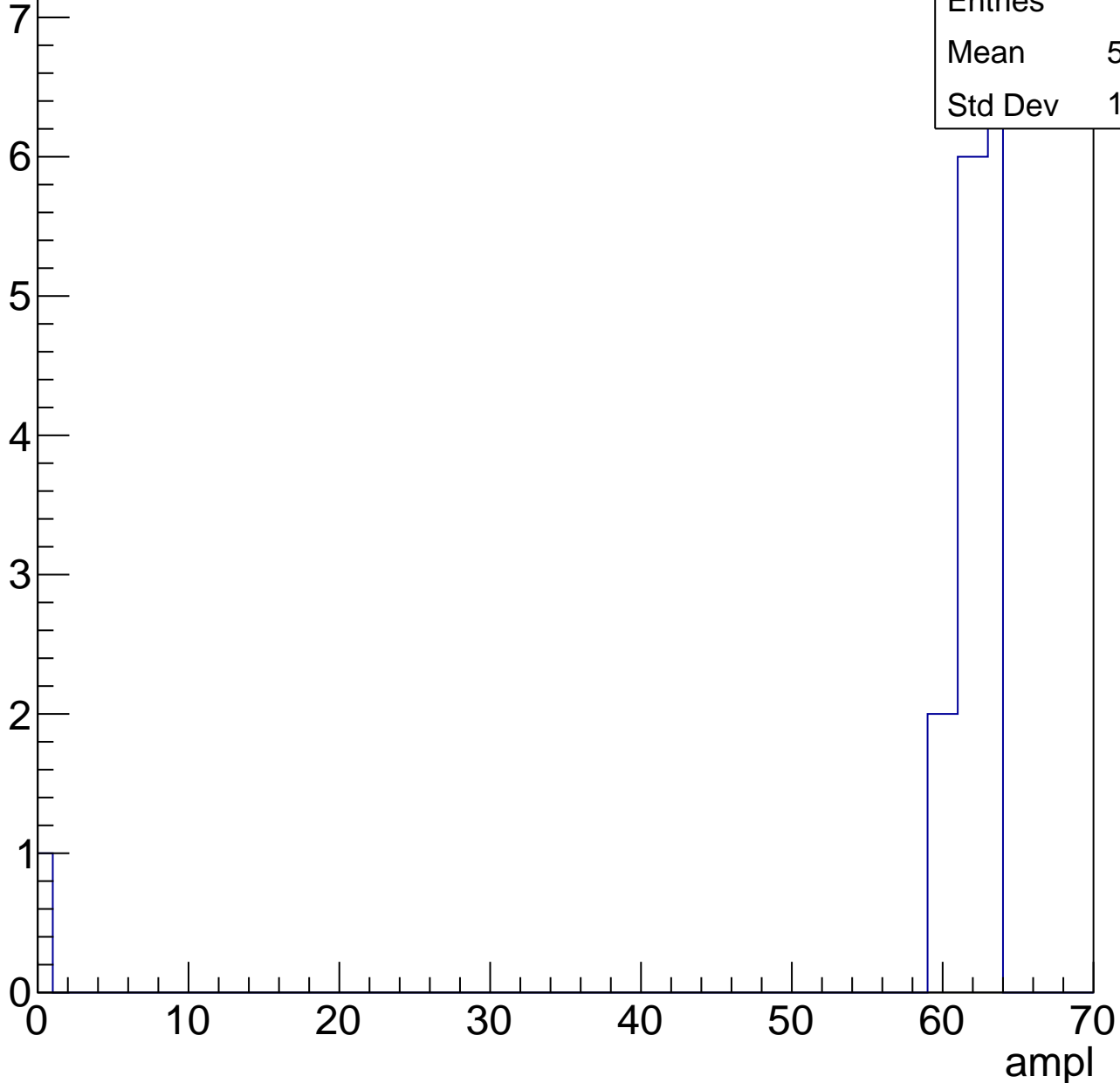


# B1L002S, U18-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	59.04
Std Dev	12.37





# B1L002S, U18-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch66, adc0

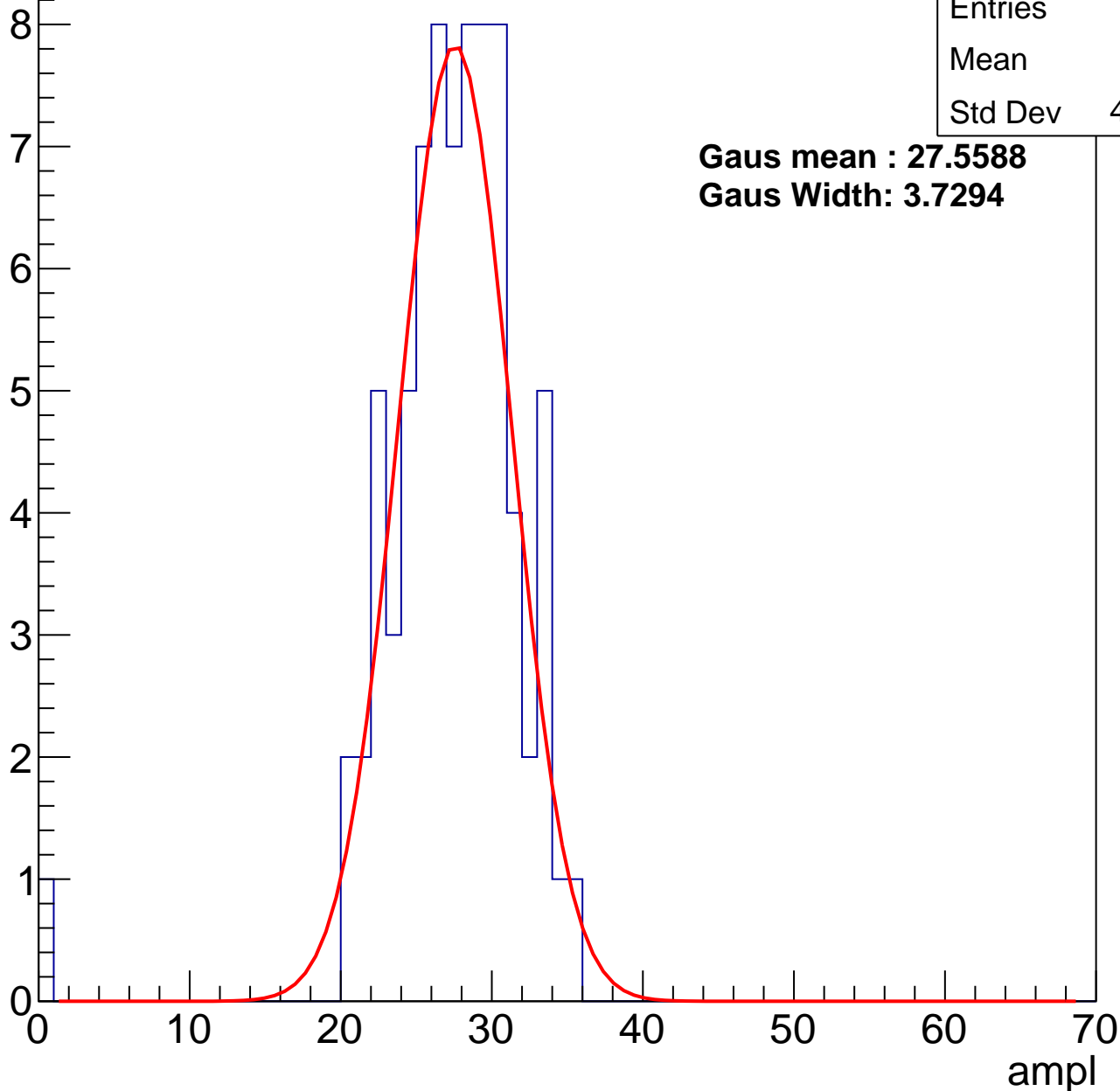
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	26.9
Std Dev	4.659

**Gaus mean : 27.5588**

**Gaus Width: 3.7294**



# B1L002S, U18-ch66, adc1

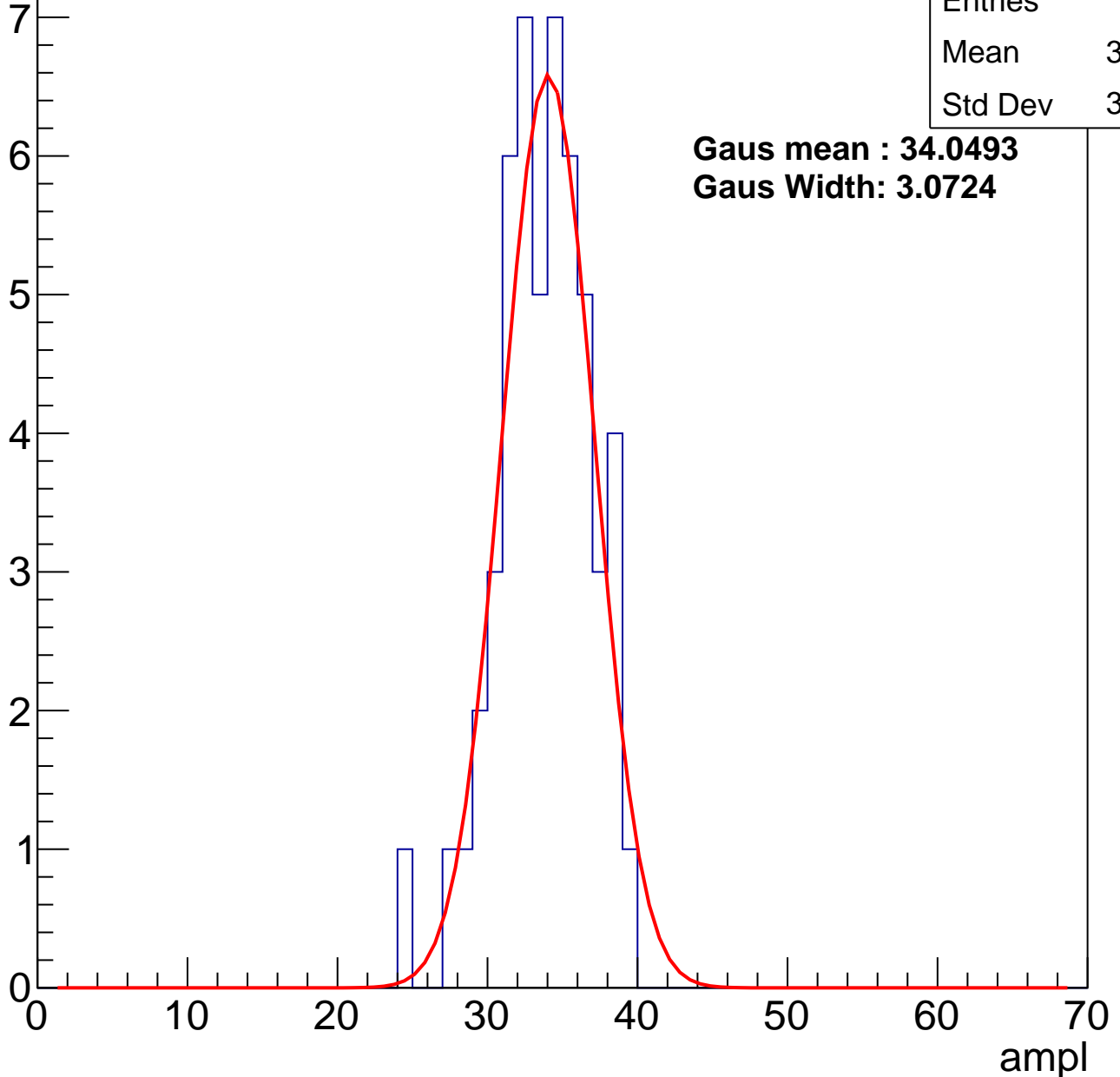
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.31
Std Dev	3.061

**Gaus mean : 34.0493**

**Gaus Width: 3.0724**



# B1L002S, U18-ch66, adc2

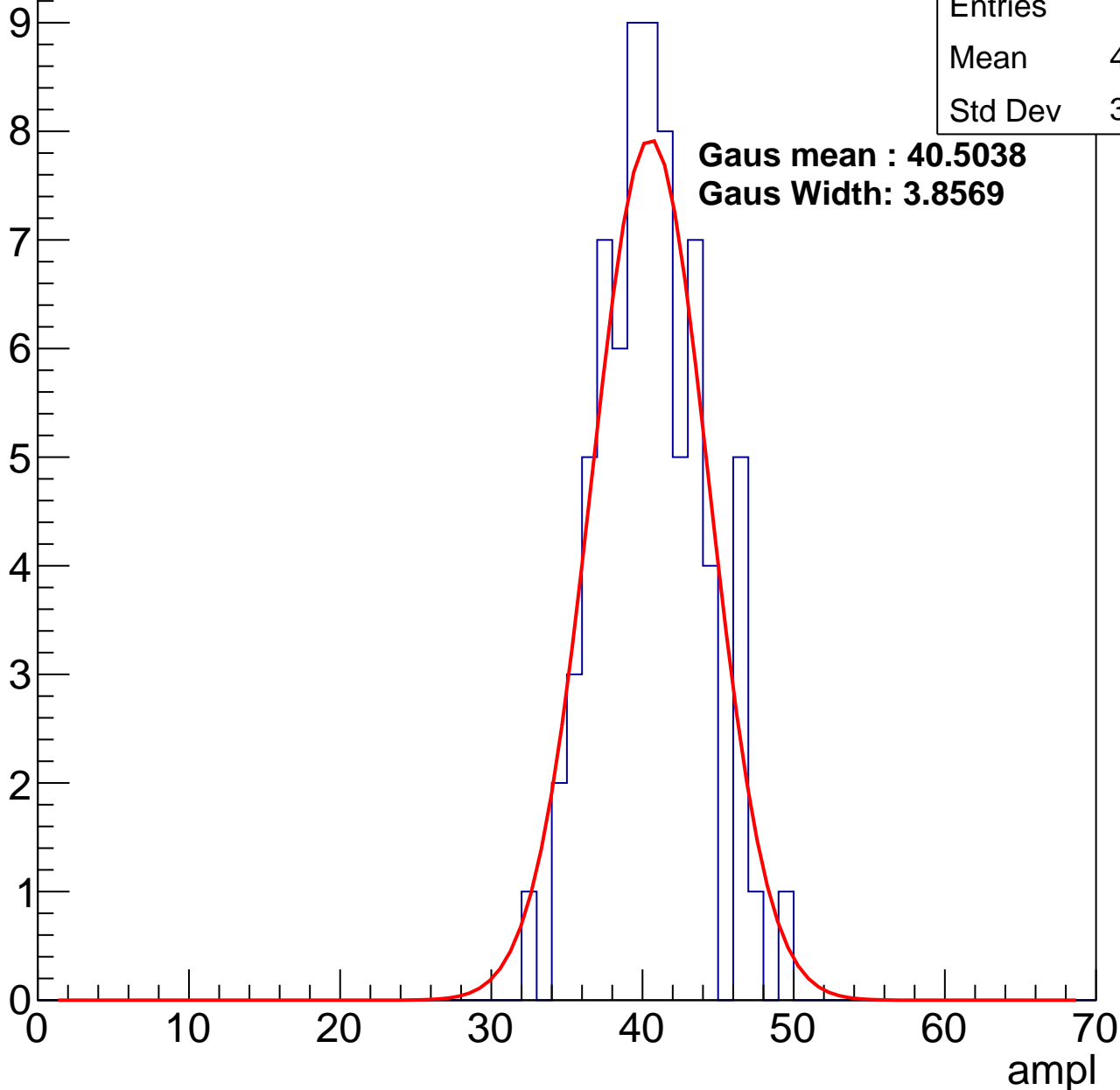
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	40.05
Std Dev	3.416

**Gaus mean : 40.5038**

**Gaus Width: 3.8569**



# B1L002S, U18-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

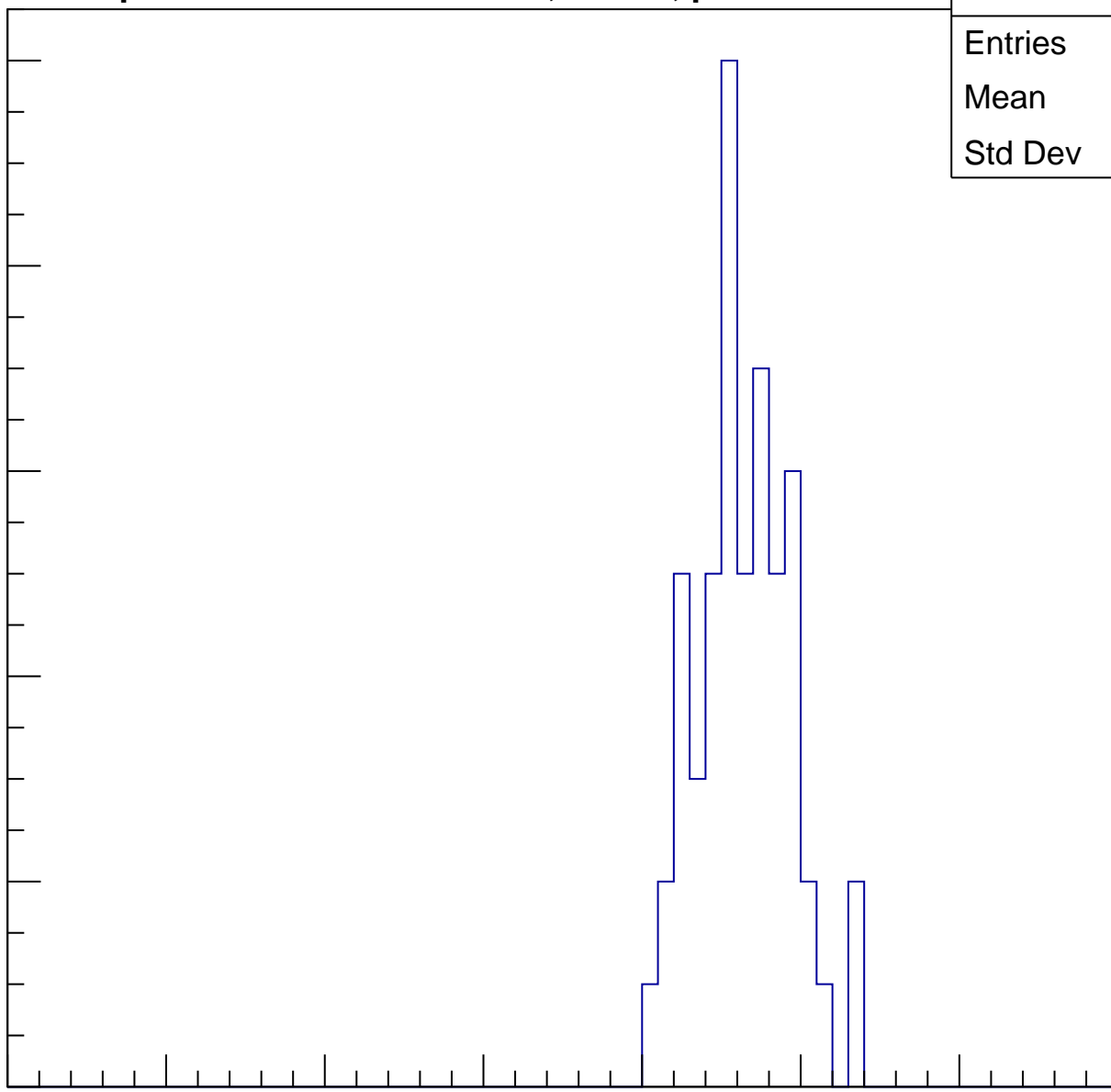
Entries	54
Mean	45.94
Std Dev	2.915

Entry

10  
8  
6  
4  
2  
0

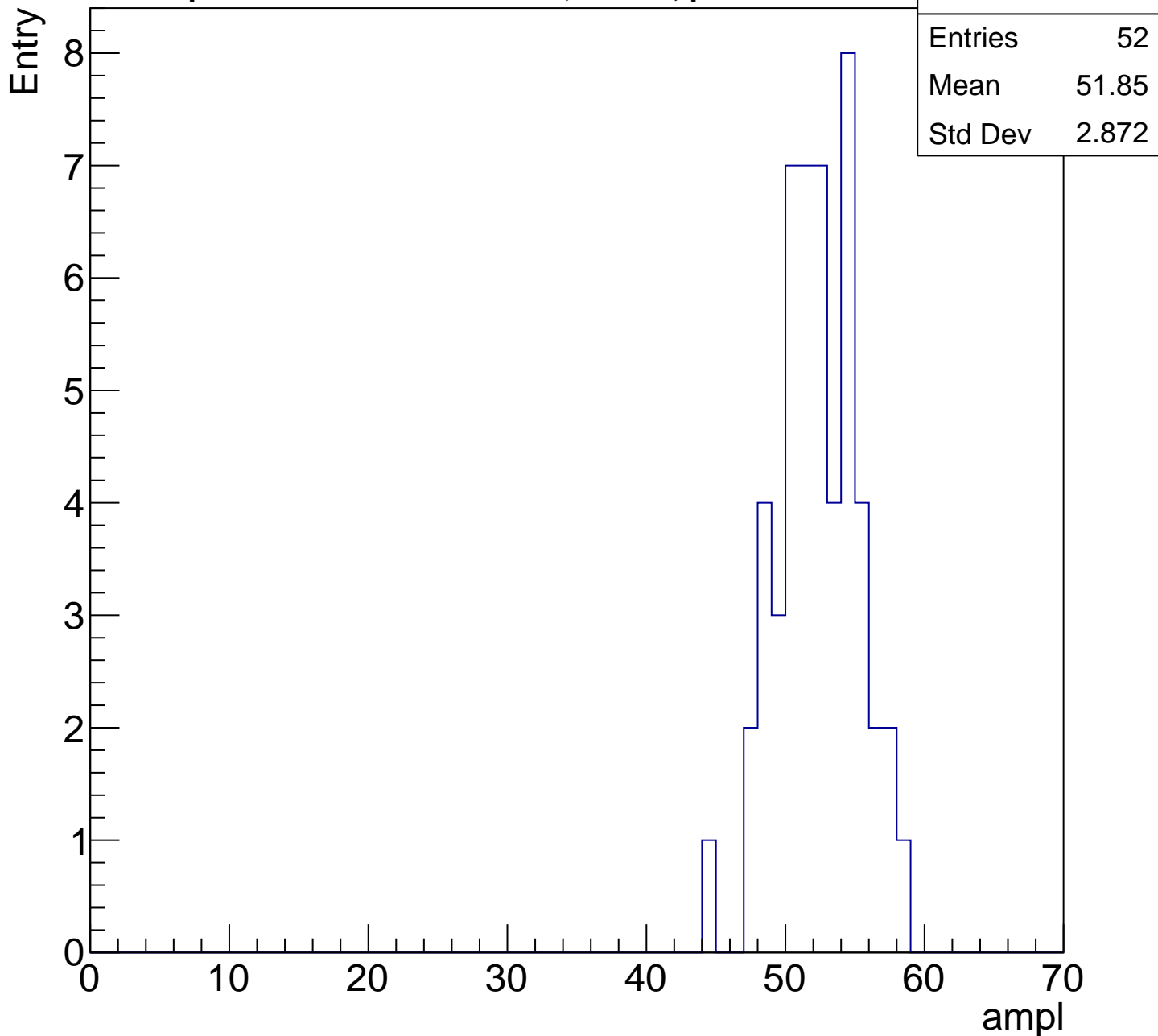
0 10 20 30 40 50 60 70

ampl



# B1L002S, U18-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

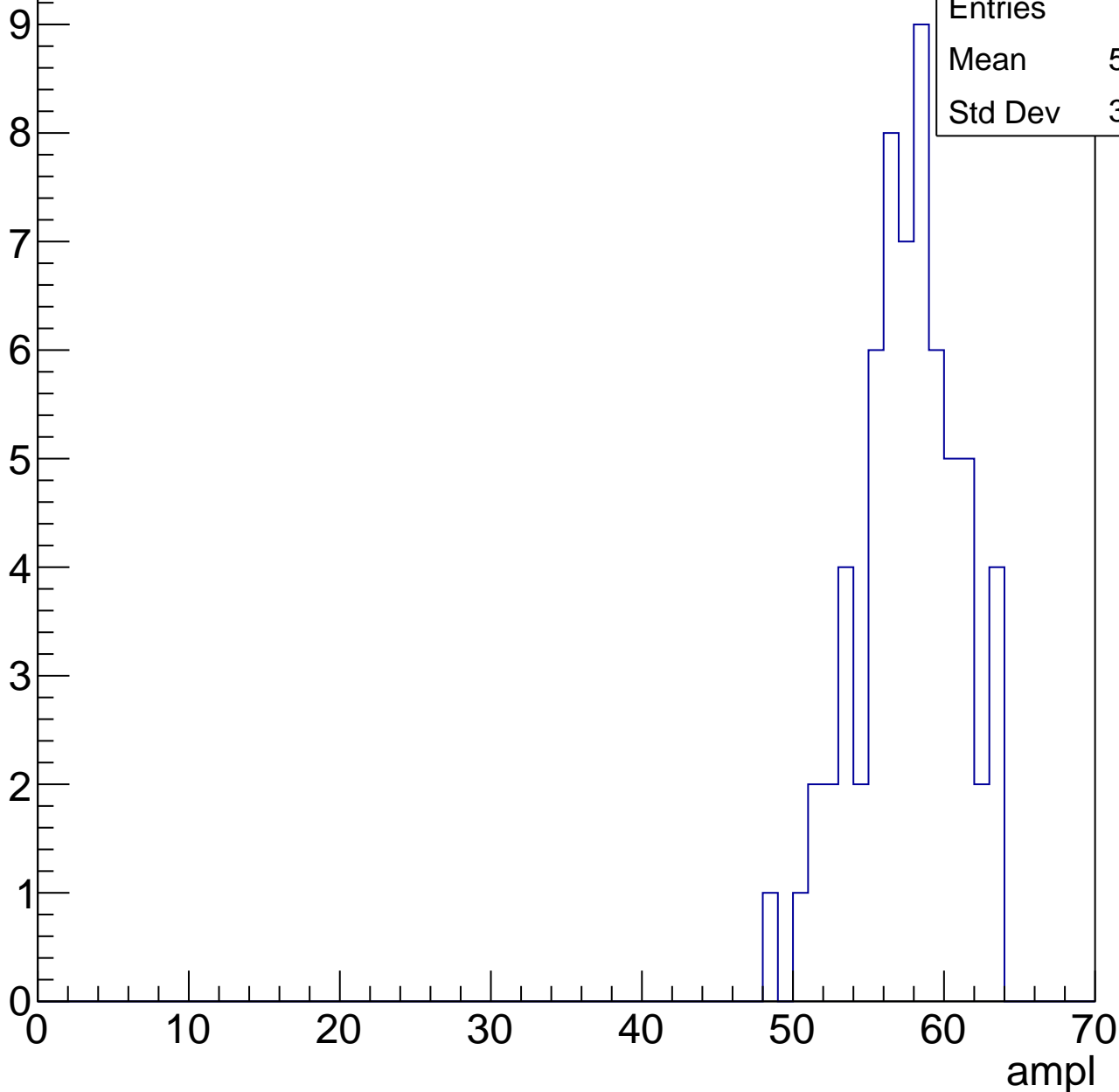


# B1L002S, U18-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.16
Std Dev	3.346

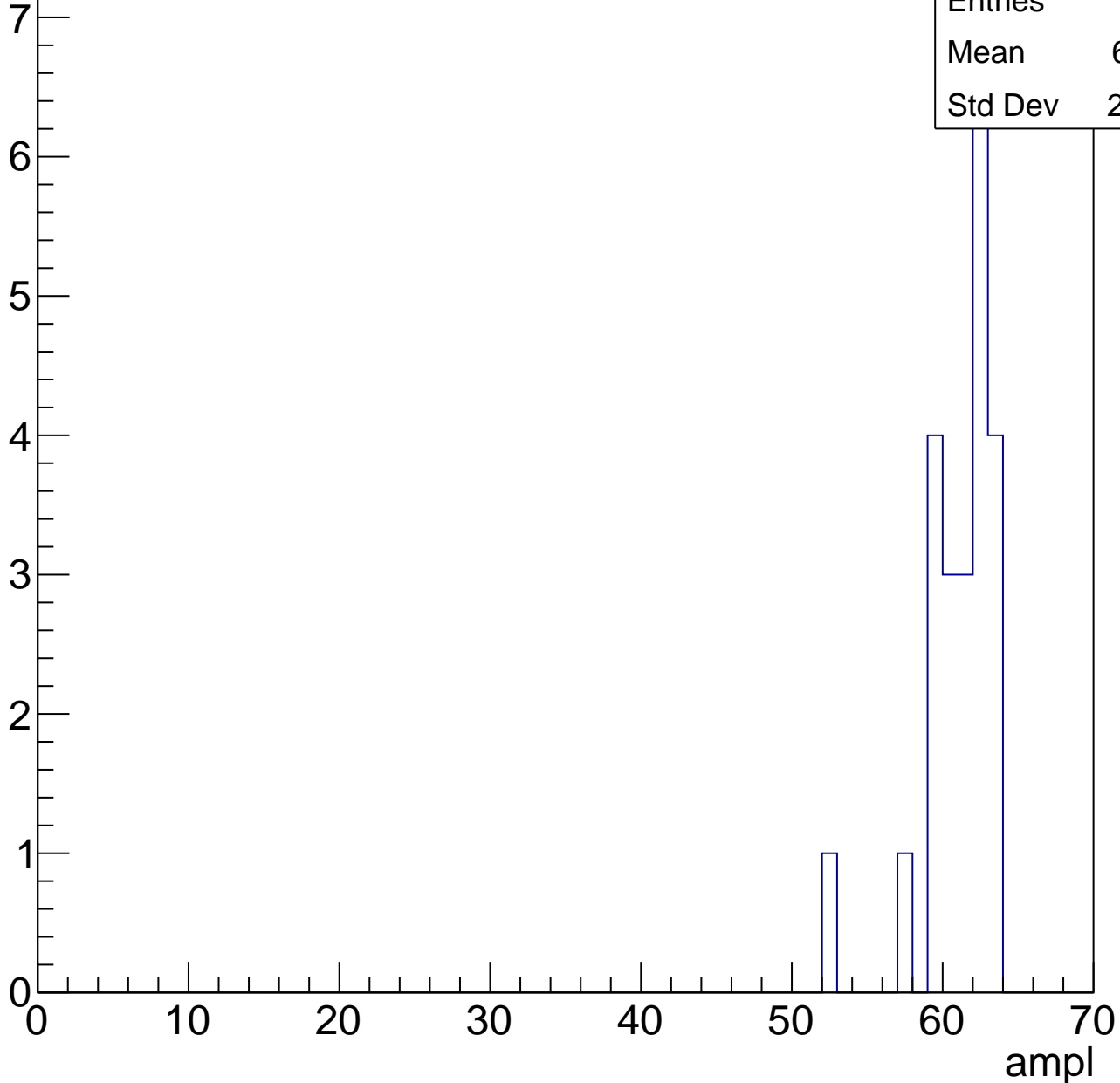


# B1L002S, U18-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	60.61
Std Dev	2.427





# B1L002S, U18-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

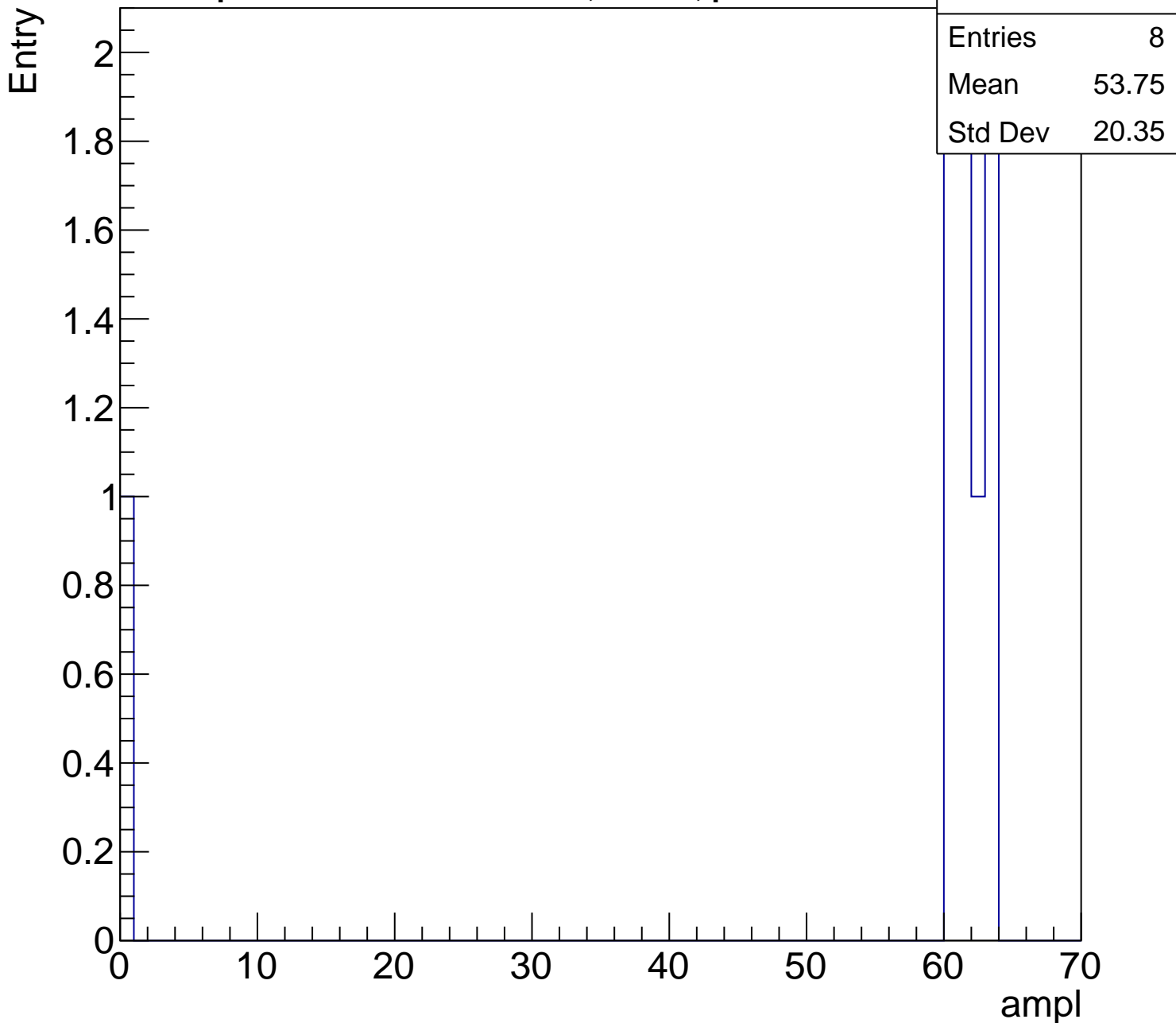
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.75
Std Dev	20.35

0 10 20 30 40 50 60 70

ampl



# B1L002S, U18-ch67, adc0

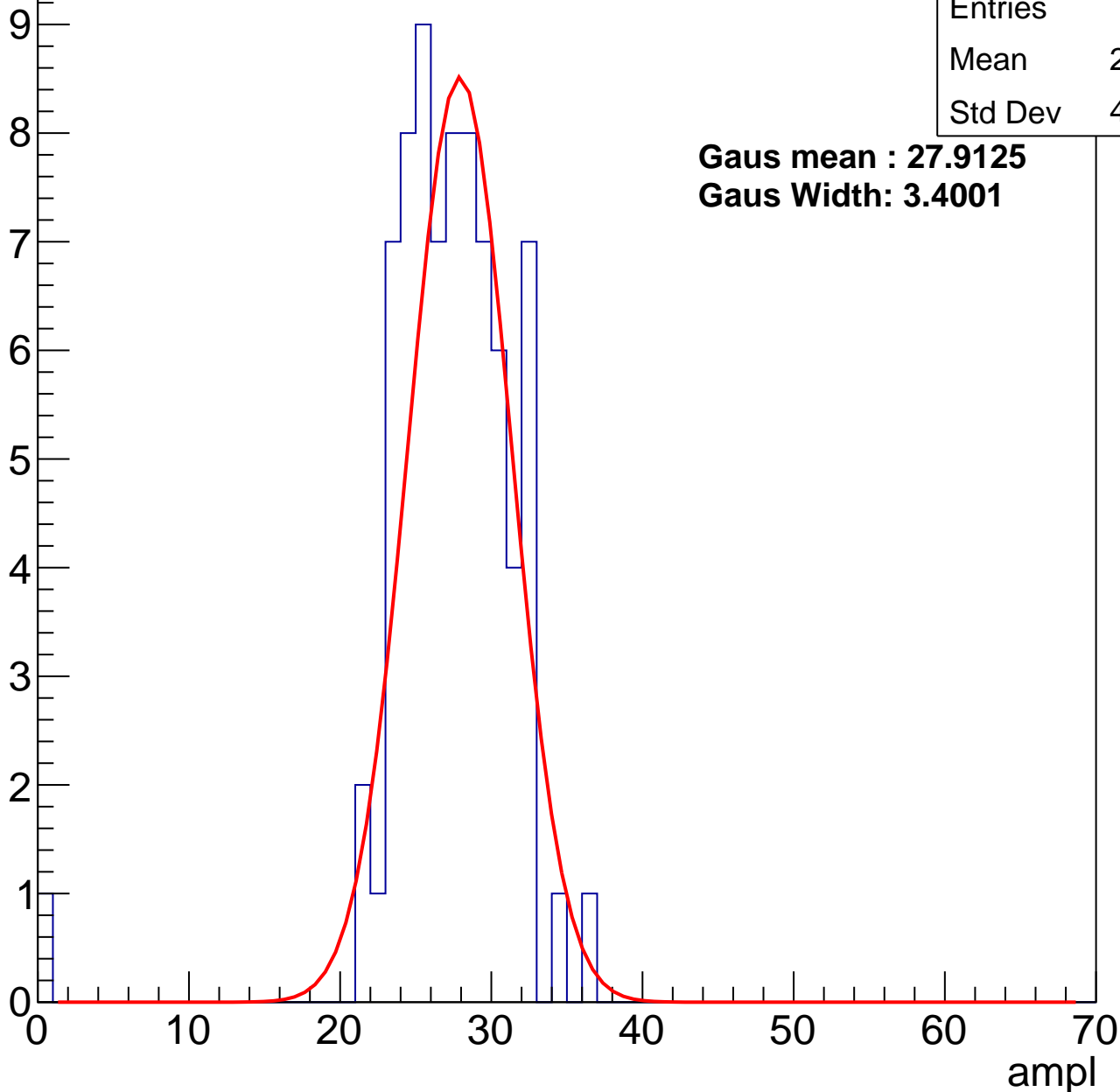
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	26.82
Std Dev	4.428

**Gaus mean : 27.9125**

**Gaus Width: 3.4001**



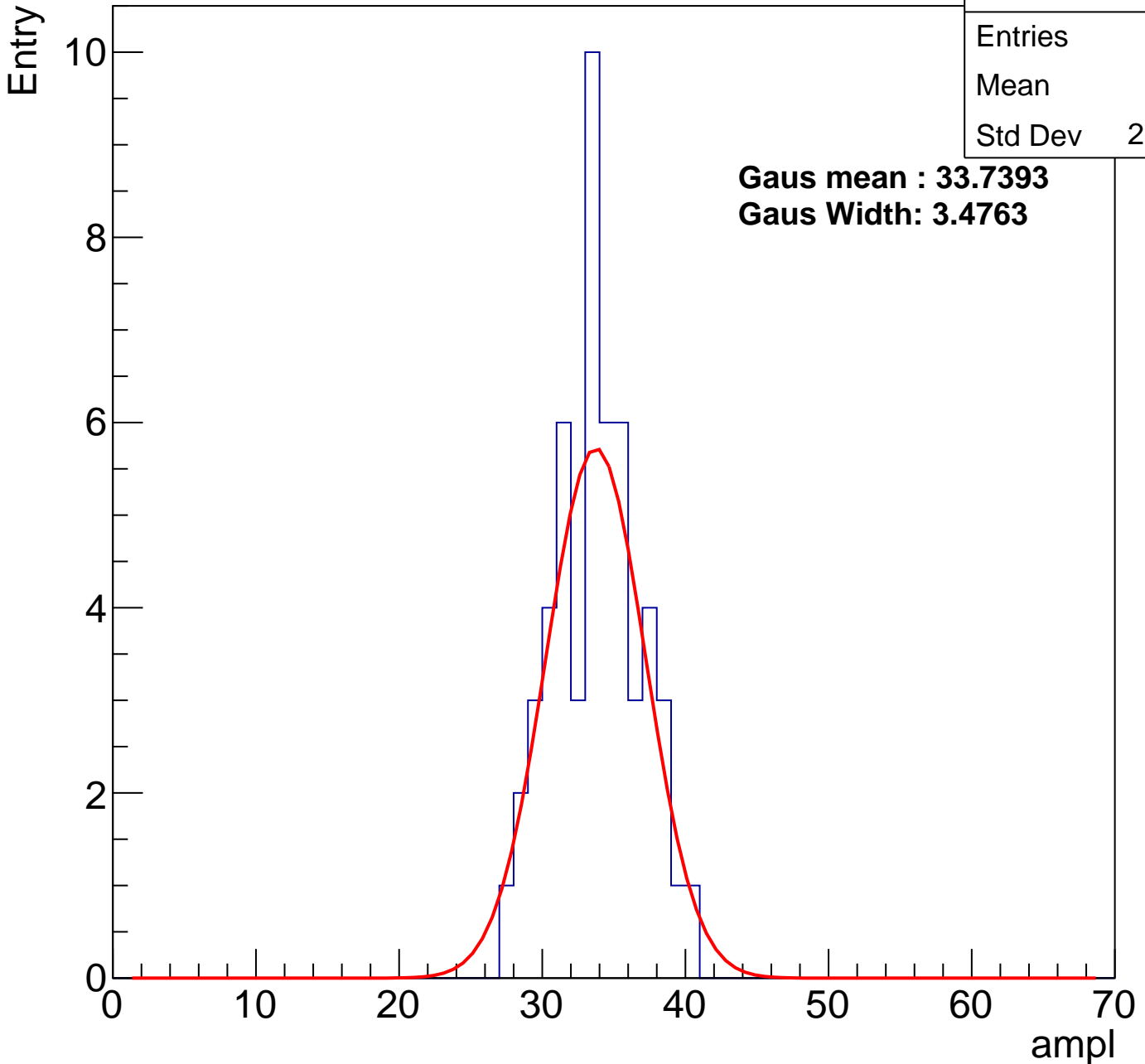
# B1L002S, U18-ch67, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	53
Mean	33.3
Std Dev	2.988

**Gaus mean : 33.7393**

**Gaus Width: 3.4763**



# B1L002S, U18-ch67, adc2

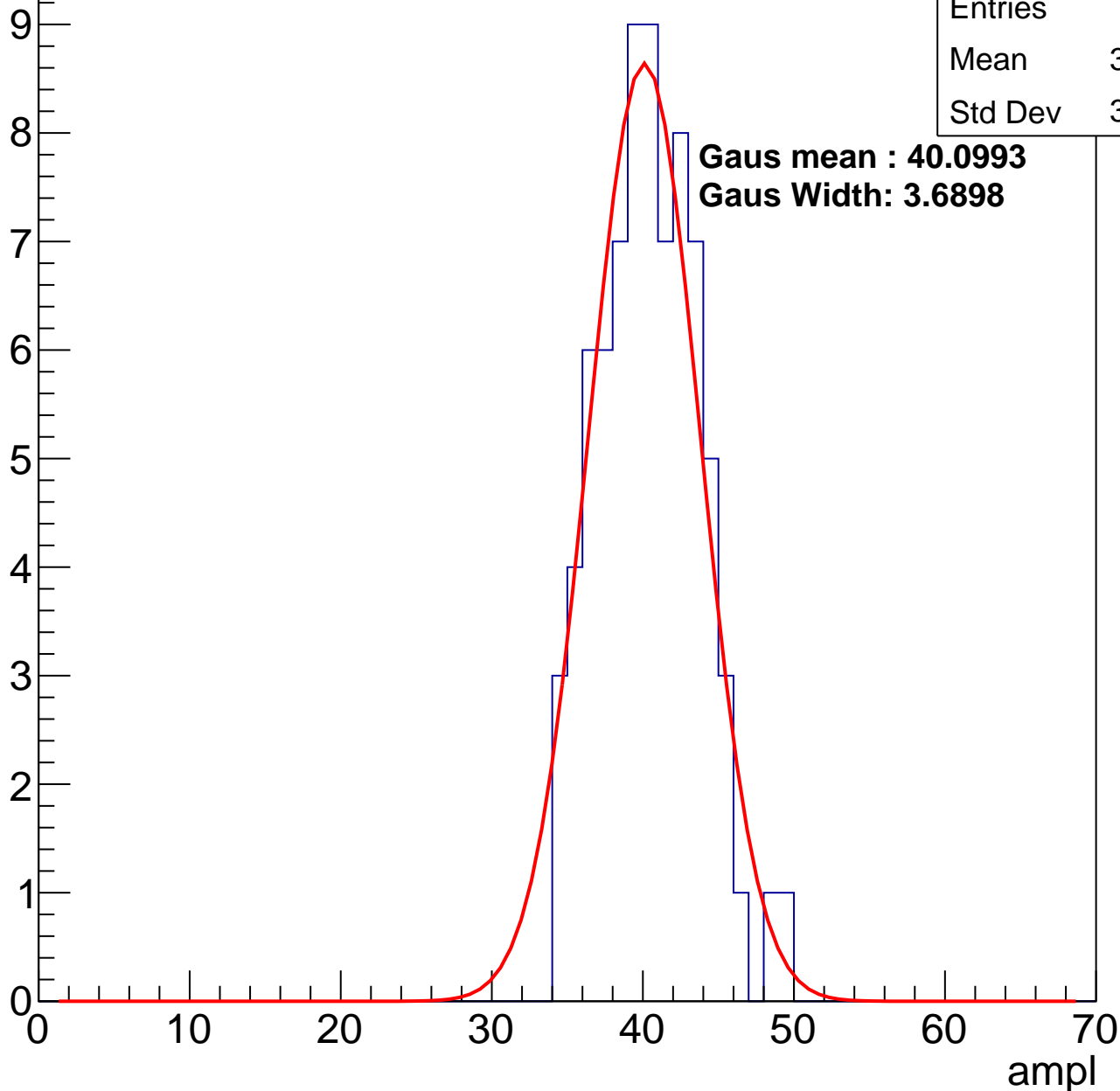
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	39.99
Std Dev	3.289

**Gaus mean : 40.0993**

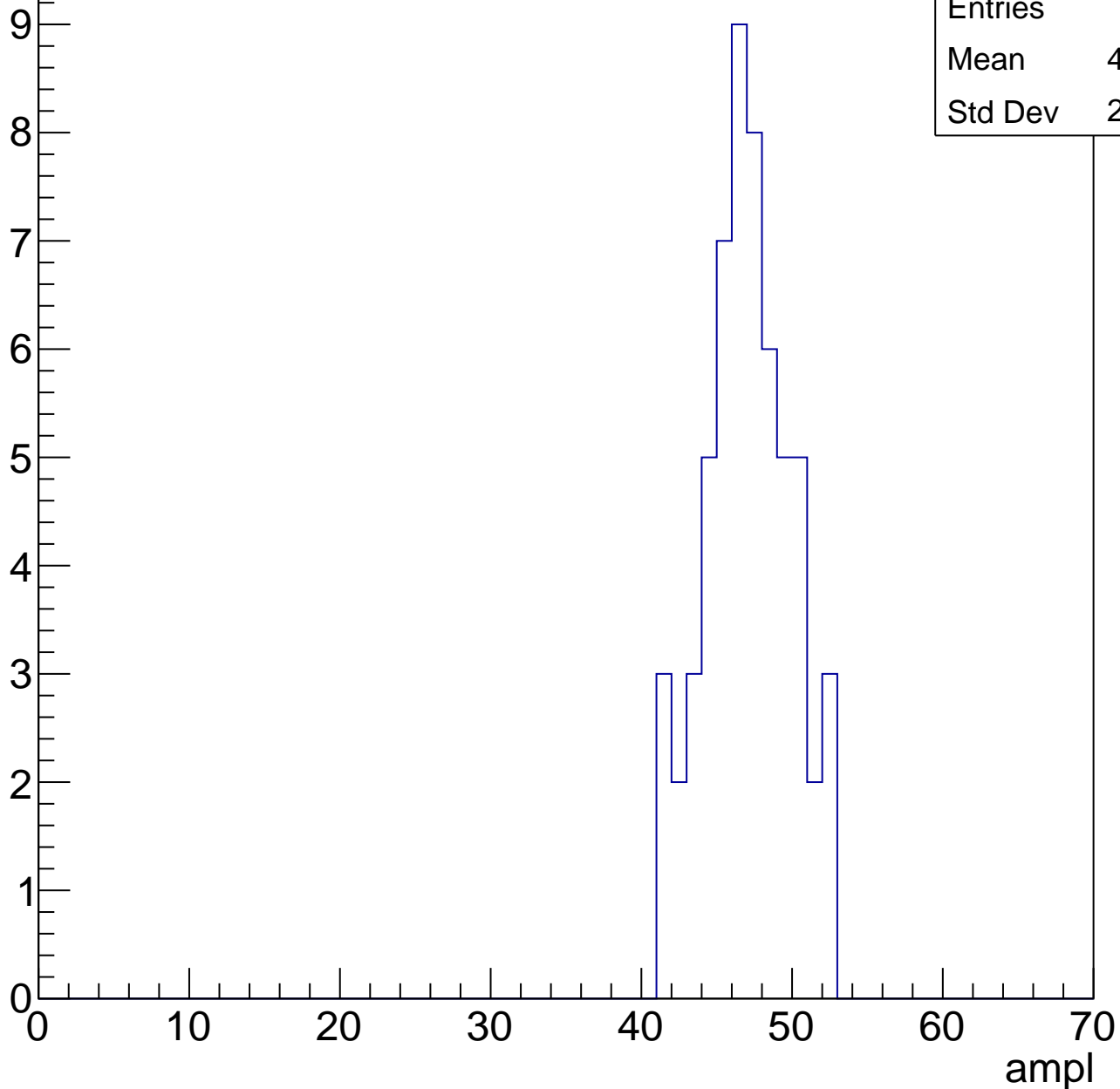
**Gaus Width: 3.6898**



# B1L002S, U18-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



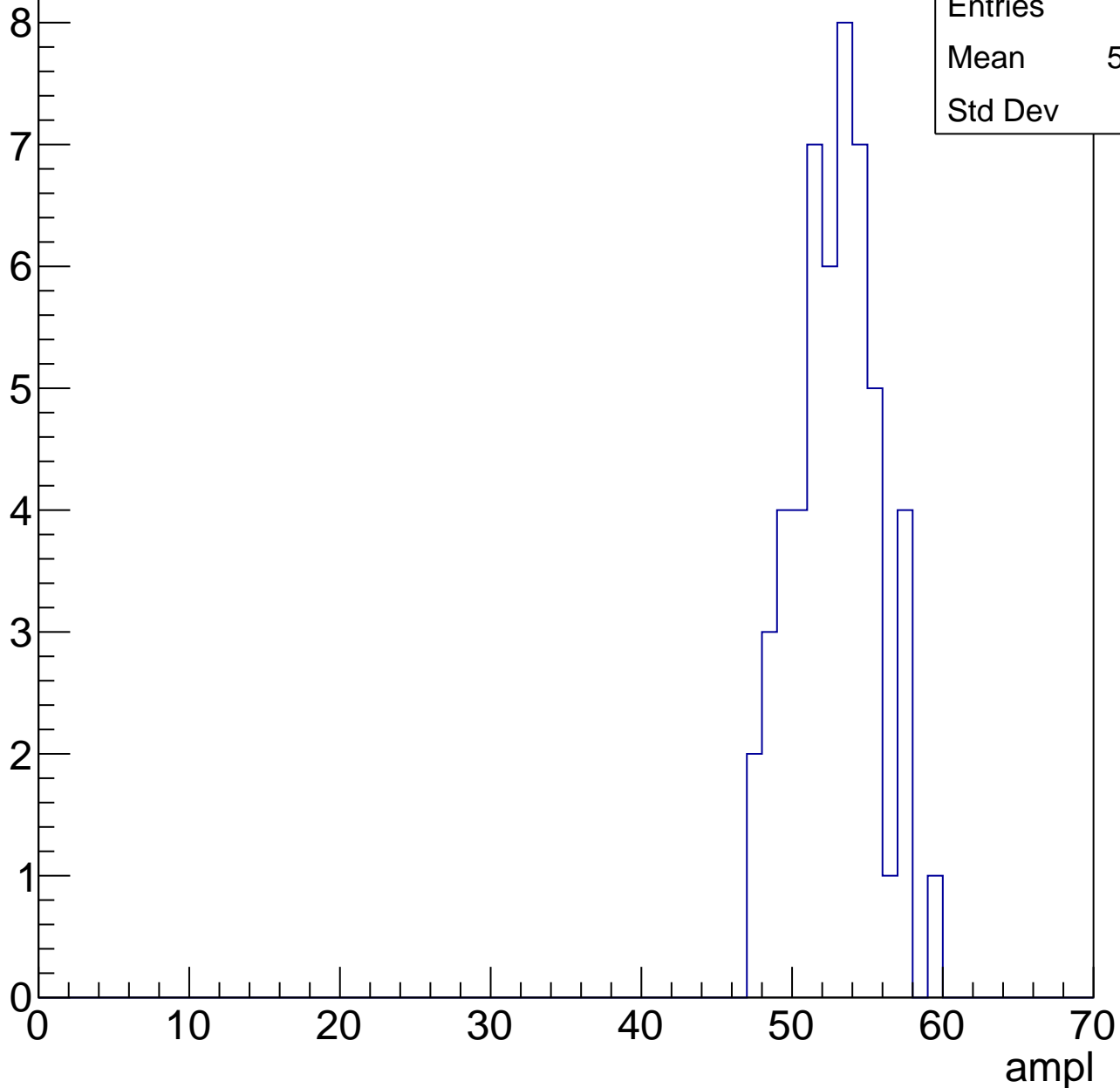
Entries	58
Mean	46.59
Std Dev	2.804

# B1L002S, U18-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.37
Std Dev	2.76

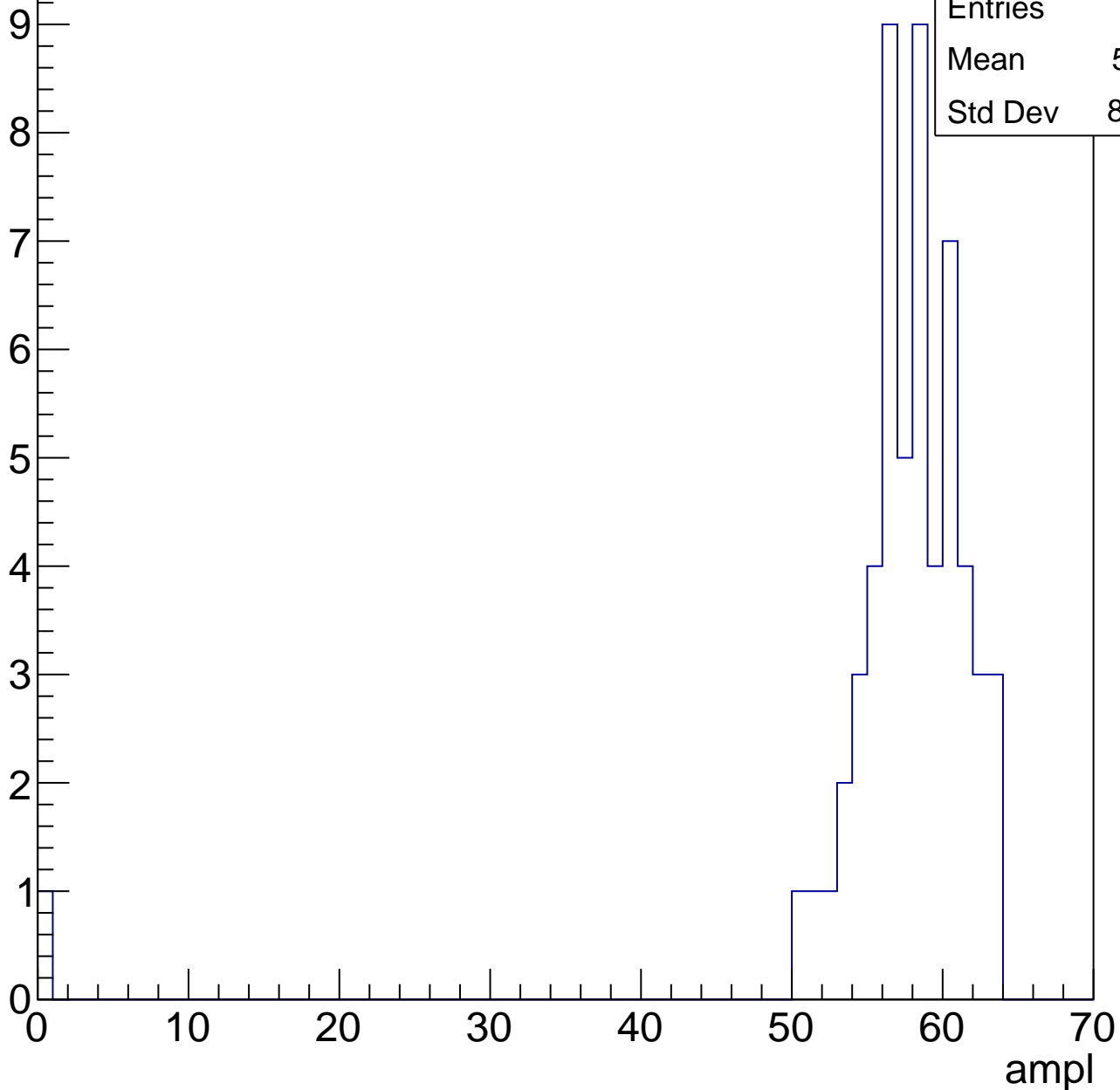


# B1L002S, U18-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	56.61
Std Dev	8.132

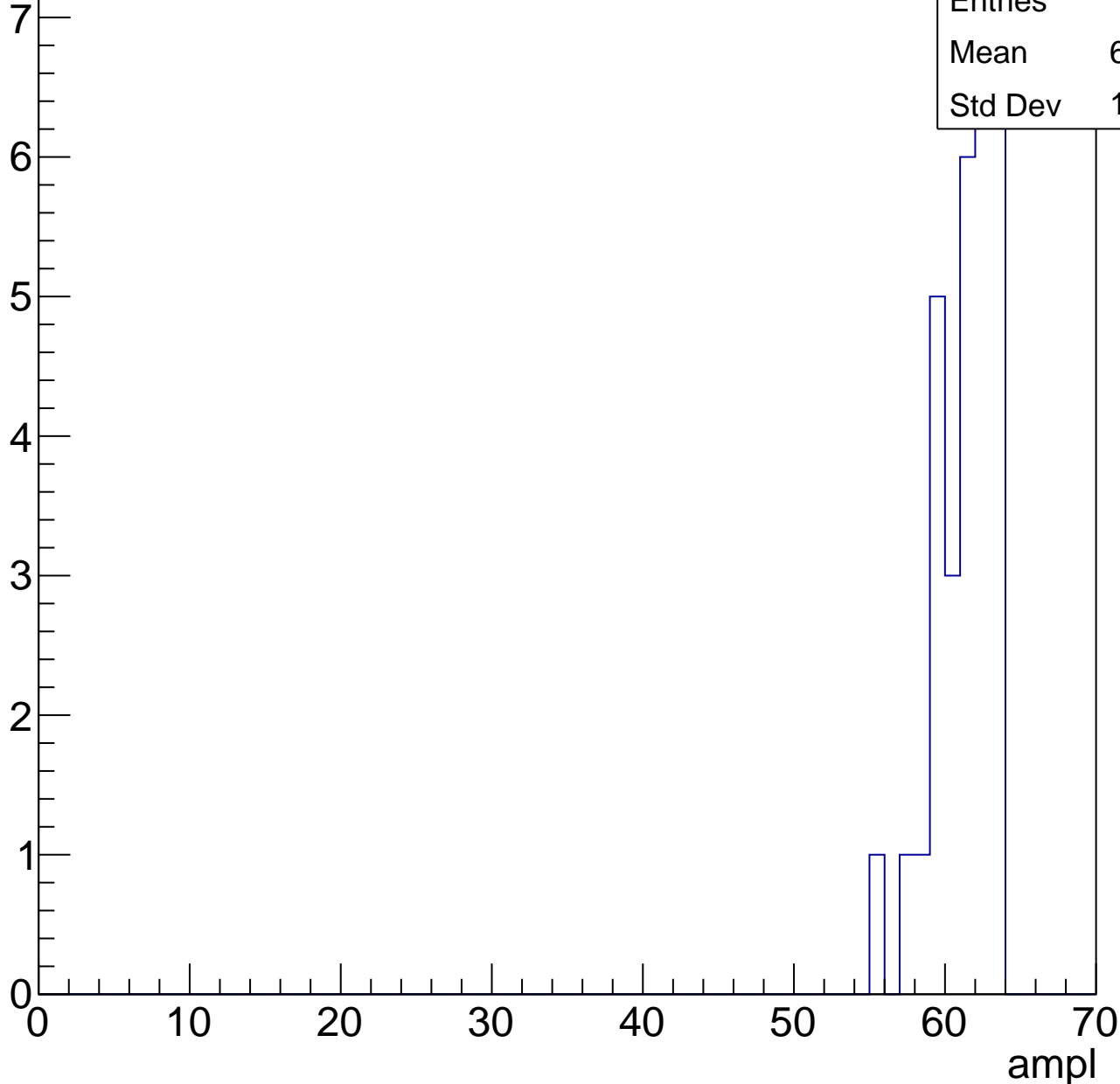


# B1L002S, U18-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	60.84
Std Dev	1.953

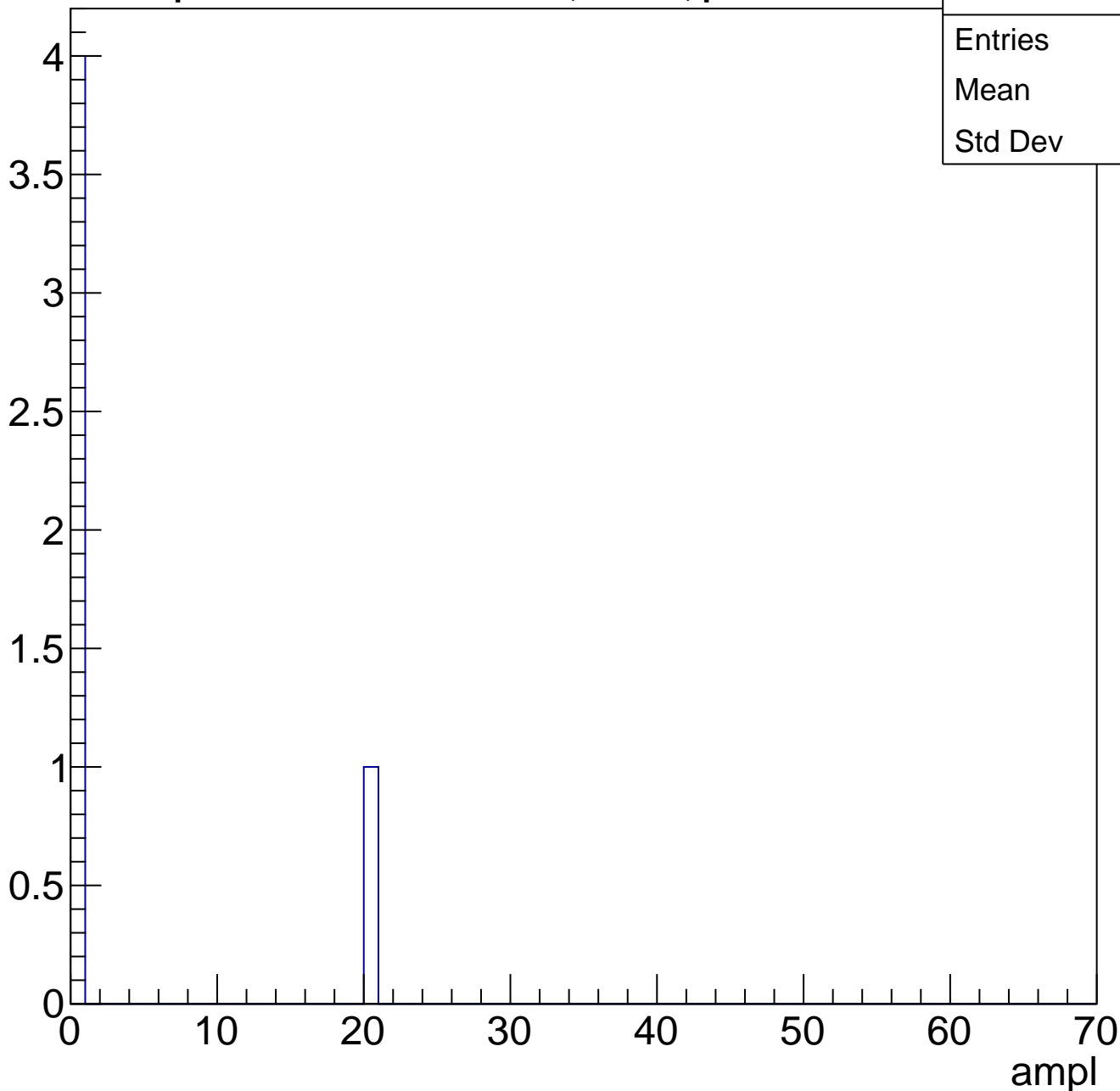




# B1L002S, U18-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	4
Std Dev	8

# B1L002S, U18-ch68, adc0

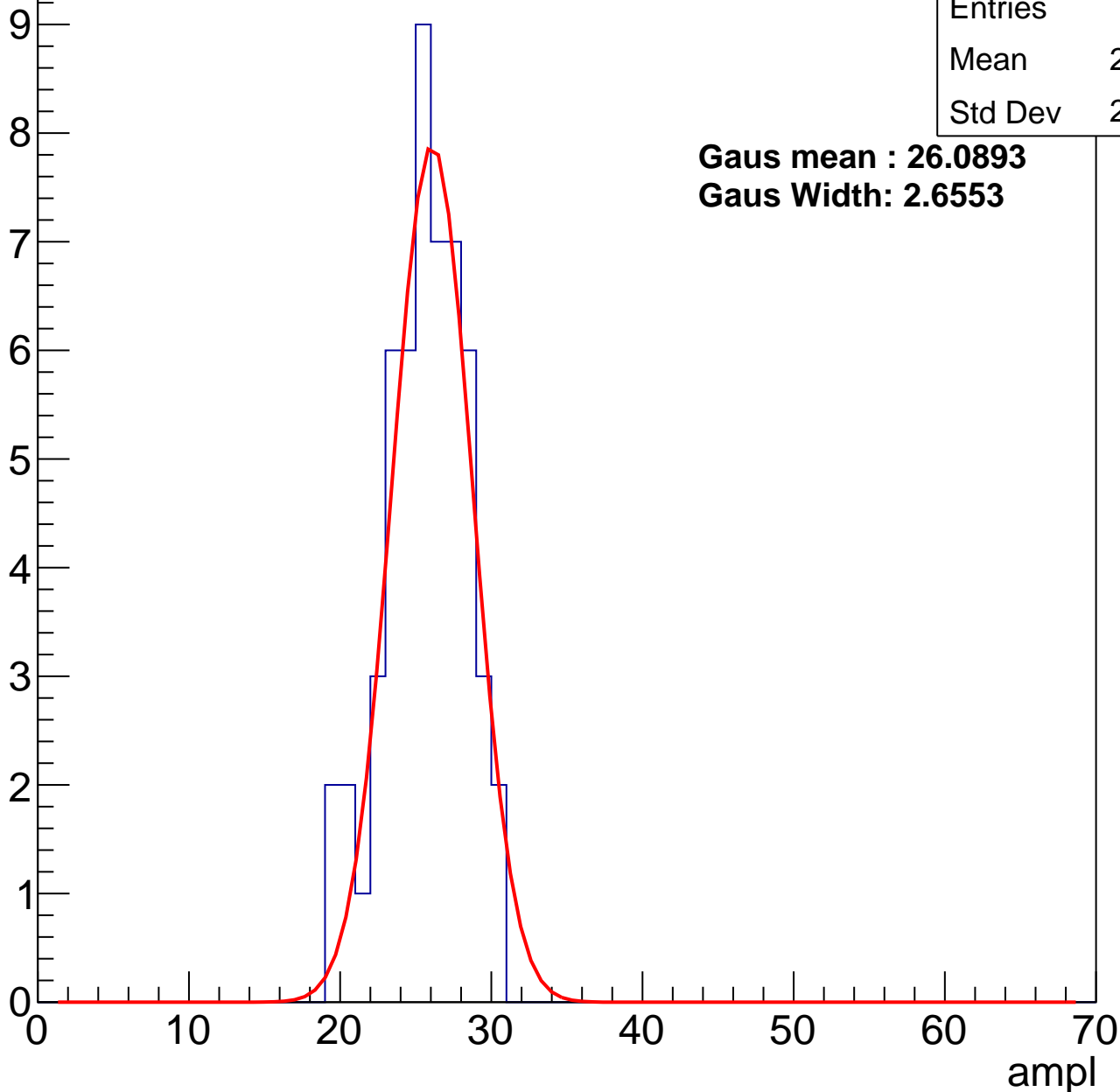
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	25.15
Std Dev	2.656

**Gaus mean : 26.0893**

**Gaus Width: 2.6553**



# B1L002S, U18-ch68, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	31.86
Std Dev	3.508

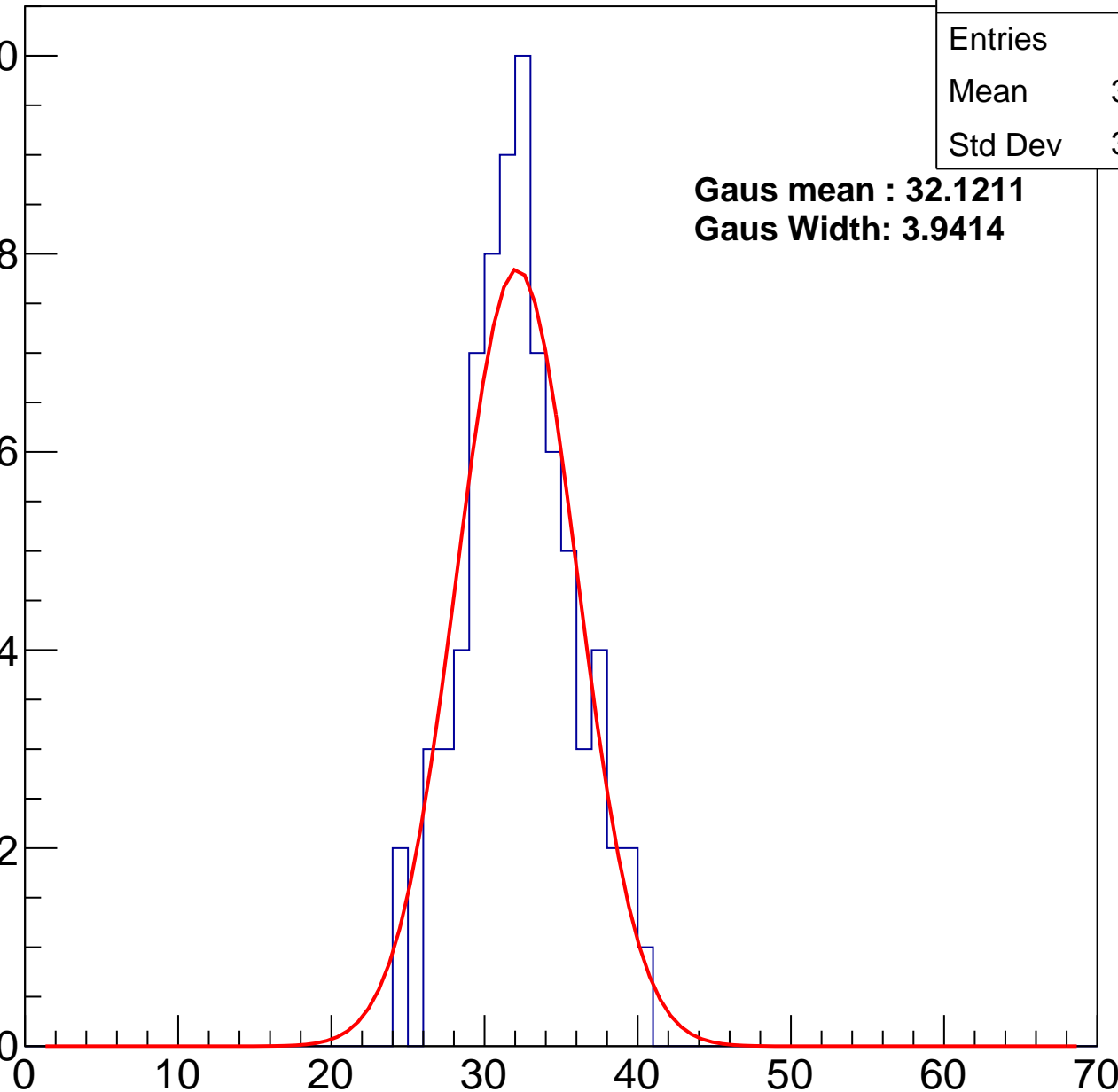
**Gaus mean : 32.1211**

**Gaus Width: 3.9414**

Entry

10  
8  
6  
4  
2  
0

ampl



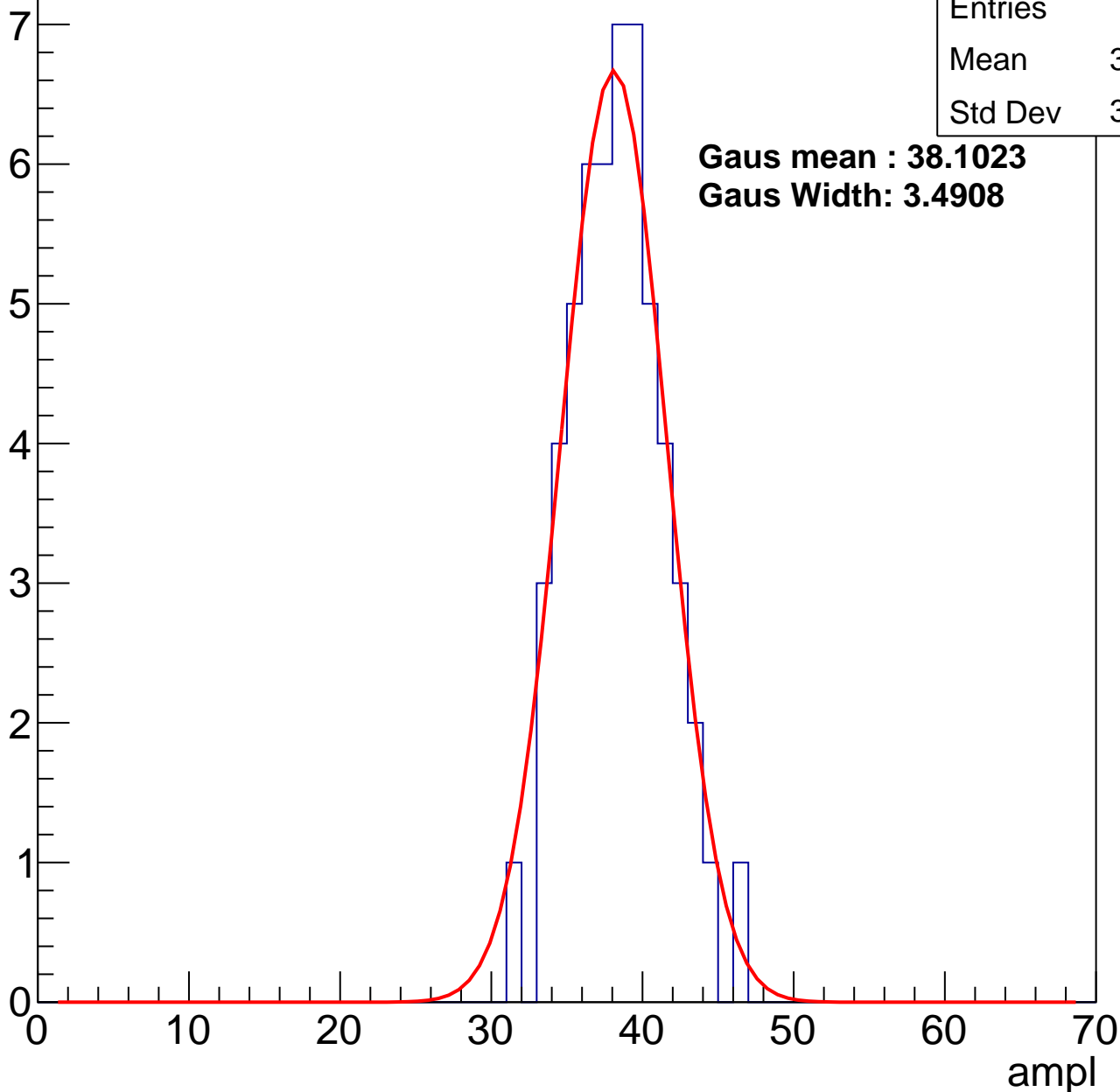
# B1L002S, U18-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	37.89
Std Dev	3.085

**Gaus mean : 38.1023**  
**Gaus Width: 3.4908**

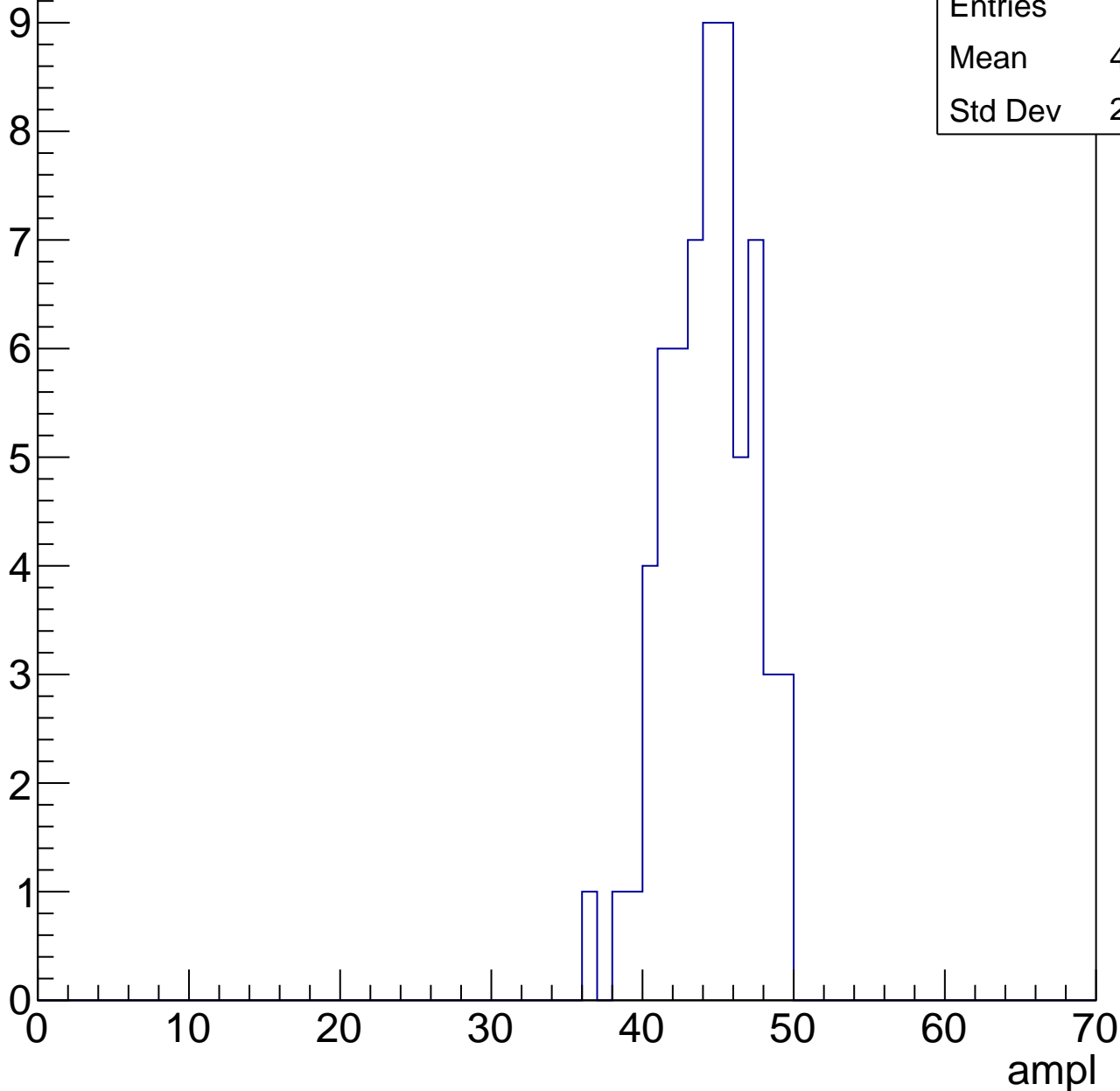


# B1L002S, U18-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	43.92
Std Dev	2.807

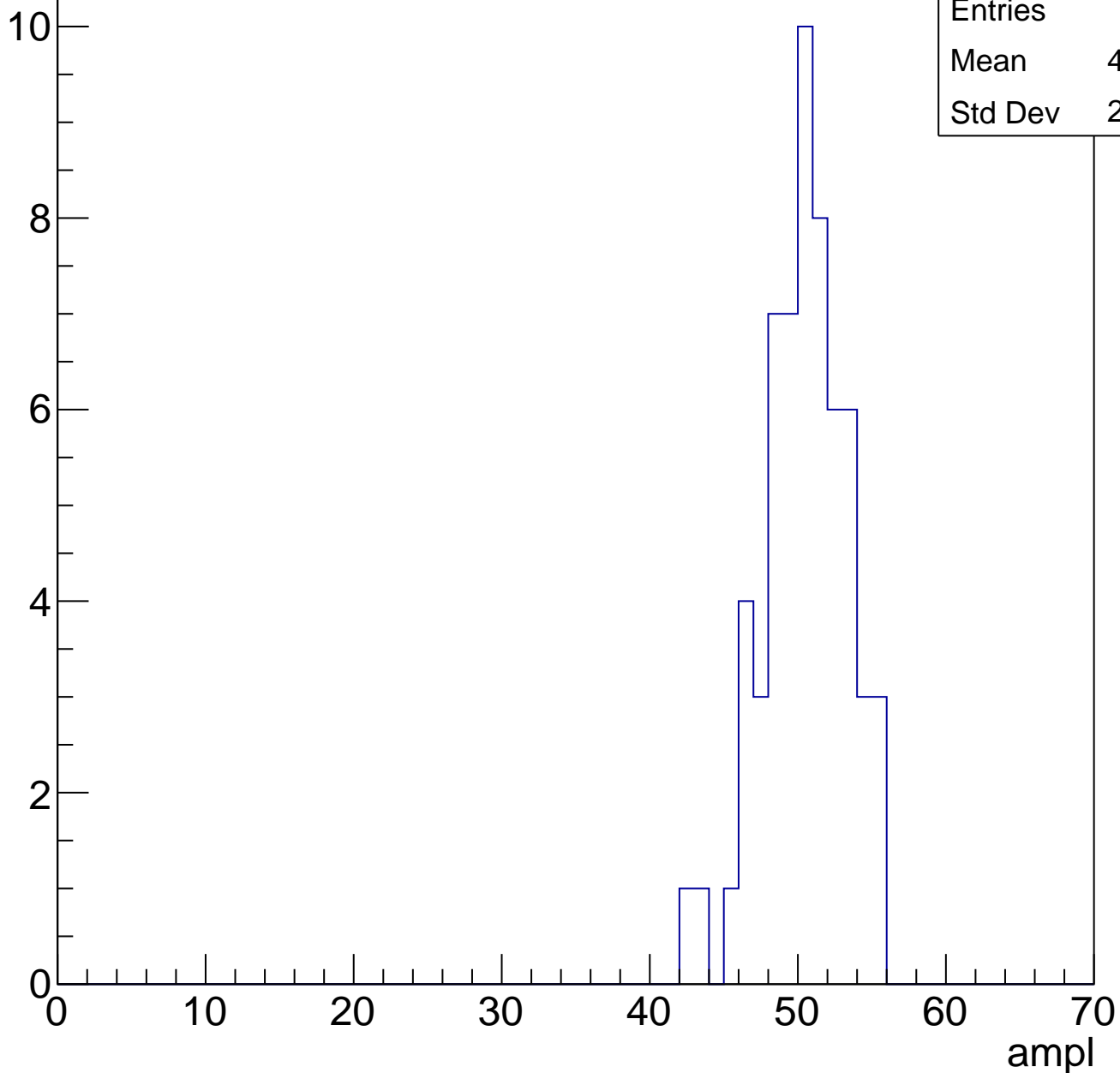


# B1L002S, U18-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	49.98
Std Dev	2.808

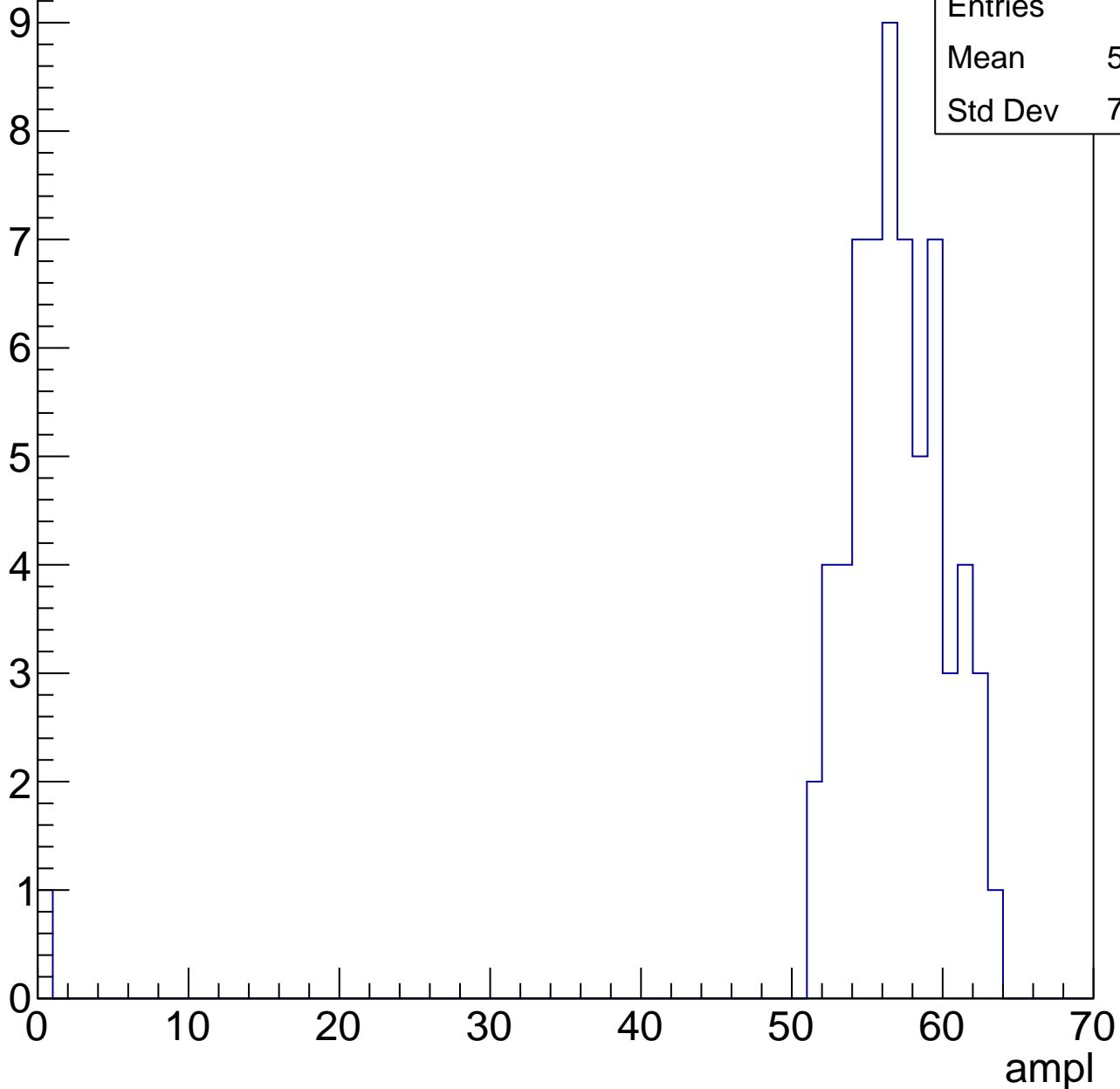


# B1L002S, U18-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	55.69
Std Dev	7.613

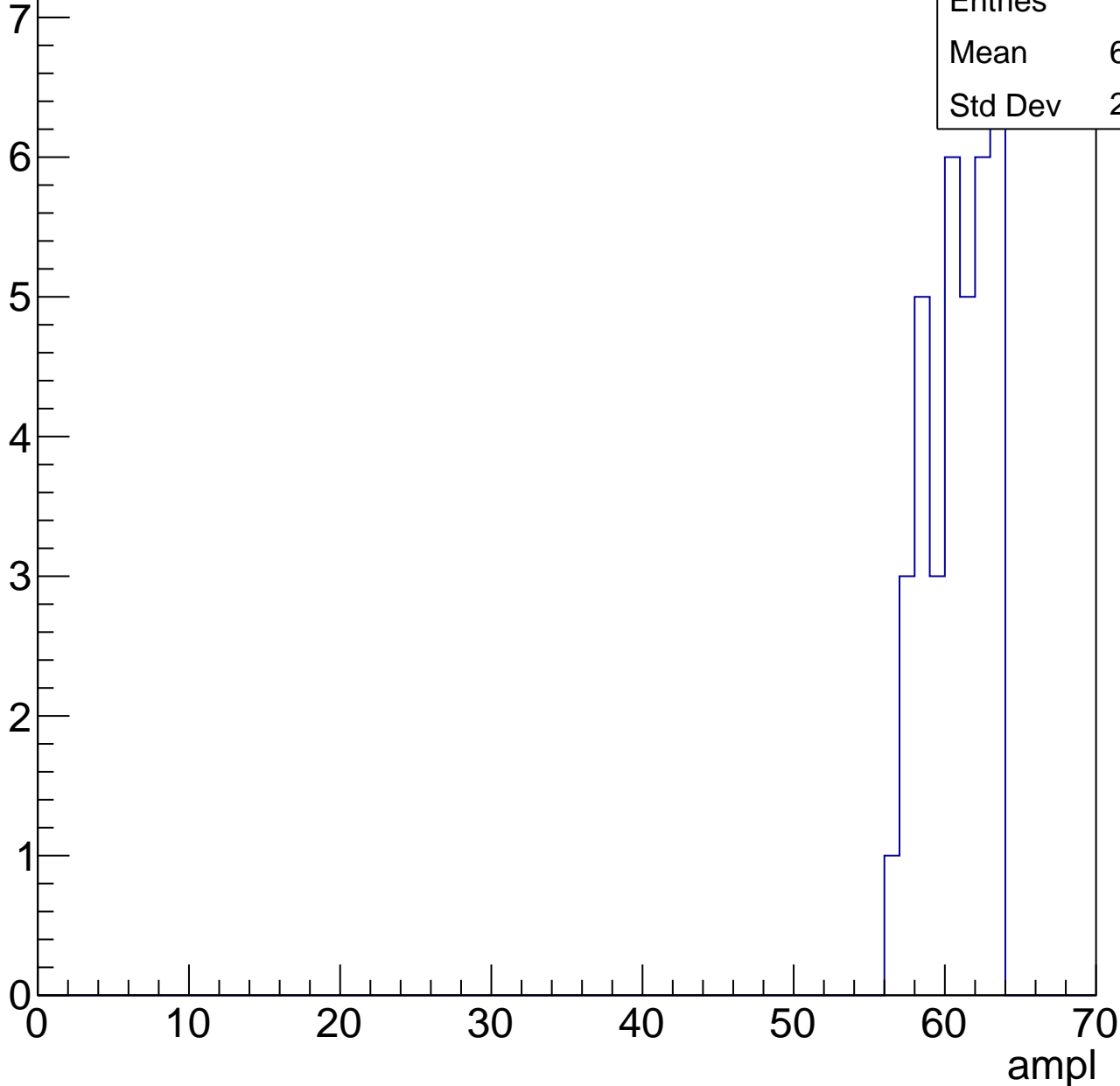


# B1L002S, U18-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	60.33
Std Dev	2.068

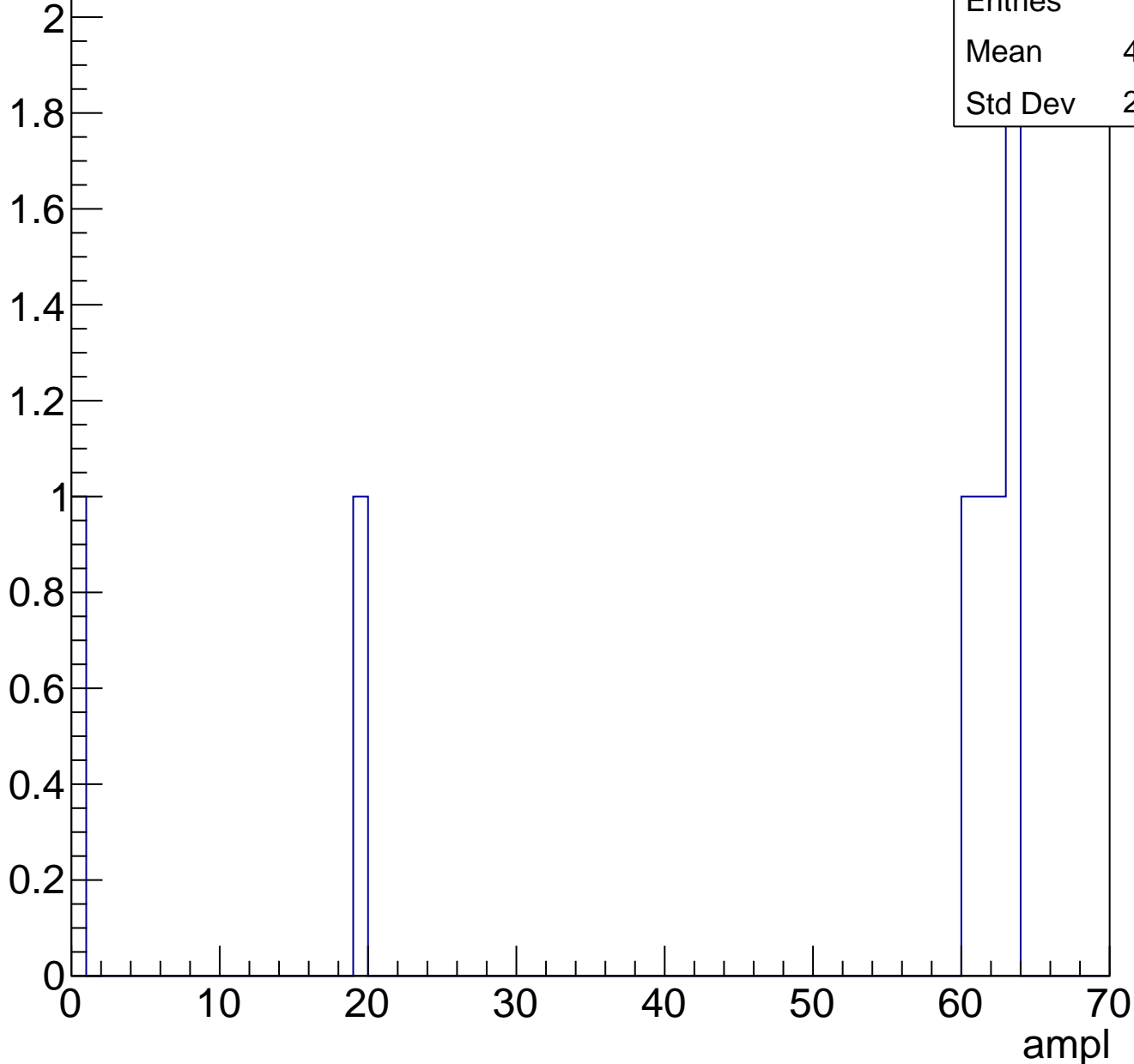




# B1L002S, U18-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch69, adc0

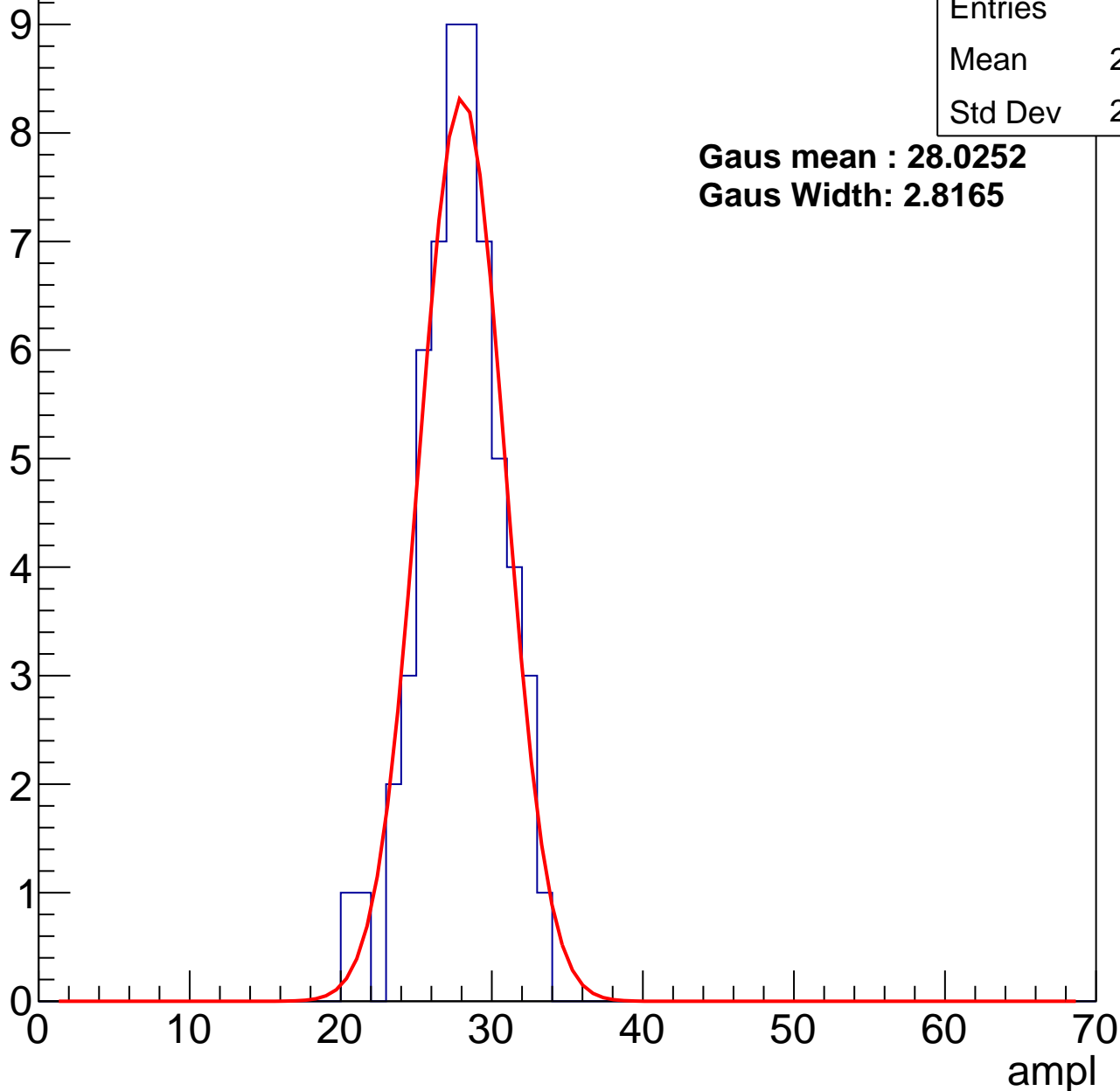
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.45
Std Dev	2.692

**Gaus mean : 28.0252**

**Gaus Width: 2.8165**



# B1L002S, U18-ch69, adc1

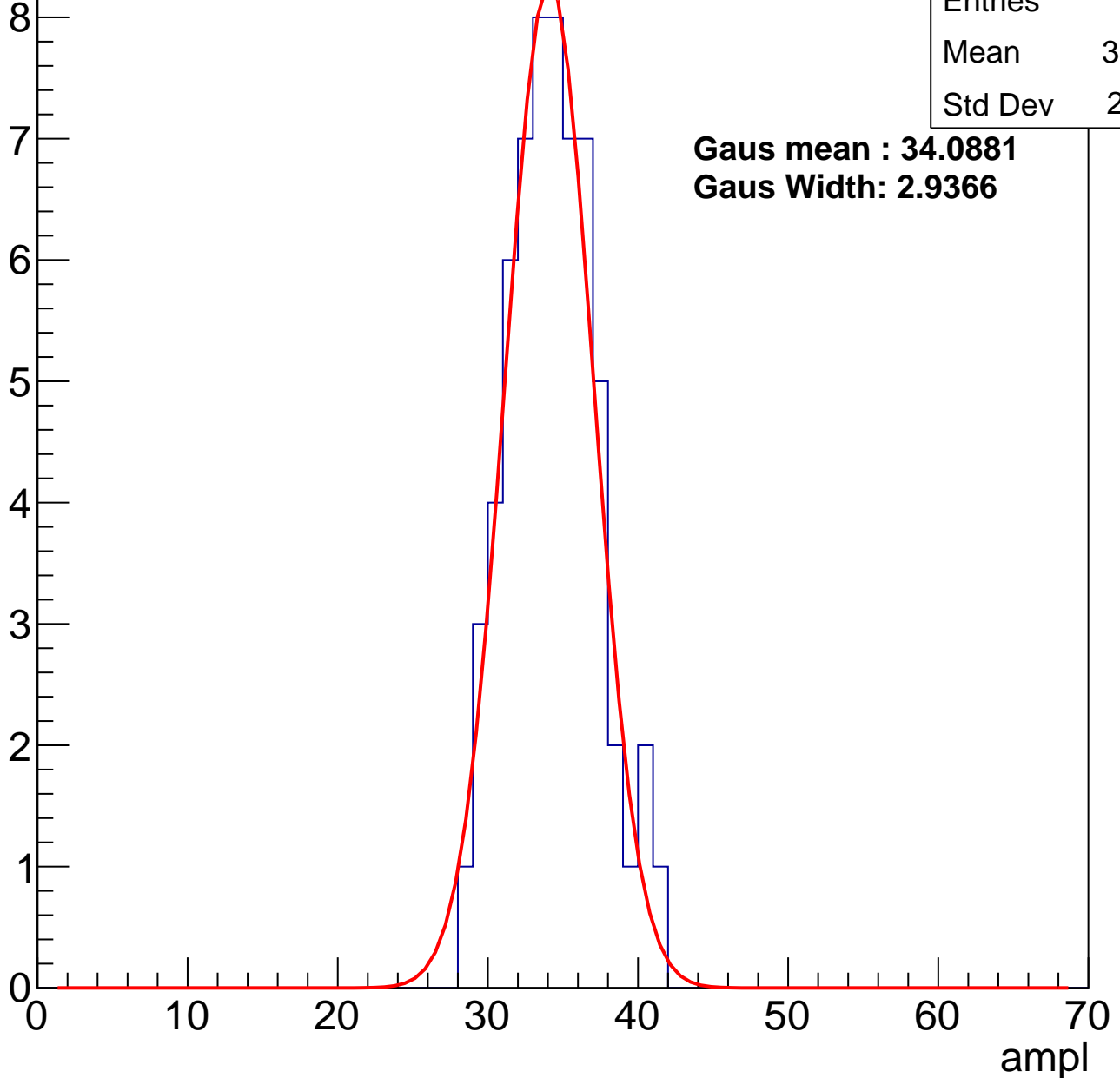
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.85
Std Dev	2.901

**Gaus mean : 34.0881**

**Gaus Width: 2.9366**



# B1L002S, U18-ch69, adc2

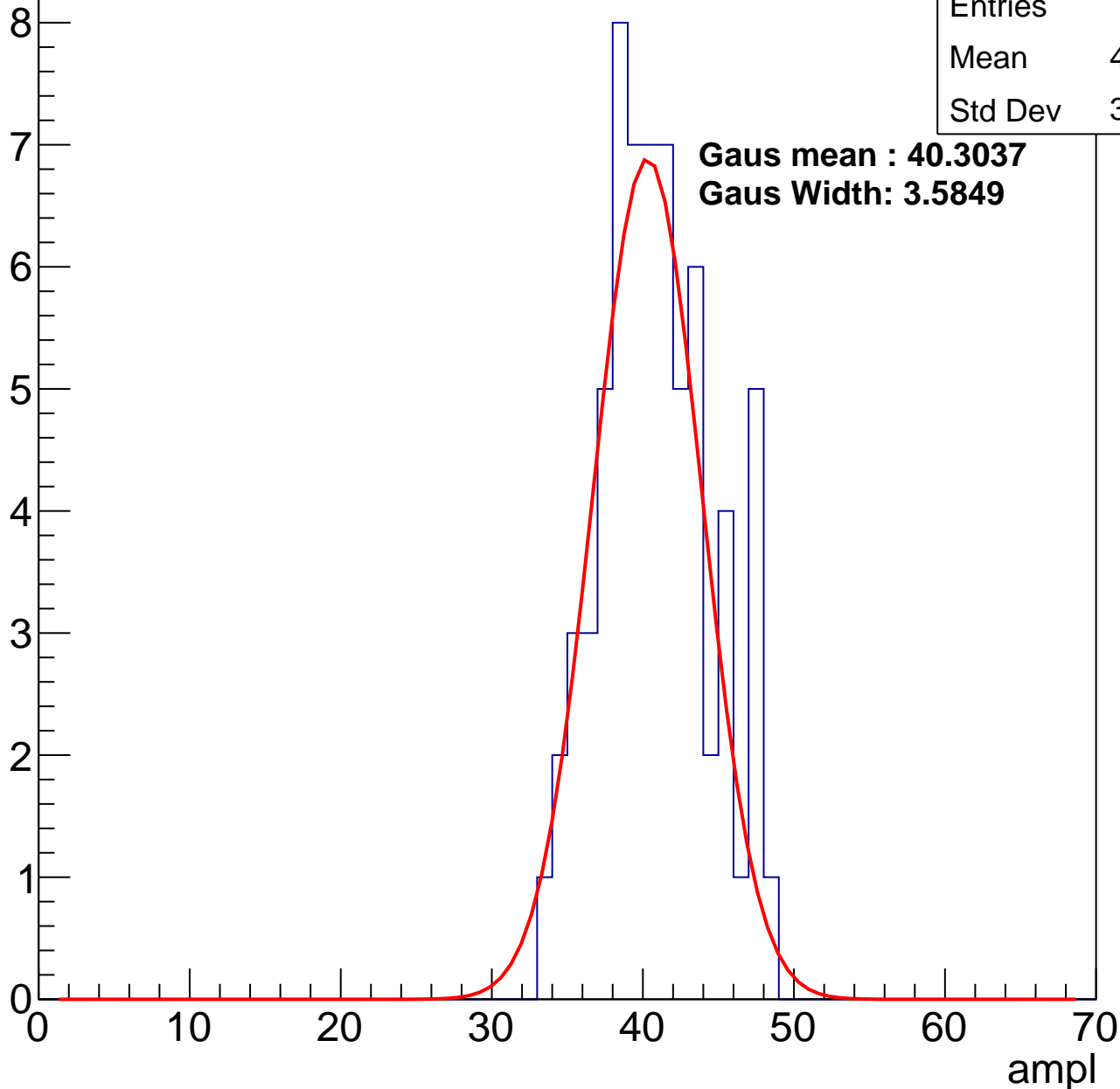
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.42
Std Dev	3.612

**Gaus mean : 40.3037**

**Gaus Width: 3.5849**

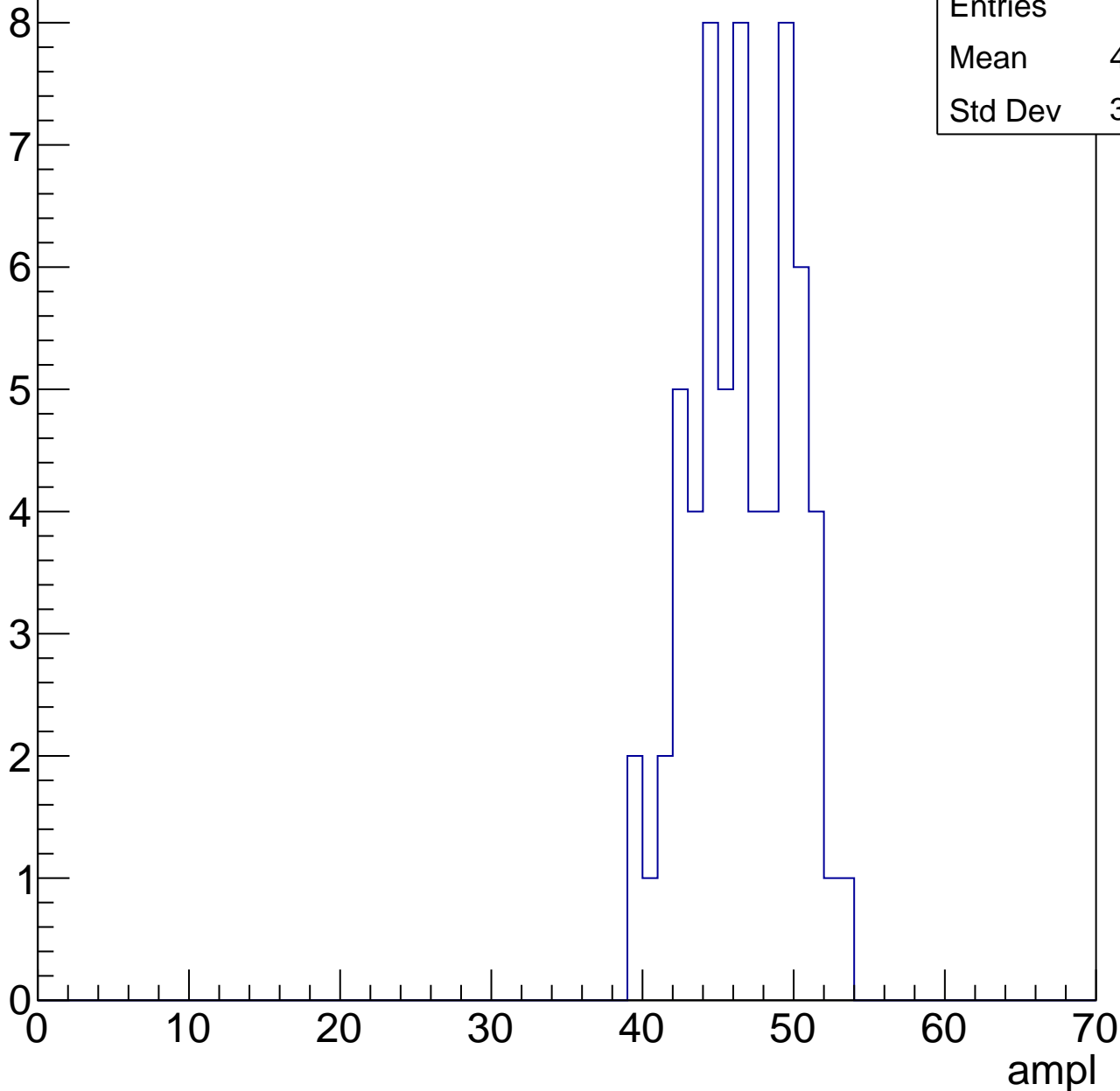


# B1L002S, U18-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

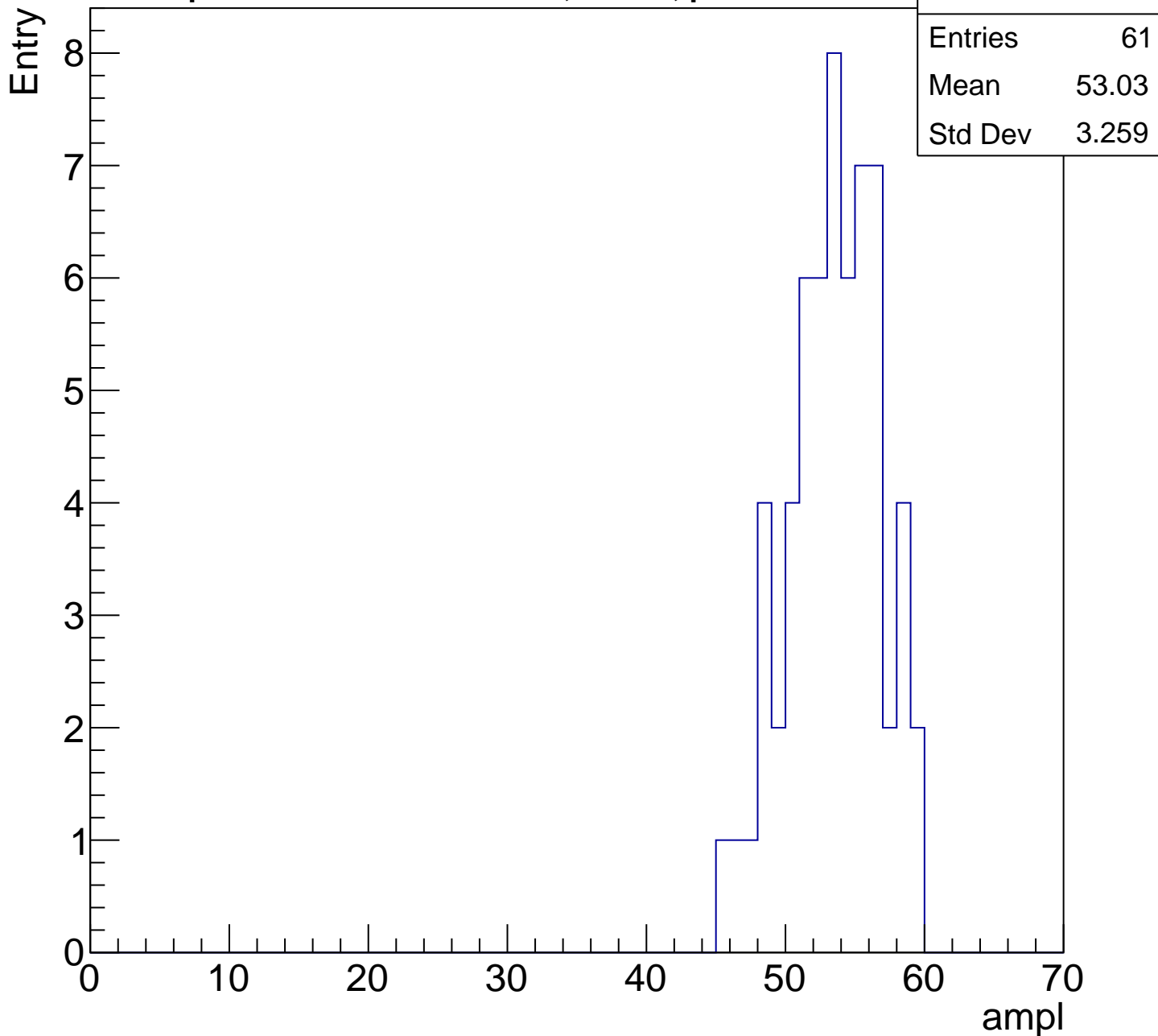
Entry

Entries	63
Mean	46.16
Std Dev	3.353



# B1L002S, U18-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

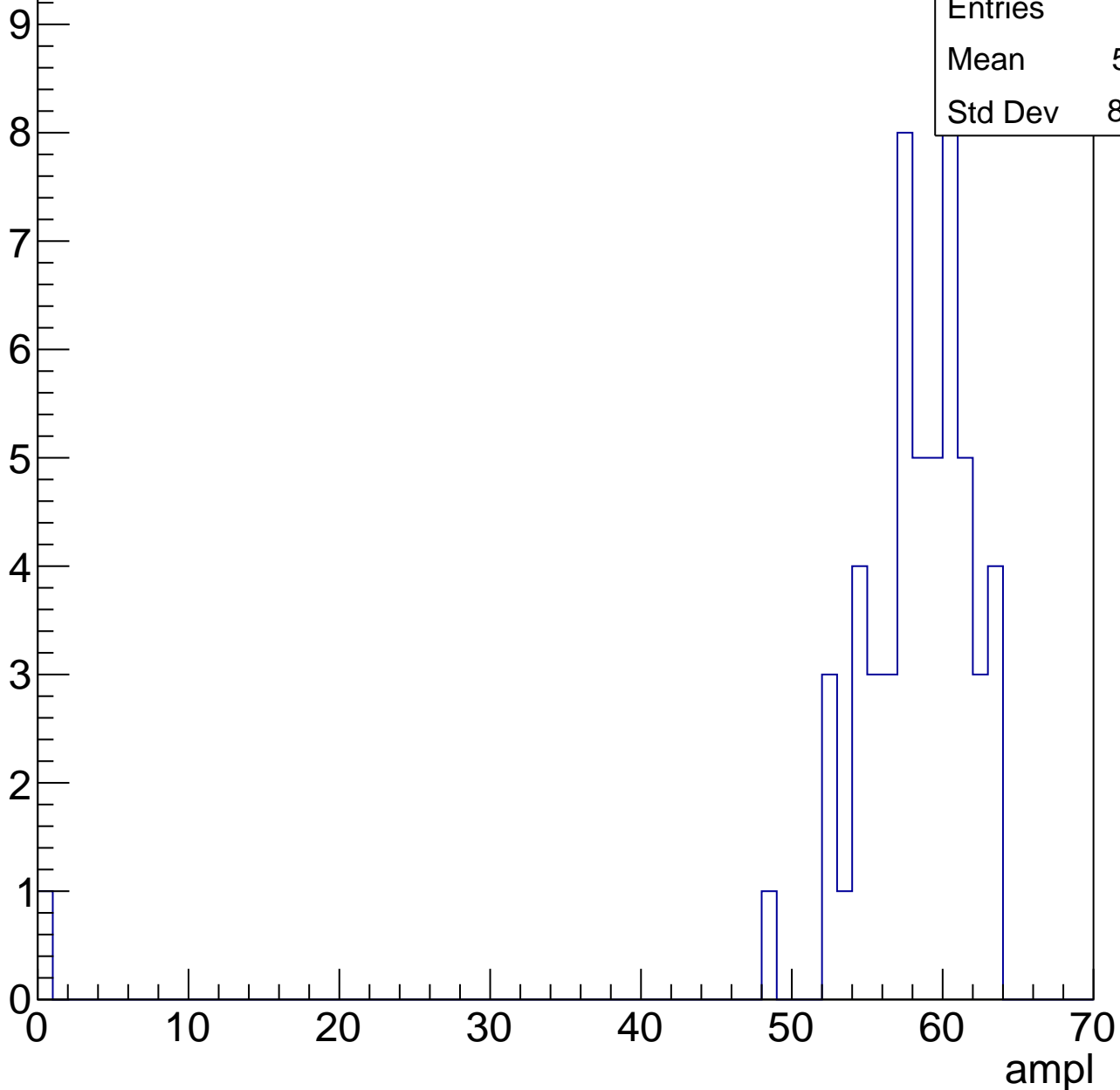


# B1L002S, U18-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	56.91
Std Dev	8.395

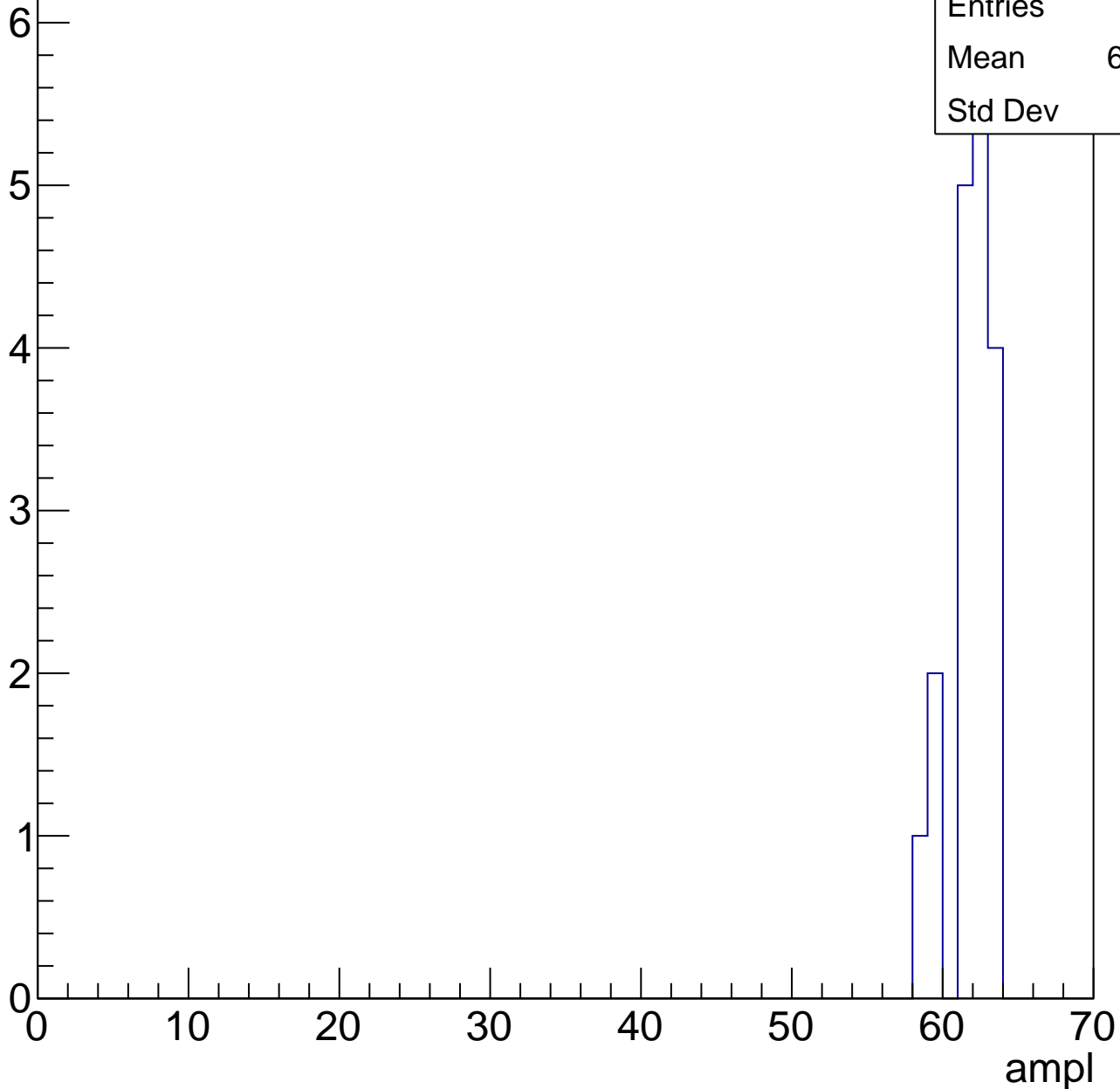


# B1L002S, U18-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.39
Std Dev	1.42

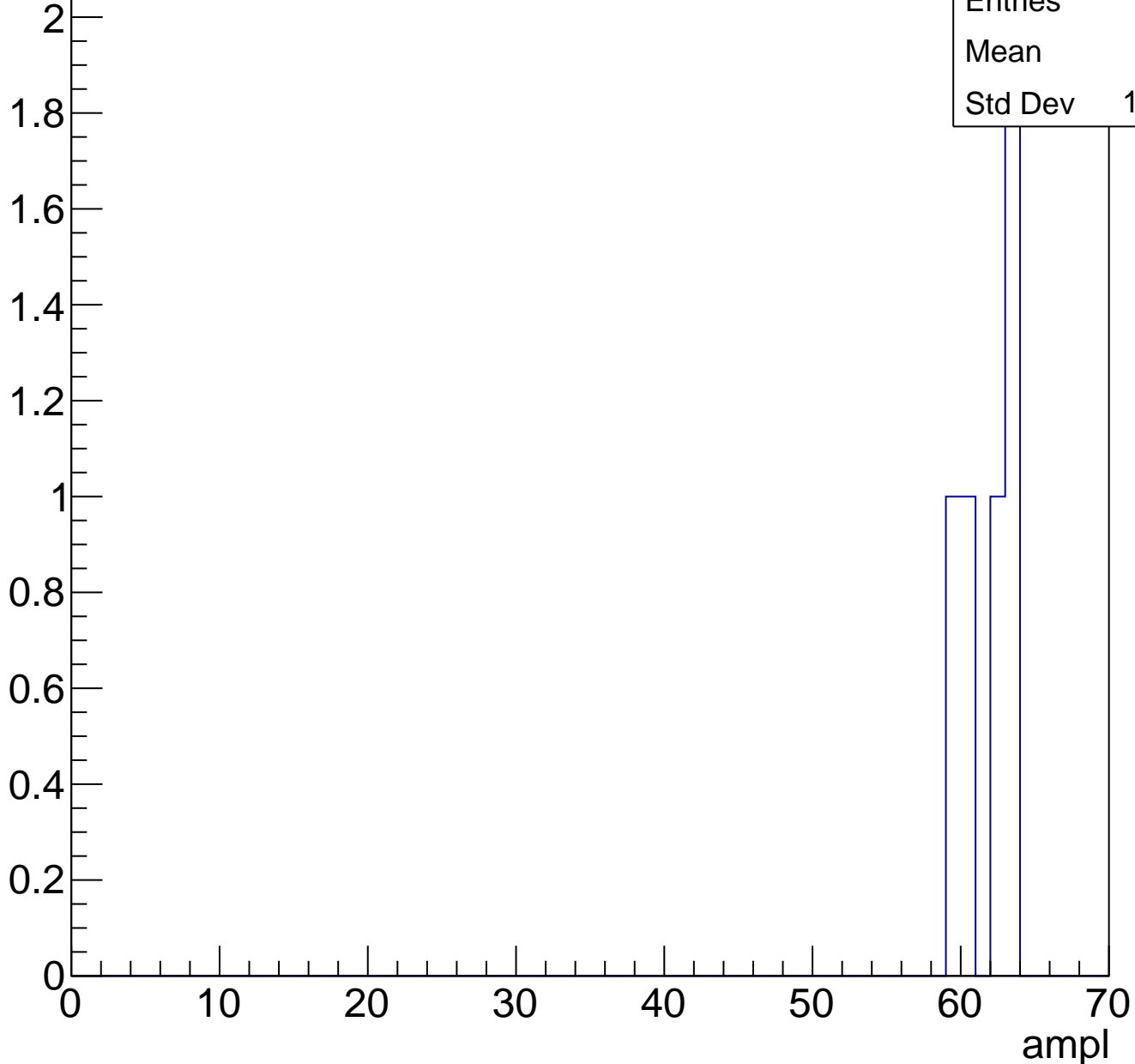




# B1L002S, U18-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch70, adc0

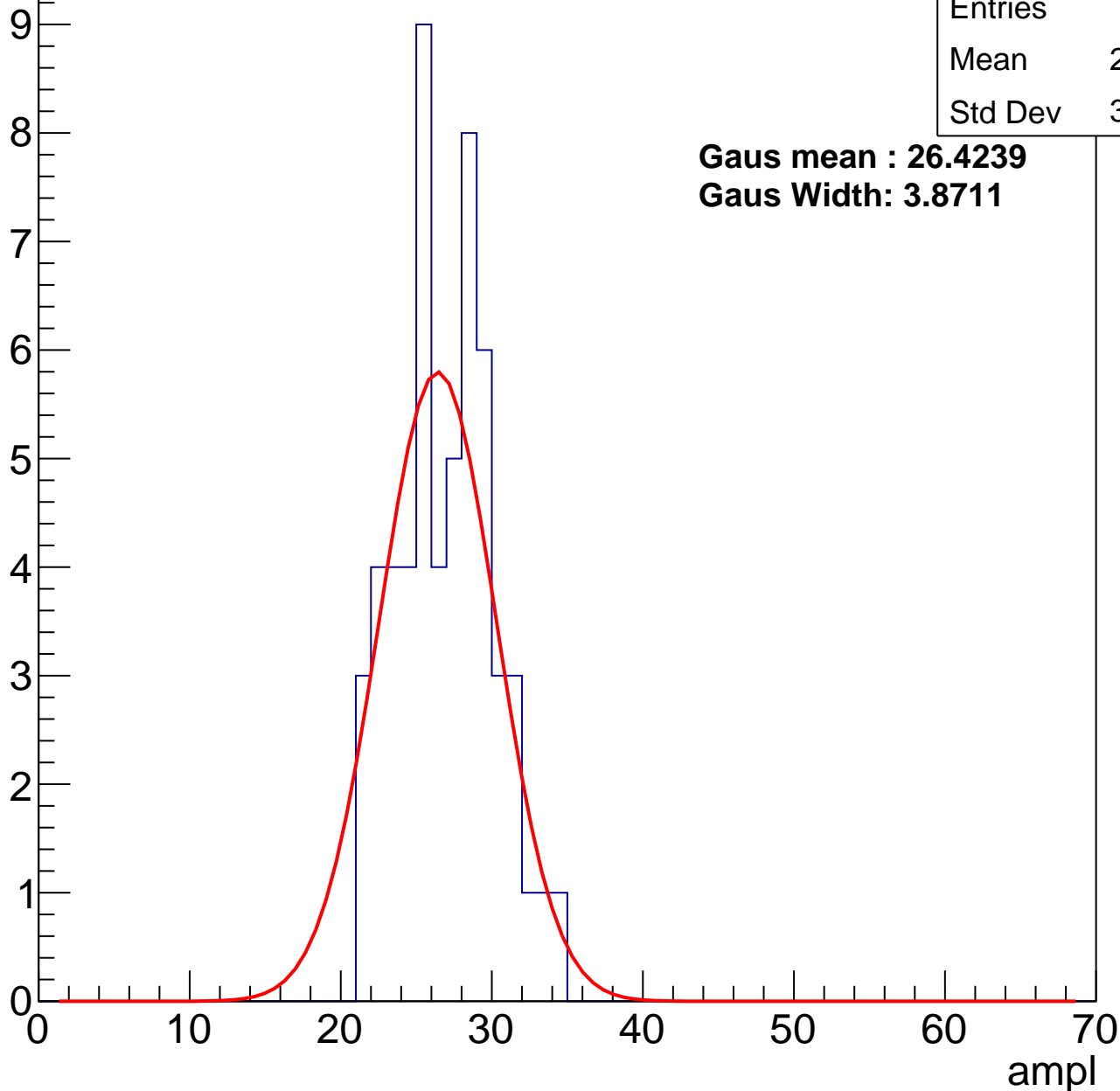
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	26.48
Std Dev	3.134

**Gaus mean : 26.4239**

**Gaus Width: 3.8711**



# B1L002S, U18-ch70, adc1

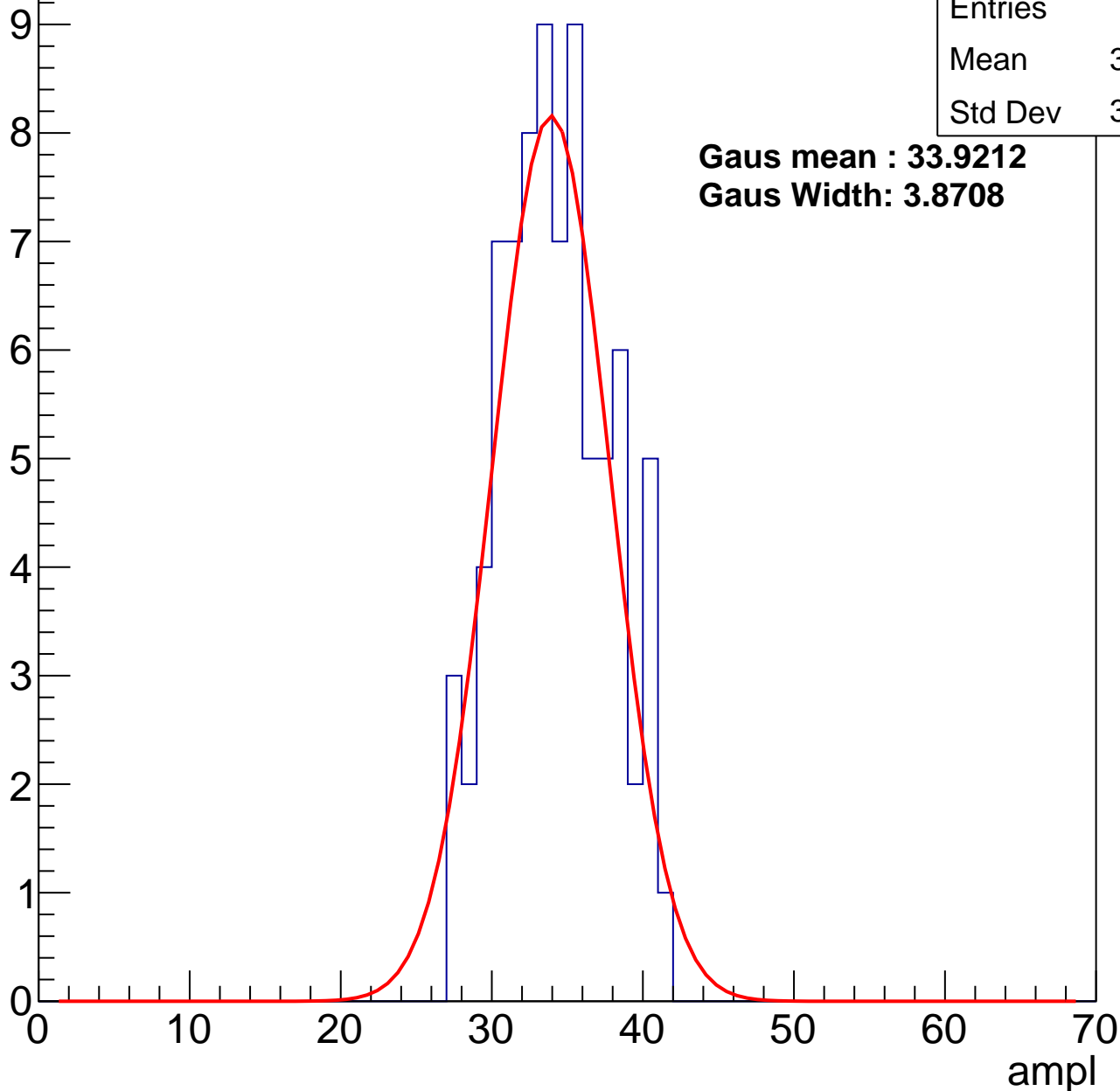
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	33.73
Std Dev	3.496

**Gaus mean : 33.9212**

**Gaus Width: 3.8708**



# B1L002S, U18-ch70, adc2

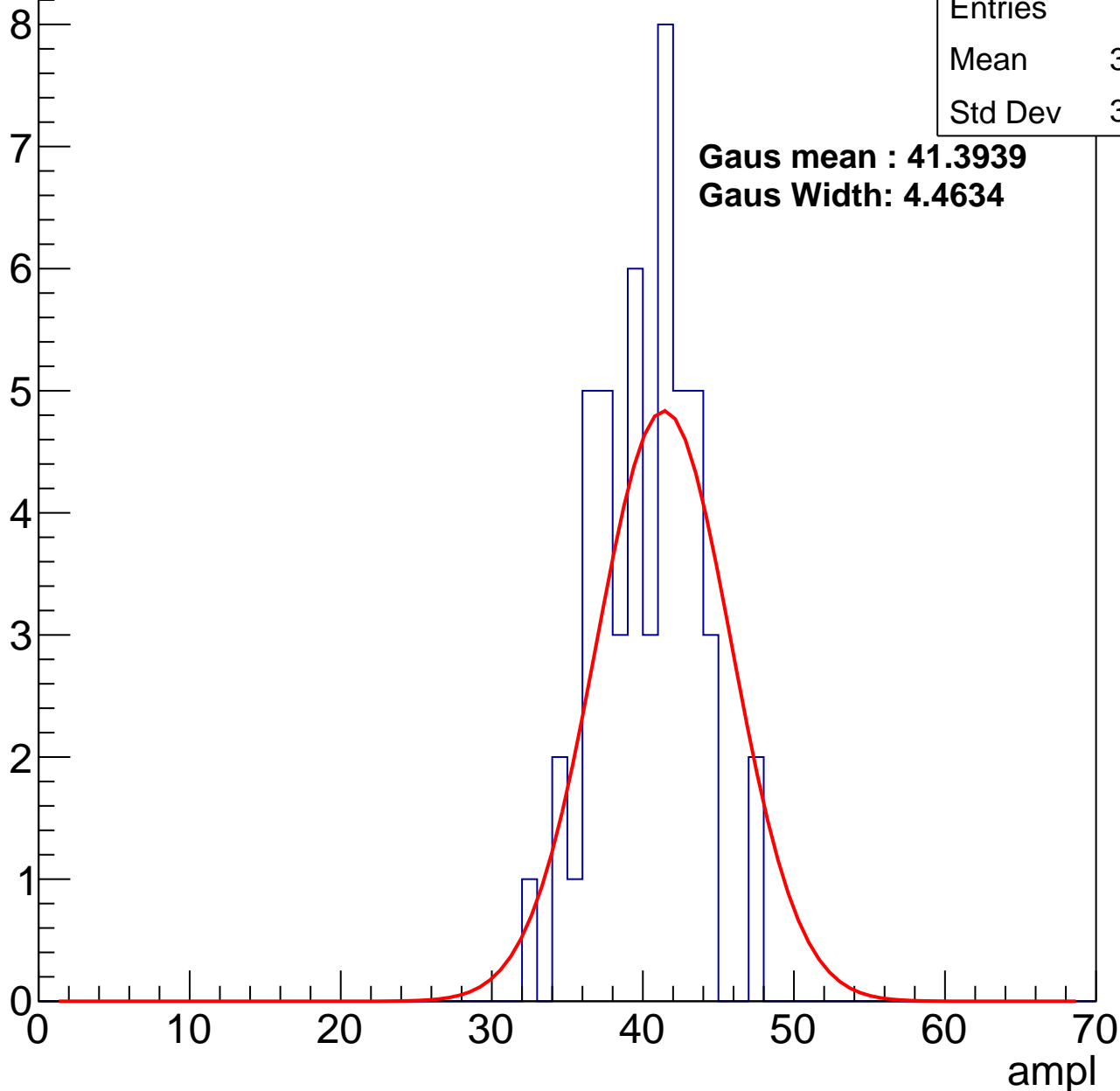
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	39.73
Std Dev	3.256

**Gaus mean : 41.3939**

**Gaus Width: 4.4634**

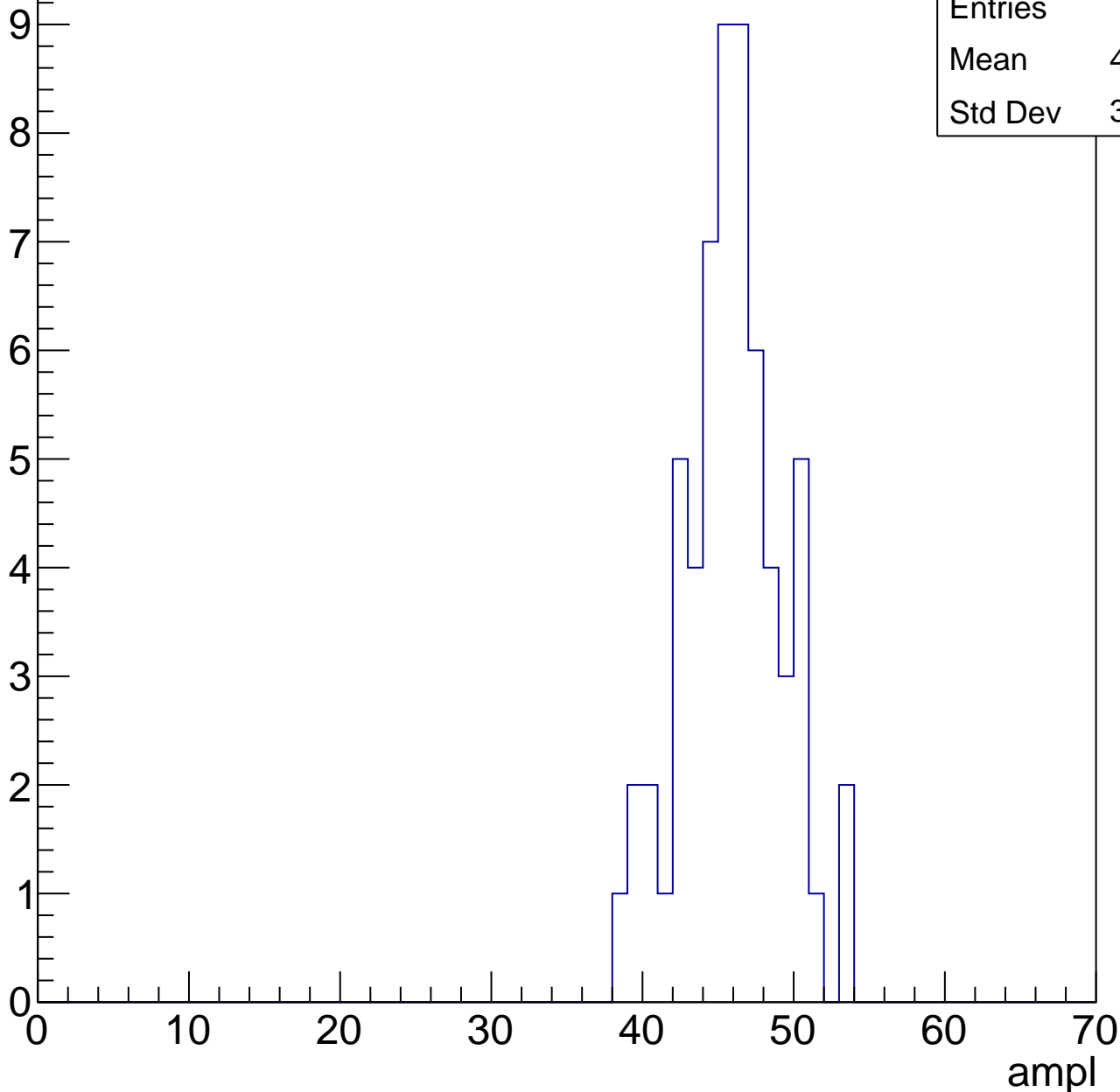


# B1L002S, U18-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	45.48
Std Dev	3.257

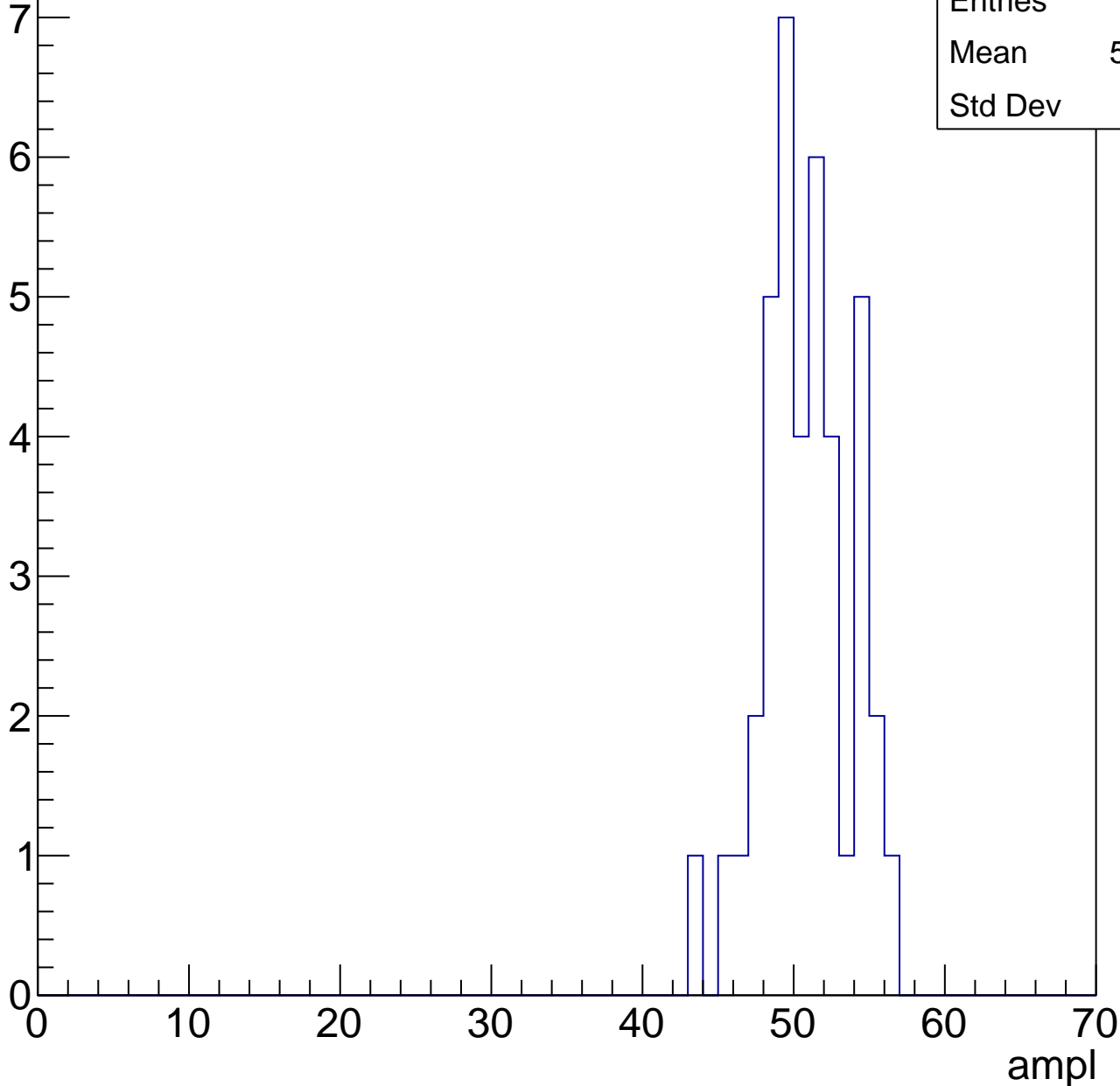


# B1L002S, U18-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	50.35
Std Dev	2.86

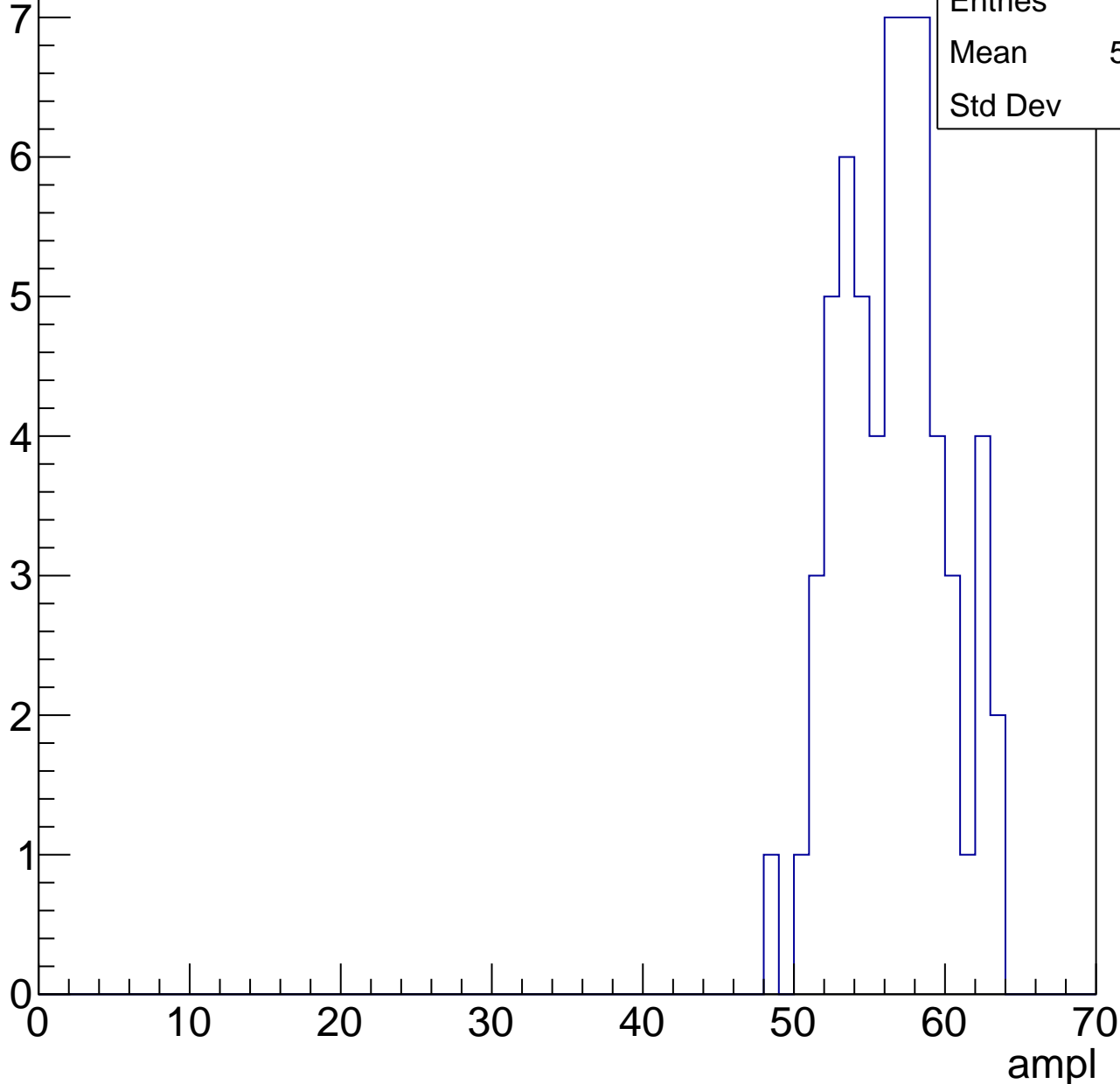


# B1L002S, U18-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.12
Std Dev	3.46

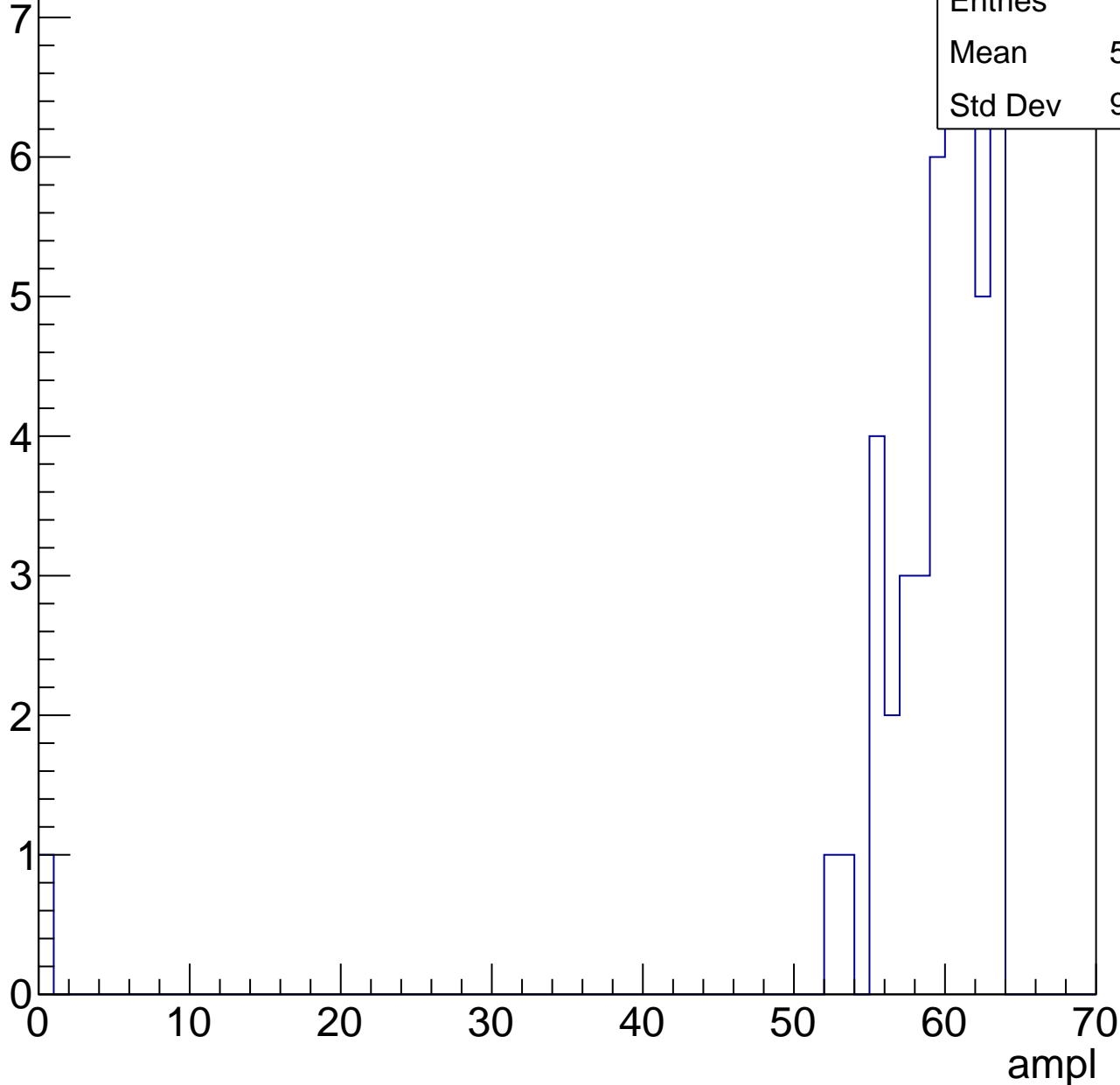


# B1L002S, U18-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	58.17
Std Dev	9.018





# B1L002S, U18-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

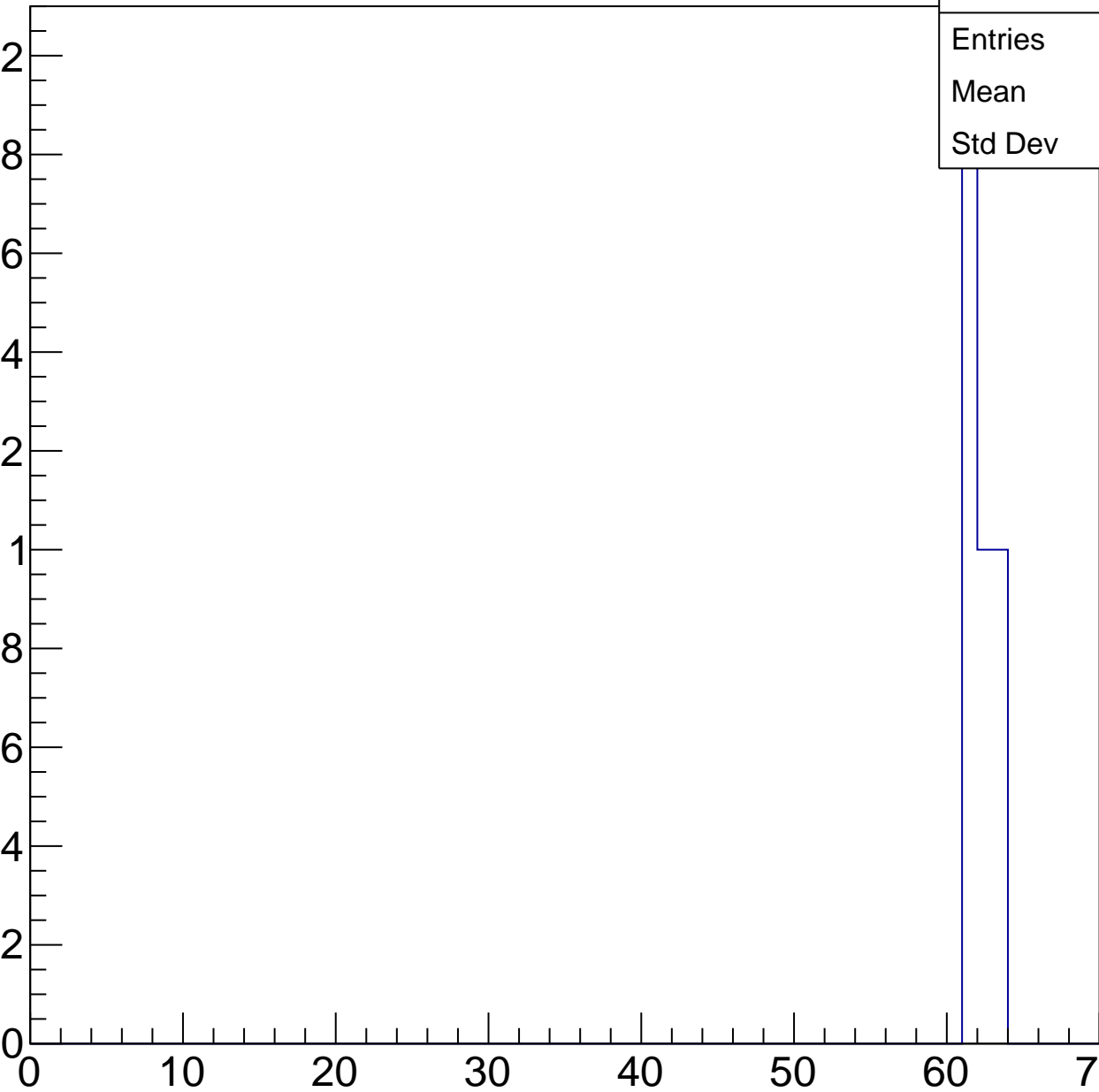
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	61.75
Std Dev	0.8292

0 10 20 30 40 50 60 70

ampl



# B1L002S, U18-ch71, adc0

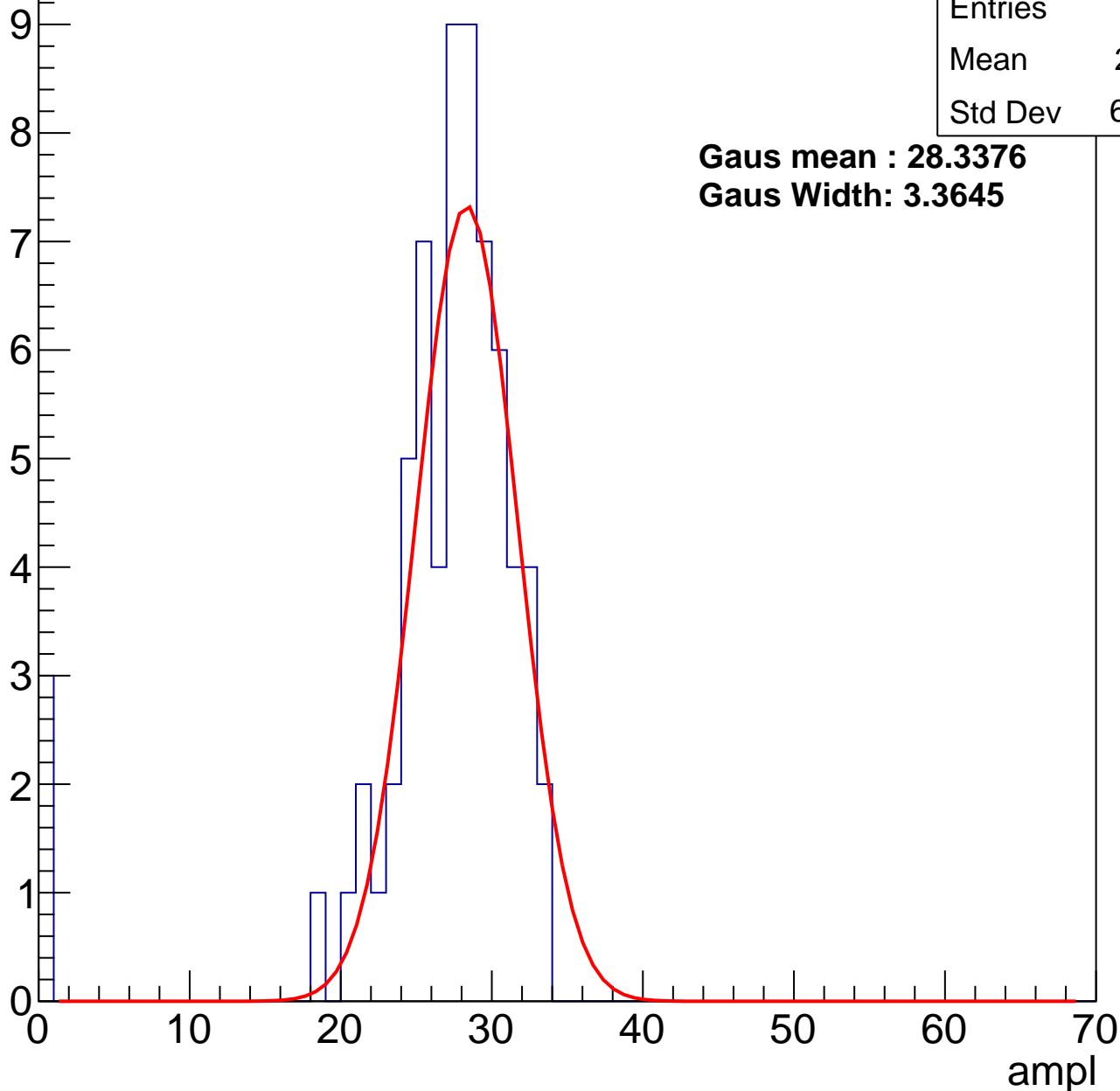
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	26.01
Std Dev	6.447

**Gaus mean : 28.3376**

**Gaus Width: 3.3645**



# B1L002S, U18-ch71, adc1

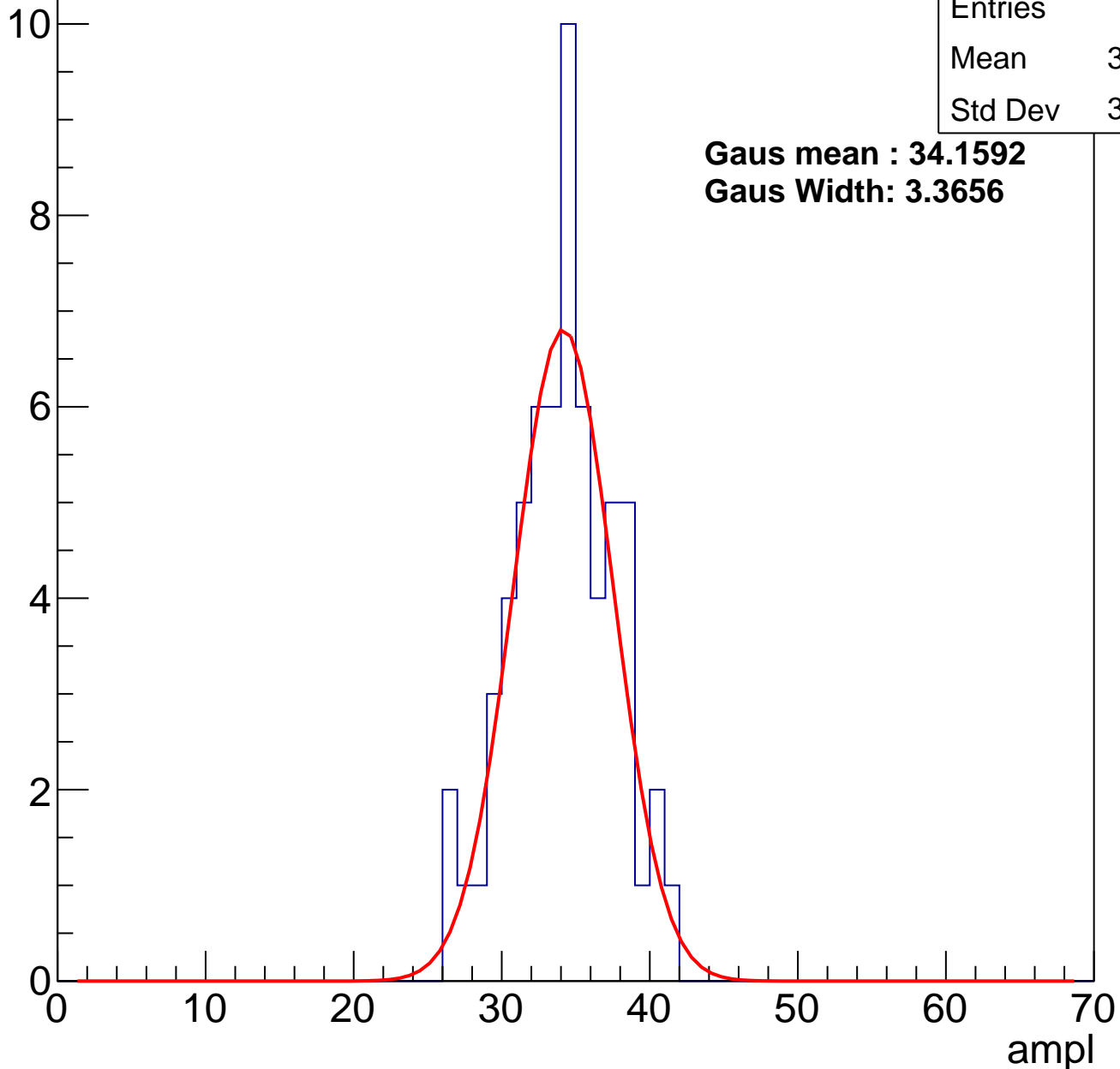
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	33.68
Std Dev	3.392

**Gaus mean : 34.1592**

**Gaus Width: 3.3656**

Entry



# B1L002S, U18-ch71, adc2

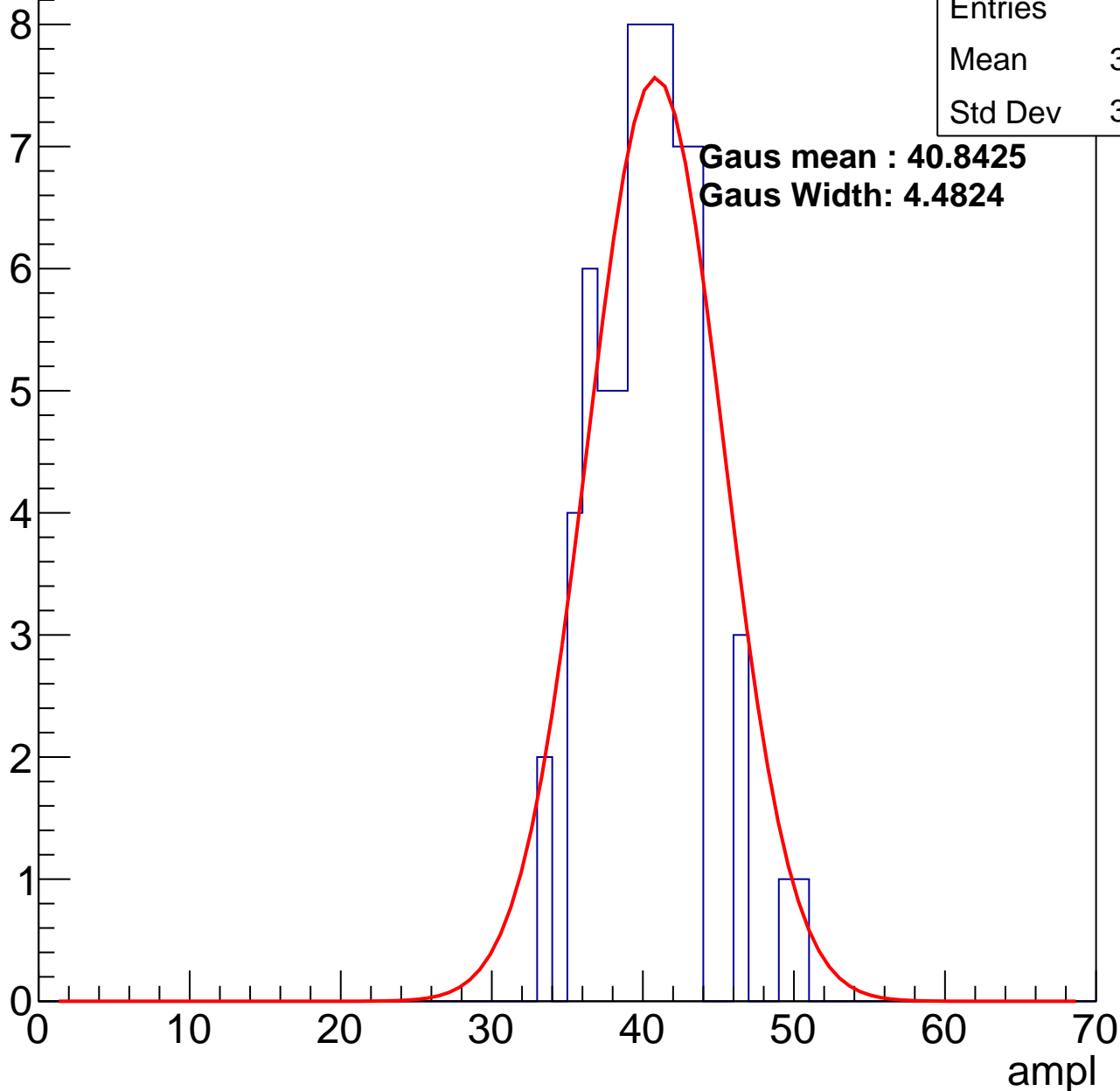
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.83
Std Dev	3.404

**Gaus mean : 40.8425**

**Gaus Width: 4.4824**



# B1L002S, U18-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

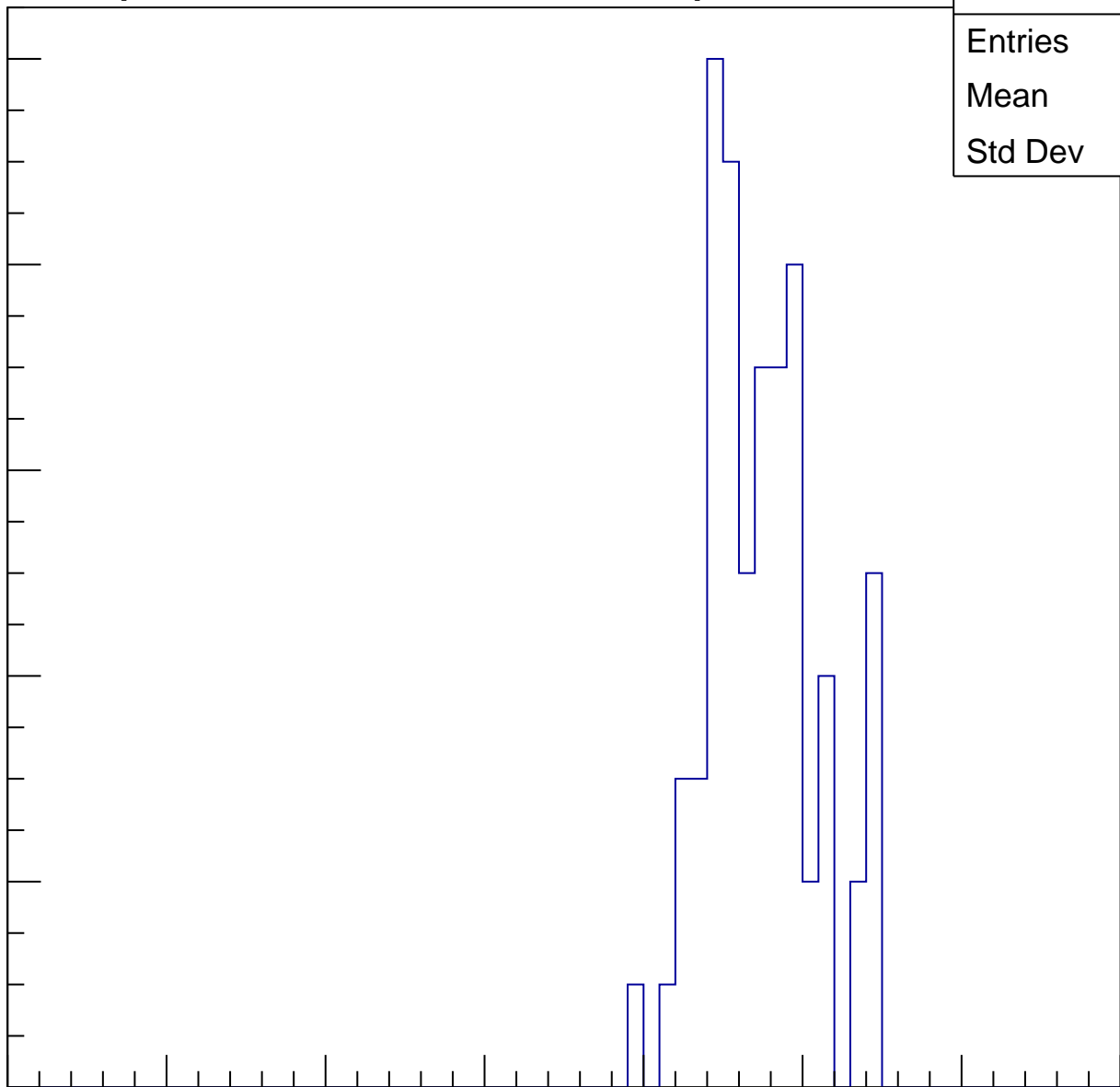
Entries	67
Mean	46.97
Std Dev	3.438

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

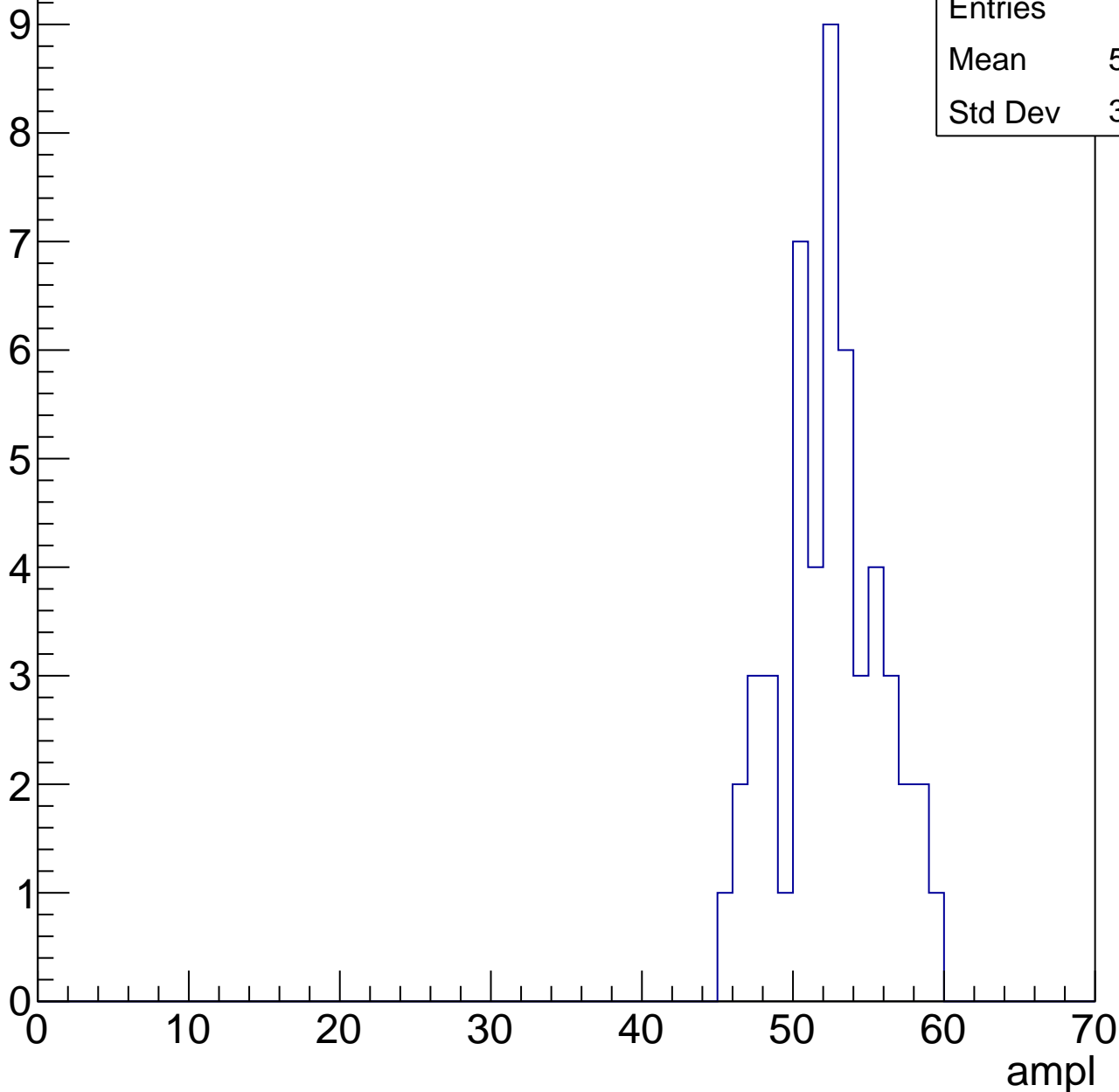


# B1L002S, U18-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	51.96
Std Dev	3.308

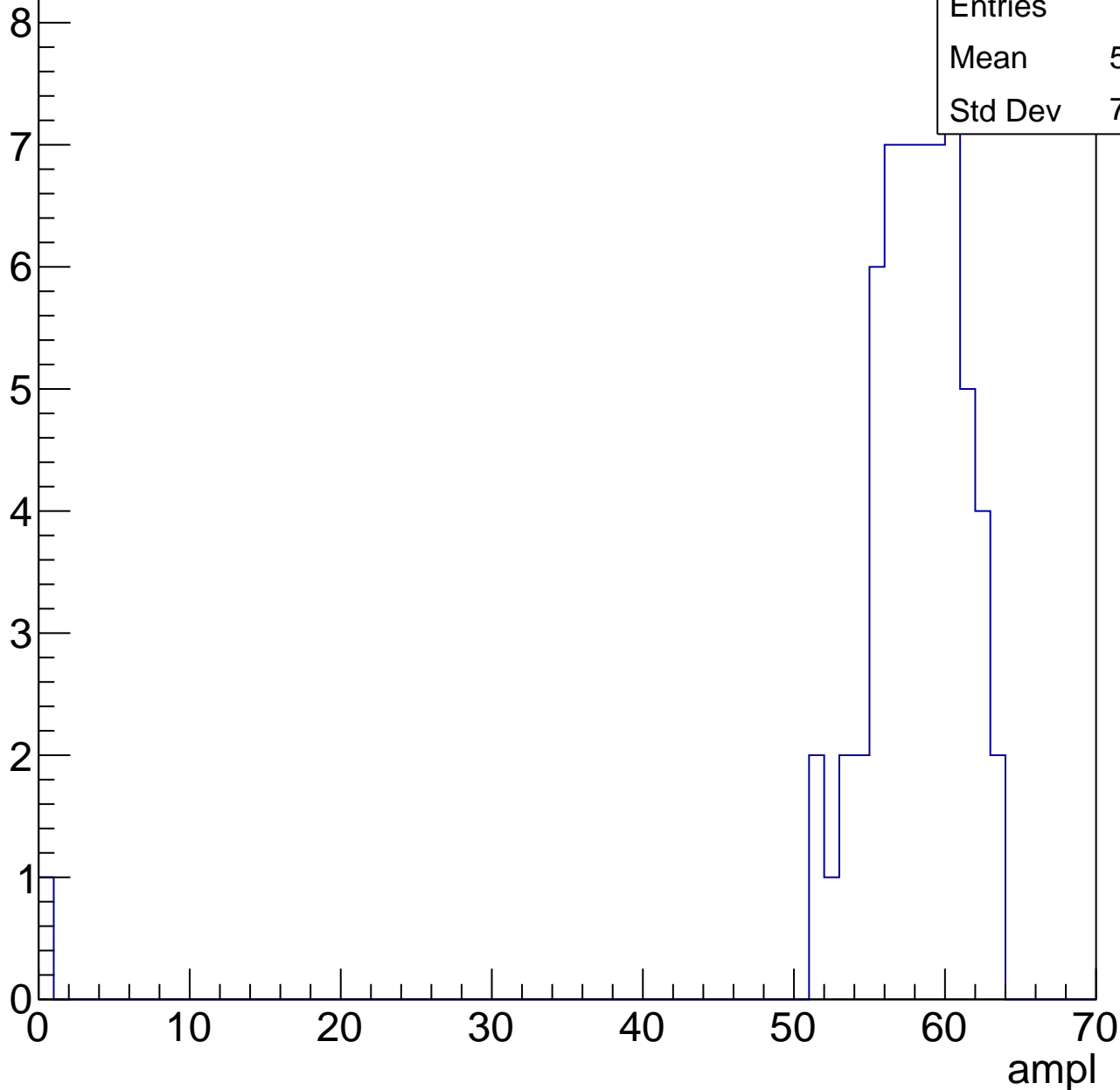


# B1L002S, U18-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	56.84
Std Dev	7.876

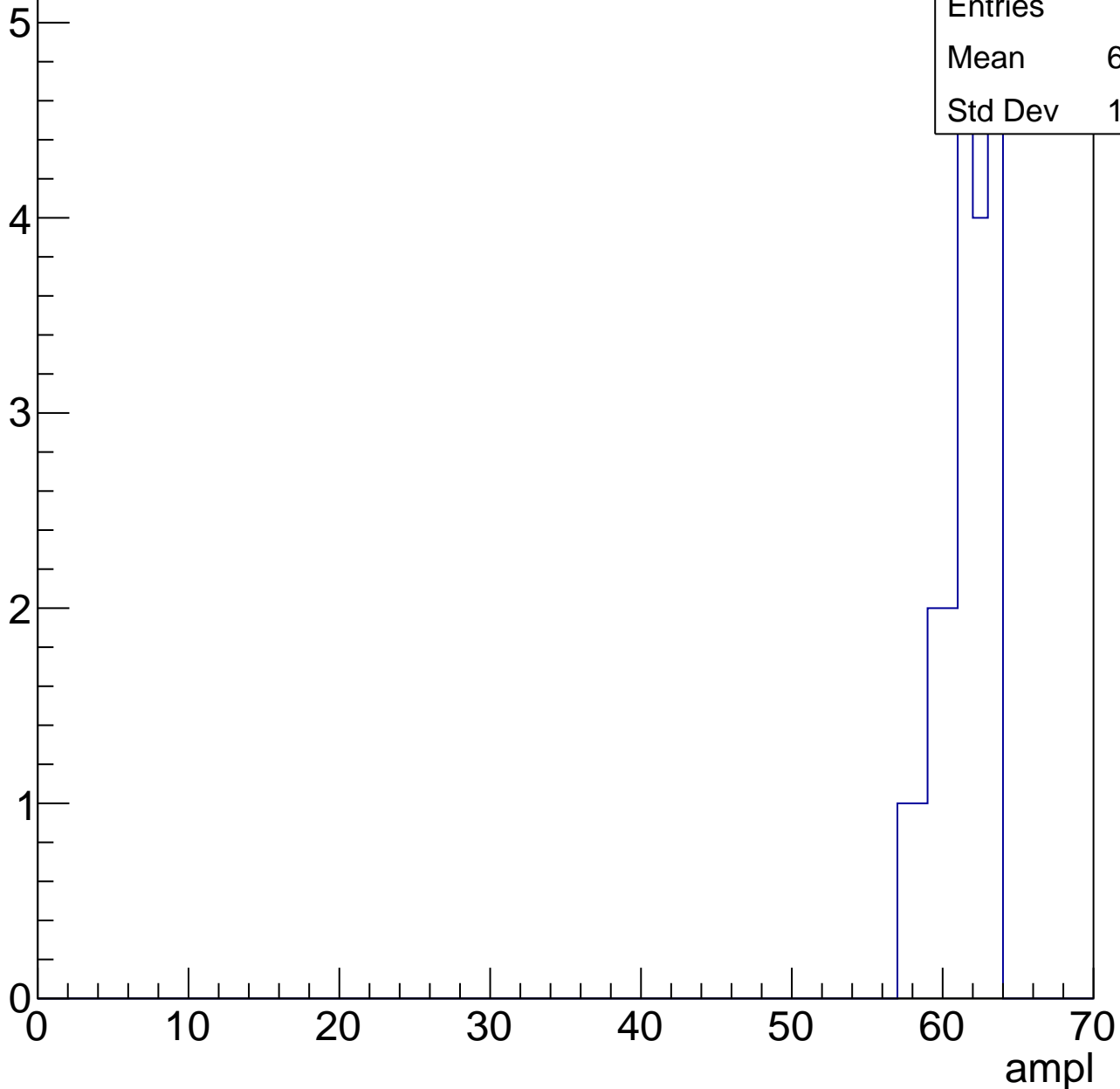


# B1L002S, U18-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.05
Std Dev	1.717

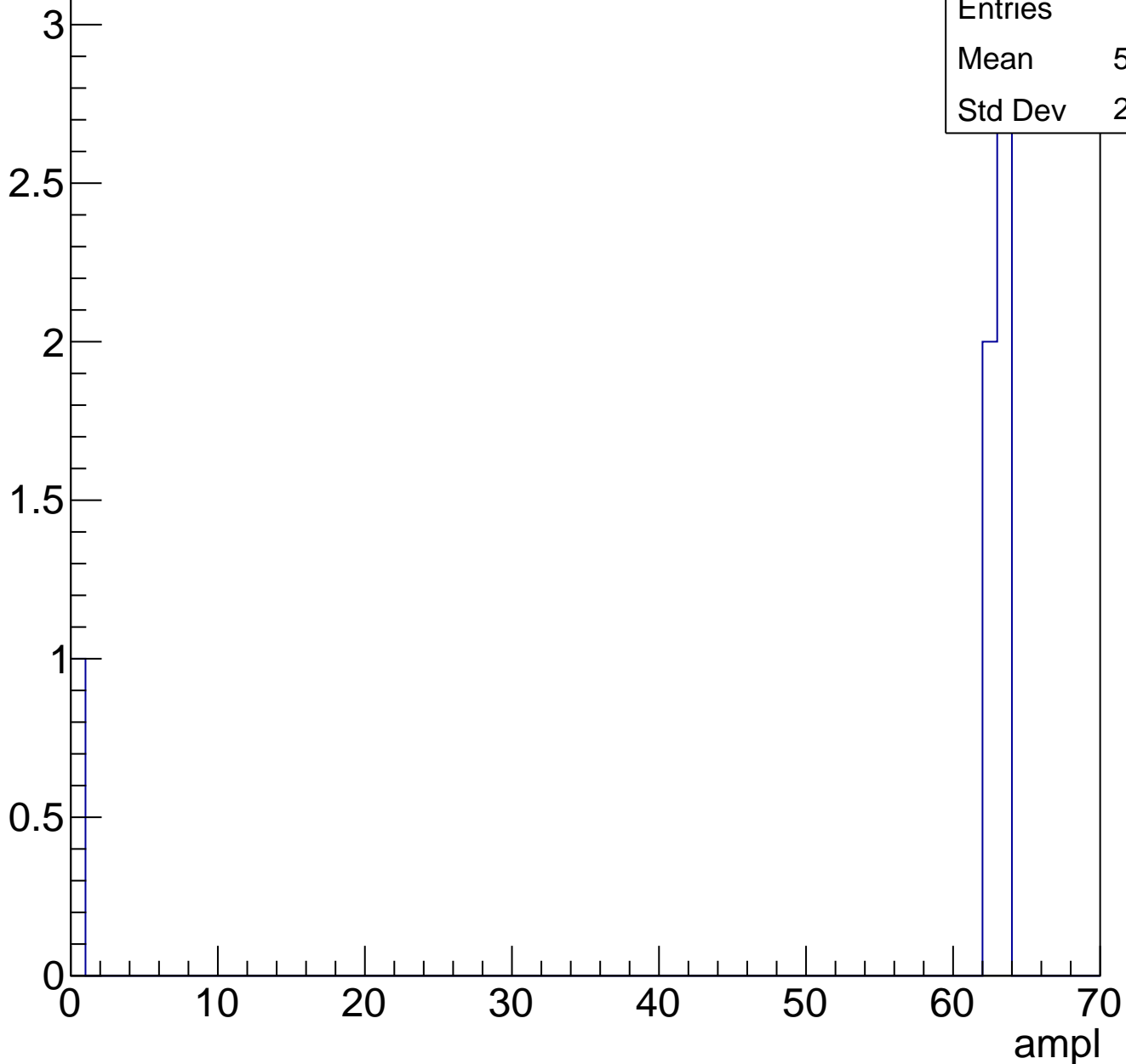




# B1L002S, U18-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch72, adc0

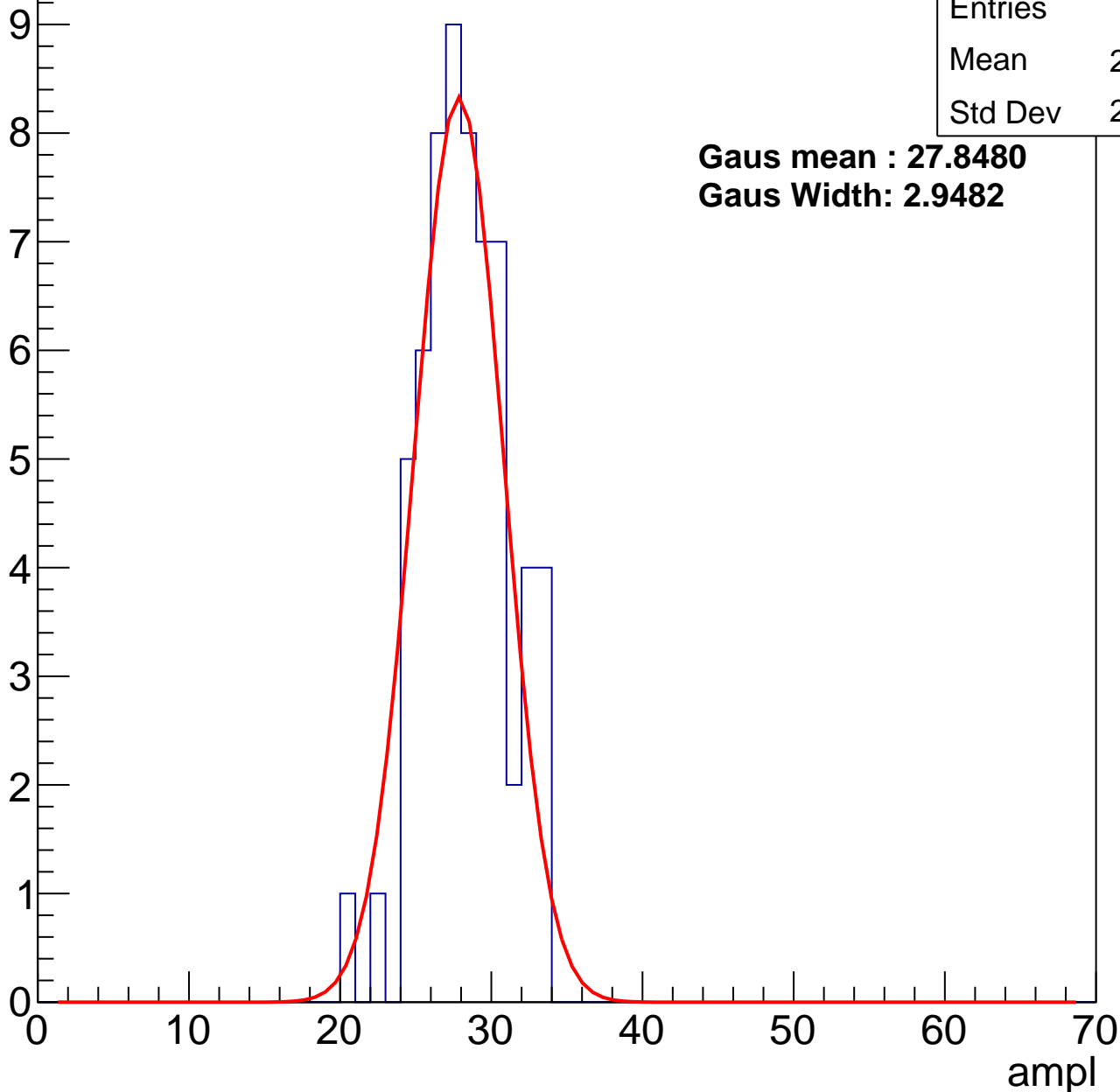
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.77
Std Dev	2.808

**Gaus mean : 27.8480**

**Gaus Width: 2.9482**



# B1L002S, U18-ch72, adc1

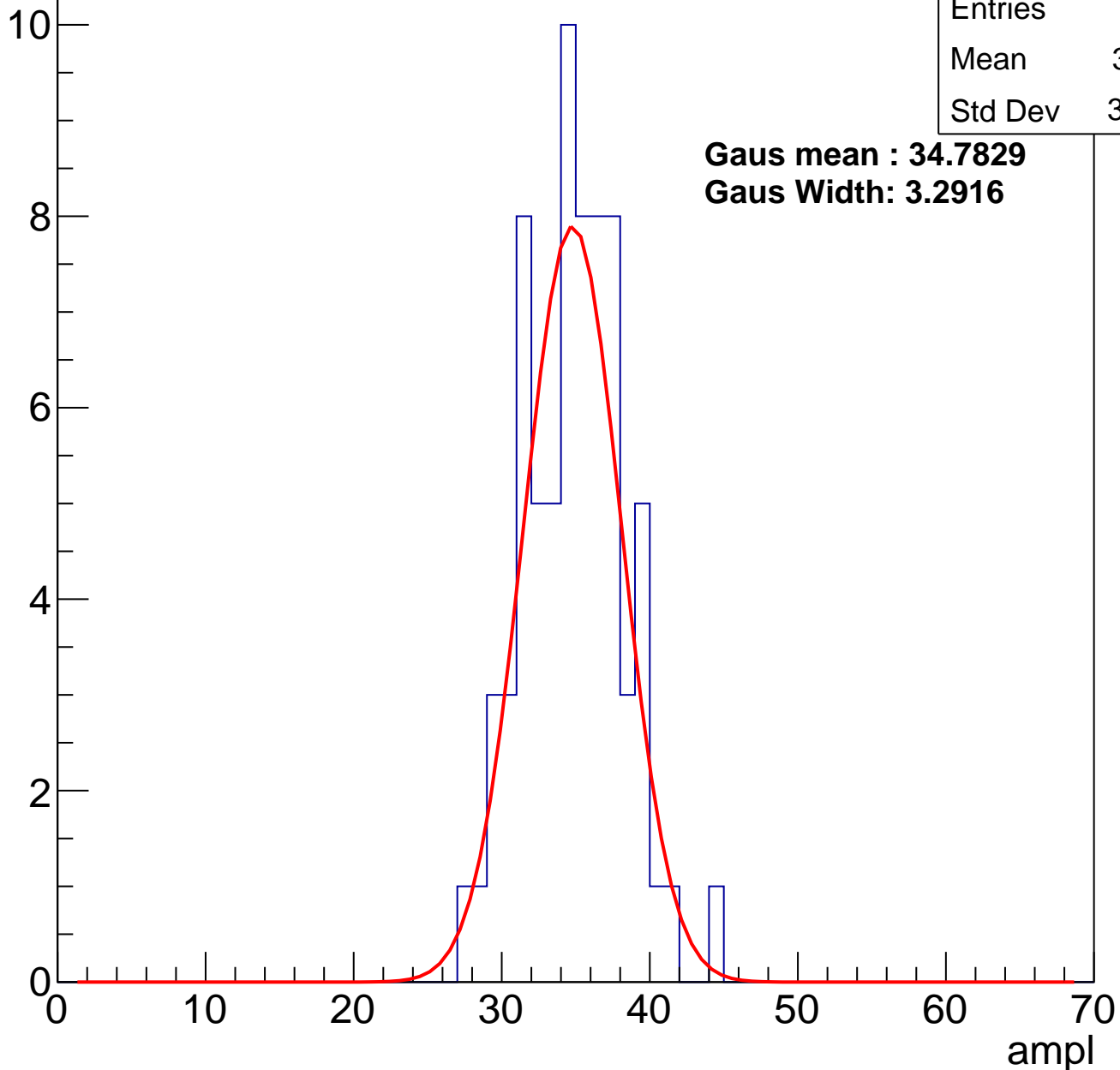
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	34.41
Std Dev	3.279

**Gaus mean : 34.7829**

**Gaus Width: 3.2916**

Entry



# B1L002S, U18-ch72, adc2

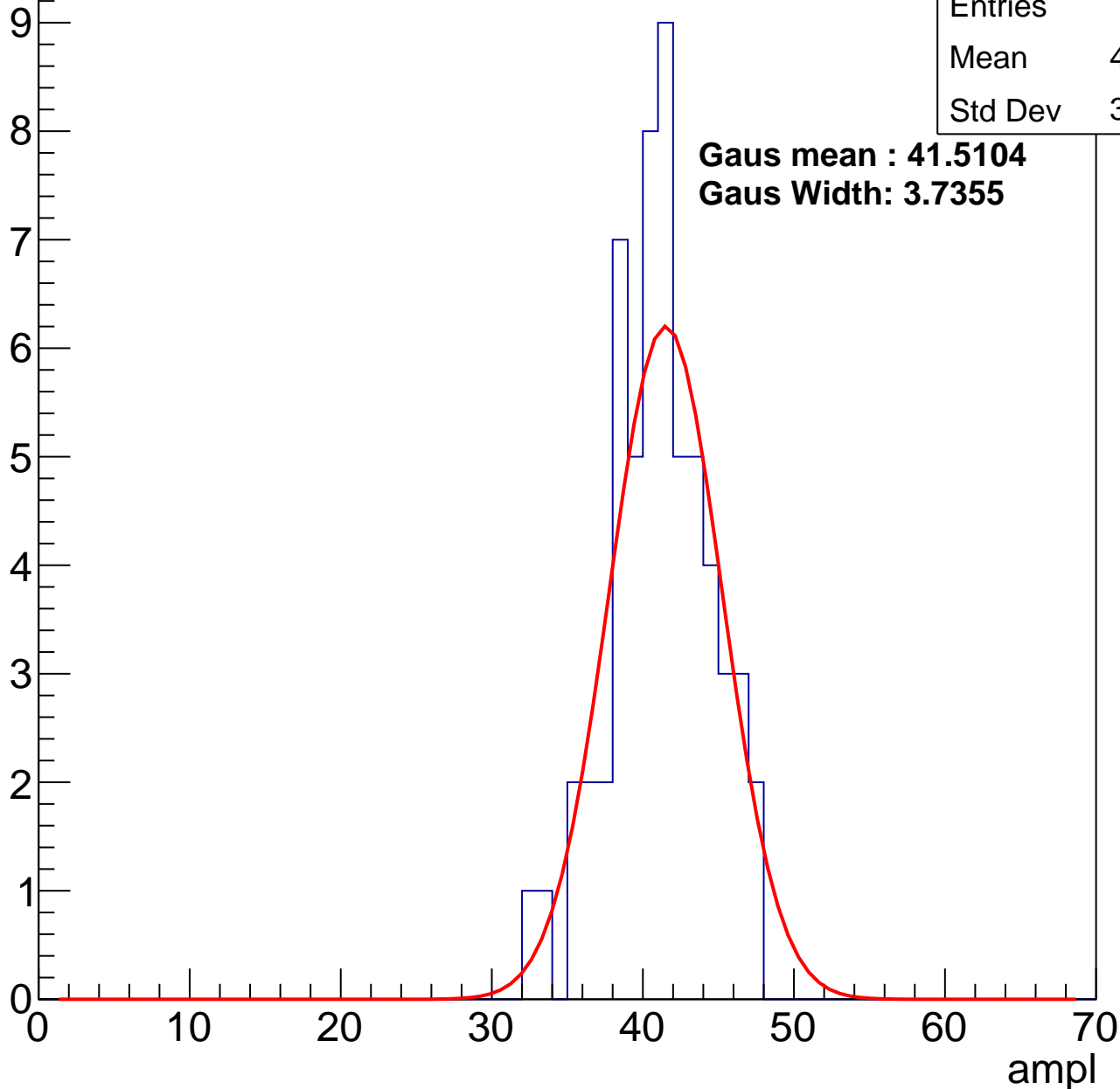
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.66
Std Dev	3.292

**Gaus mean : 41.5104**

**Gaus Width: 3.7355**

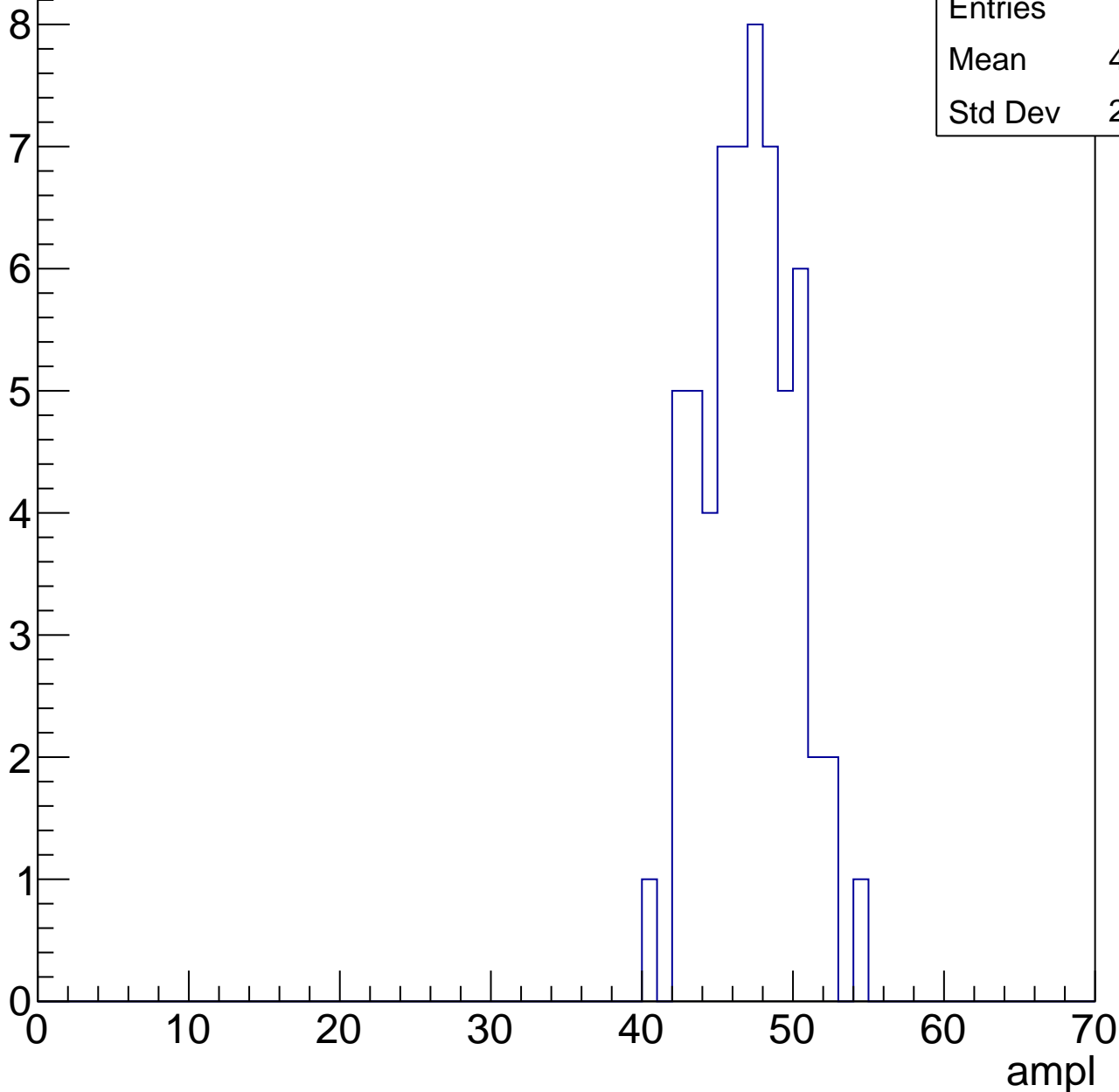


# B1L002S, U18-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

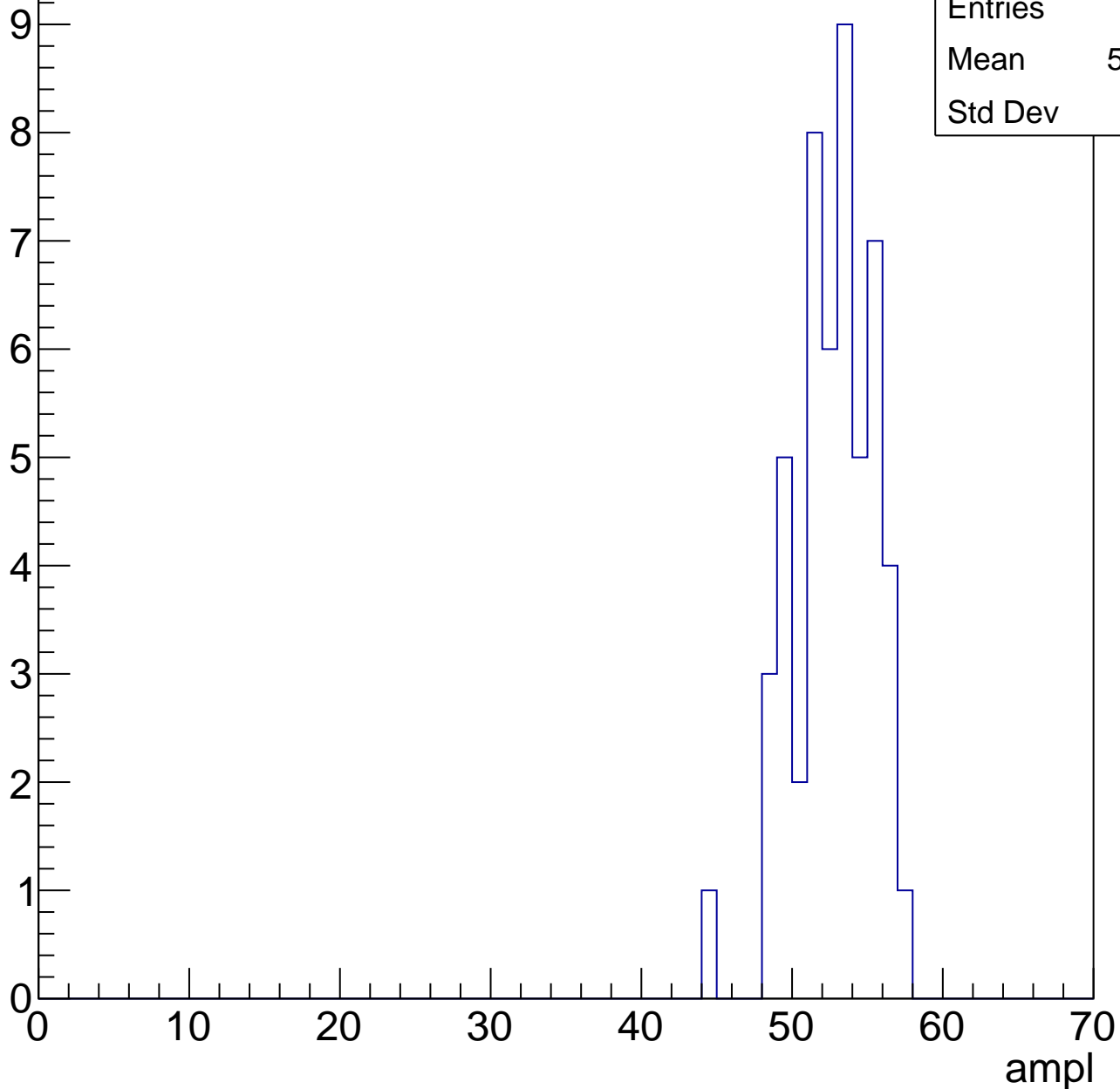
Entries	60
Mean	46.58
Std Dev	2.962



# B1L002S, U18-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



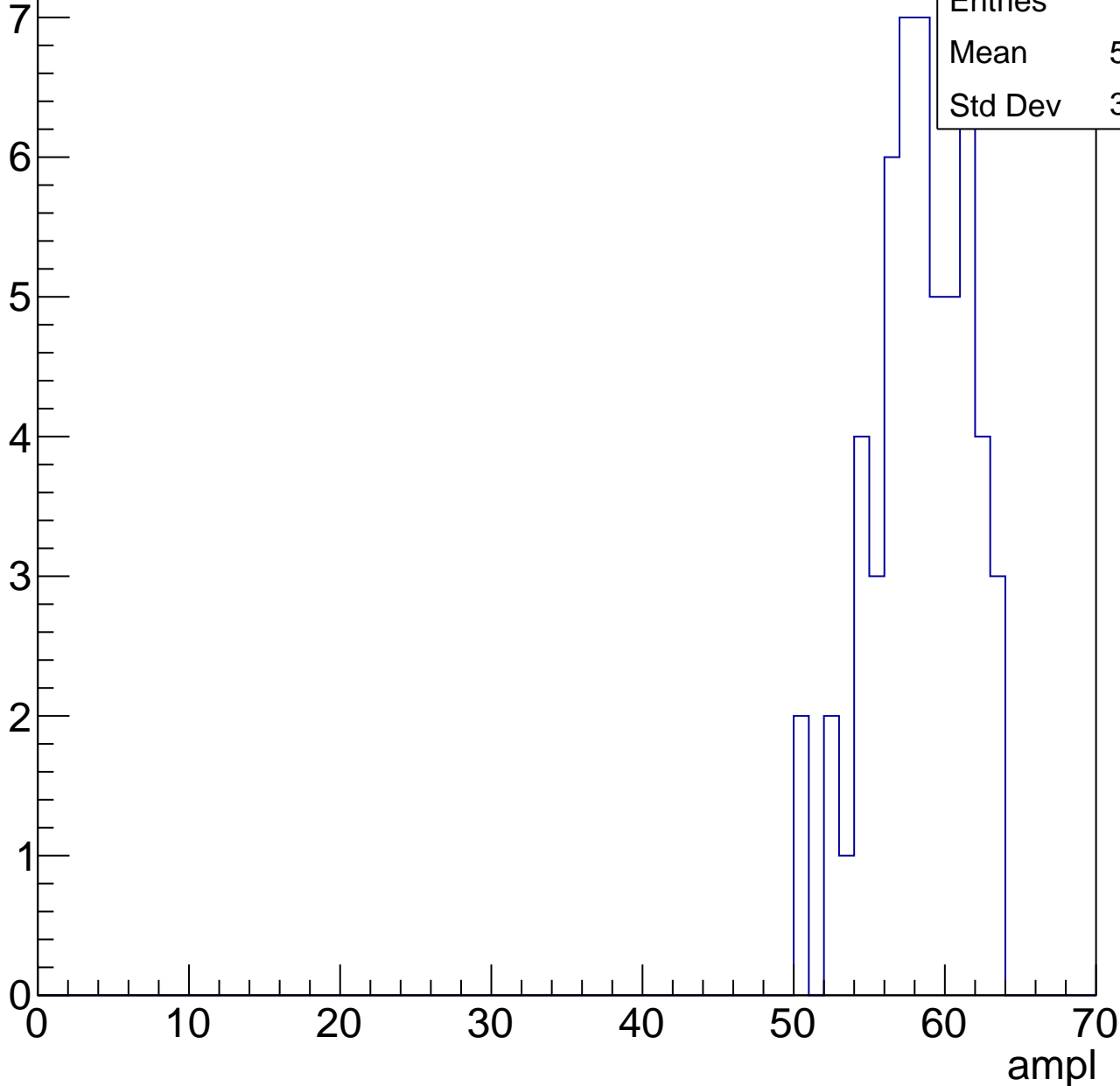
Entries	51
Mean	52.27
Std Dev	2.62

# B1L002S, U18-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.82
Std Dev	3.197

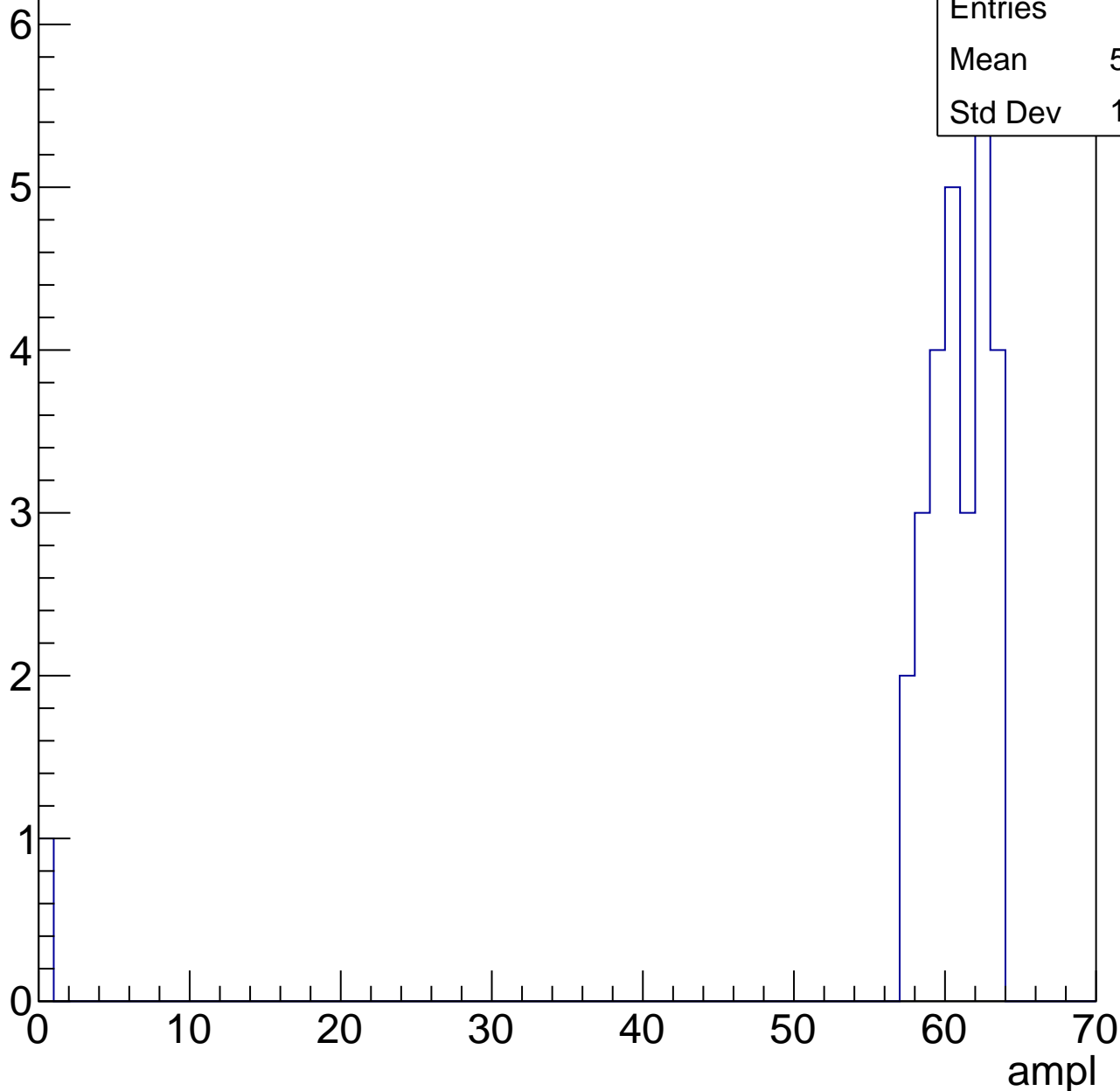


# B1L002S, U18-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.25
Std Dev	11.36

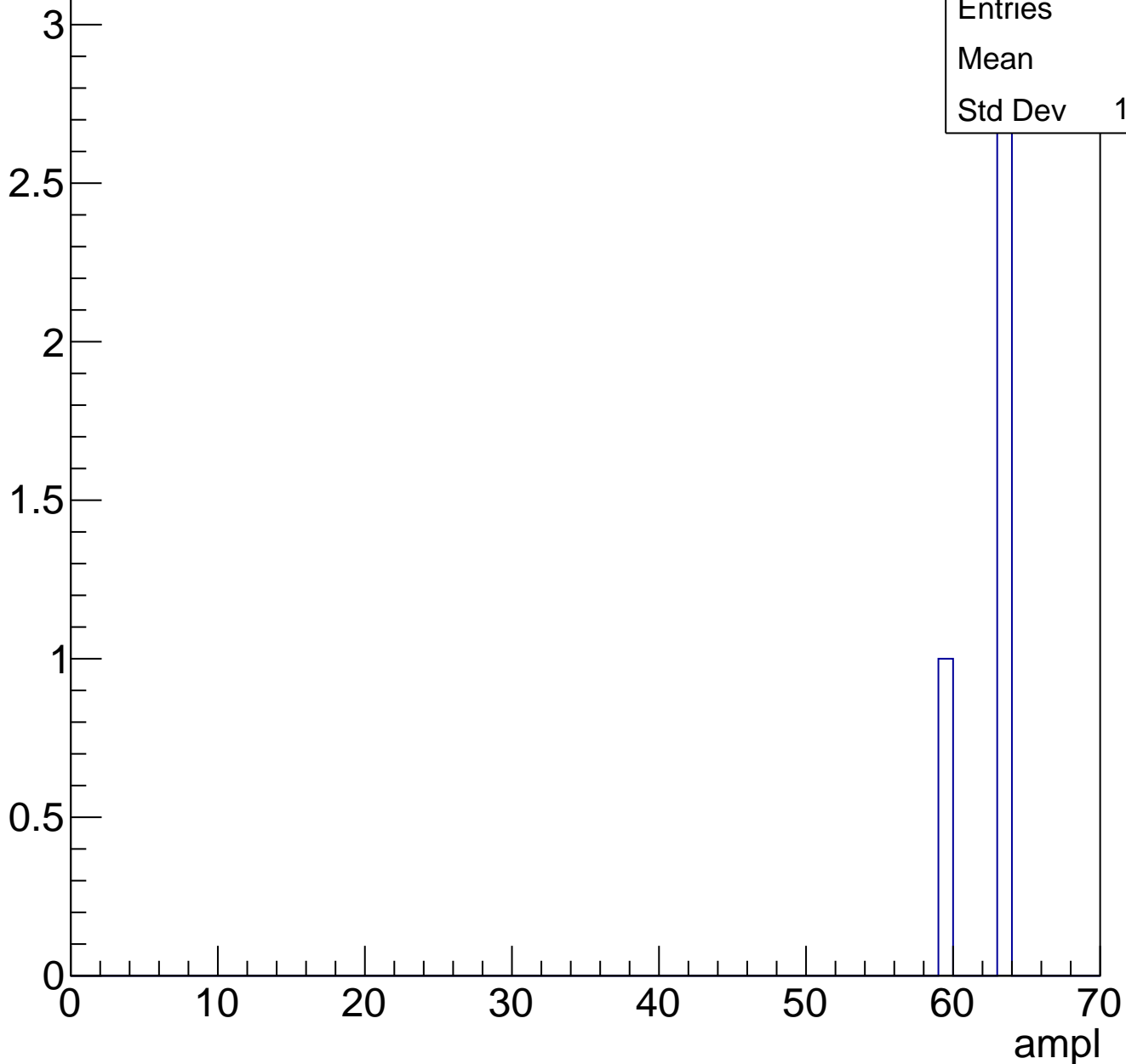




# B1L002S, U18-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch73, adc0

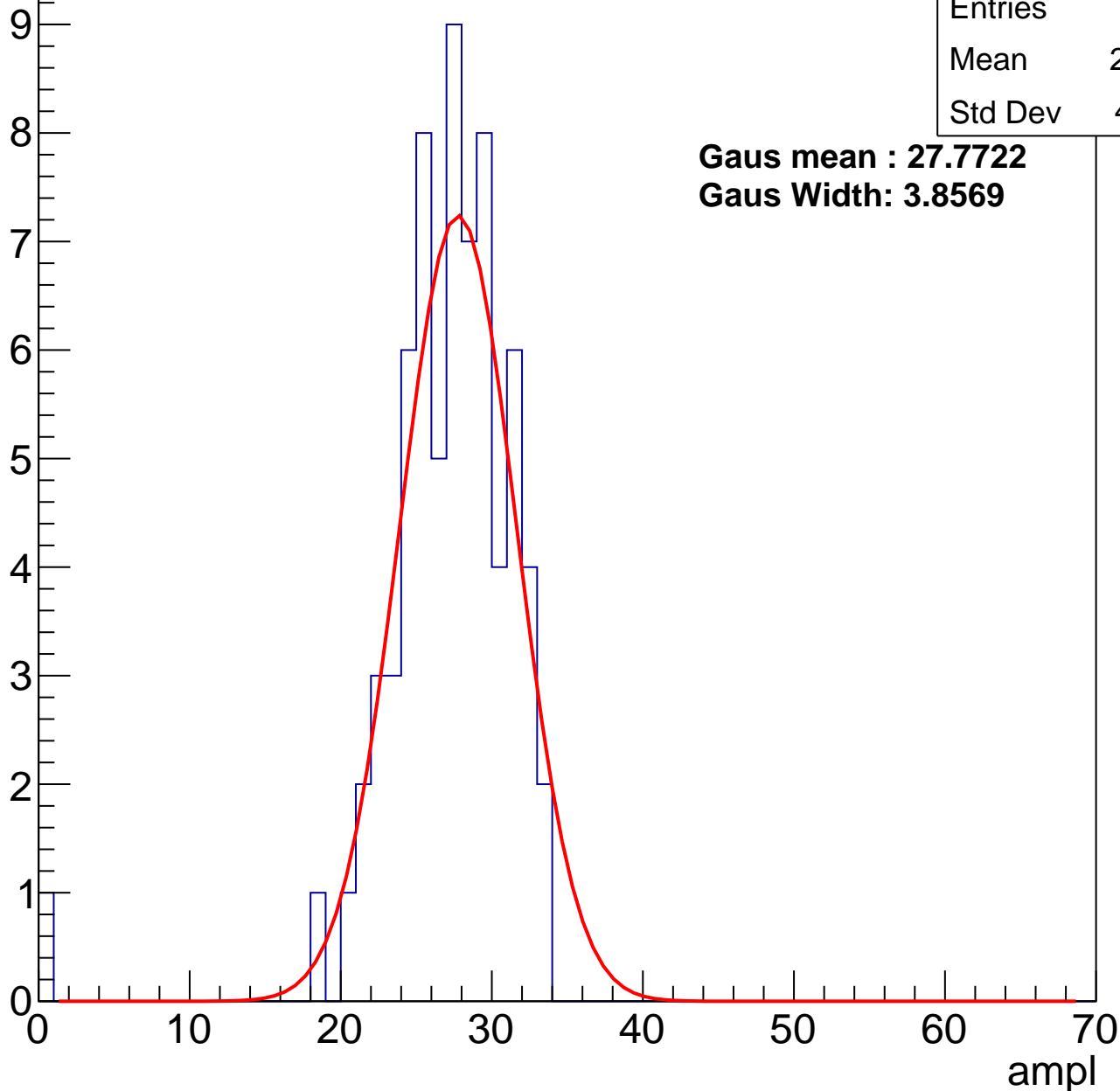
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	26.57
Std Dev	4.591

**Gaus mean : 27.7722**

**Gaus Width: 3.8569**



# B1L002S, U18-ch73, adc1

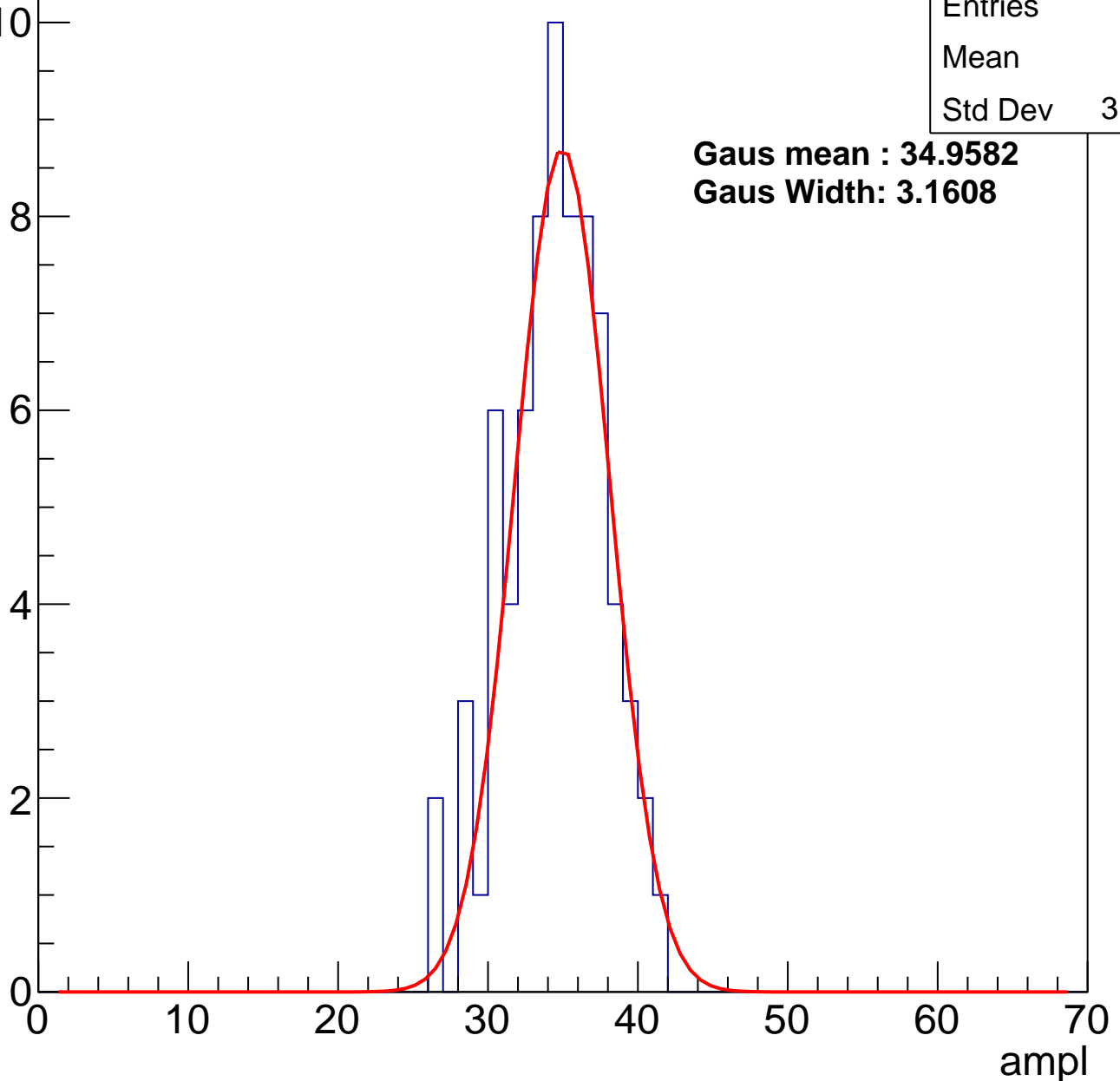
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34
Std Dev	3.286

**Gaus mean : 34.9582**

**Gaus Width: 3.1608**



# B1L002S, U18-ch73, adc2

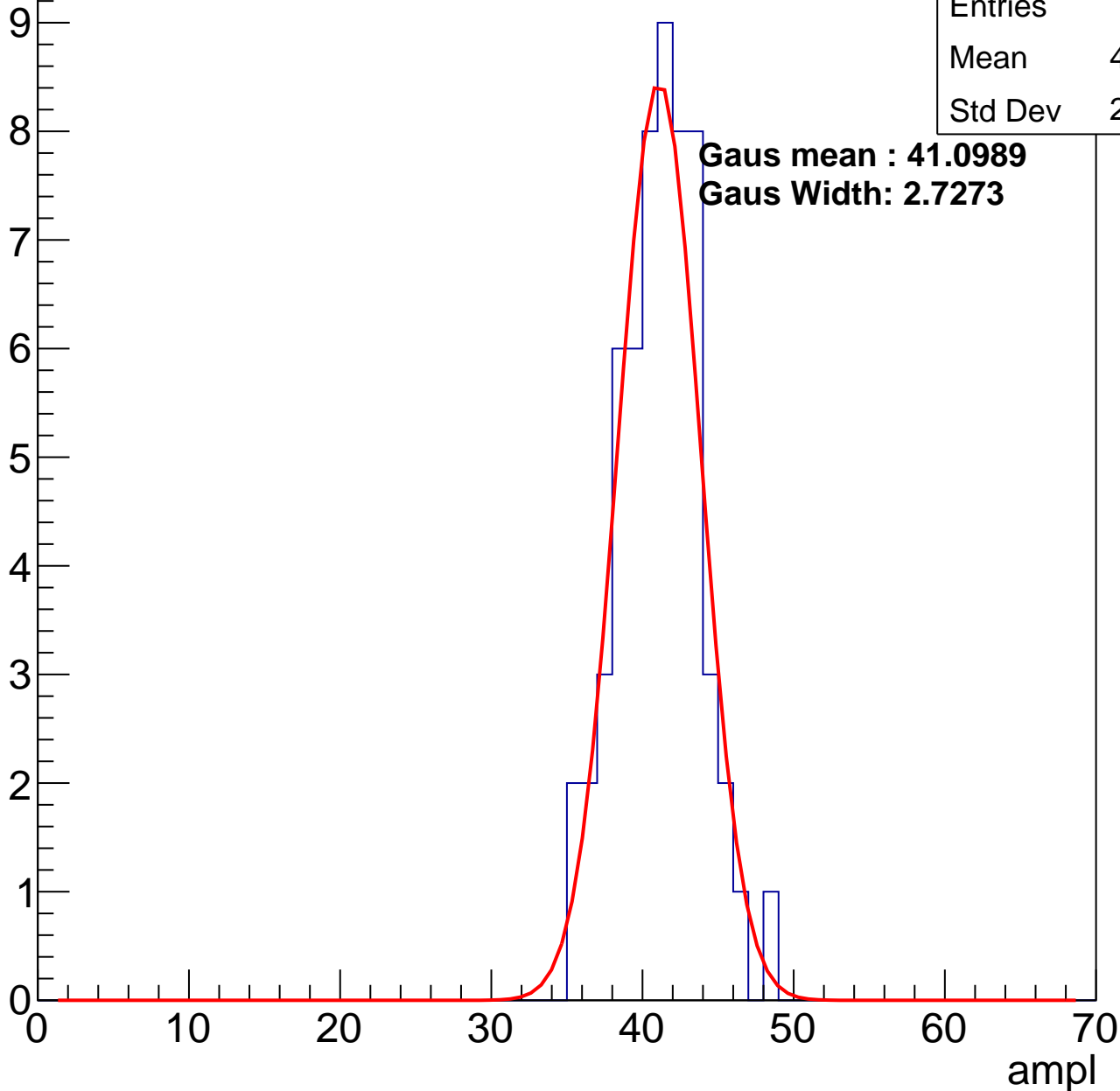
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.68
Std Dev	2.677

**Gaus mean : 41.0989**

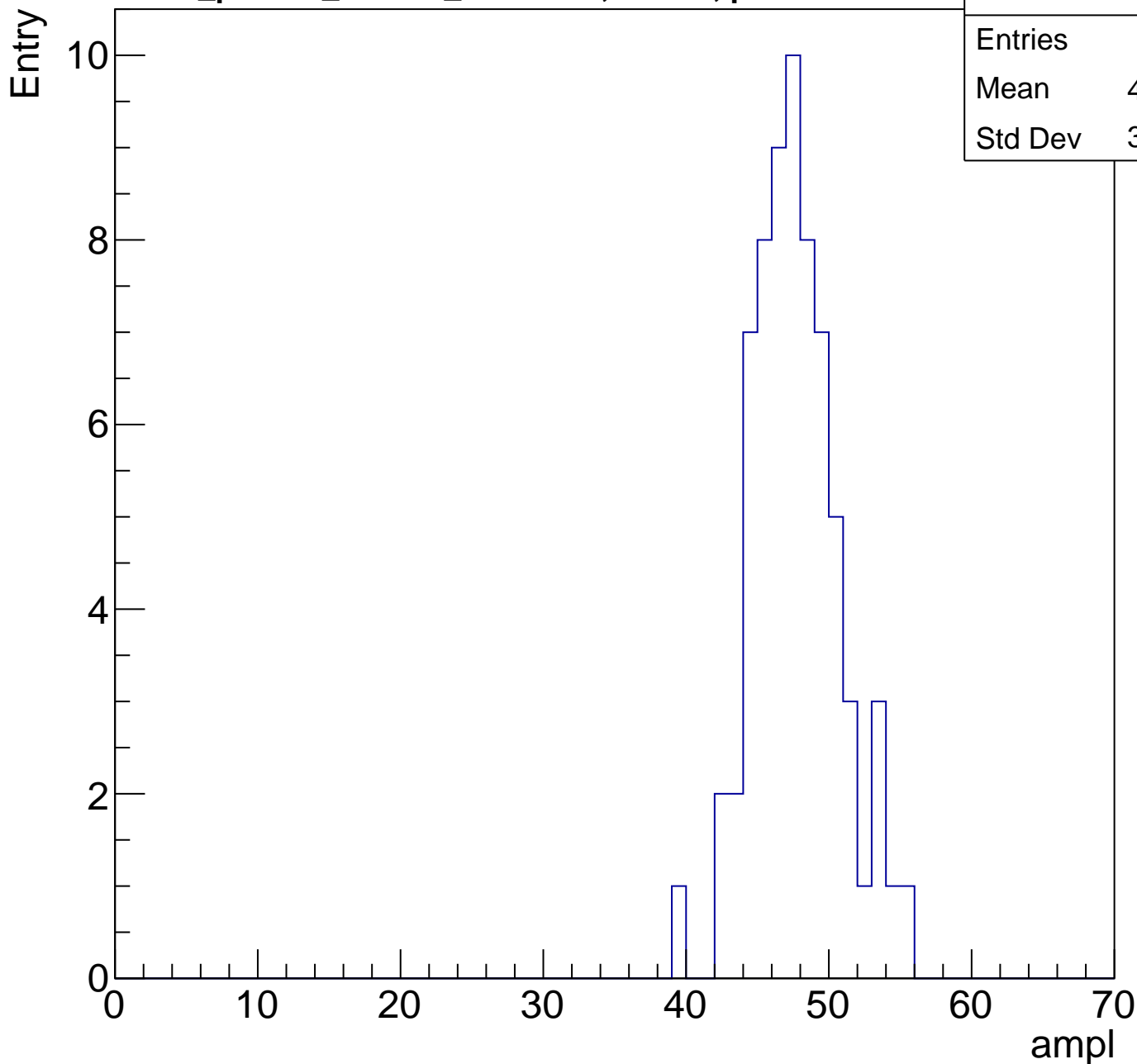
**Gaus Width: 2.7273**



# B1L002S, U18-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	47.22
Std Dev	3.024

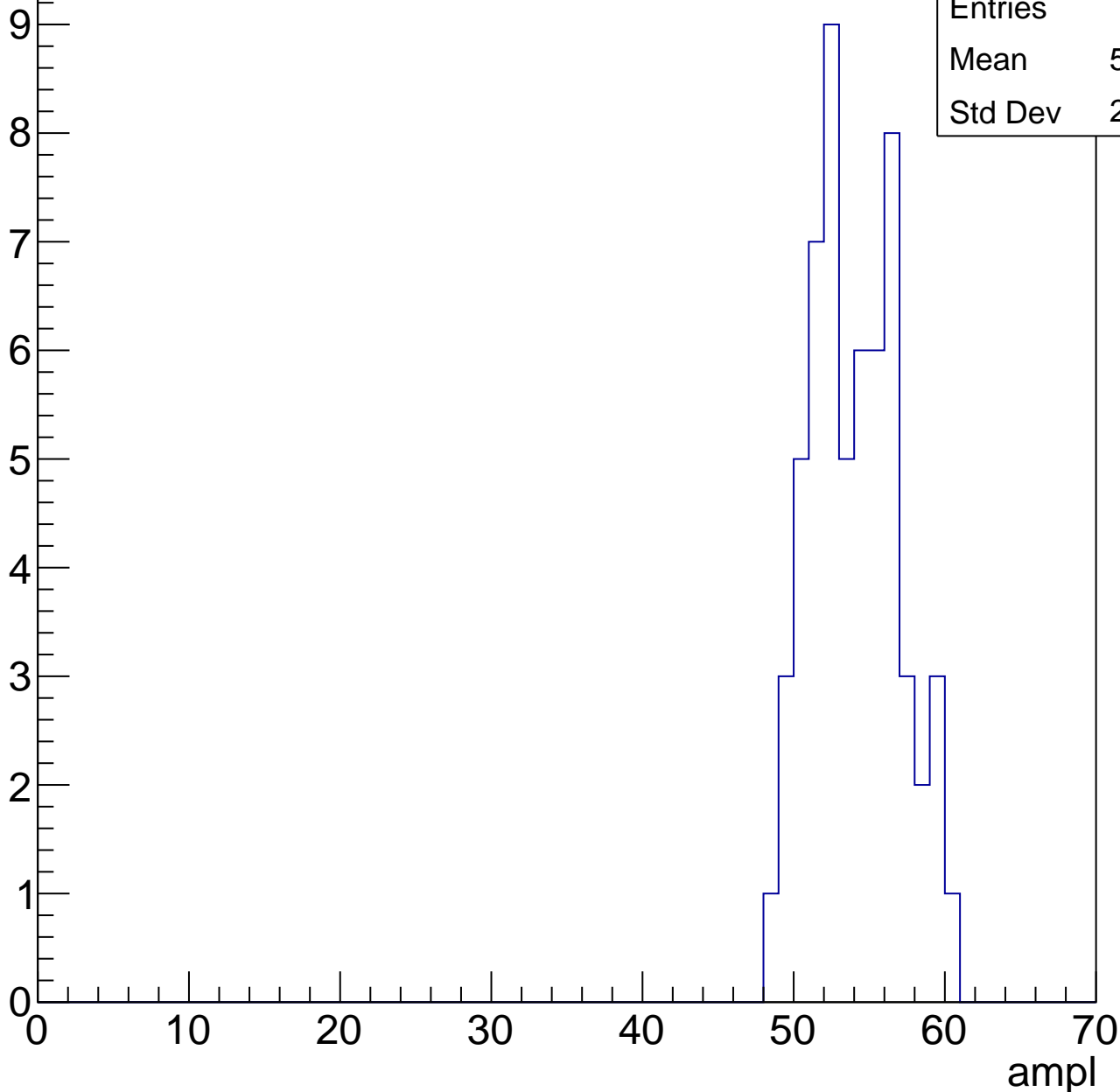


# B1L002S, U18-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

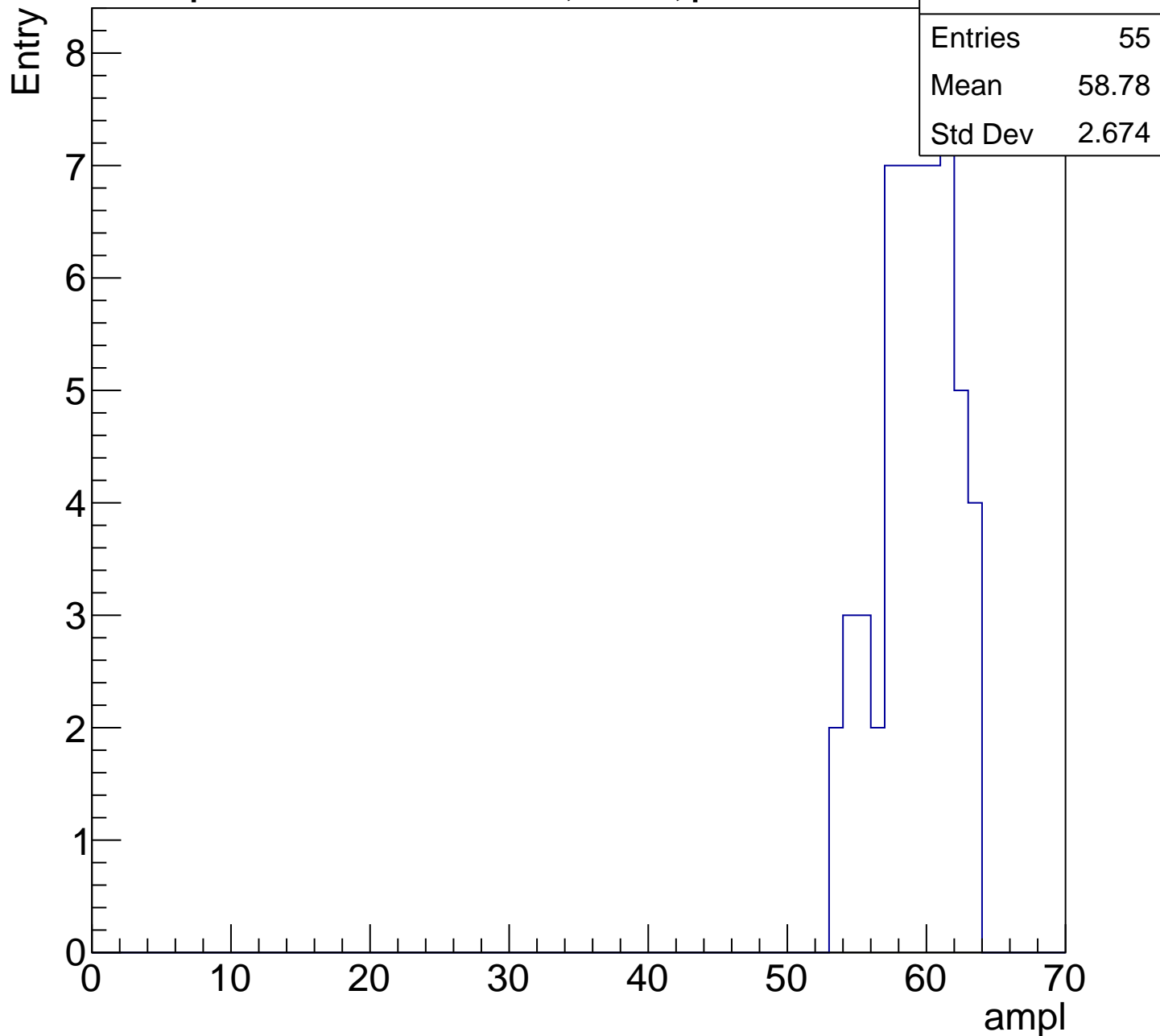
Entry

Entries	59
Mean	53.58
Std Dev	2.889



# B1L002S, U18-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

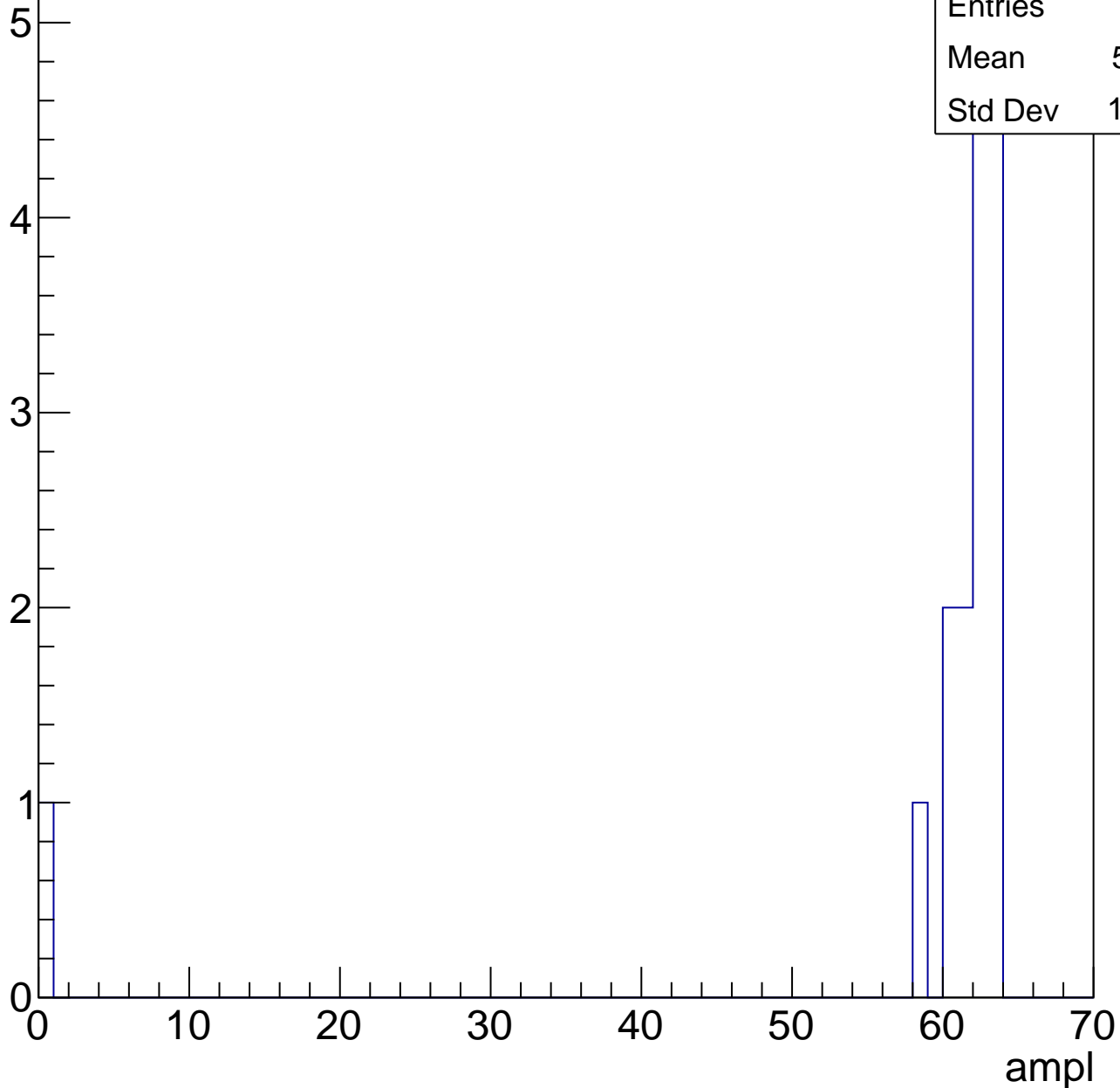


# B1L002S, U18-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	57.81
Std Dev	14.99





# B1L002S, U18-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U18-ch74, adc0

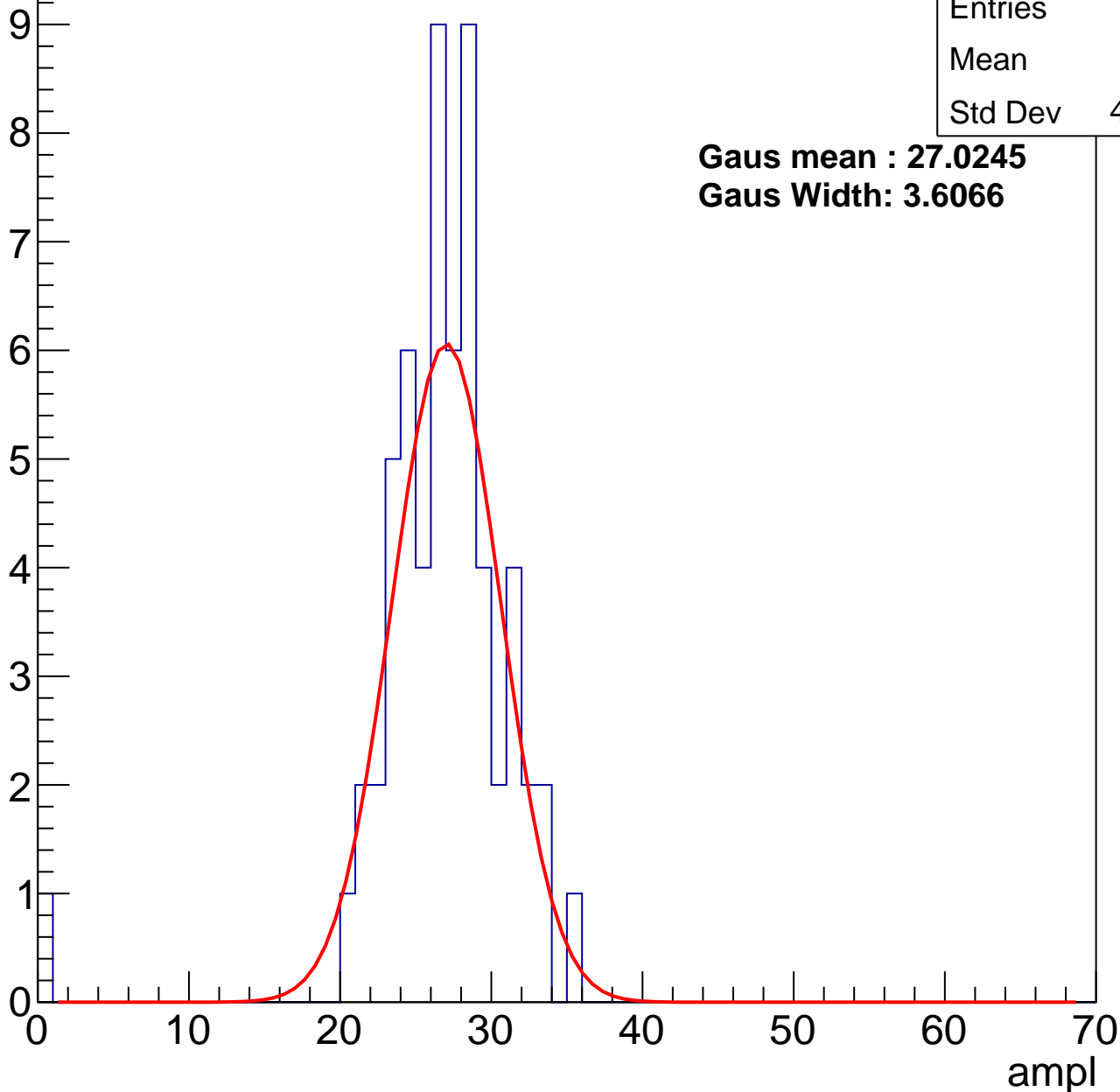
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	26.3
Std Dev	4.695

**Gaus mean : 27.0245**

**Gaus Width: 3.6066**



# B1L002S, U18-ch74, adc1

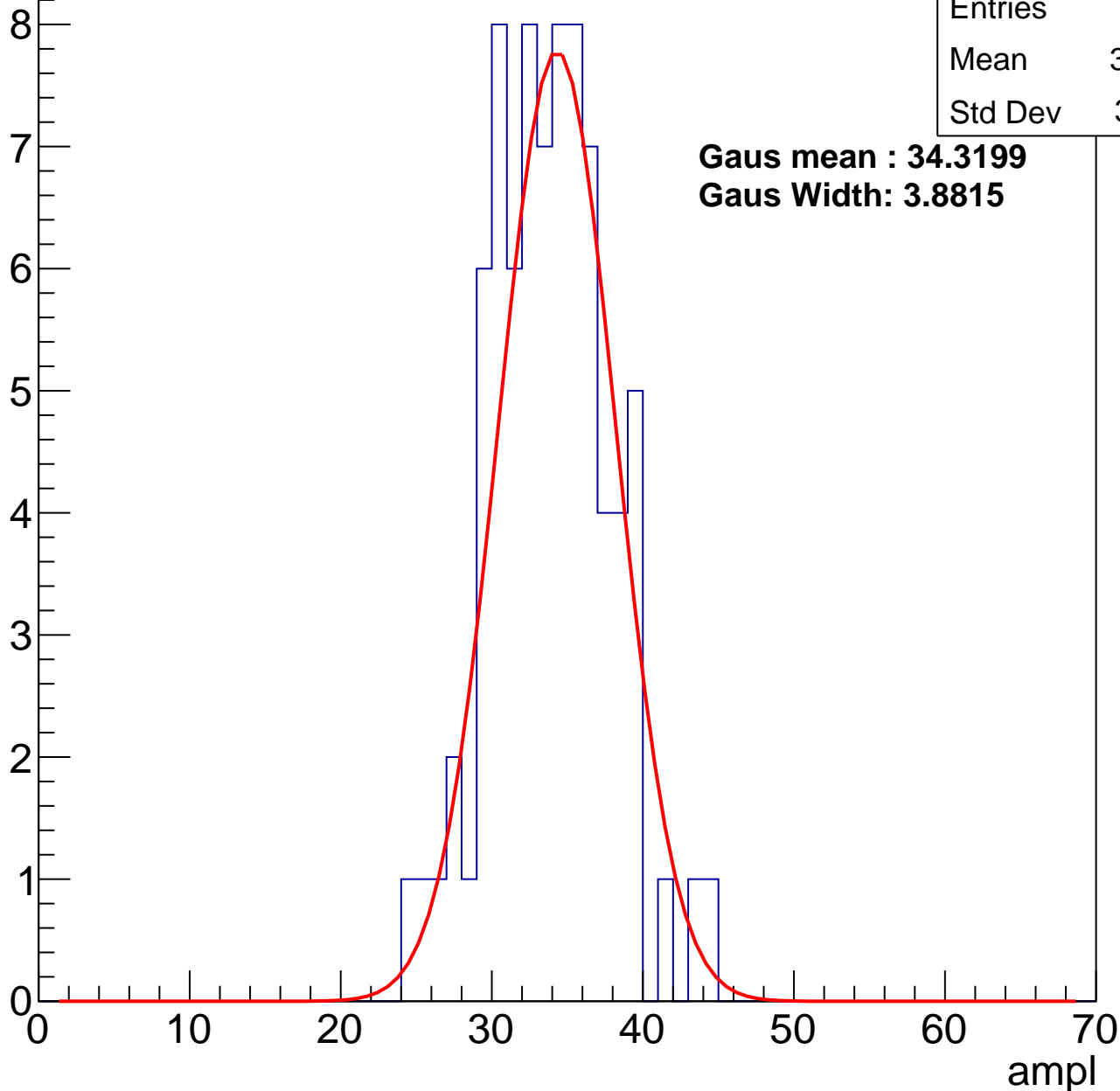
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	33.39
Std Dev	3.891

**Gaus mean : 34.3199**

**Gaus Width: 3.8815**



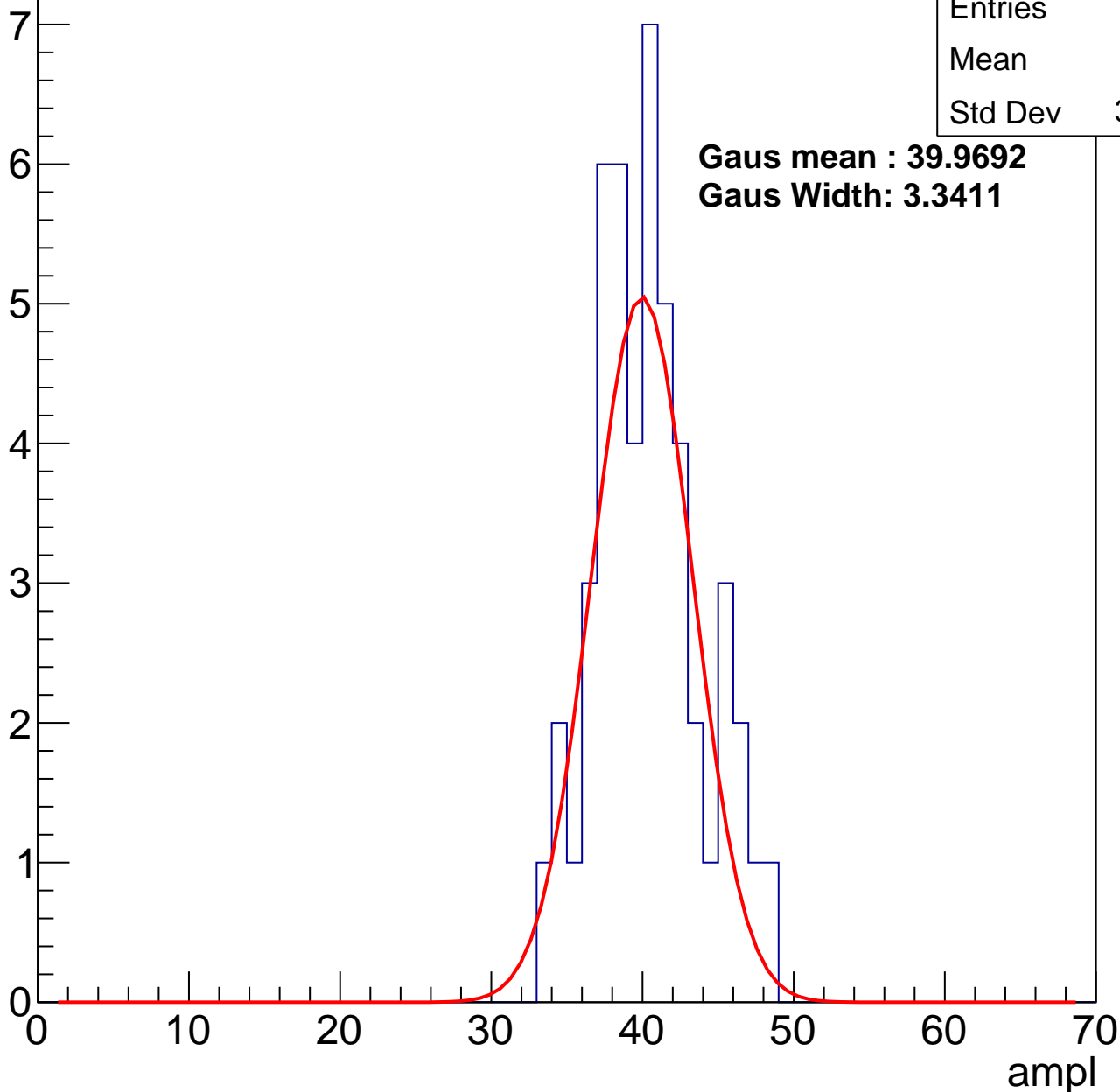
# B1L002S, U18-ch74, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	39.9
Std Dev	3.471

**Gaus mean : 39.9692**  
**Gaus Width: 3.3411**

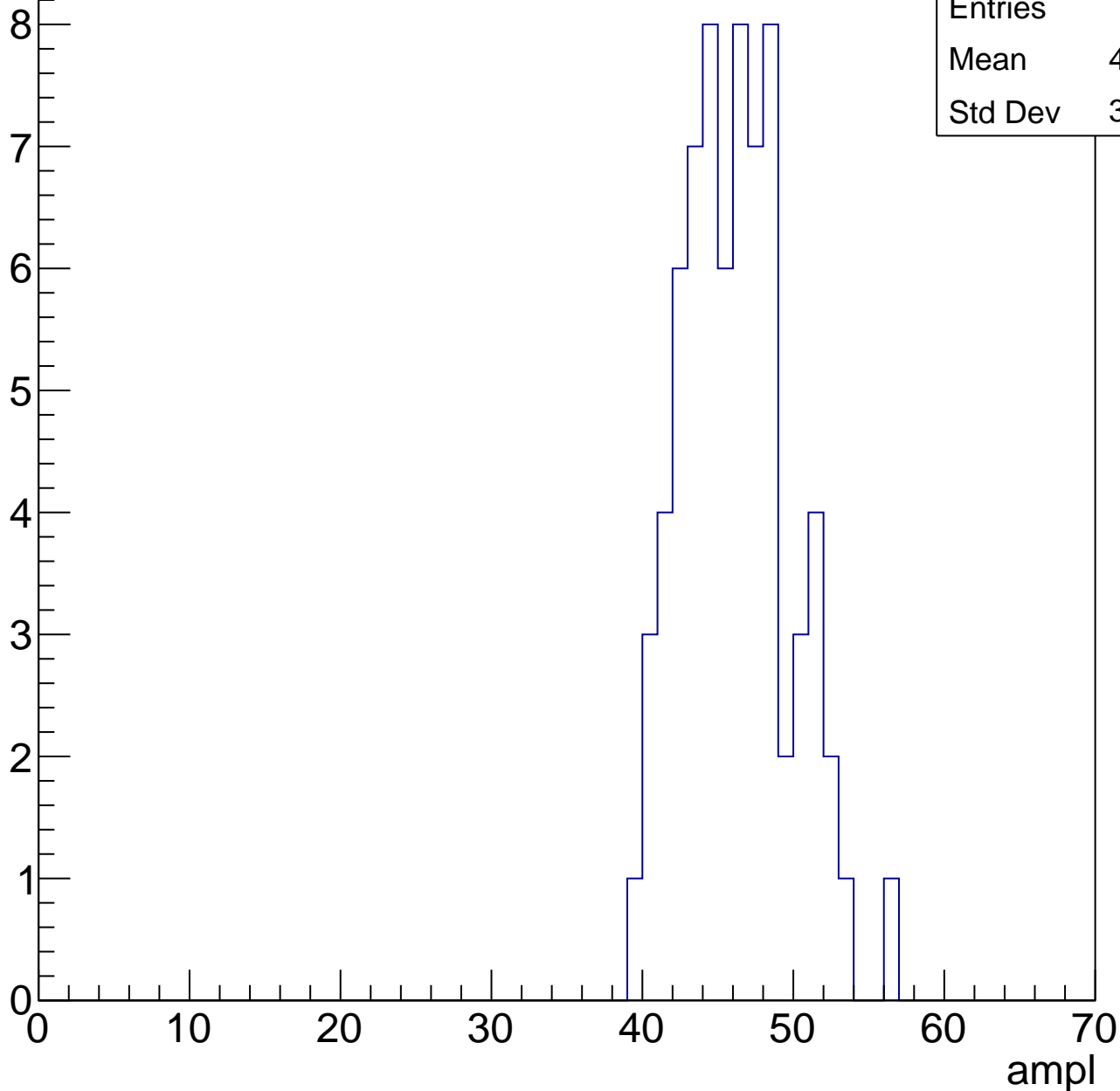


# B1L002S, U18-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	45.69
Std Dev	3.507

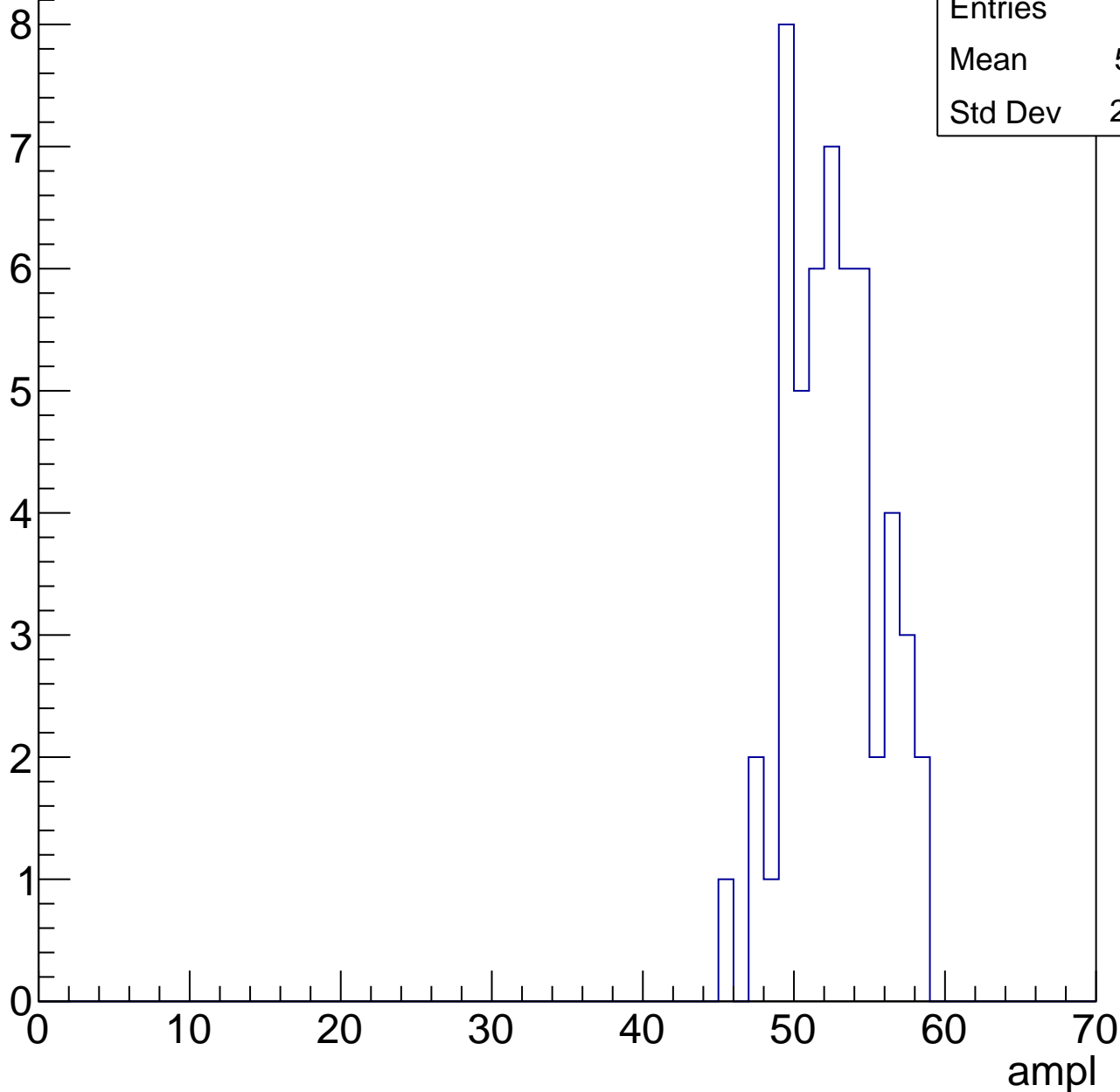


# B1L002S, U18-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.11
Std Dev	2.982

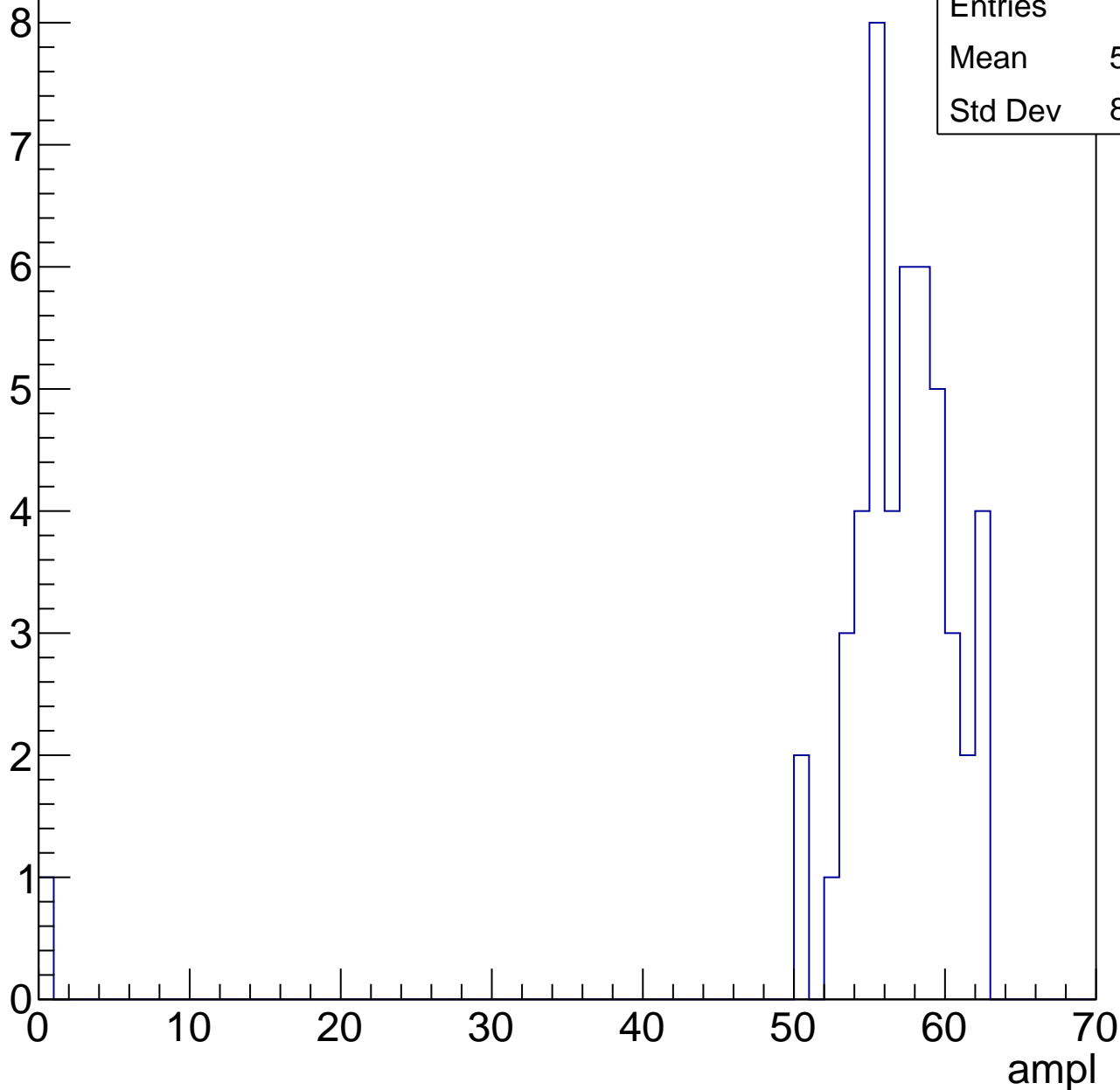


# B1L002S, U18-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

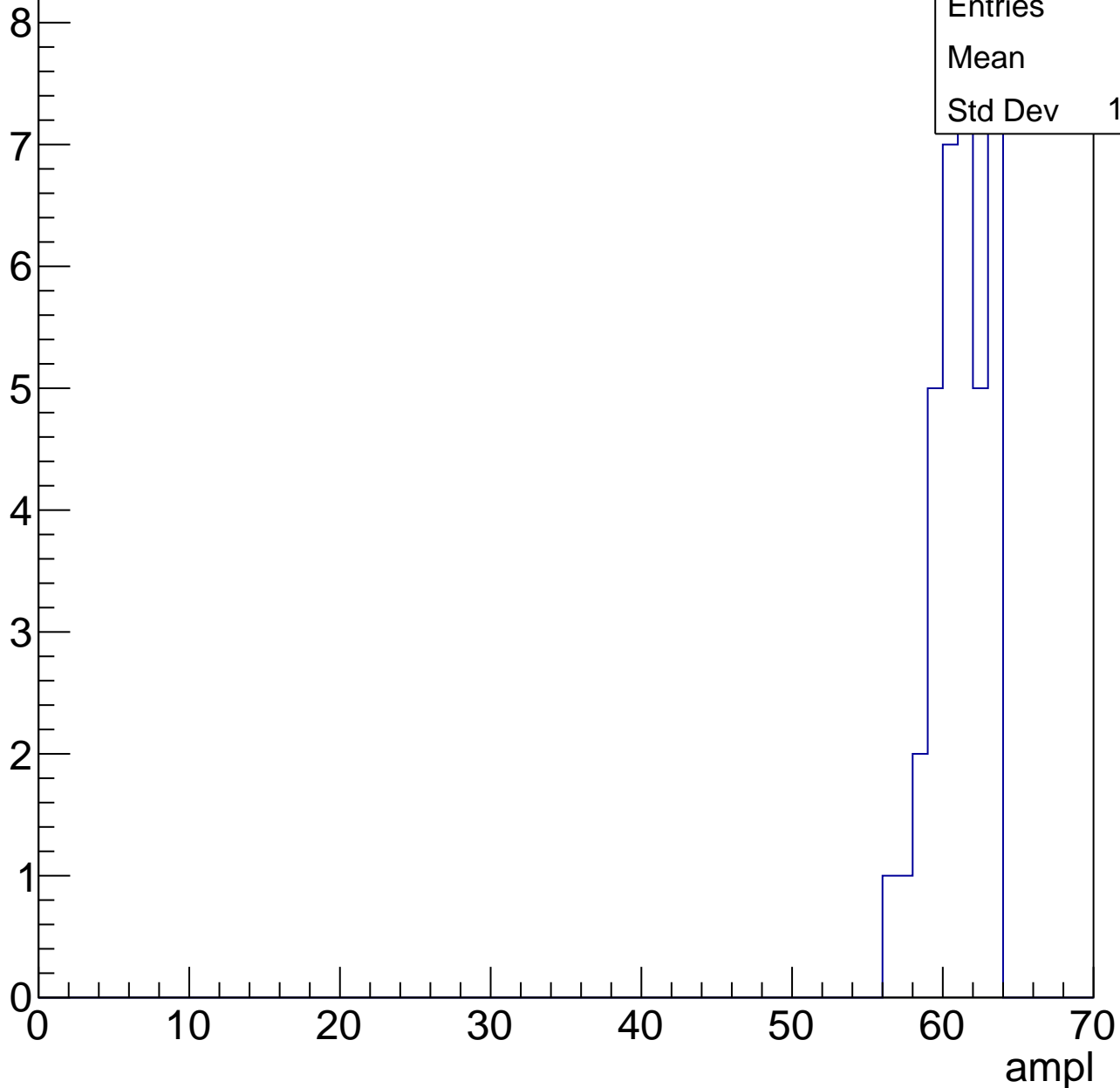
Entries	49
Mean	55.63
Std Dev	8.554



# B1L002S, U18-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

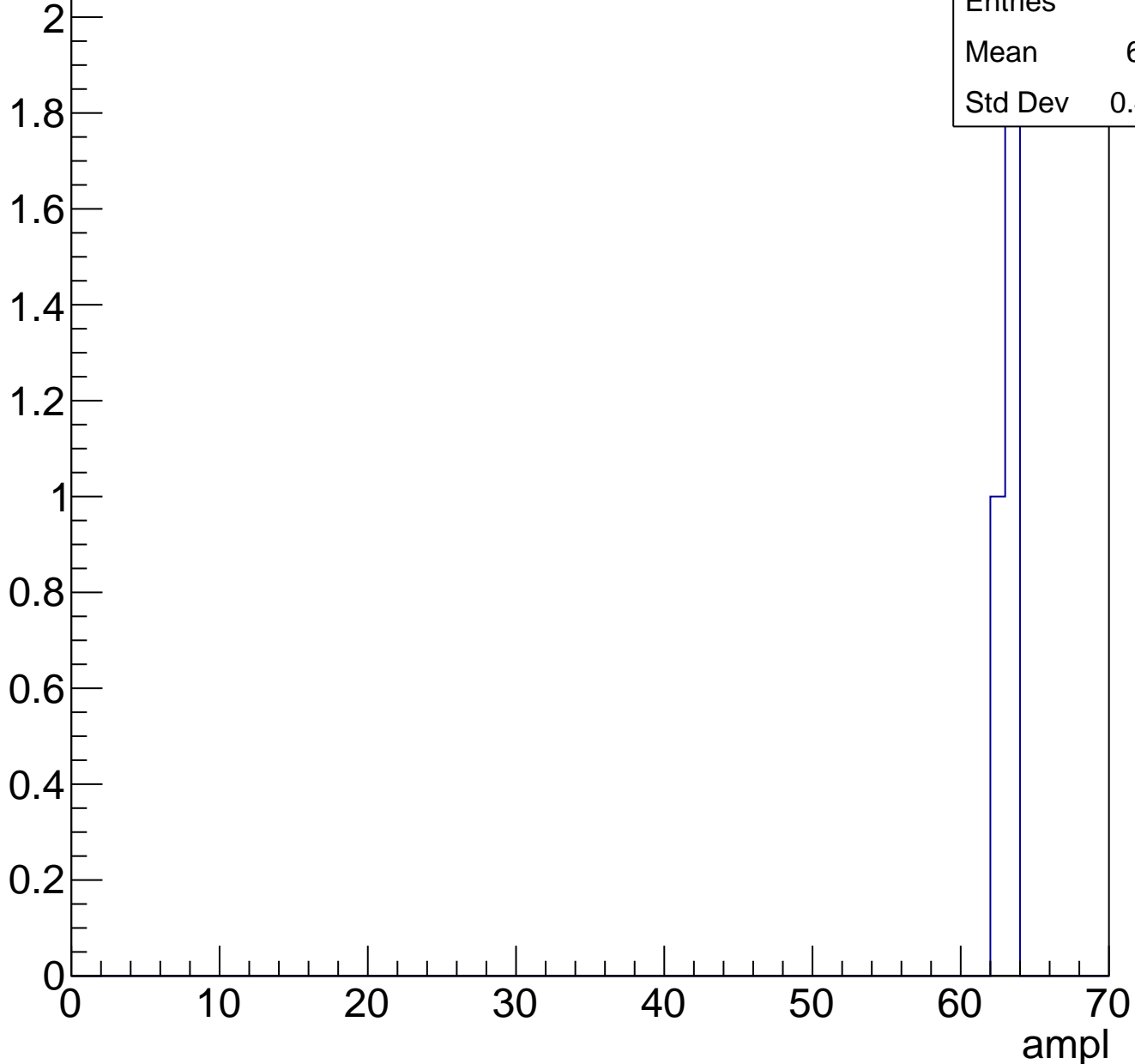




# B1L002S, U18-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

# B1L002S, U18-ch75, adc0

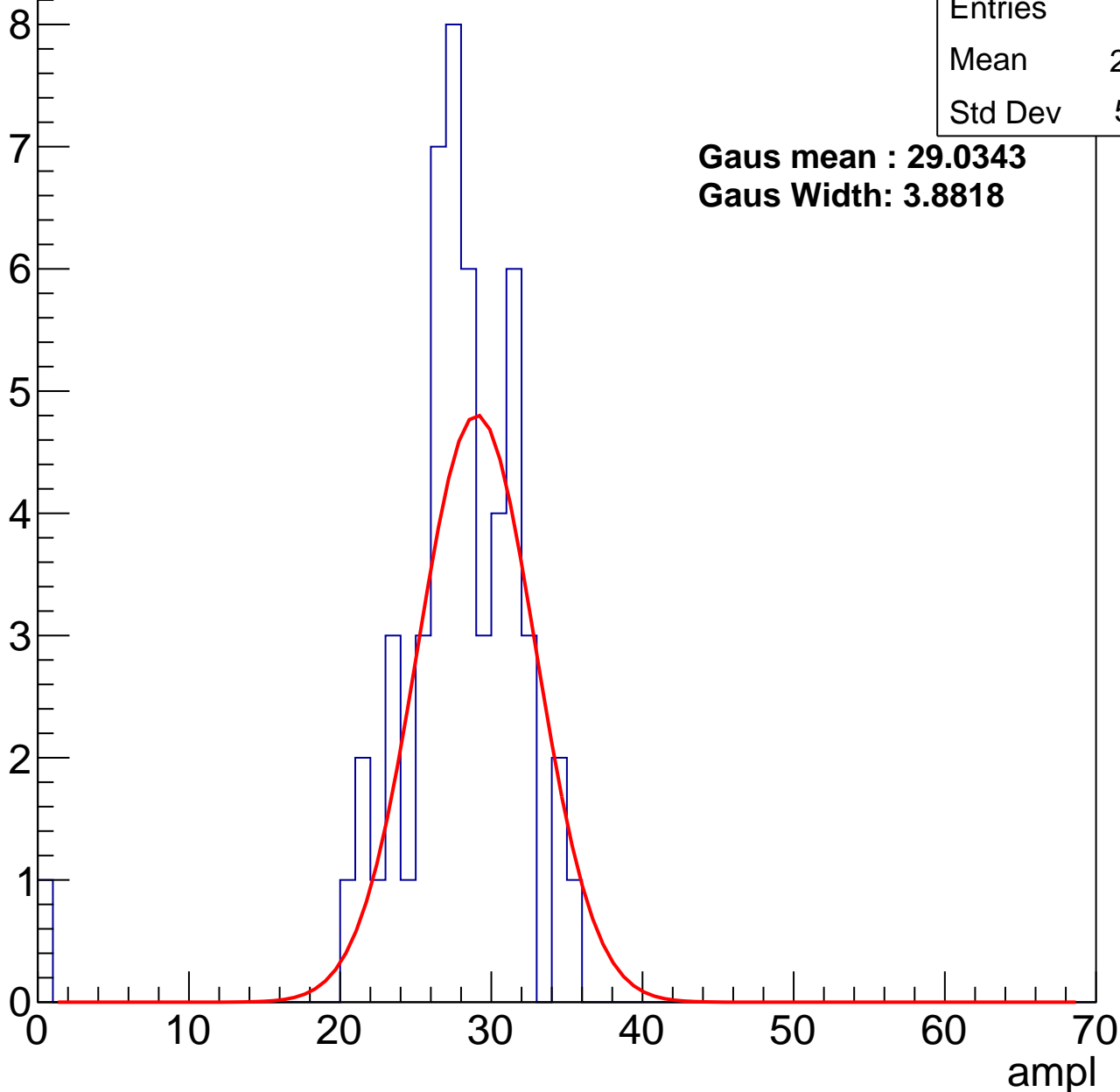
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.12
Std Dev	5.071

**Gaus mean : 29.0343**

**Gaus Width: 3.8818**



# B1L002S, U18-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	33.67
Std Dev	3.385

**Gaus mean : 33.8407**

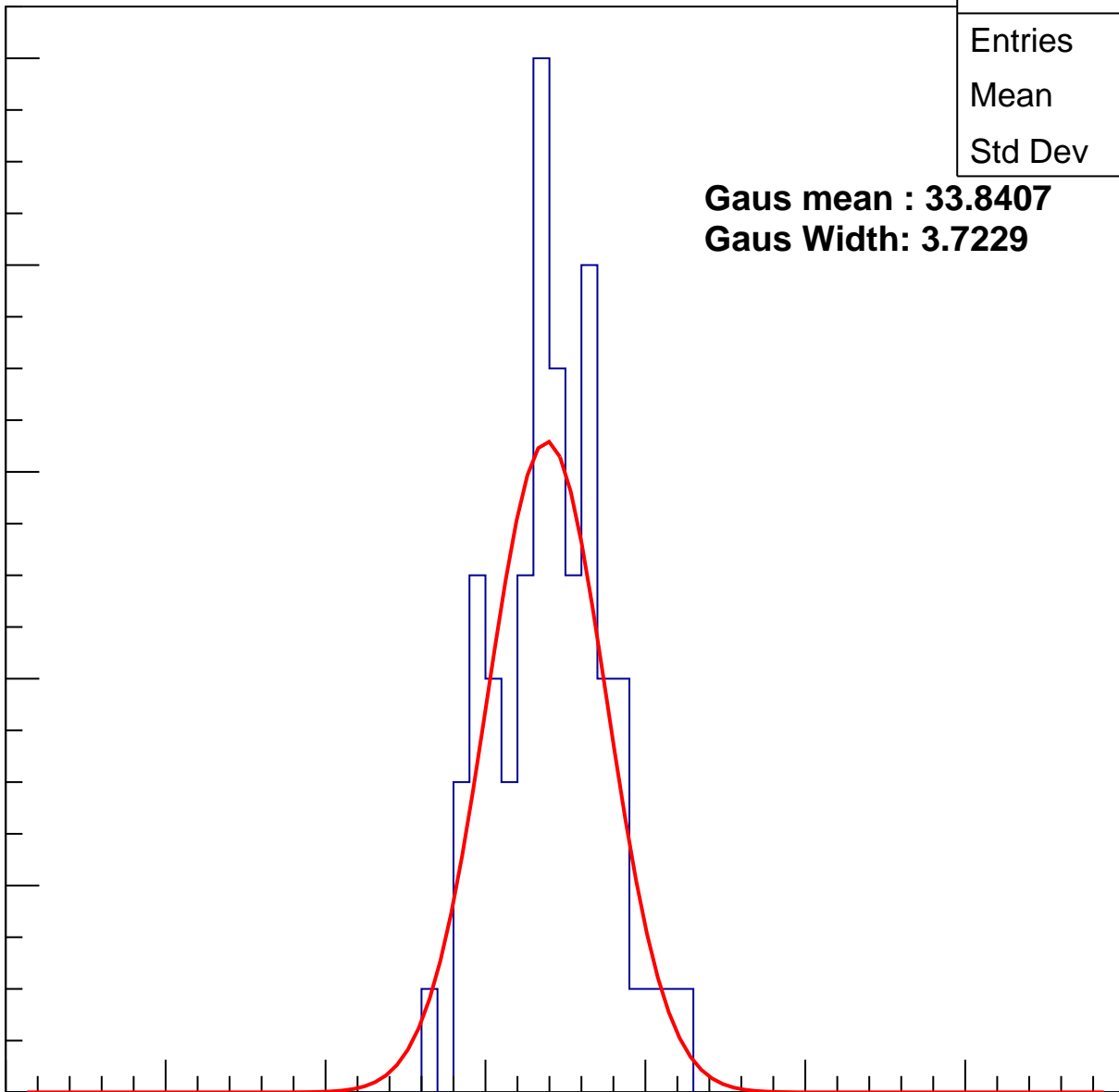
**Gaus Width: 3.7229**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch75, adc2

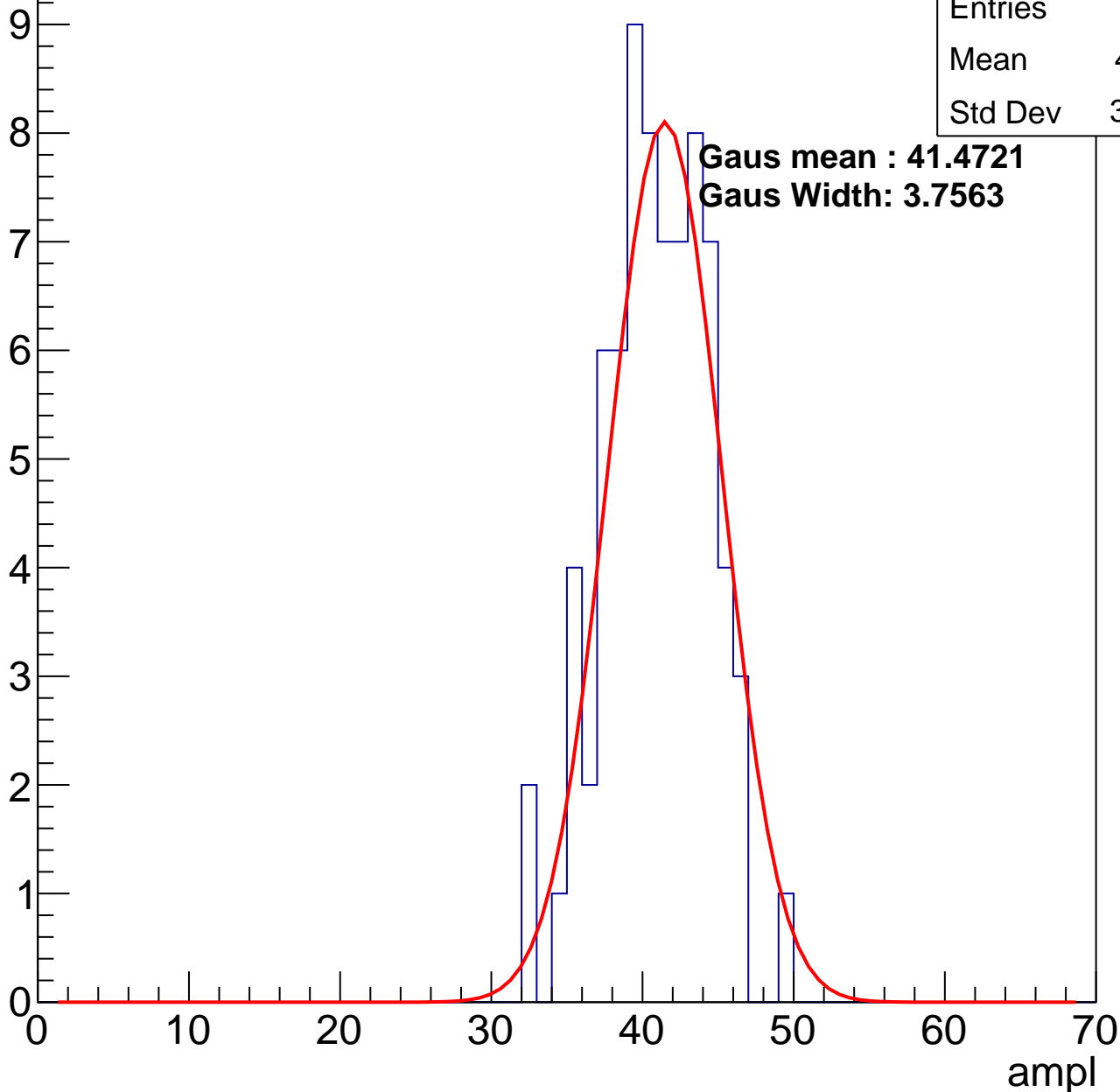
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	40.41
Std Dev	3.422

**Gaus mean : 41.4721**

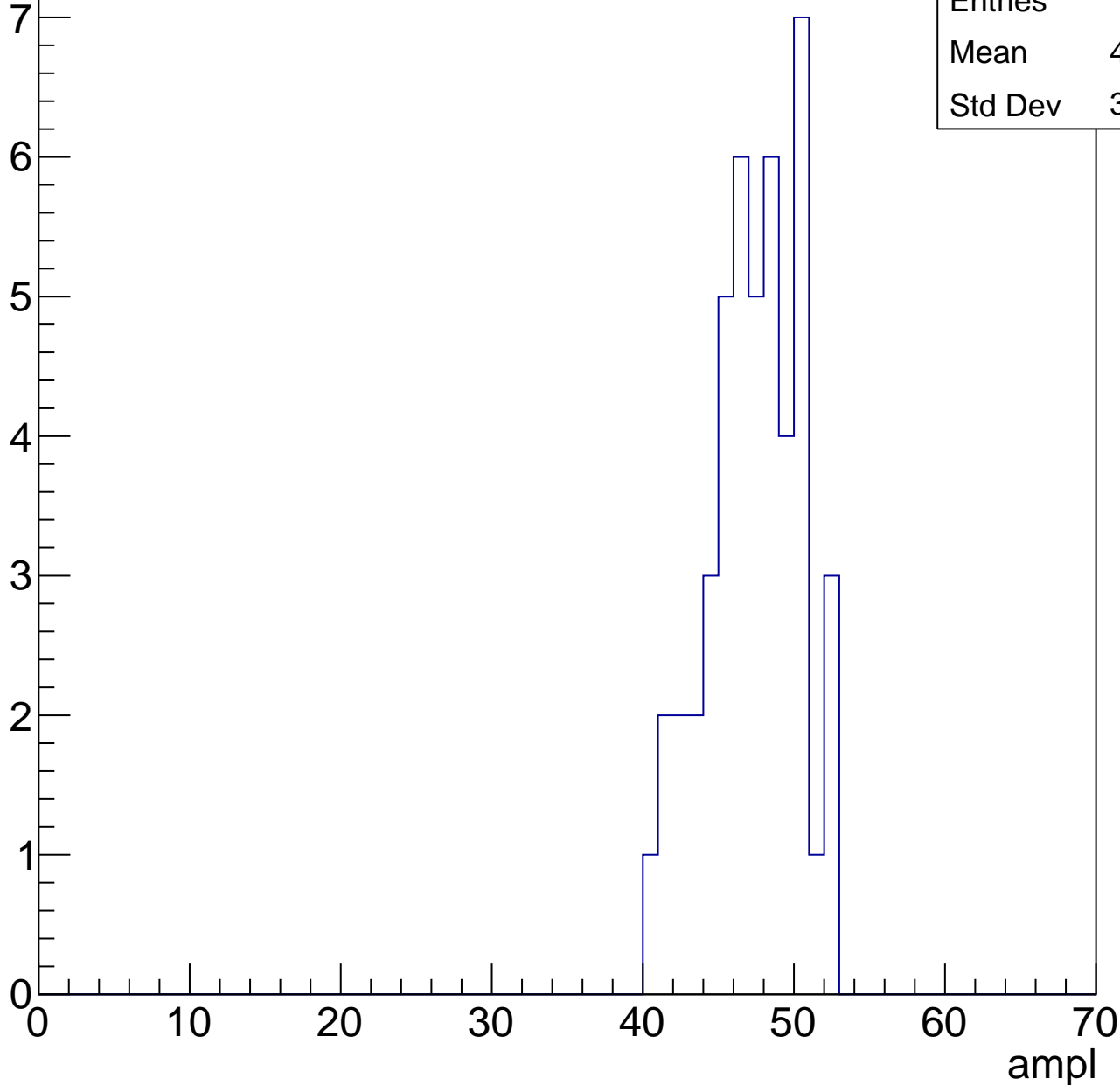
**Gaus Width: 3.7563**



# B1L002S, U18-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



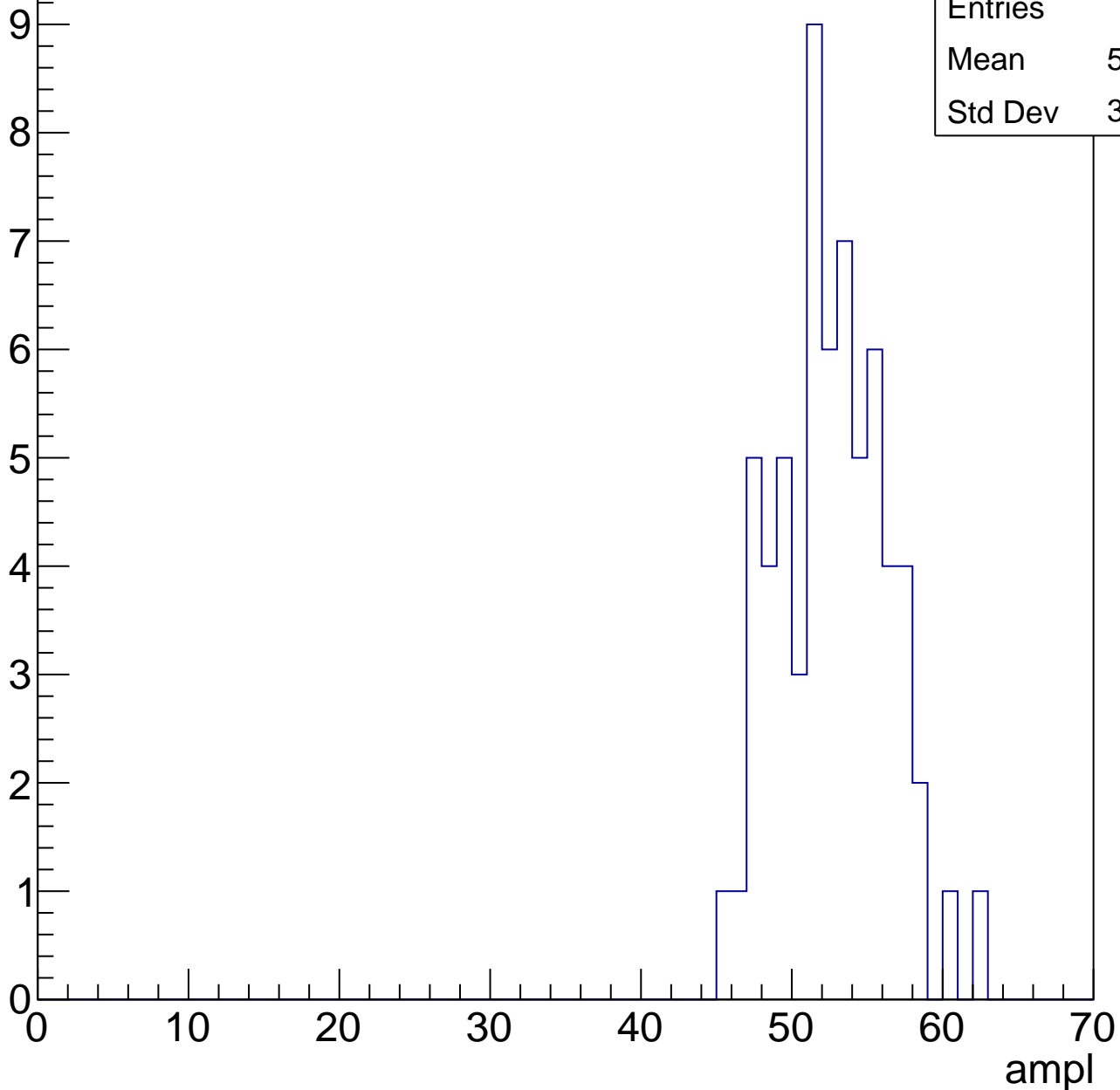
Entries	47
Mean	46.83
Std Dev	3.027

# B1L002S, U18-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	52.27
Std Dev	3.572

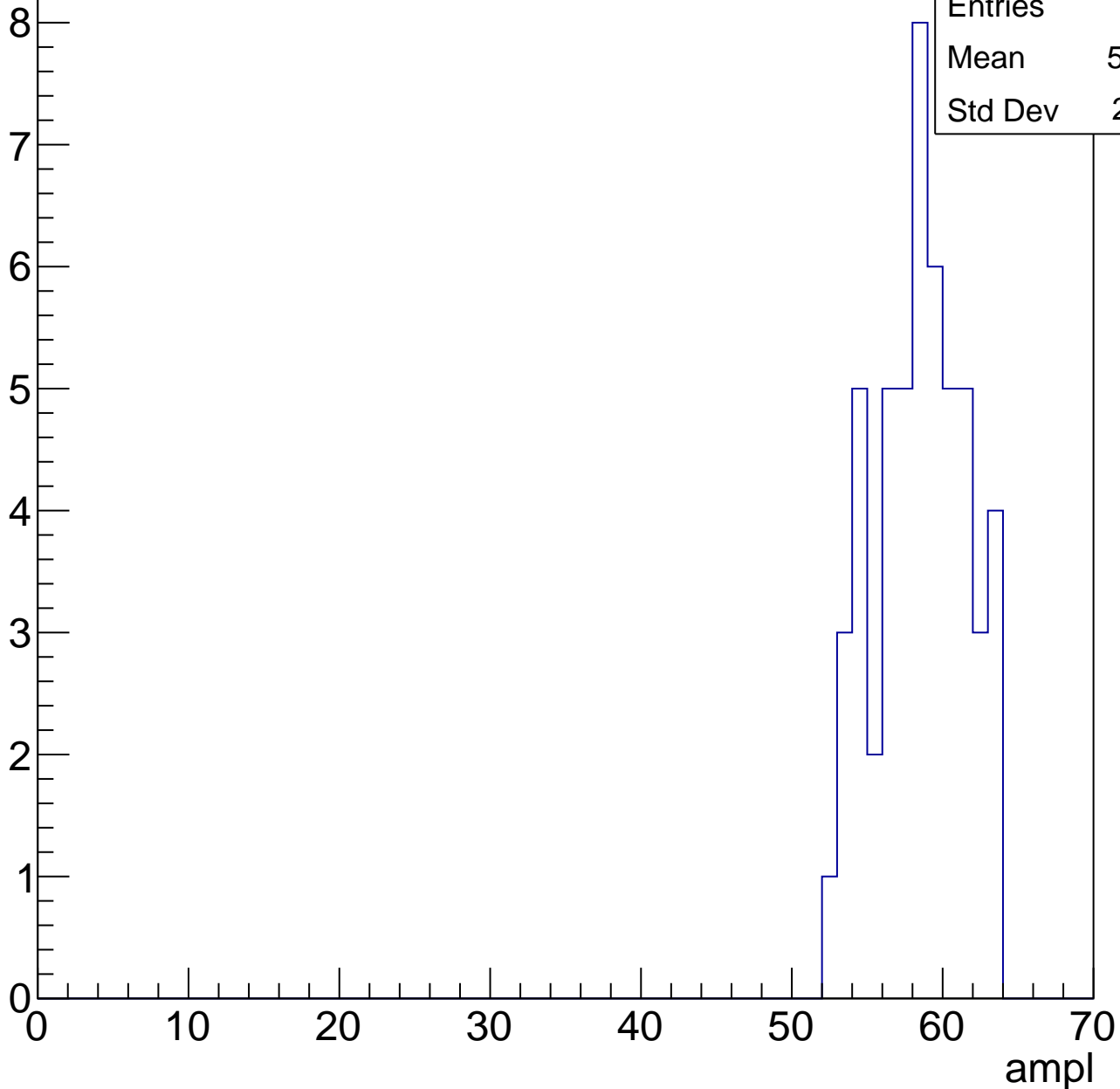


# B1L002S, U18-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.02
Std Dev	2.951

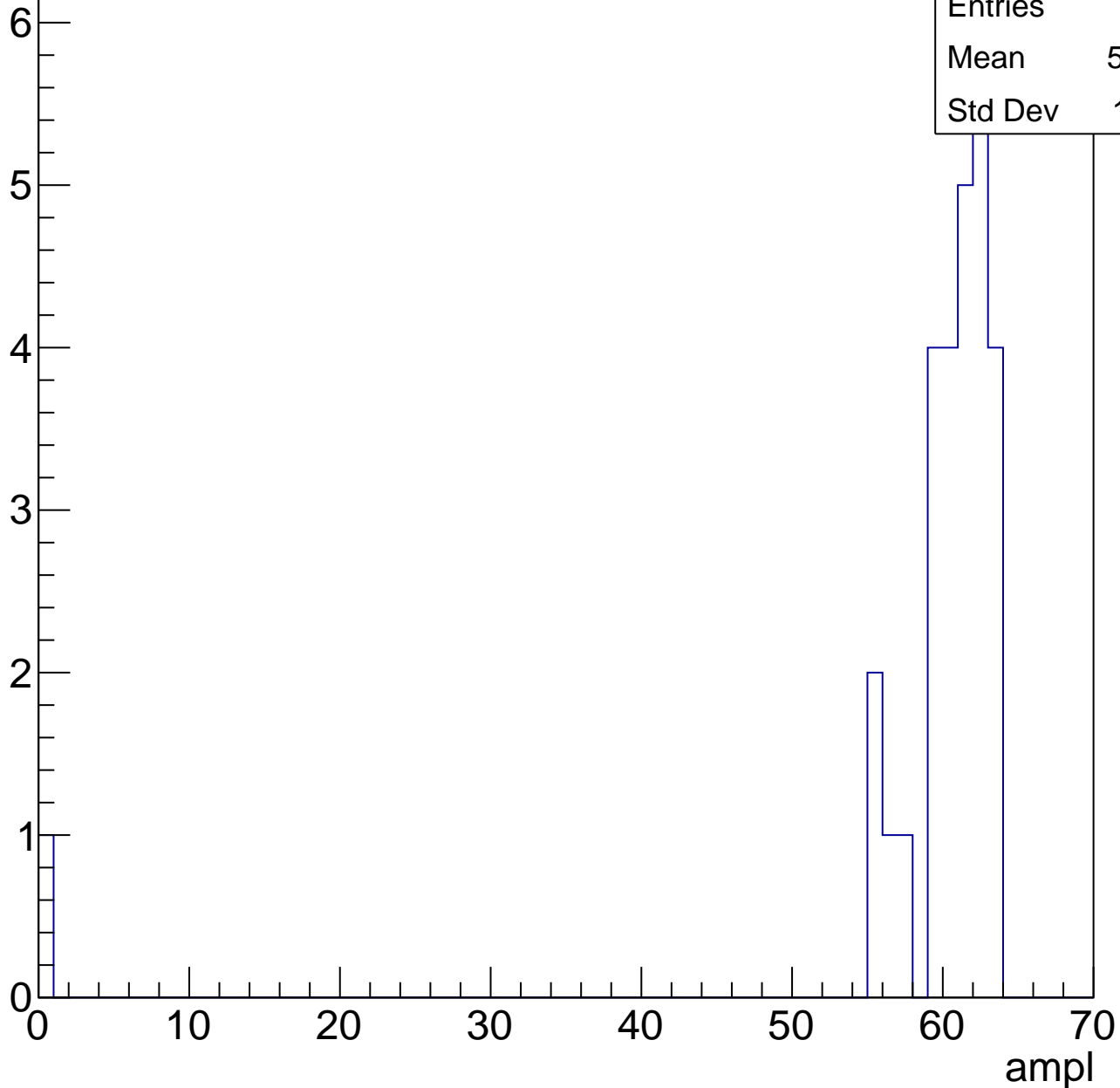


# B1L002S, U18-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.14
Std Dev	11.41





# B1L002S, U18-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch76, adc0

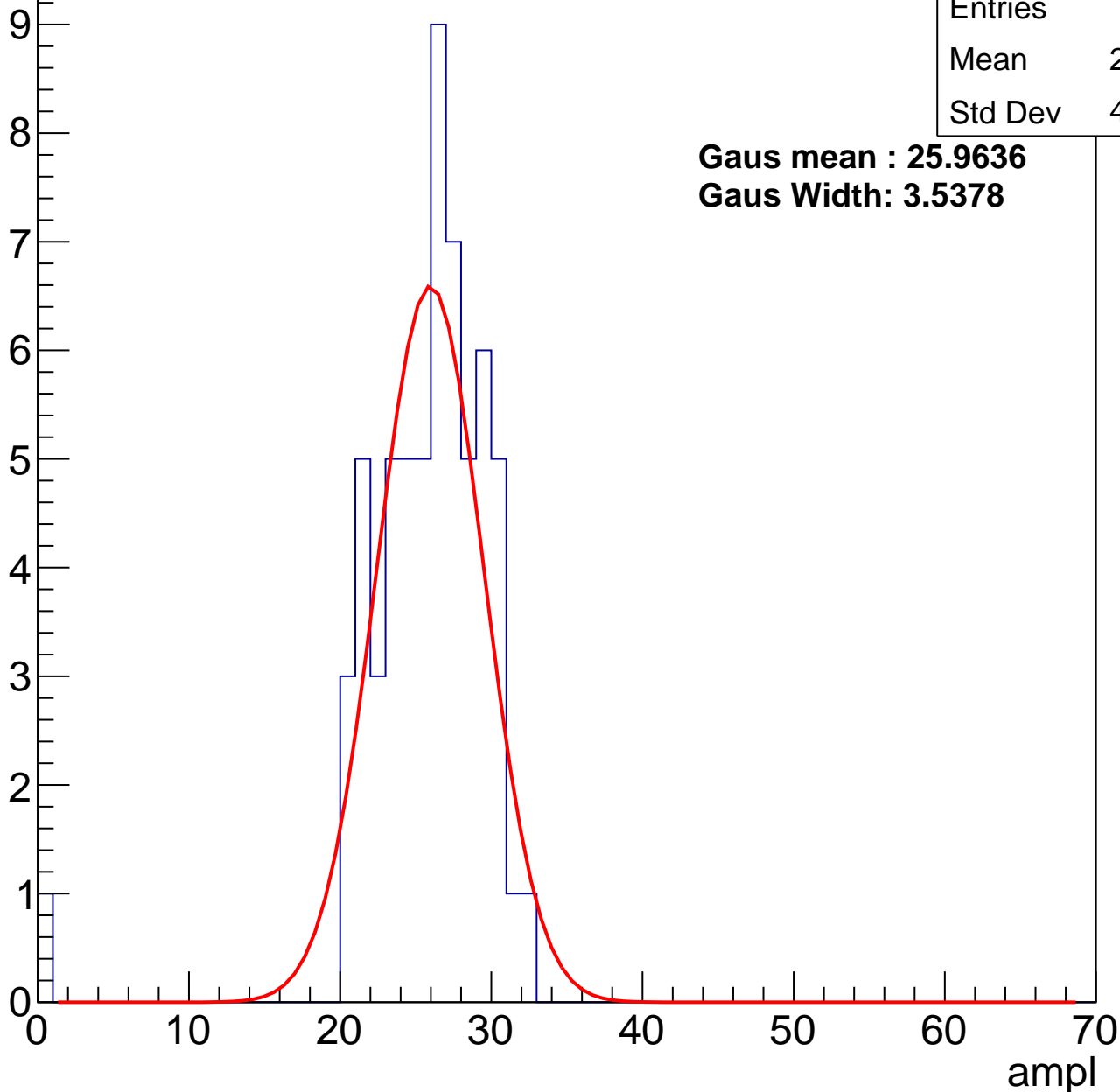
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	25.26
Std Dev	4.464

**Gaus mean : 25.9636**

**Gaus Width: 3.5378**



# B1L002S, U18-ch76, adc1

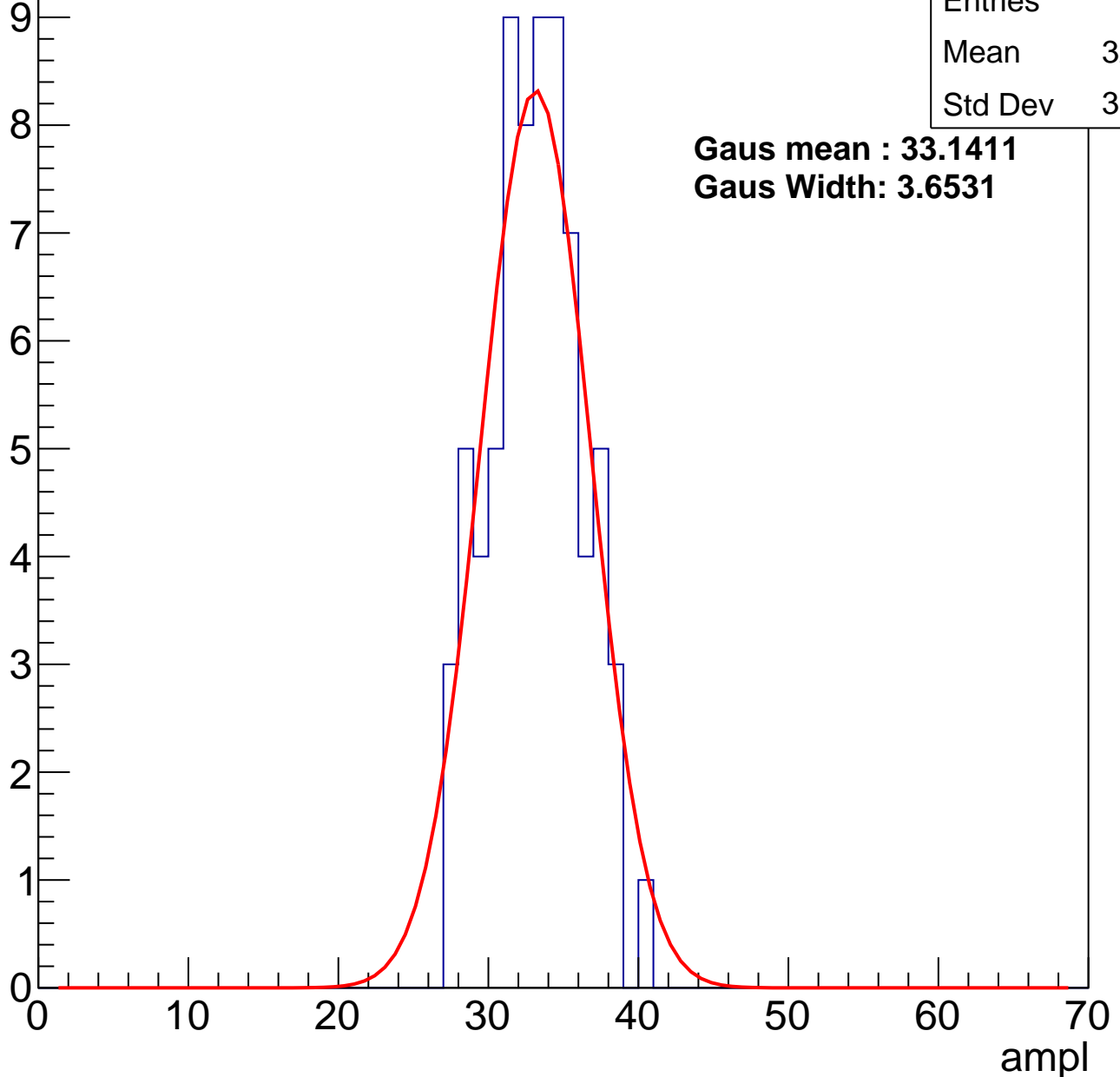
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	32.68
Std Dev	3.018

**Gaus mean : 33.1411**

**Gaus Width: 3.6531**



# B1L002S, U18-ch76, adc2

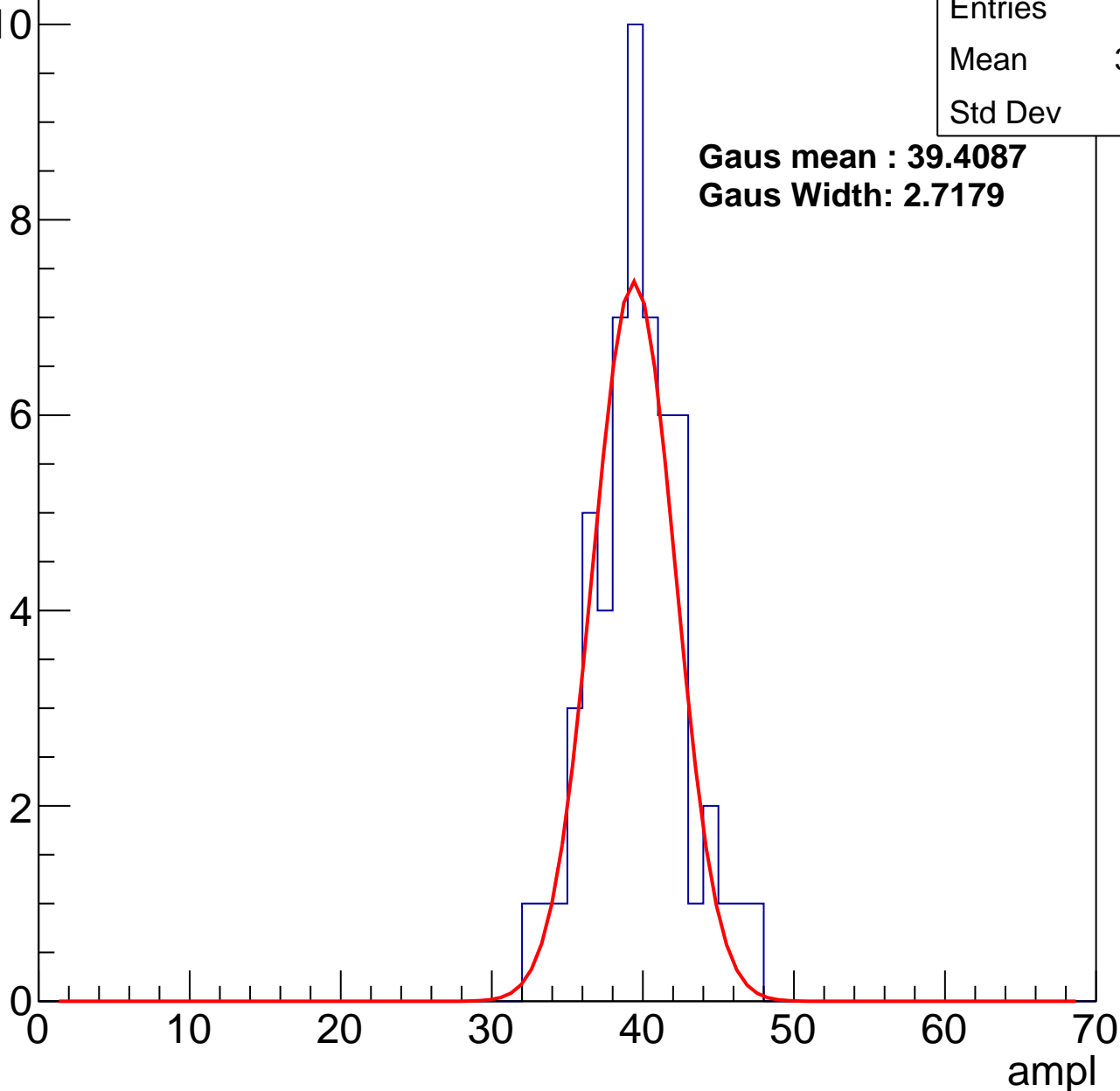
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.21
Std Dev	3.03

**Gaus mean : 39.4087**

**Gaus Width: 2.7179**

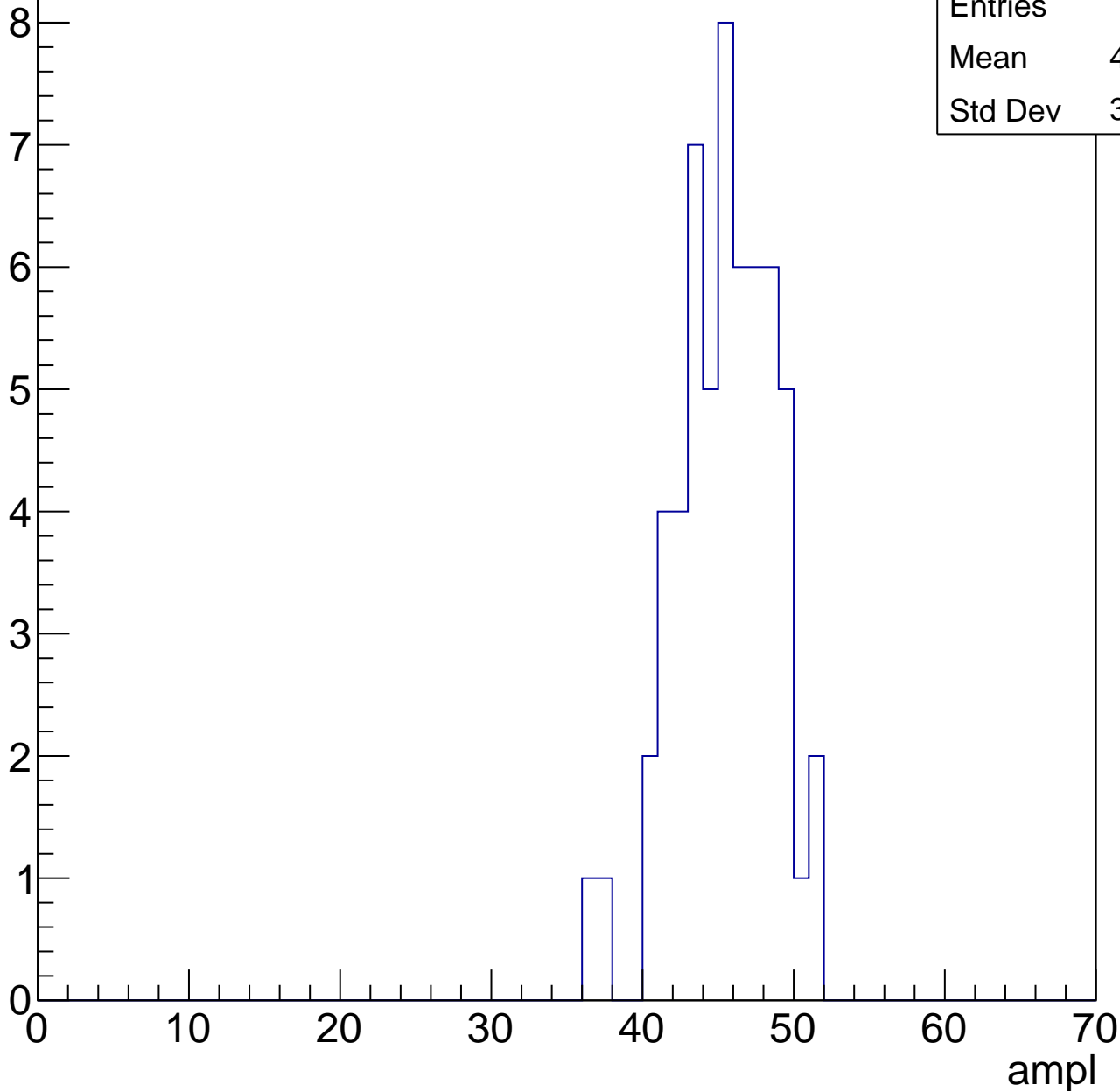


# B1L002S, U18-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

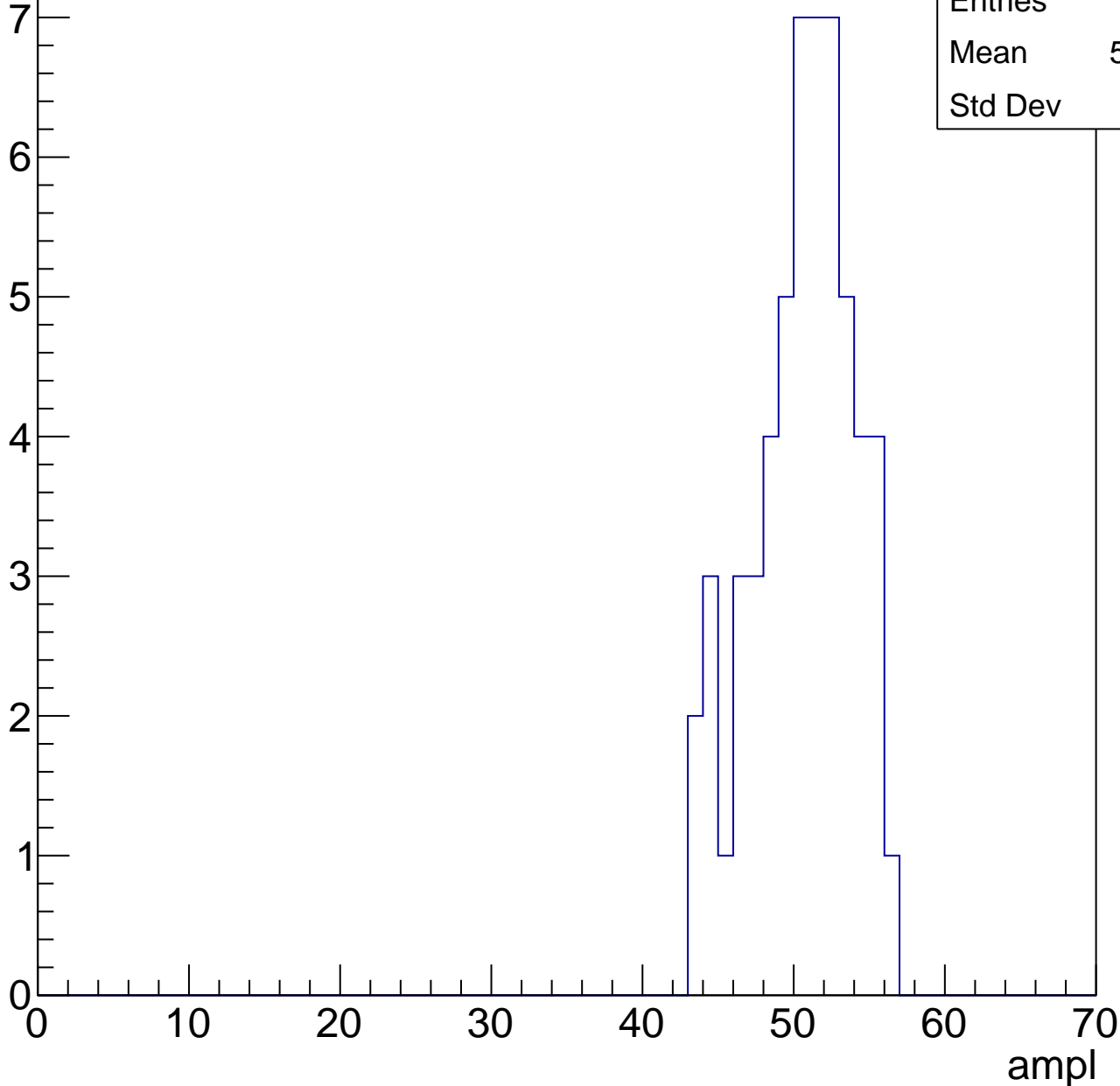
Entries	58
Mean	44.98
Std Dev	3.187



# B1L002S, U18-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



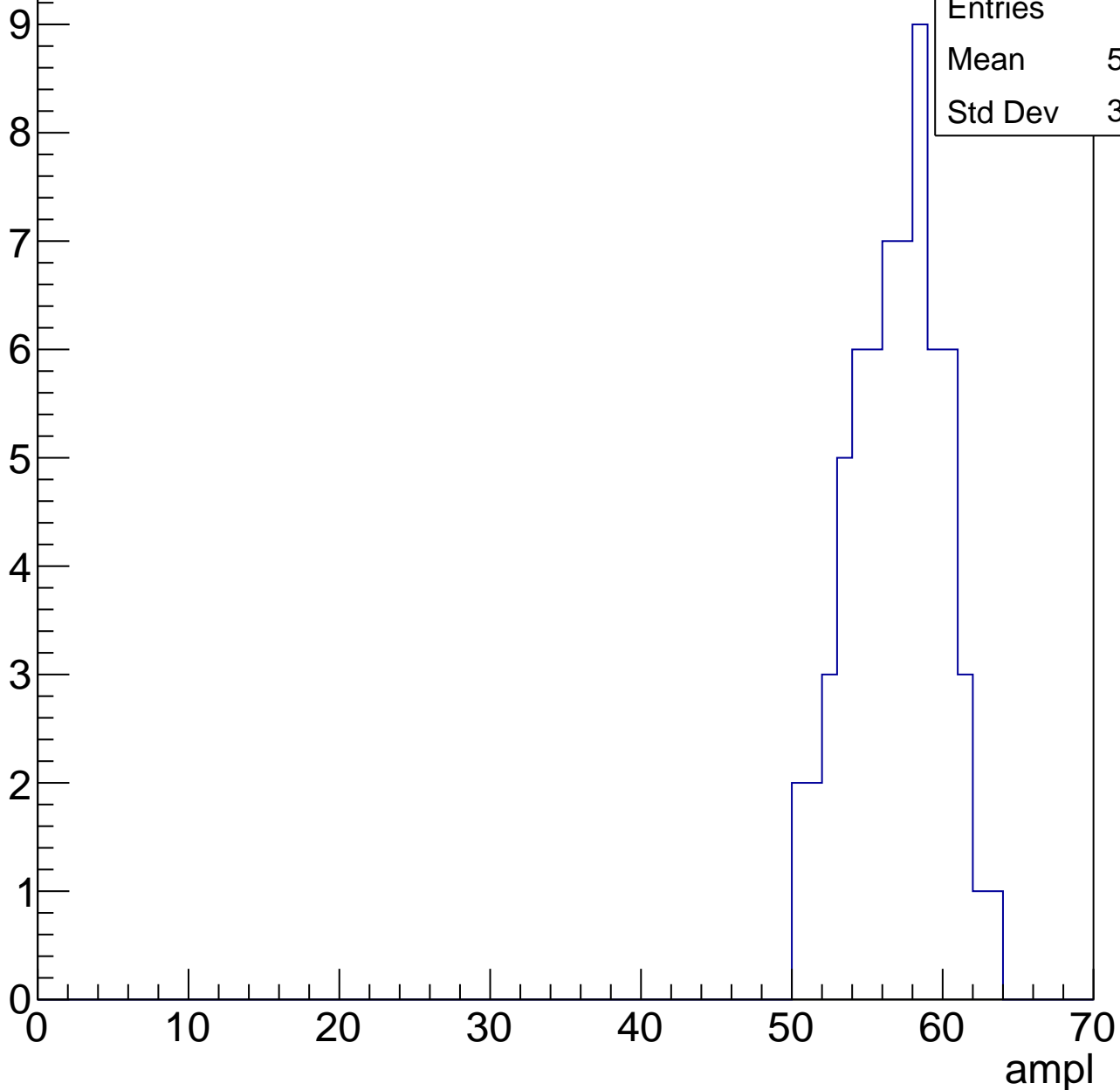
Entries	56
Mean	50.12
Std Dev	3.29

# B1L002S, U18-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	56.44
Std Dev	3.025

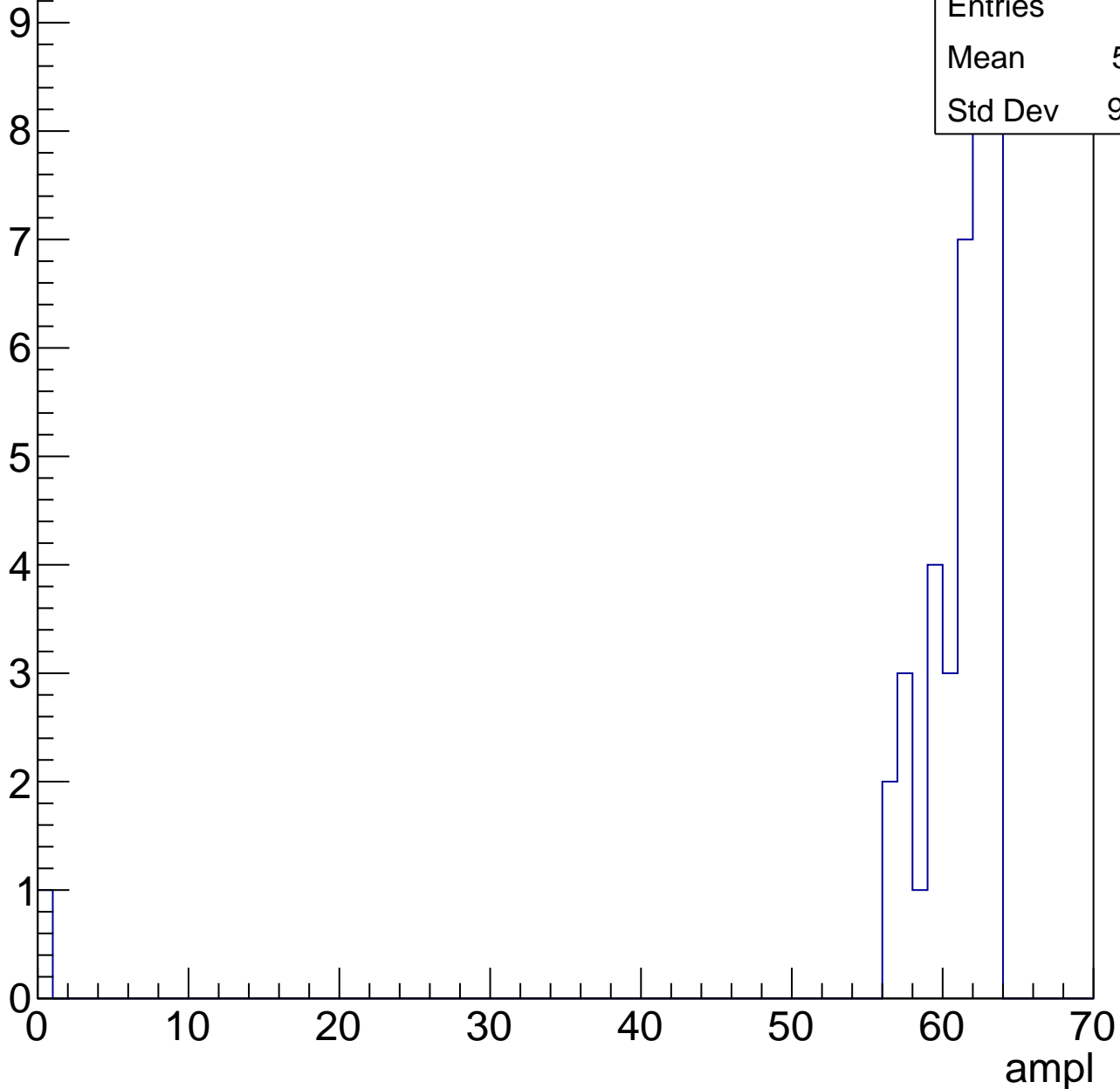


# B1L002S, U18-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	59.21
Std Dev	9.827

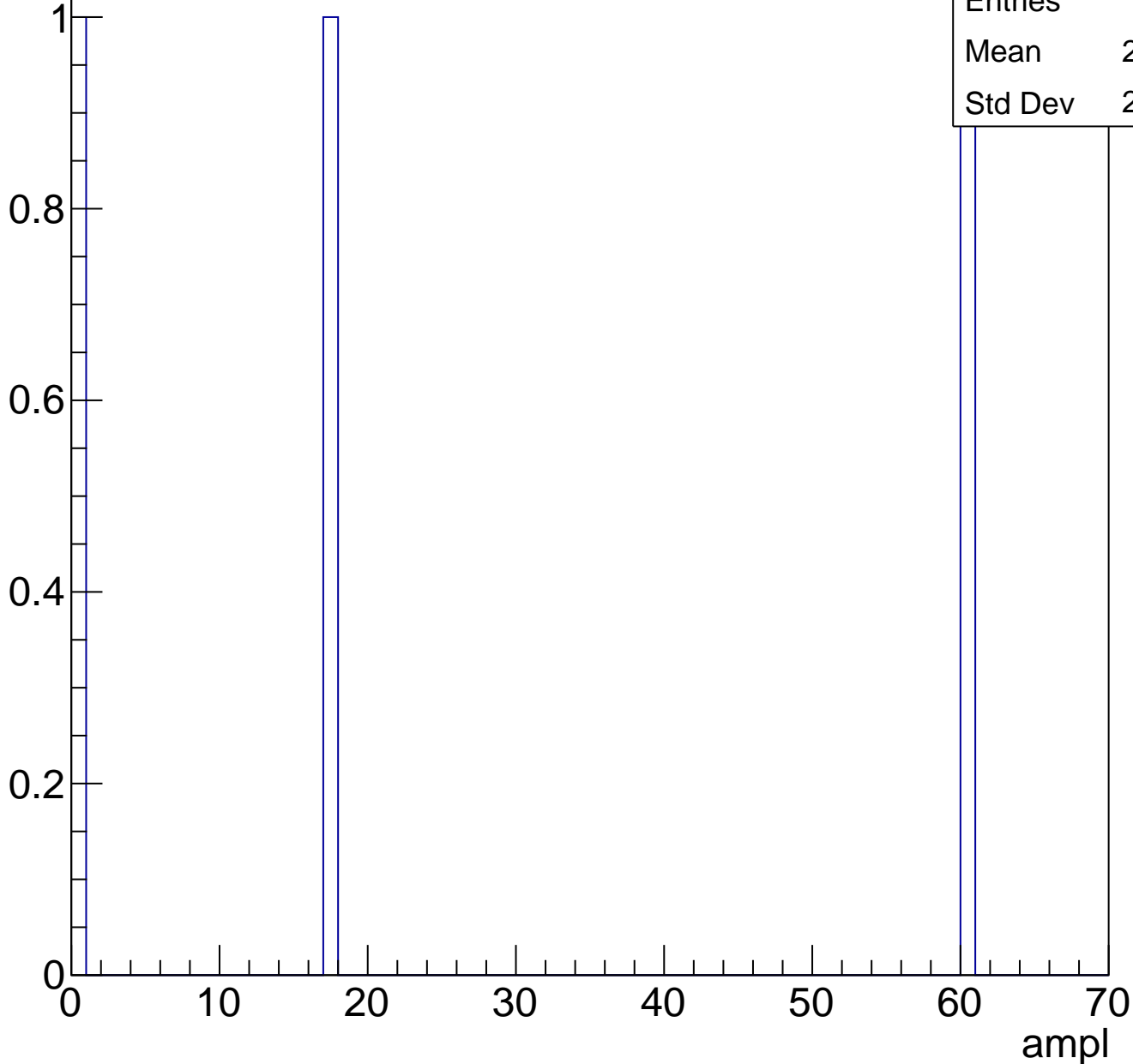




# B1L002S, U18-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27.65
Std Dev	3.125

**Gaus mean : 28.0683**

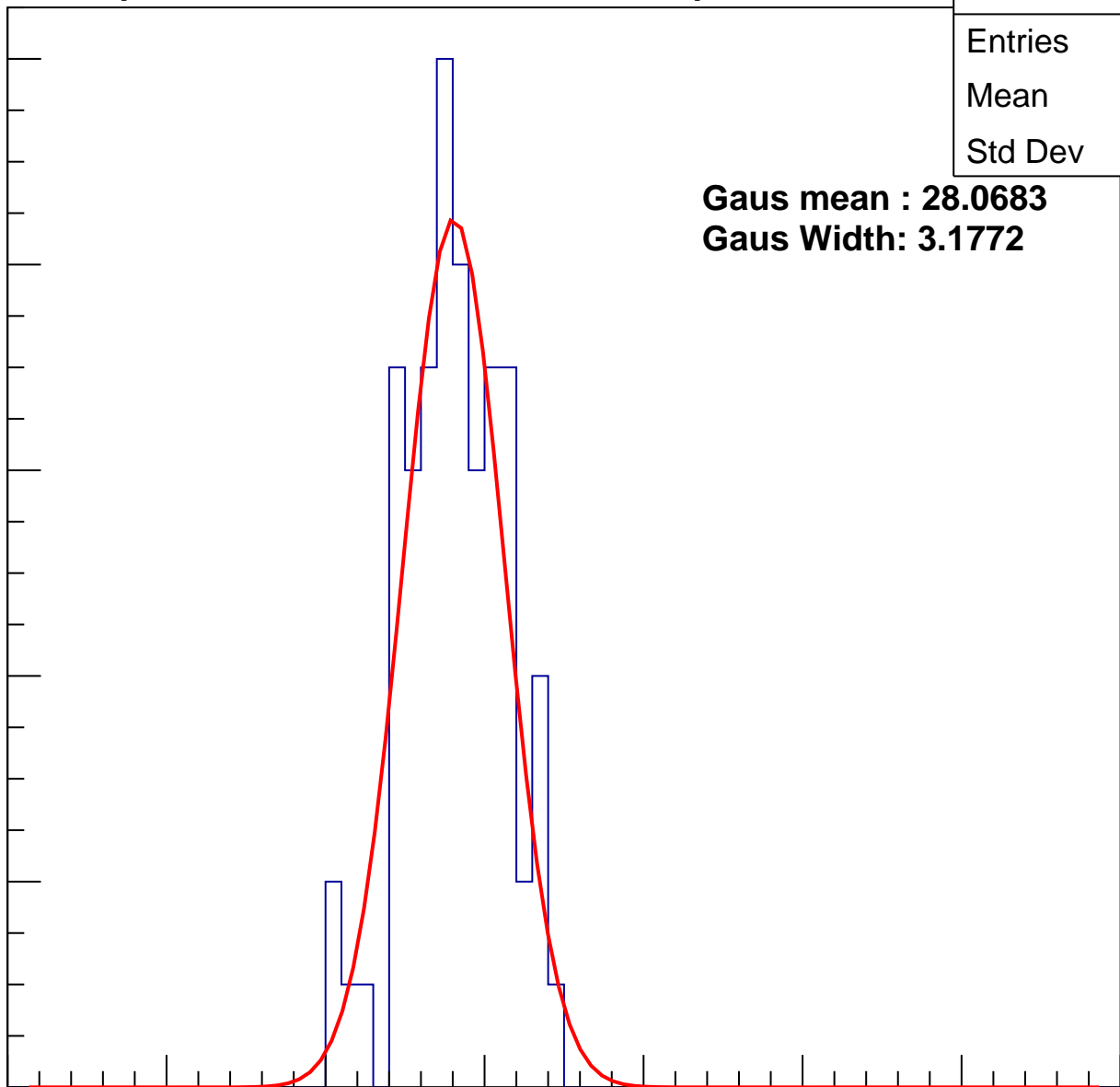
**Gaus Width: 3.1772**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch77, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	34.25
Std Dev	3.388

**Gaus mean : 35.0508**

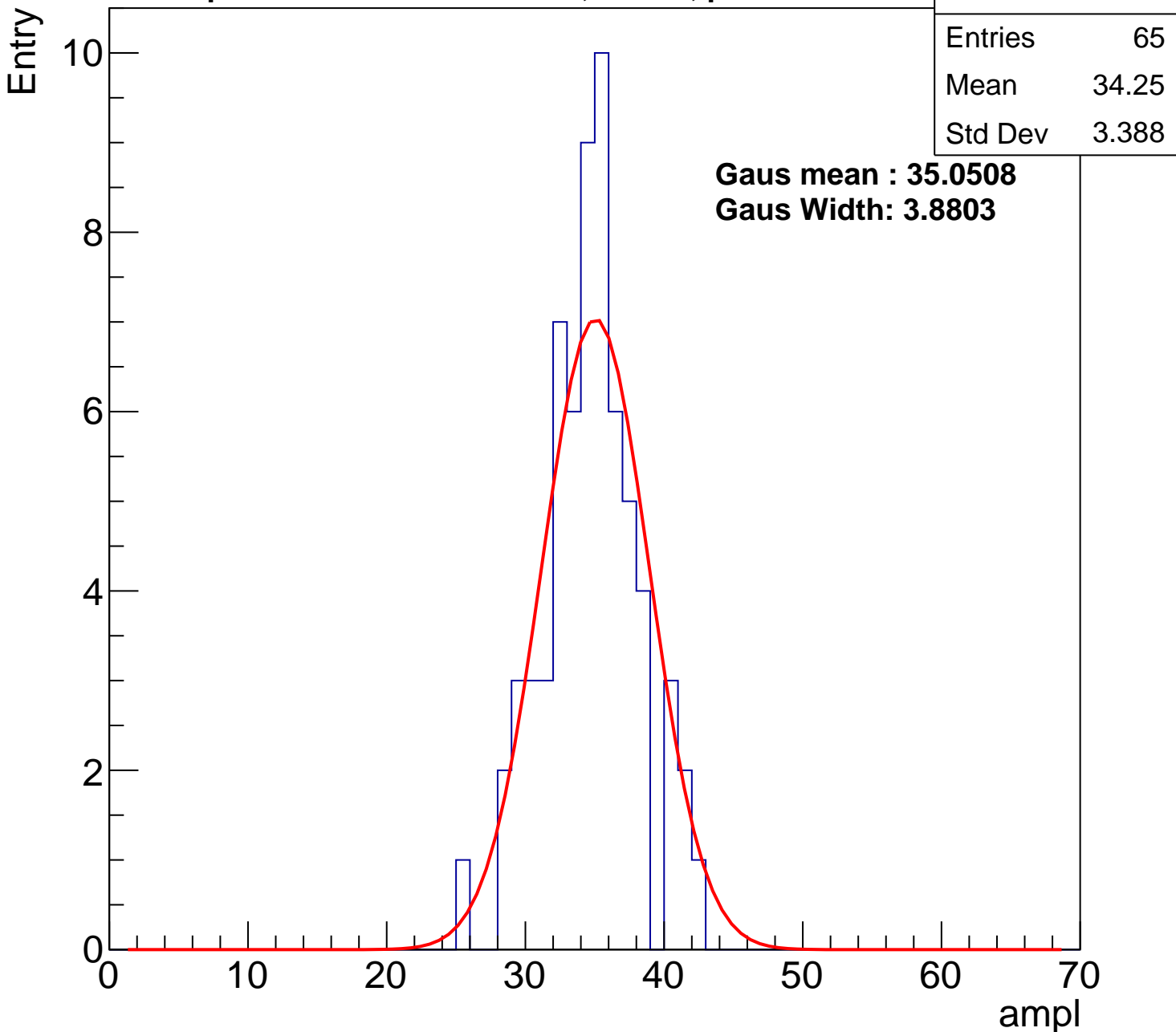
**Gaus Width: 3.8803**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch77, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	40.48
Std Dev	2.933

**Gaus mean : 40.5439**

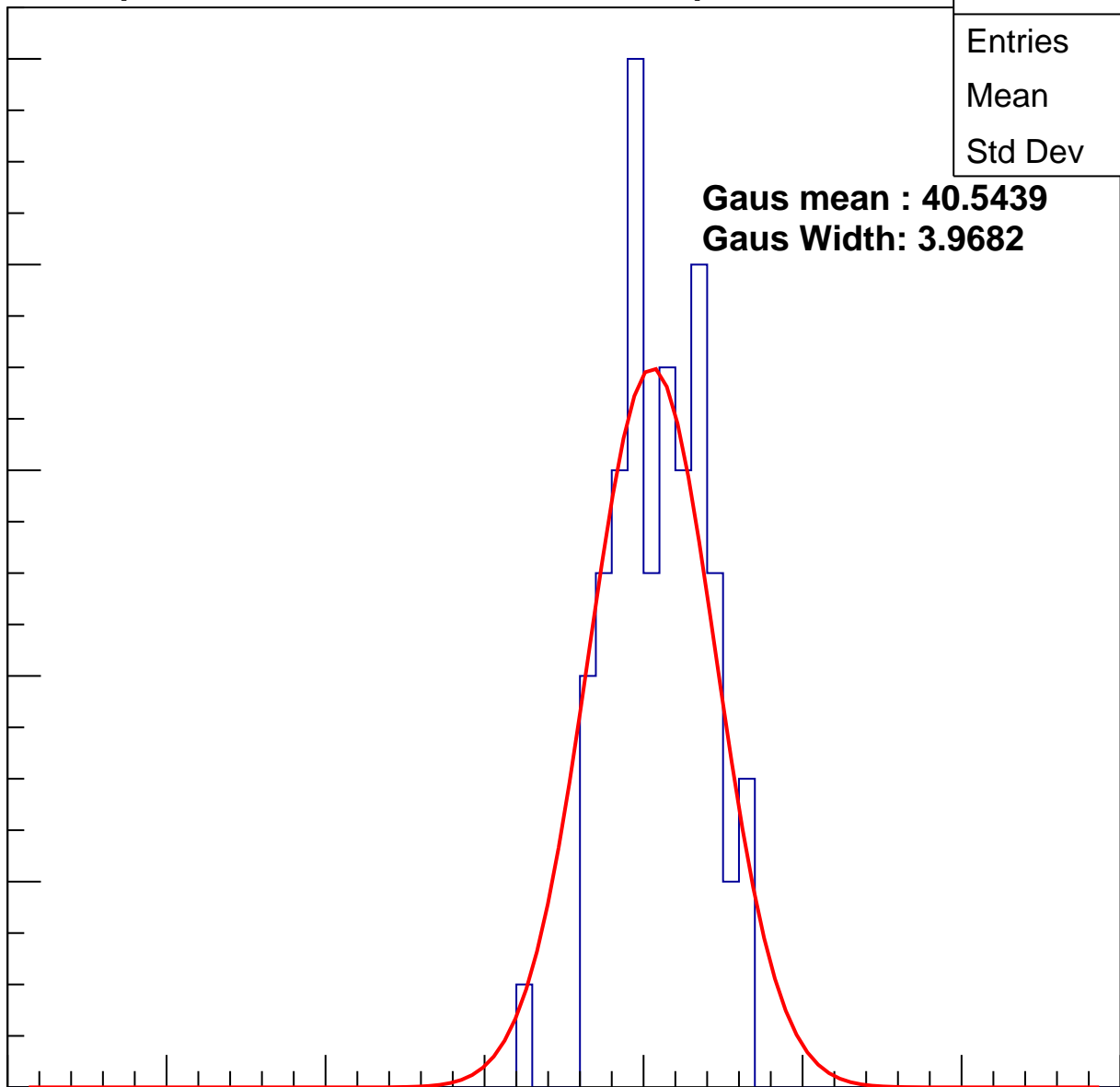
**Gaus Width: 3.9682**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

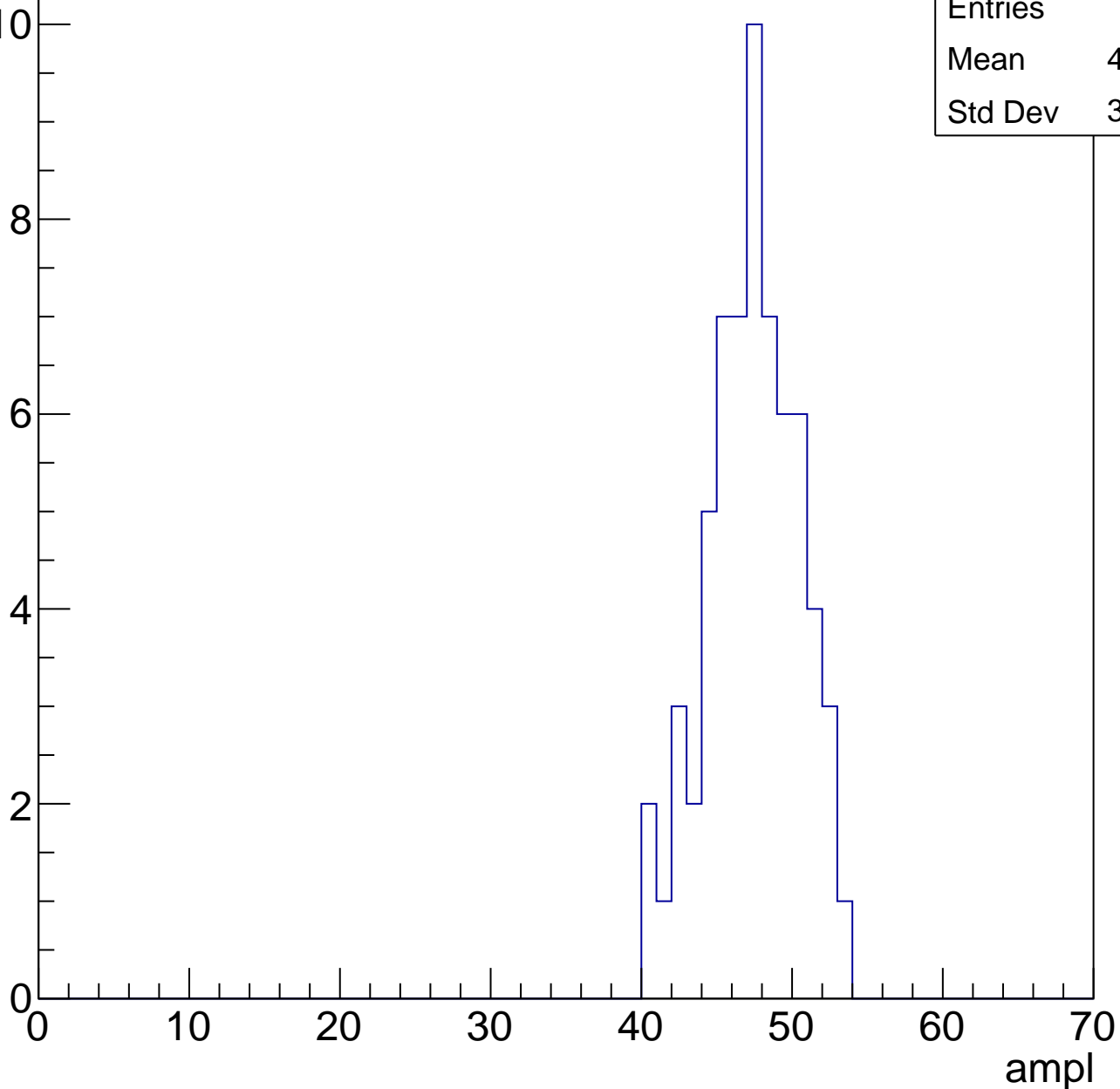


# B1L002S, U18-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.92
Std Dev	3.012

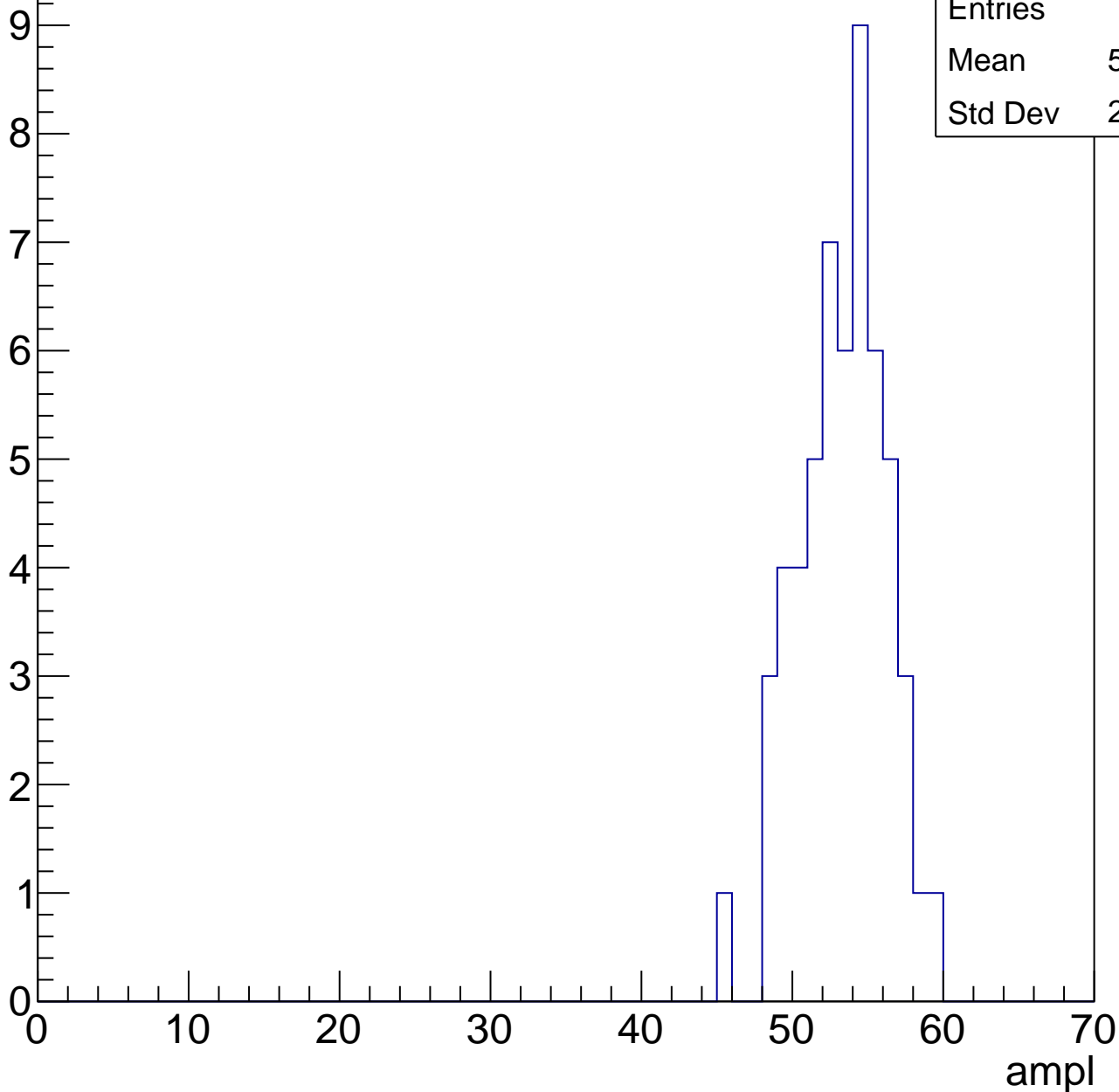


# B1L002S, U18-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.84
Std Dev	2.865

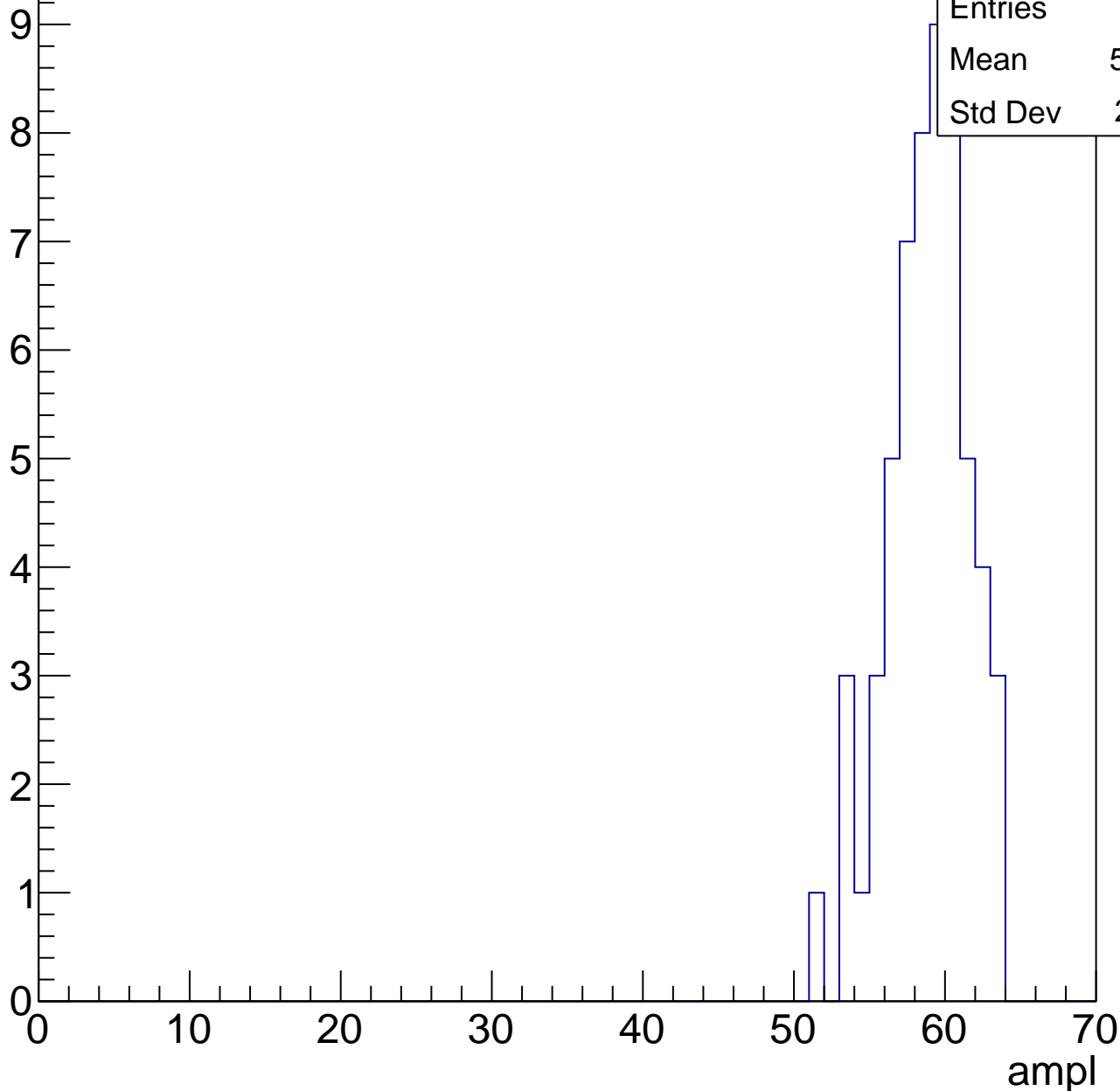


# B1L002S, U18-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	58.33
Std Dev	2.691

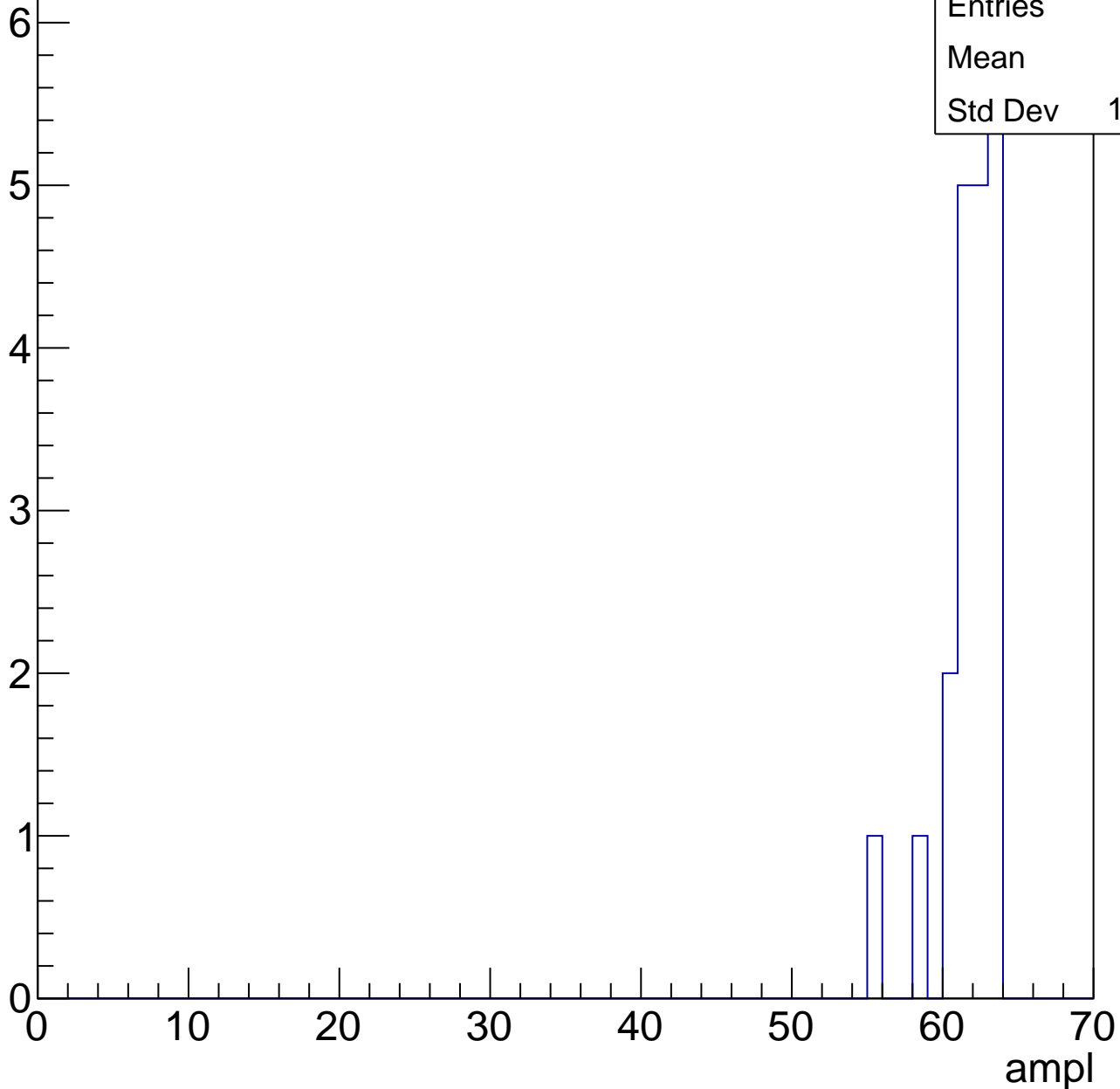


# B1L002S, U18-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.3
Std Dev	1.926





# B1L002S, U18-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch78, adc0

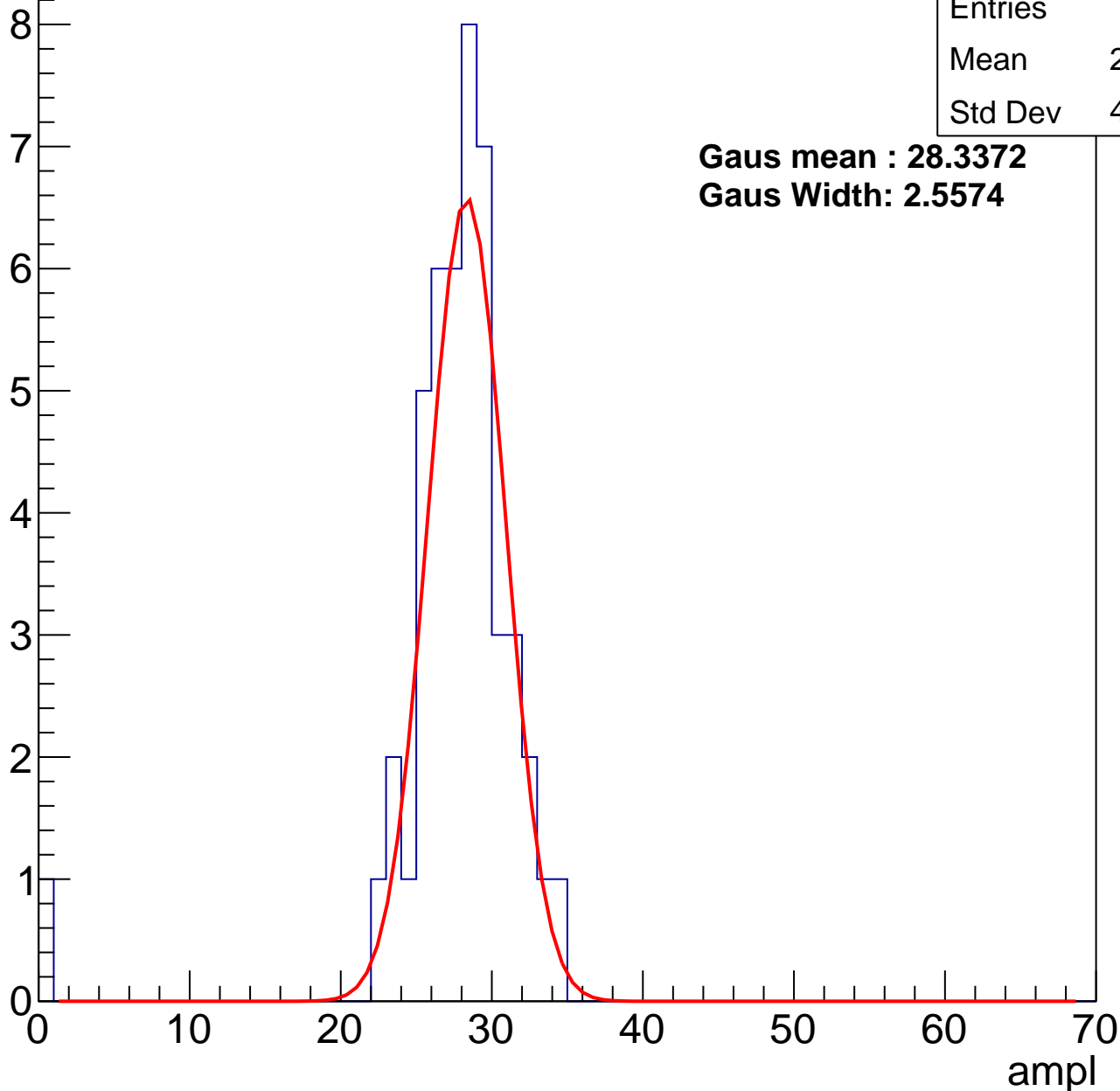
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	27.15
Std Dev	4.762

**Gaus mean : 28.3372**

**Gaus Width: 2.5574**



# B1L002S, U18-ch78, adc1

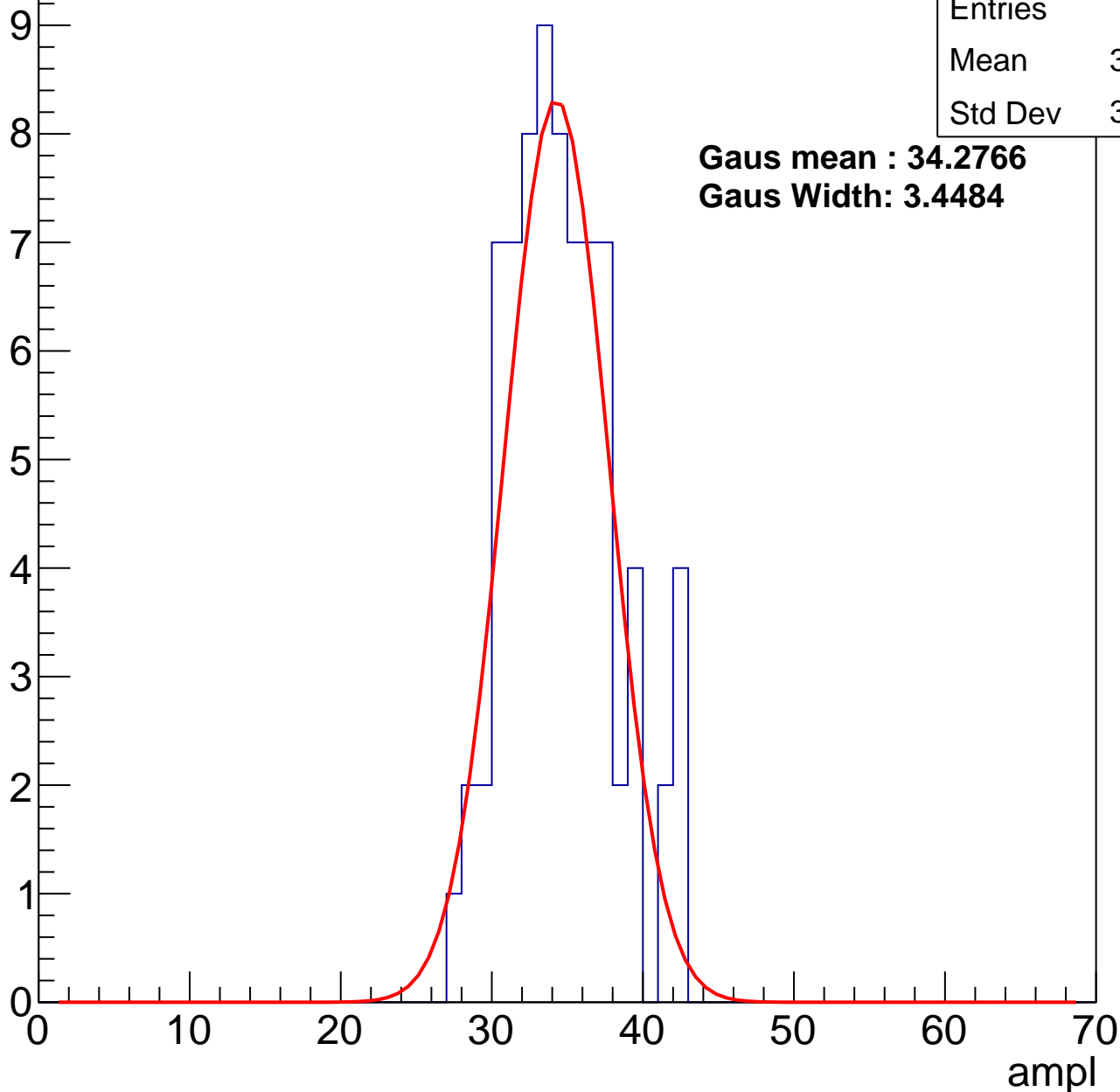
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	34.17
Std Dev	3.547

**Gaus mean : 34.2766**

**Gaus Width: 3.4484**



# B1L002S, U18-ch78, adc2

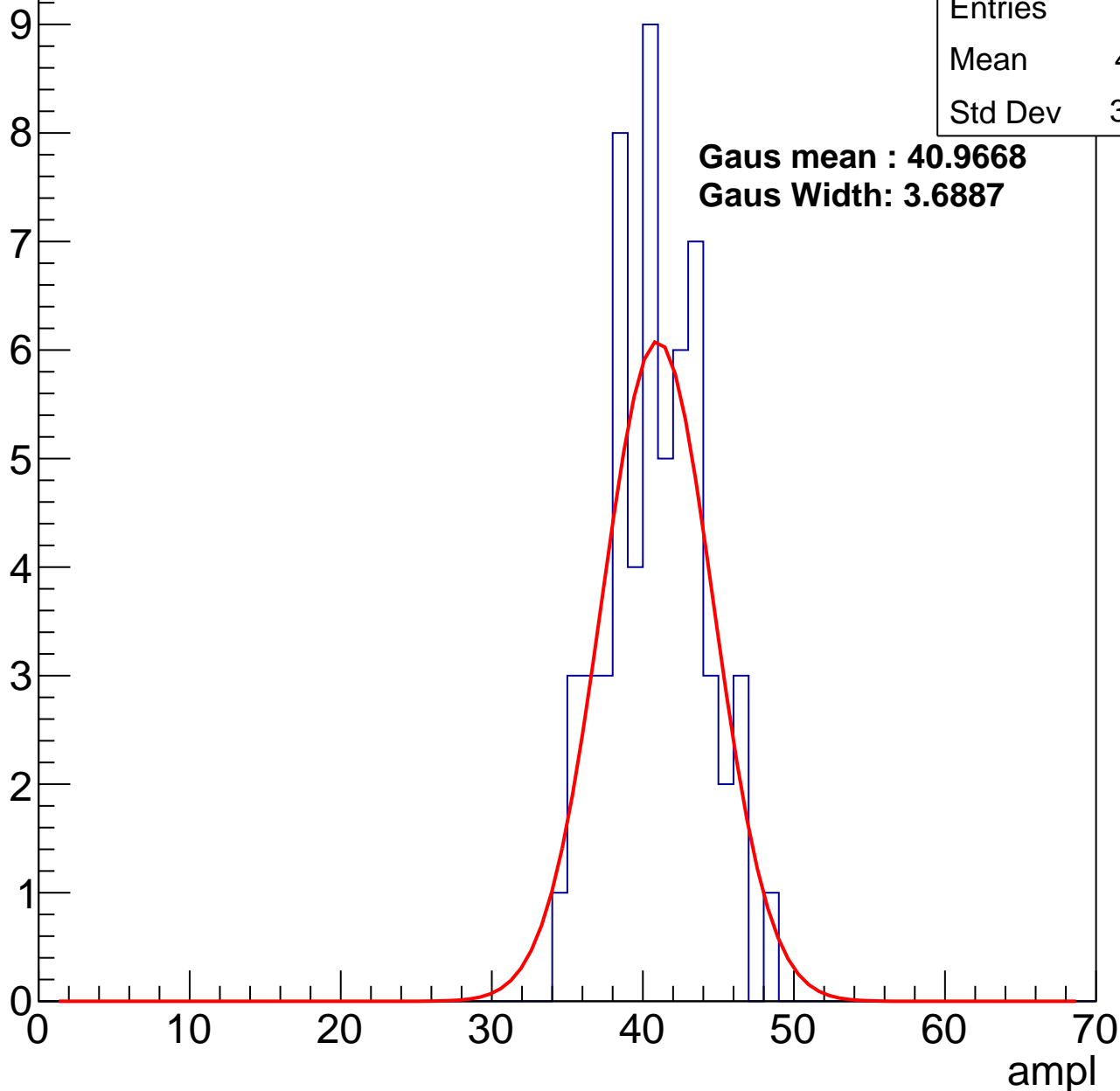
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.41
Std Dev	3.146

**Gaus mean : 40.9668**

**Gaus Width: 3.6887**

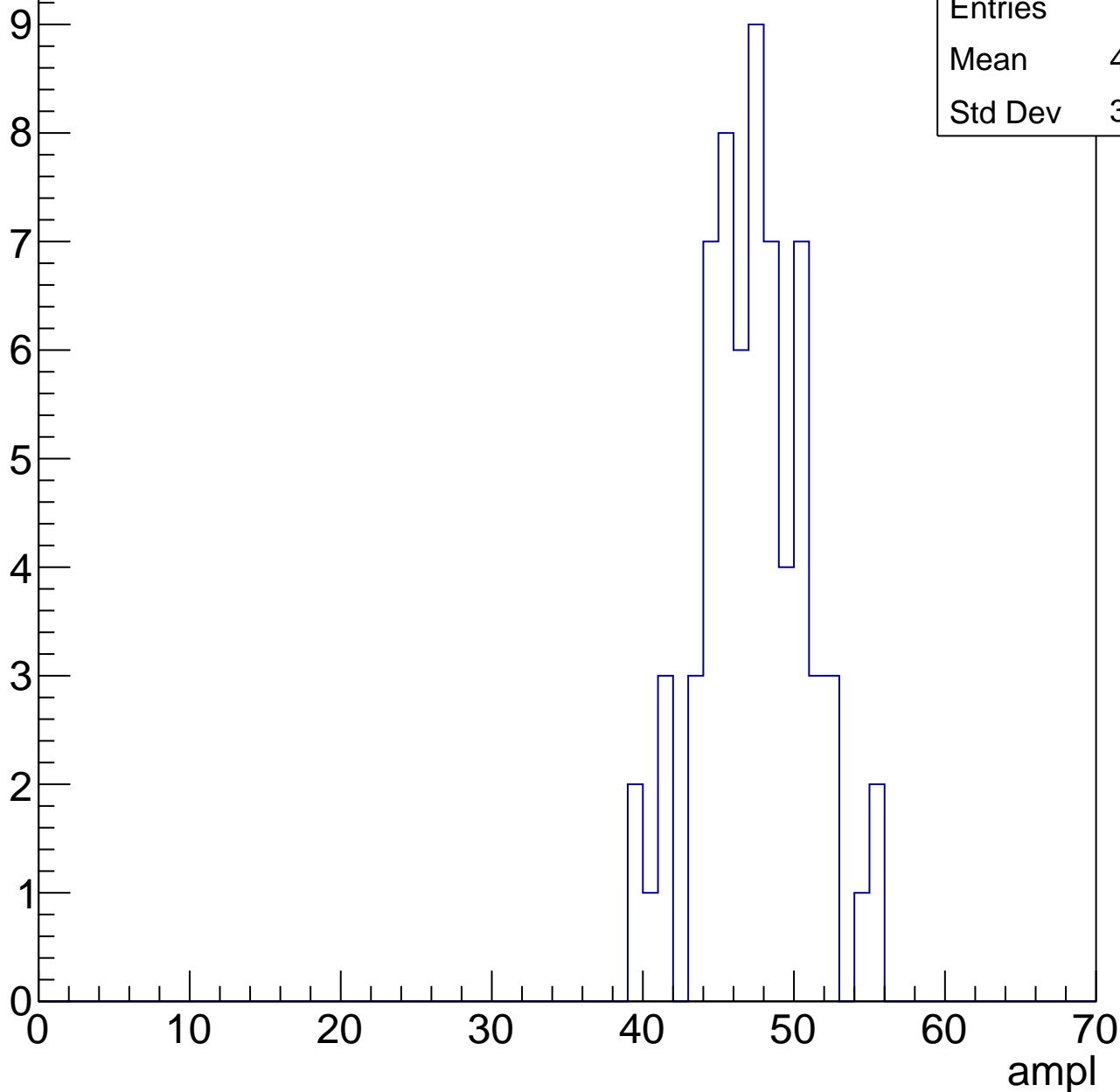


# B1L002S, U18-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.85
Std Dev	3.522

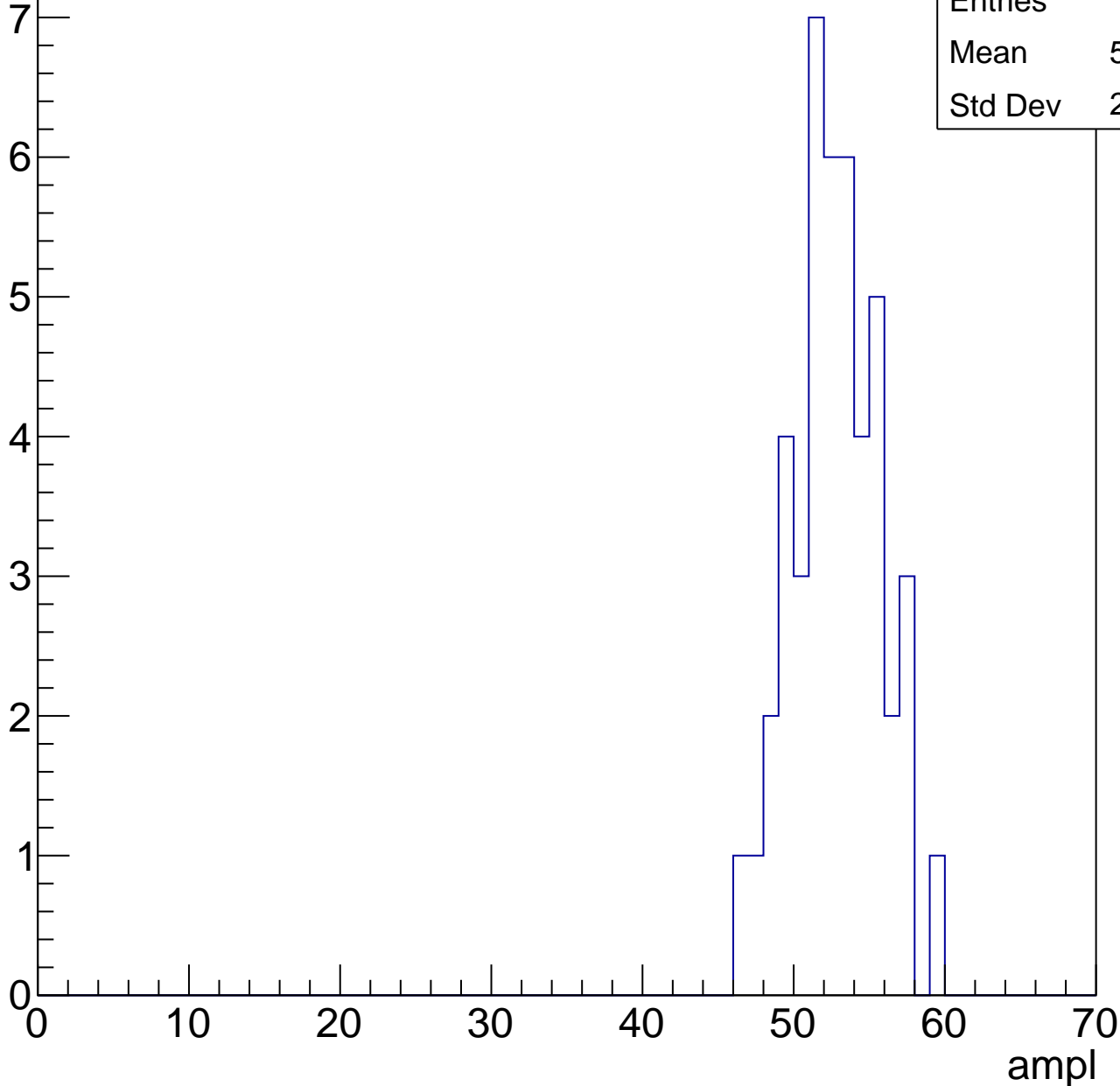


# B1L002S, U18-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

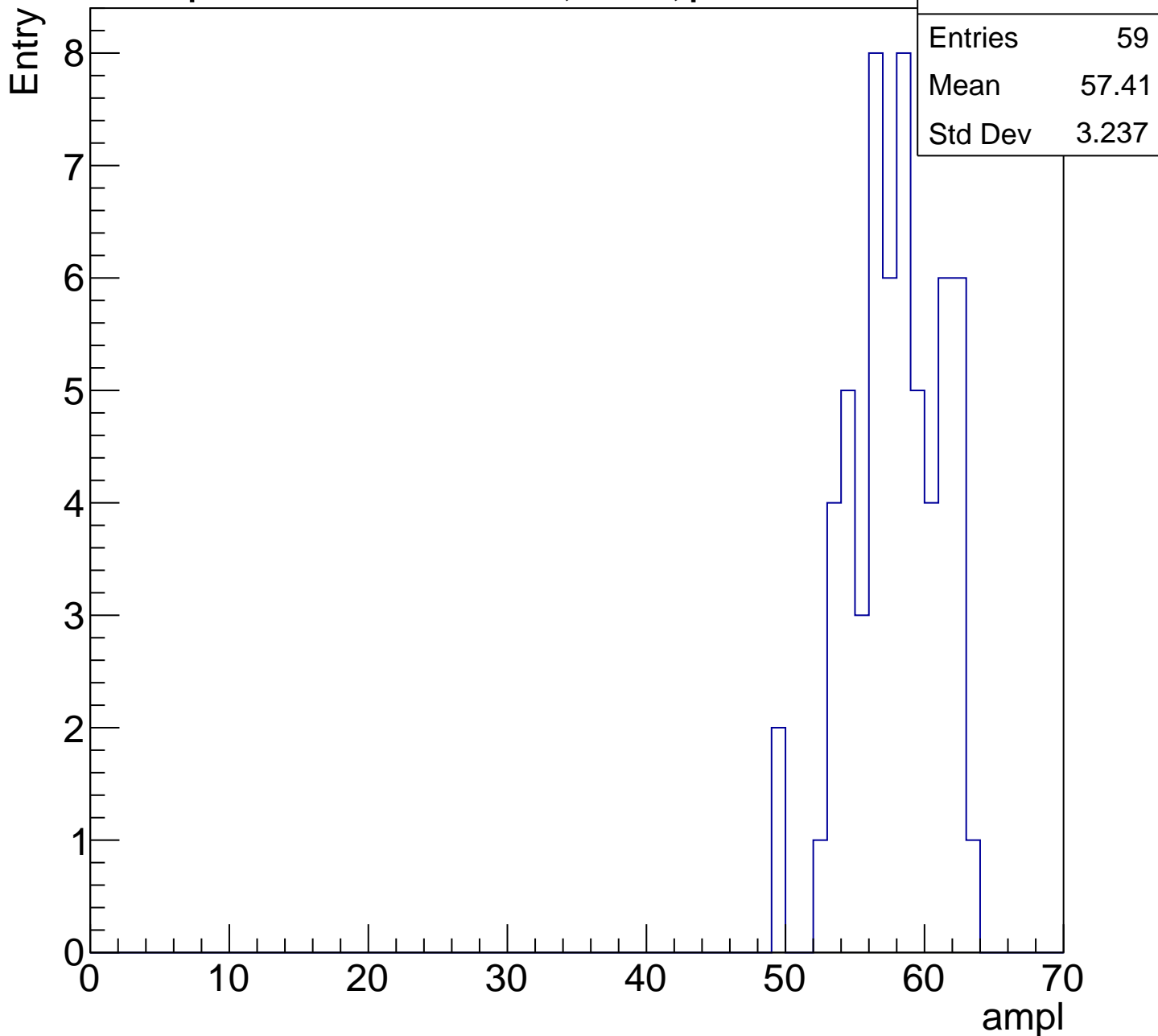
Entry

Entries	45
Mean	52.33
Std Dev	2.852



# B1L002S, U18-ch78, adc5

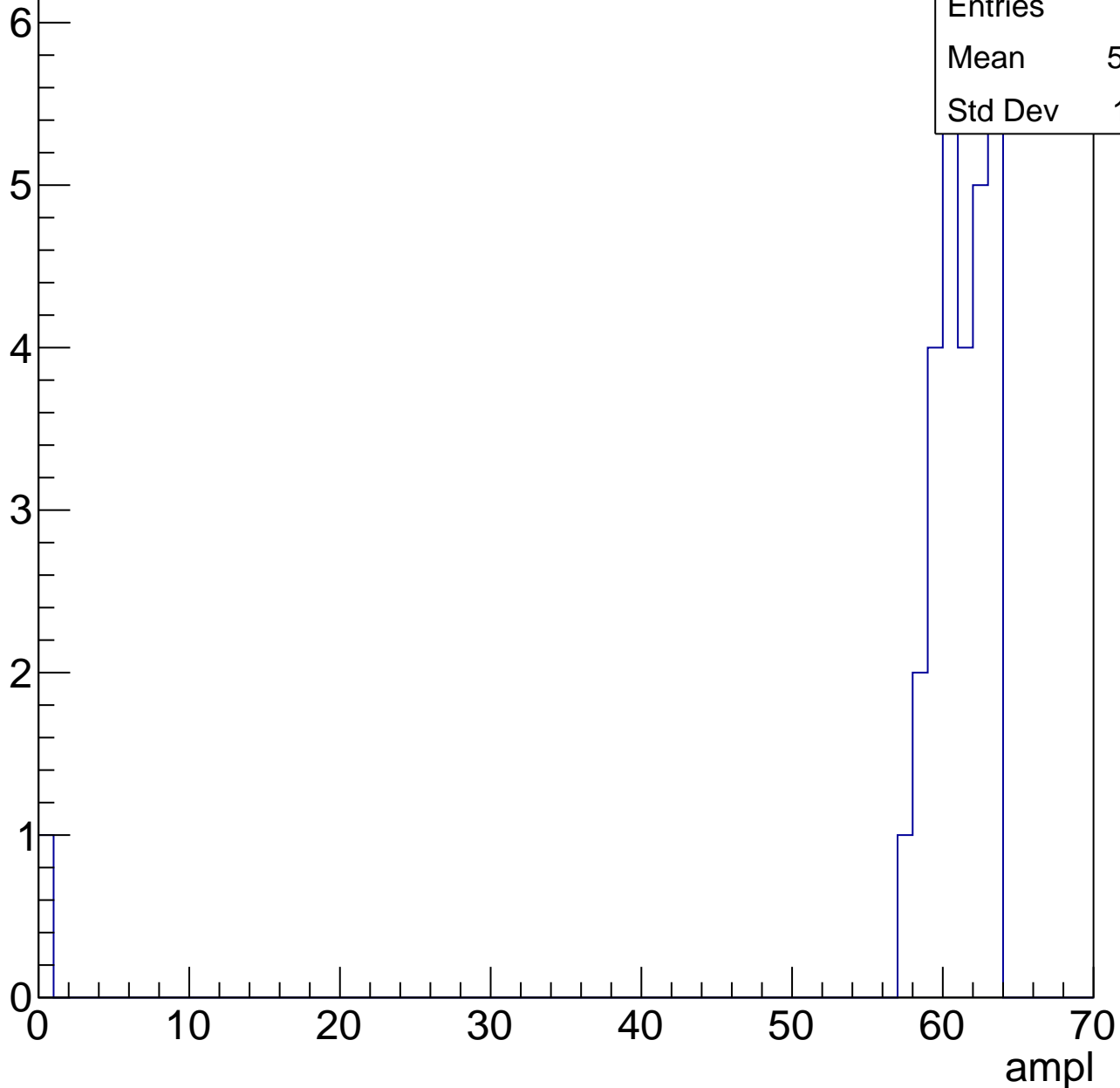
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



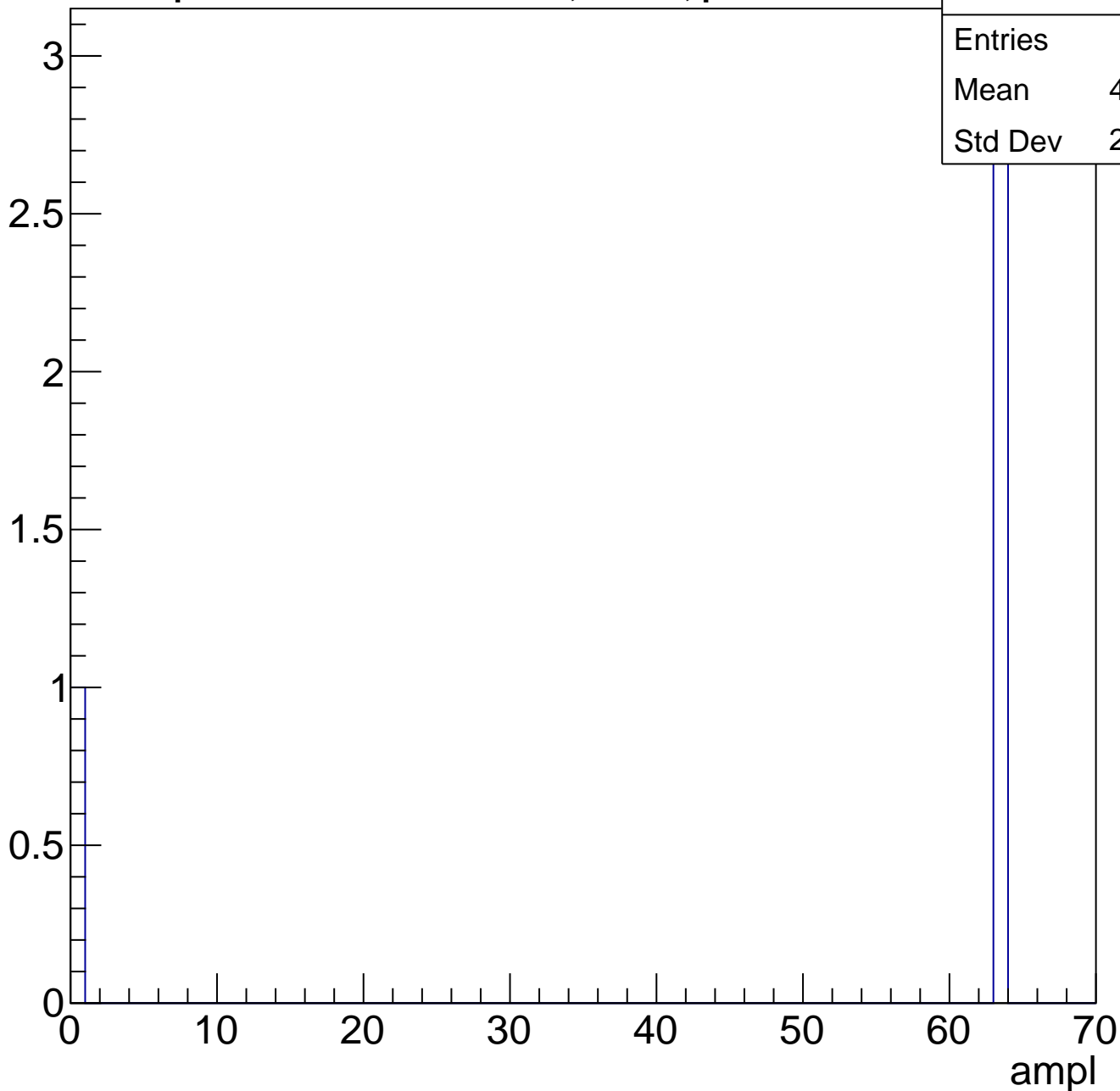
Entries	29
Mean	58.66
Std Dev	11.21



# B1L002S, U18-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch79, adc0

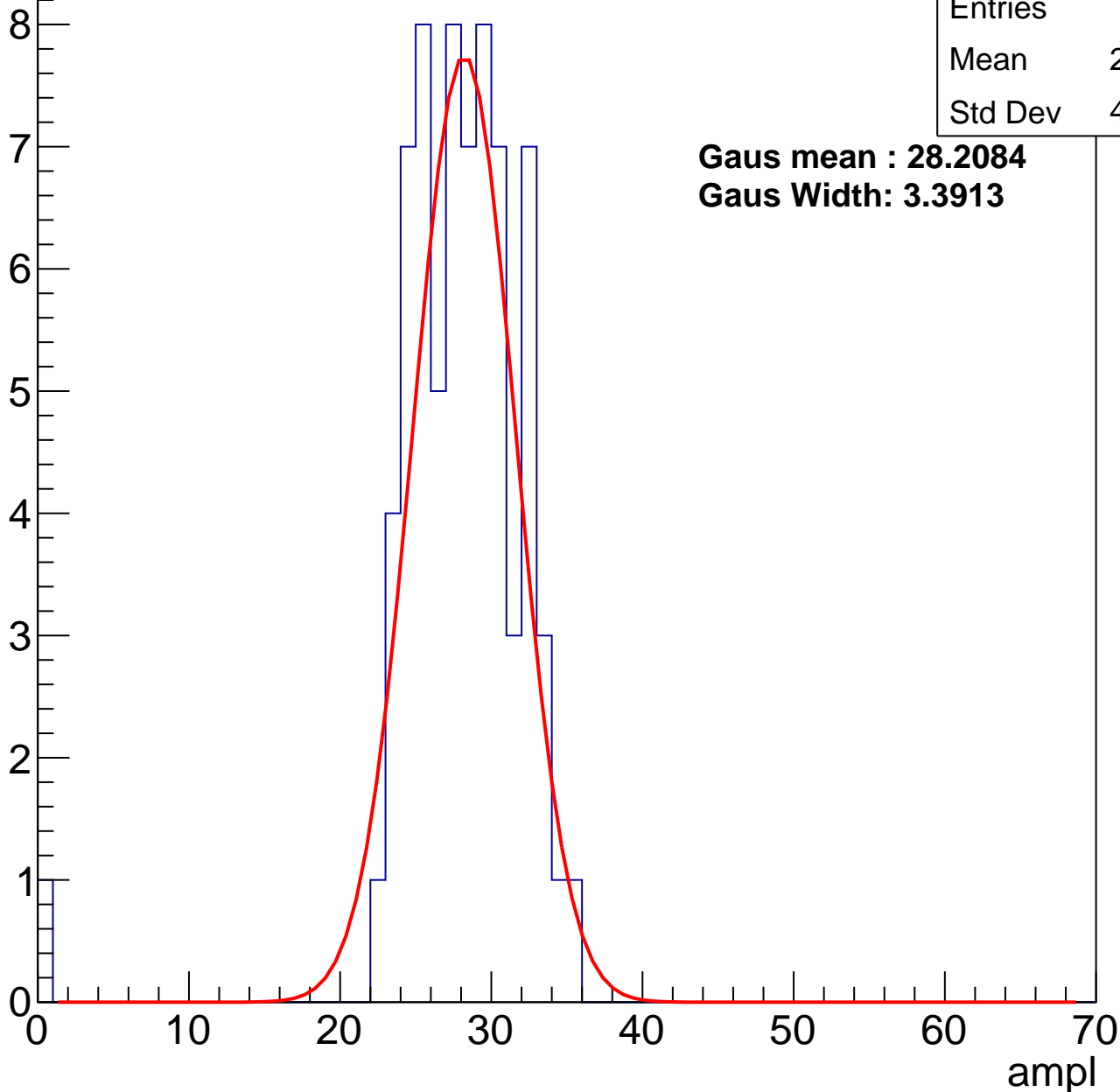
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	27.48
Std Dev	4.516

**Gaus mean : 28.2084**

**Gaus Width: 3.3913**



# B1L002S, U18-ch79, adc1

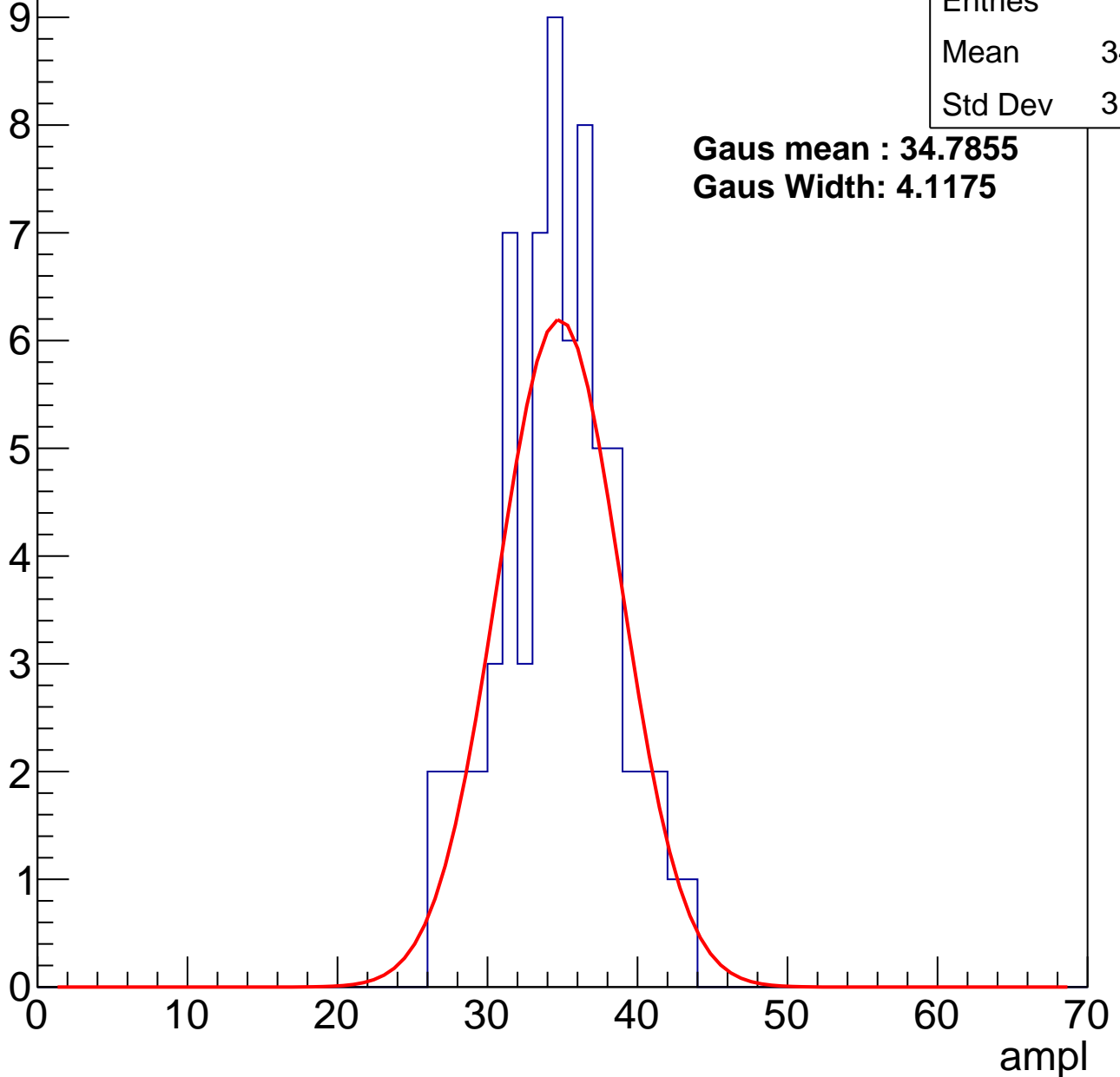
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.17
Std Dev	3.814

**Gaus mean : 34.7855**

**Gaus Width: 4.1175**



# B1L002S, U18-ch79, adc2

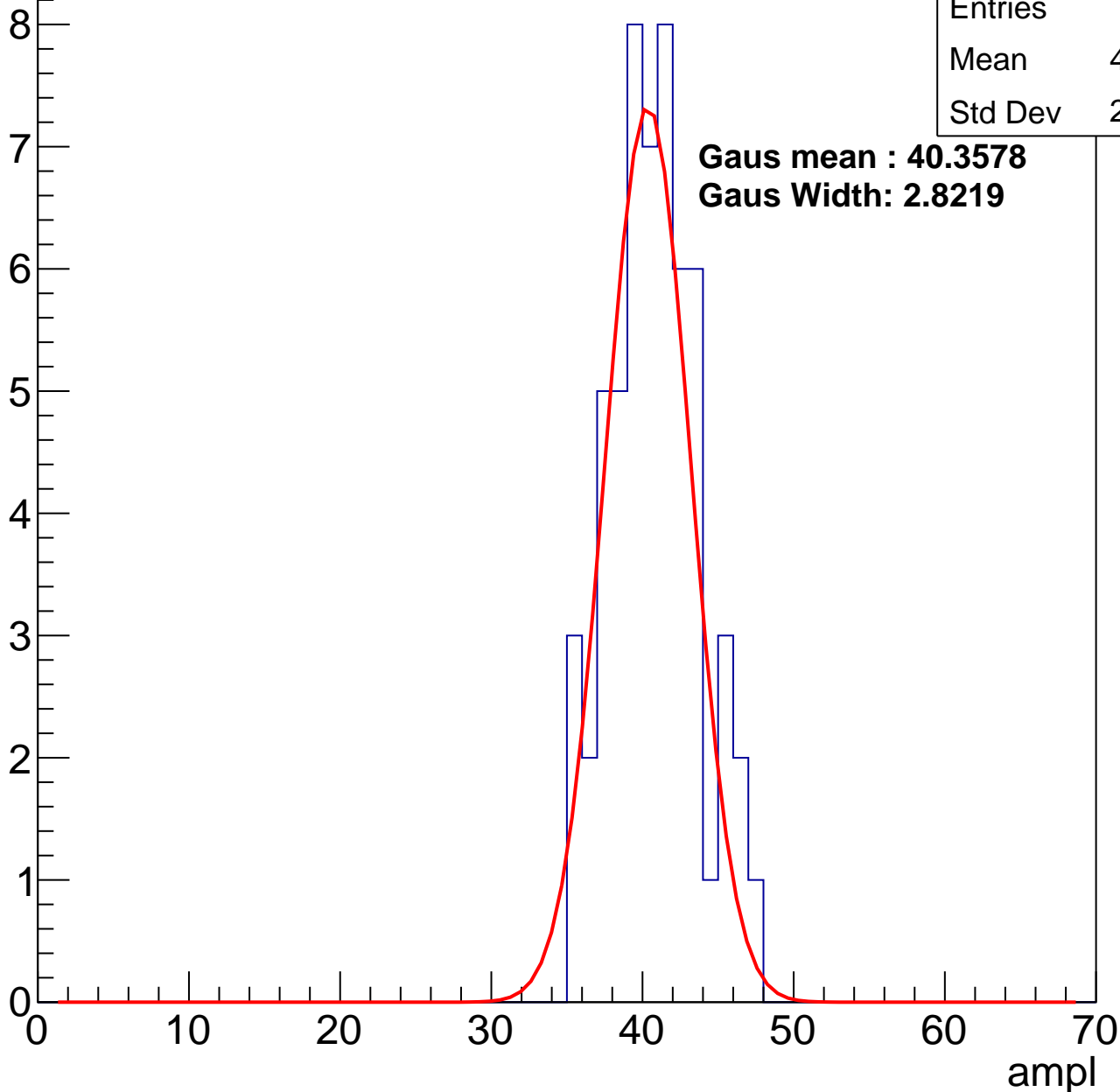
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.35
Std Dev	2.875

**Gaus mean : 40.3578**

**Gaus Width: 2.8219**

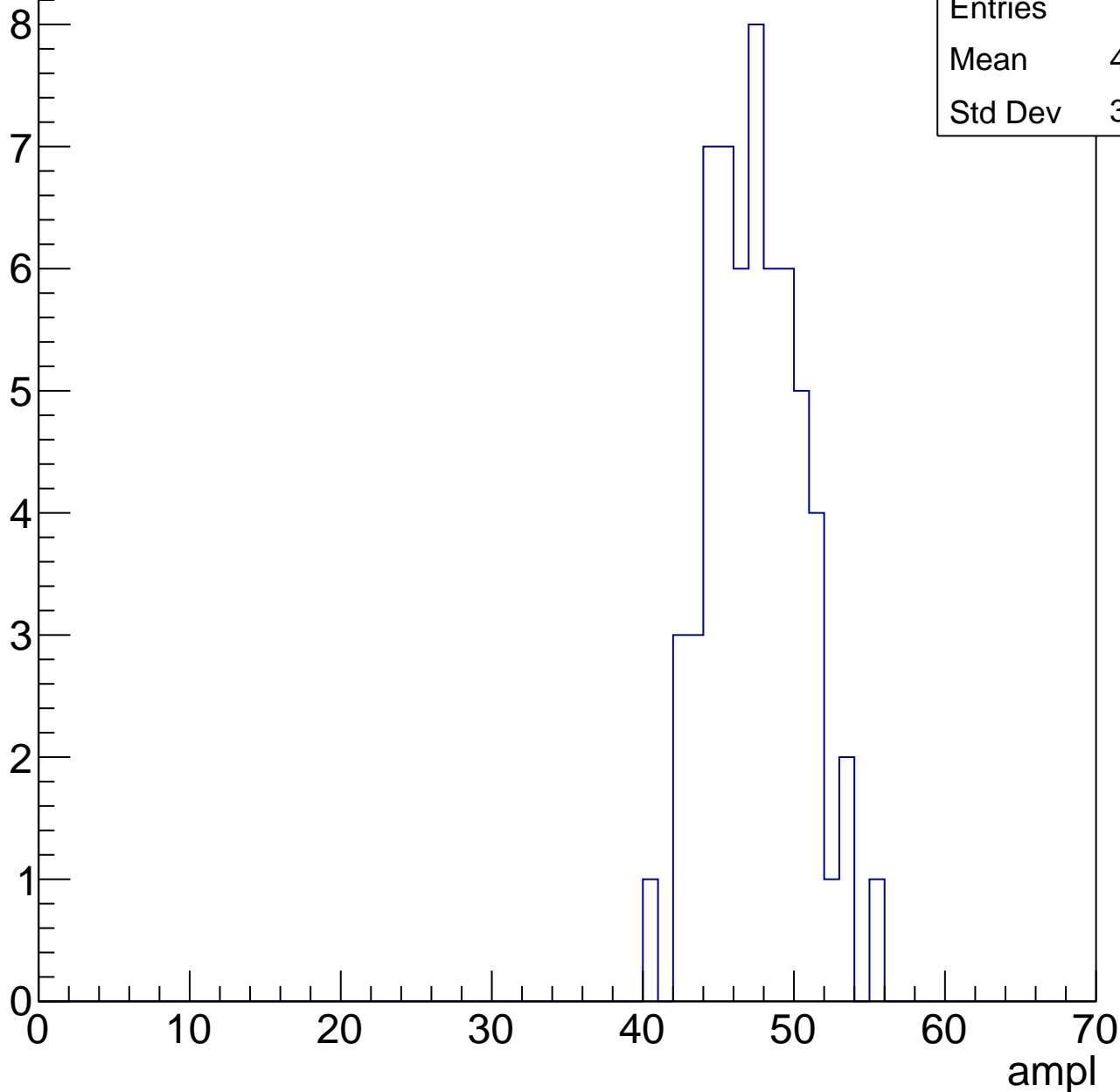


# B1L002S, U18-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.98
Std Dev	3.079

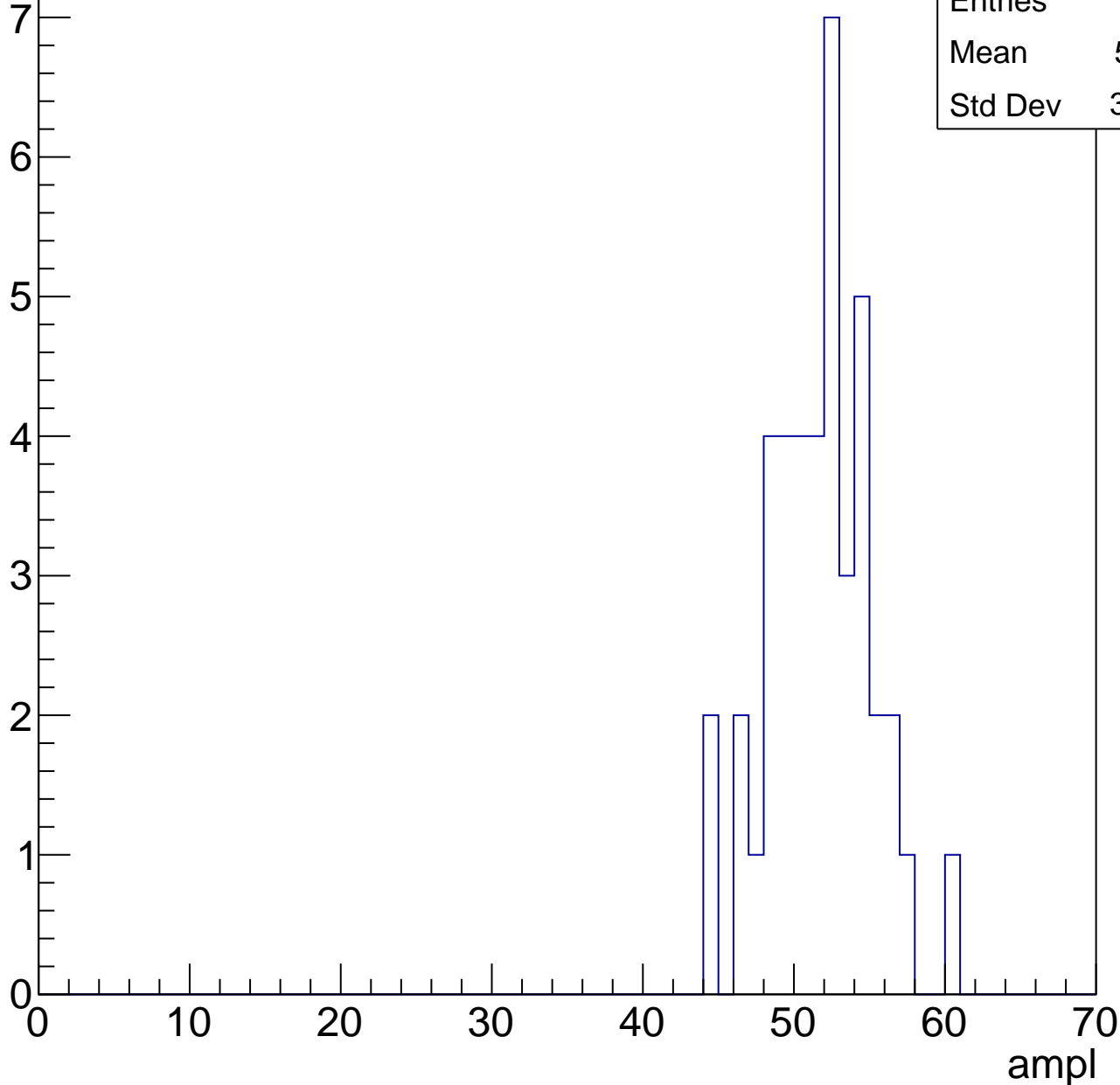


# B1L002S, U18-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

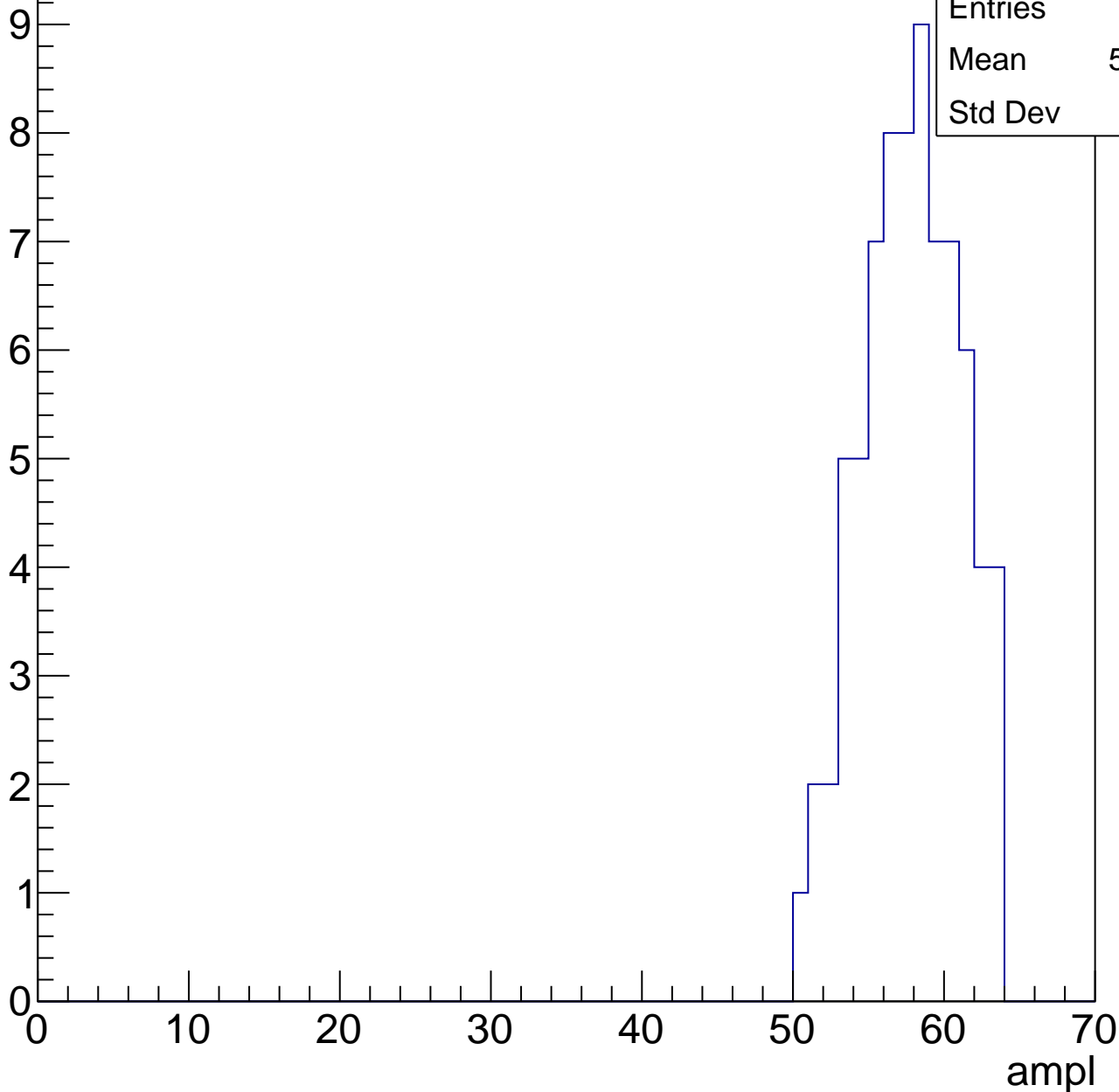
Entries	42
Mean	51.21
Std Dev	3.384



# B1L002S, U18-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



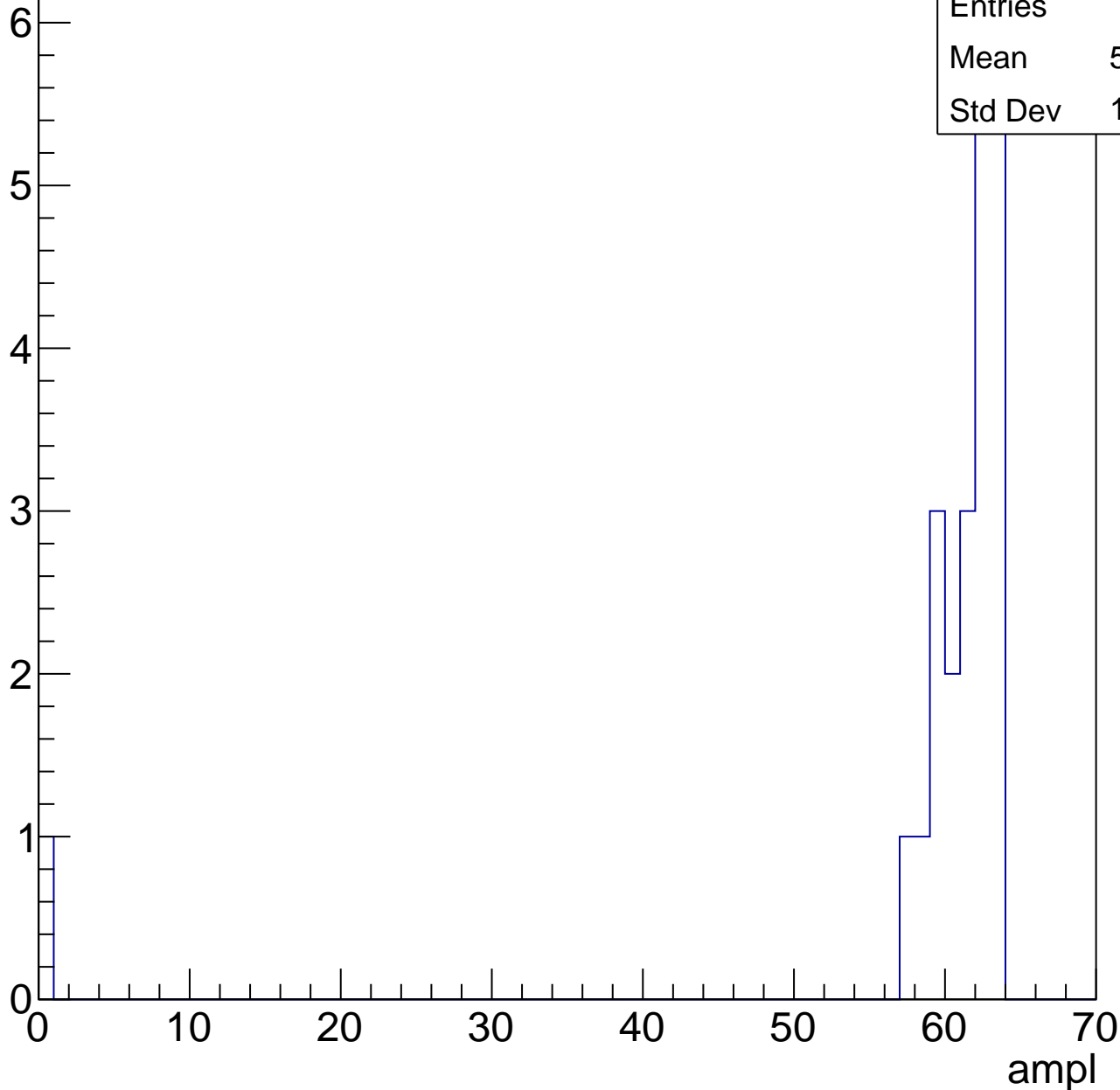
Entries	75
Mean	57.35
Std Dev	3.19

# B1L002S, U18-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.48
Std Dev	12.59

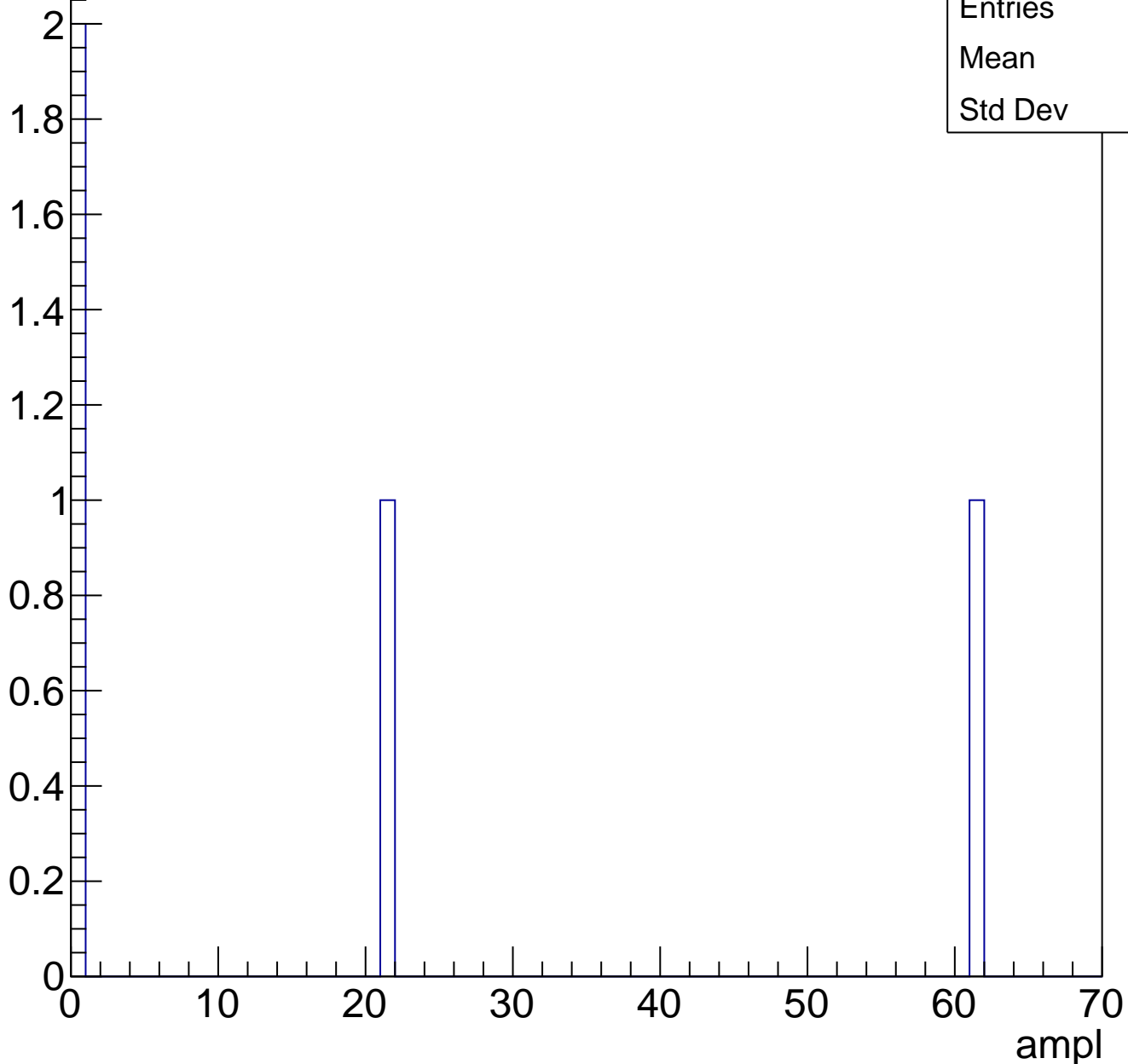




# B1L002S, U18-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.5
Std Dev	24.9

# B1L002S, U18-ch80, adc0

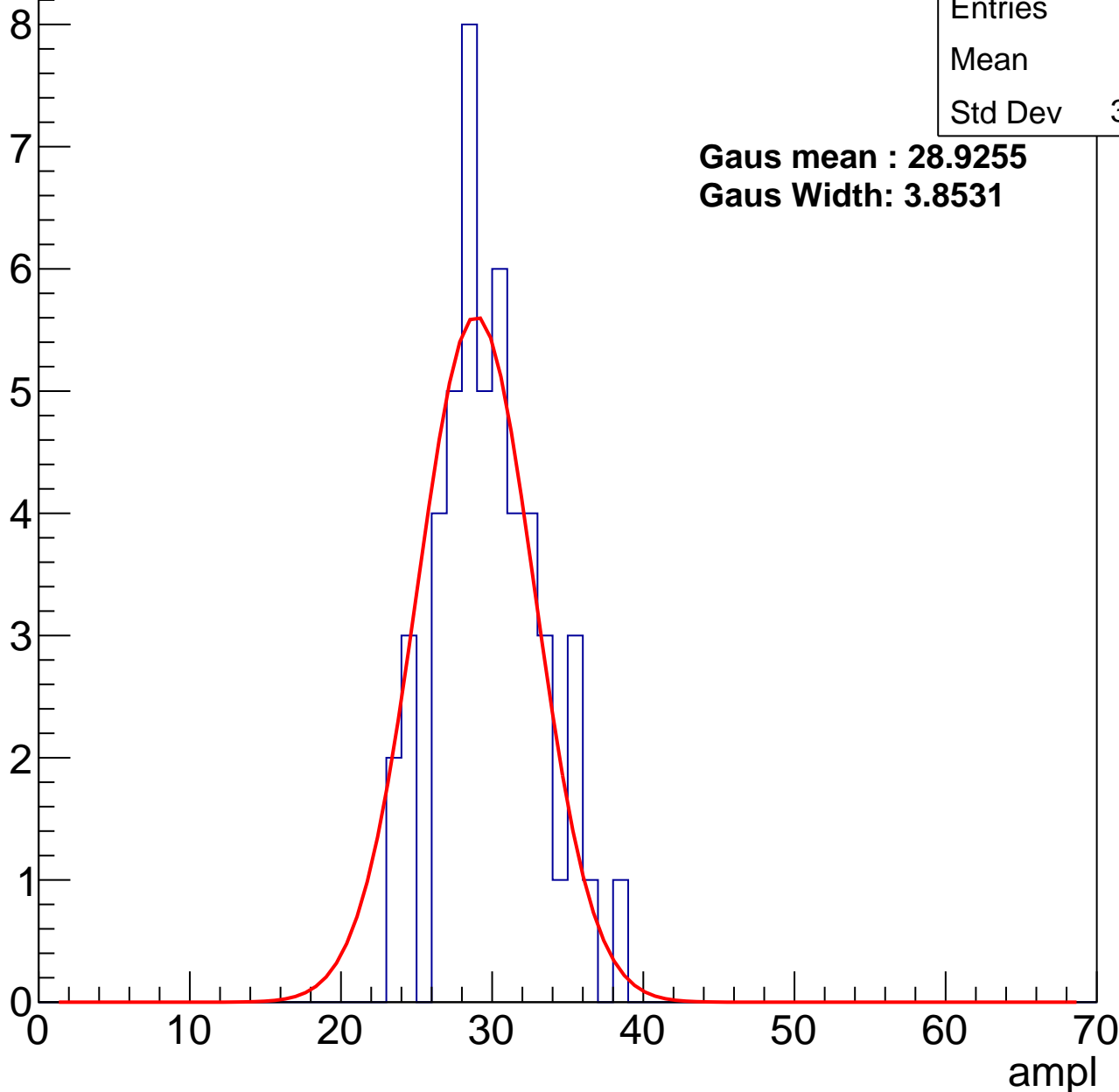
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	29.4
Std Dev	3.382

**Gaus mean : 28.9255**

**Gaus Width: 3.8531**



# B1L002S, U18-ch80, adc1

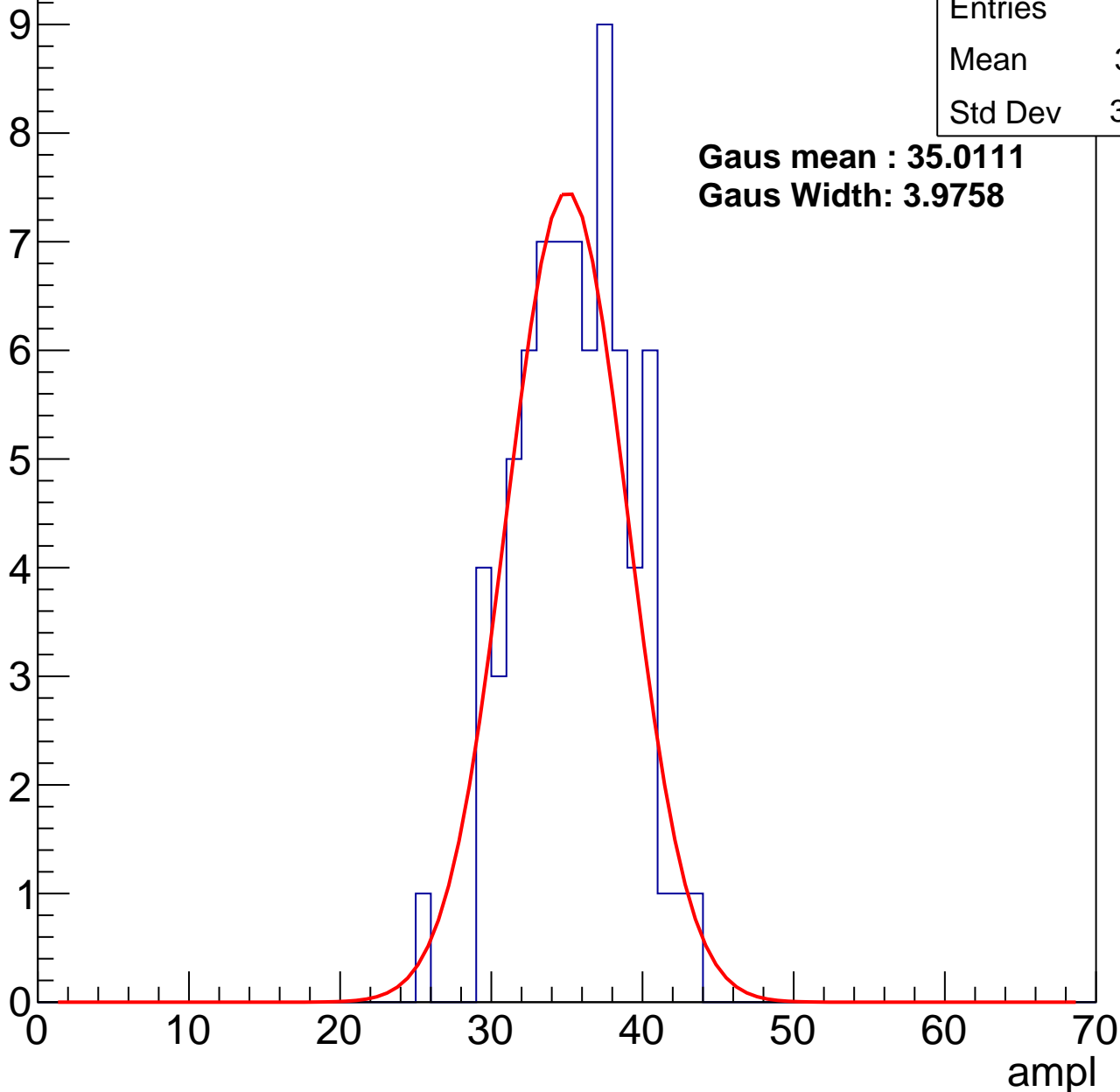
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	35.01
Std Dev	3.577

**Gaus mean : 35.0111**

**Gaus Width: 3.9758**



# B1L002S, U18-ch80, adc2

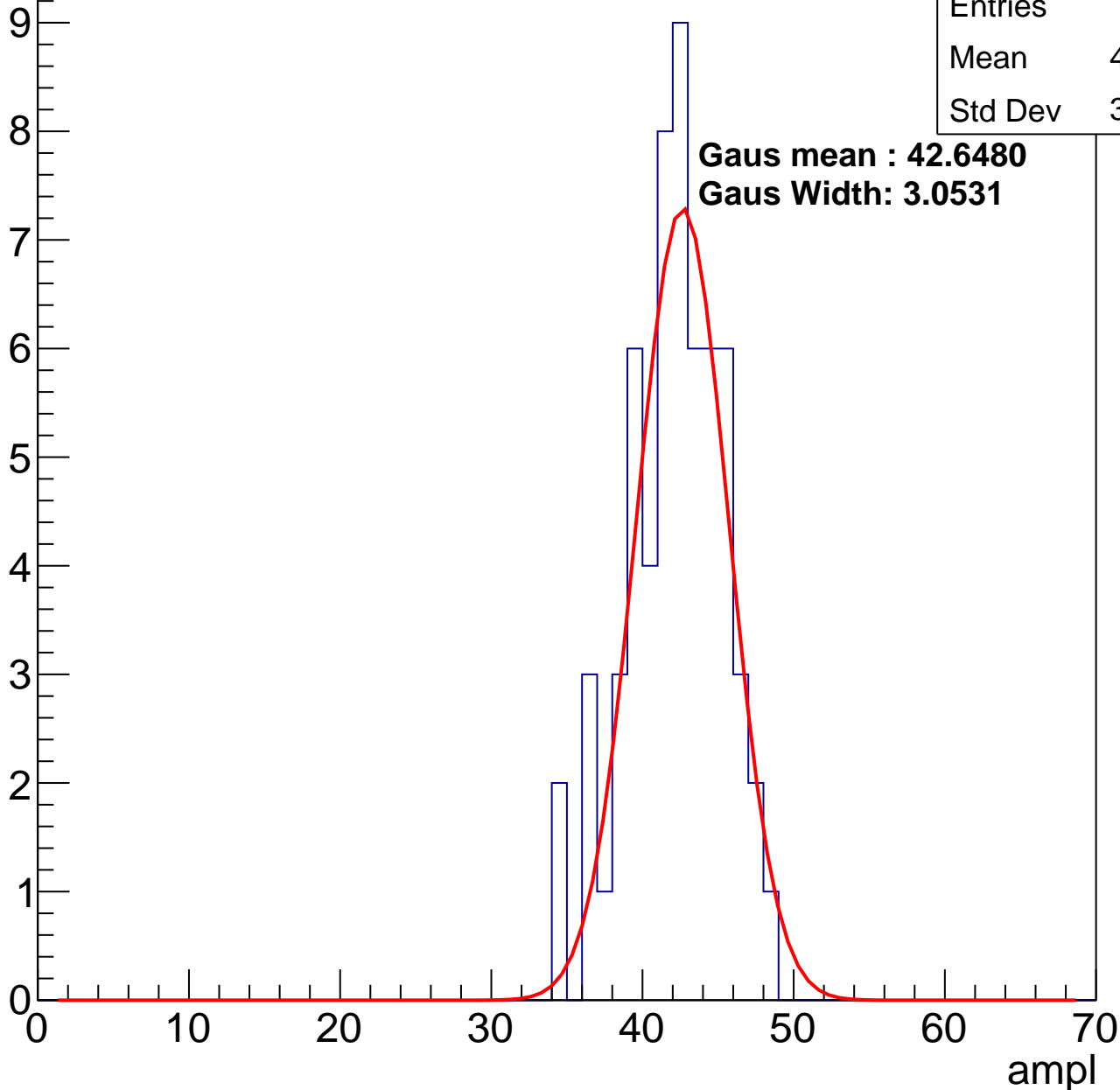
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.65
Std Dev	3.156

**Gaus mean : 42.6480**

**Gaus Width: 3.0531**

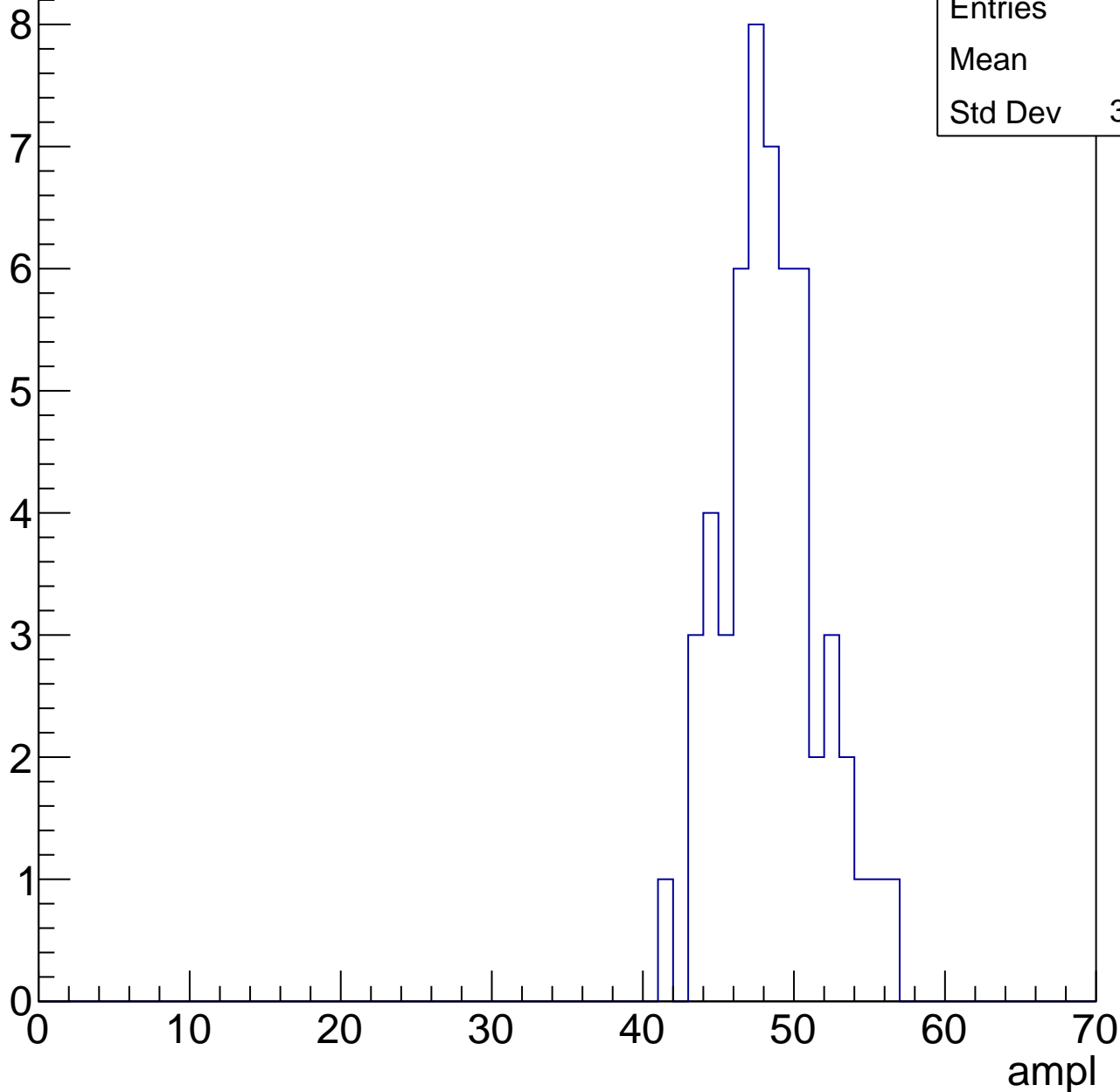


# B1L002S, U18-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	48
Std Dev	3.168

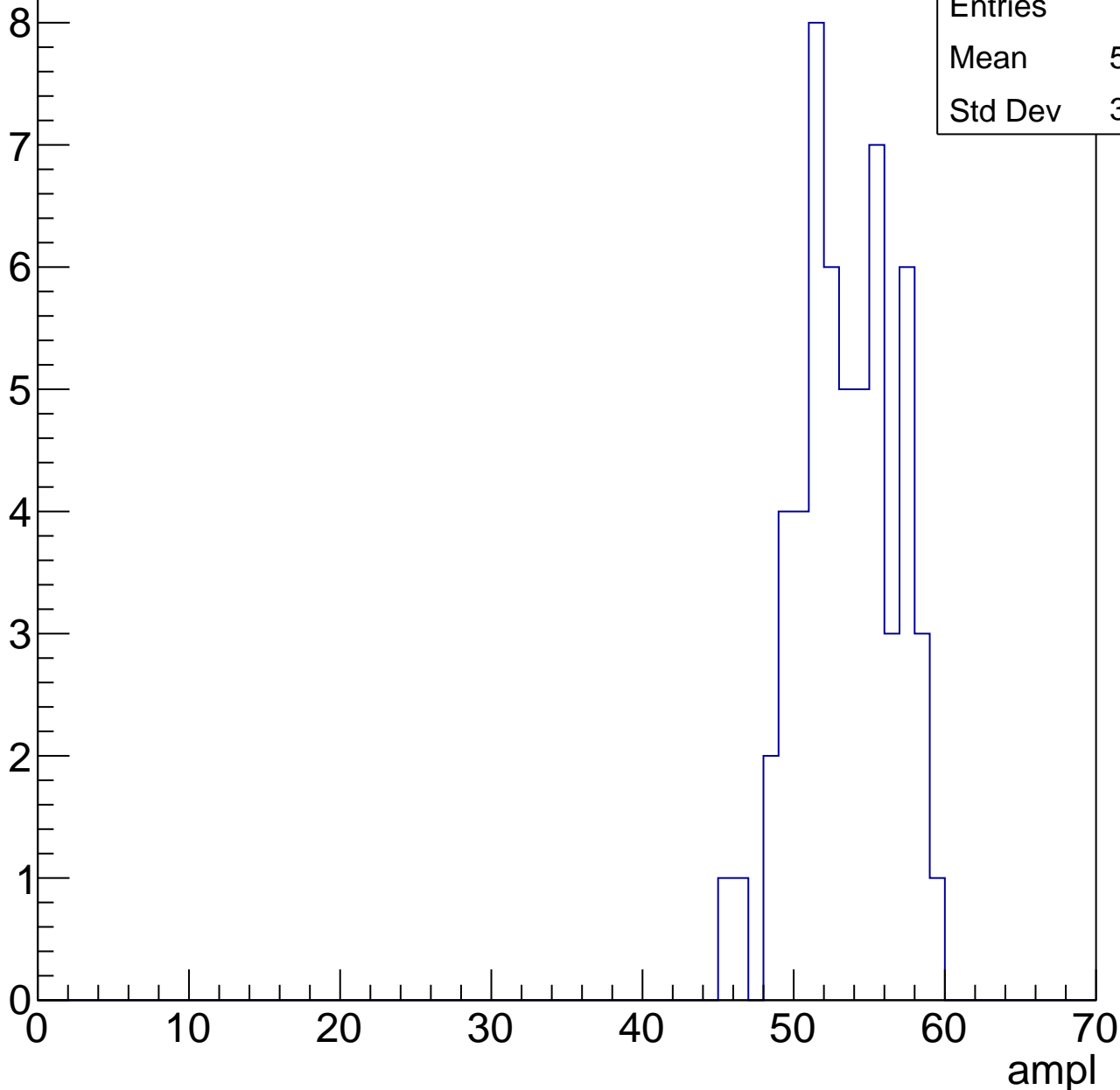


# B1L002S, U18-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	52.96
Std Dev	3.185

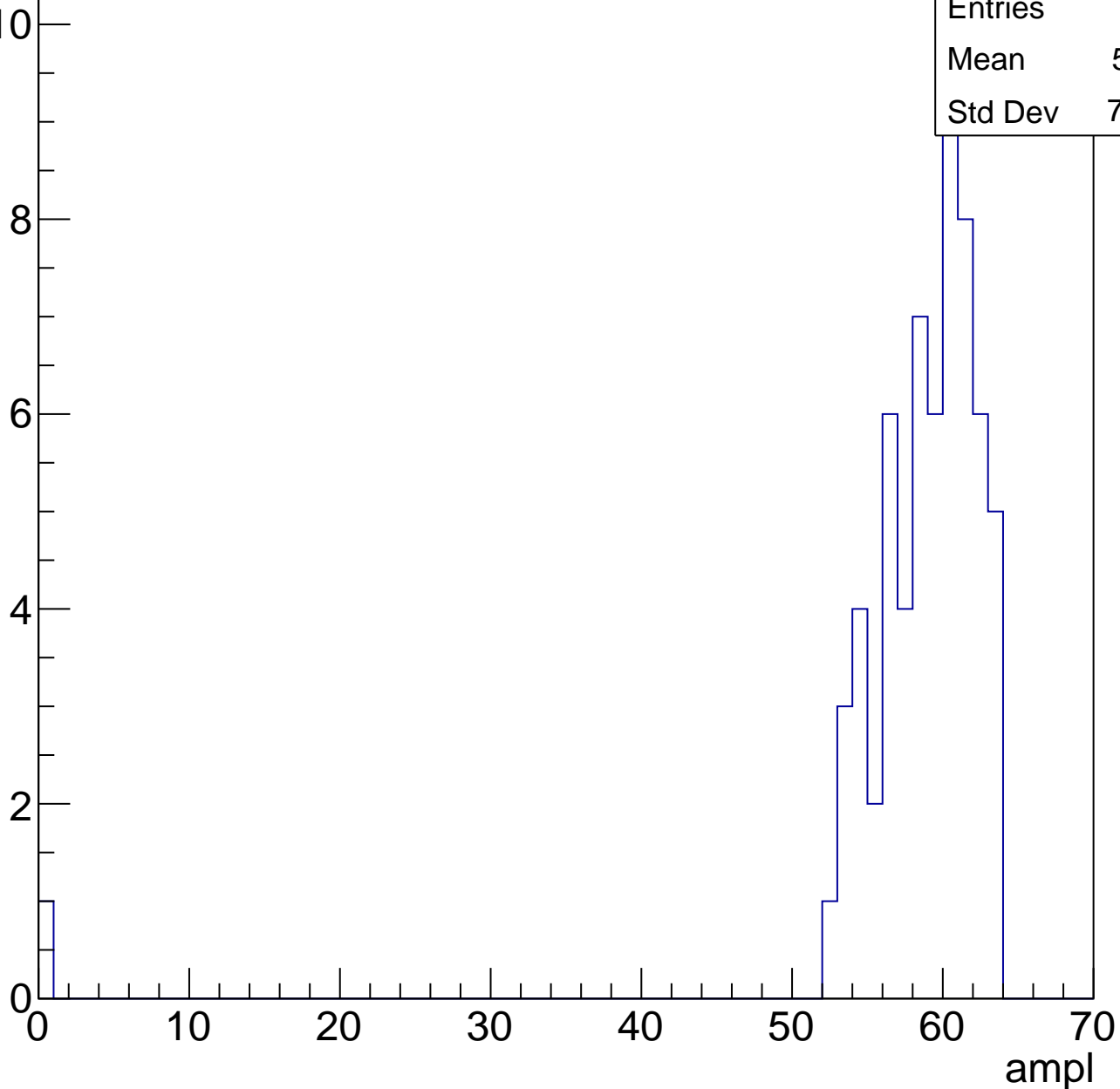


# B1L002S, U18-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	57.71
Std Dev	7.887

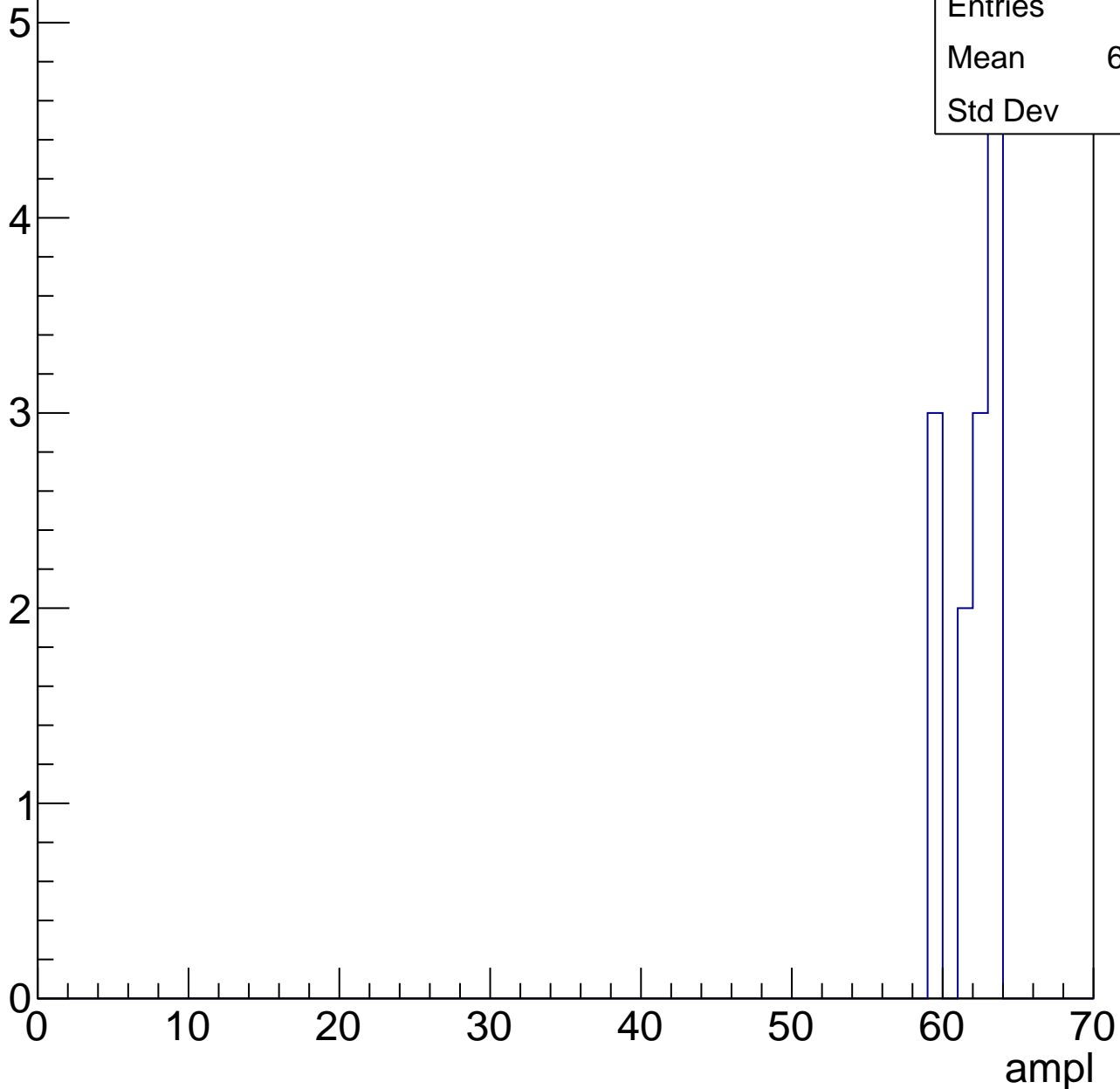


# B1L002S, U18-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.54
Std Dev	1.55

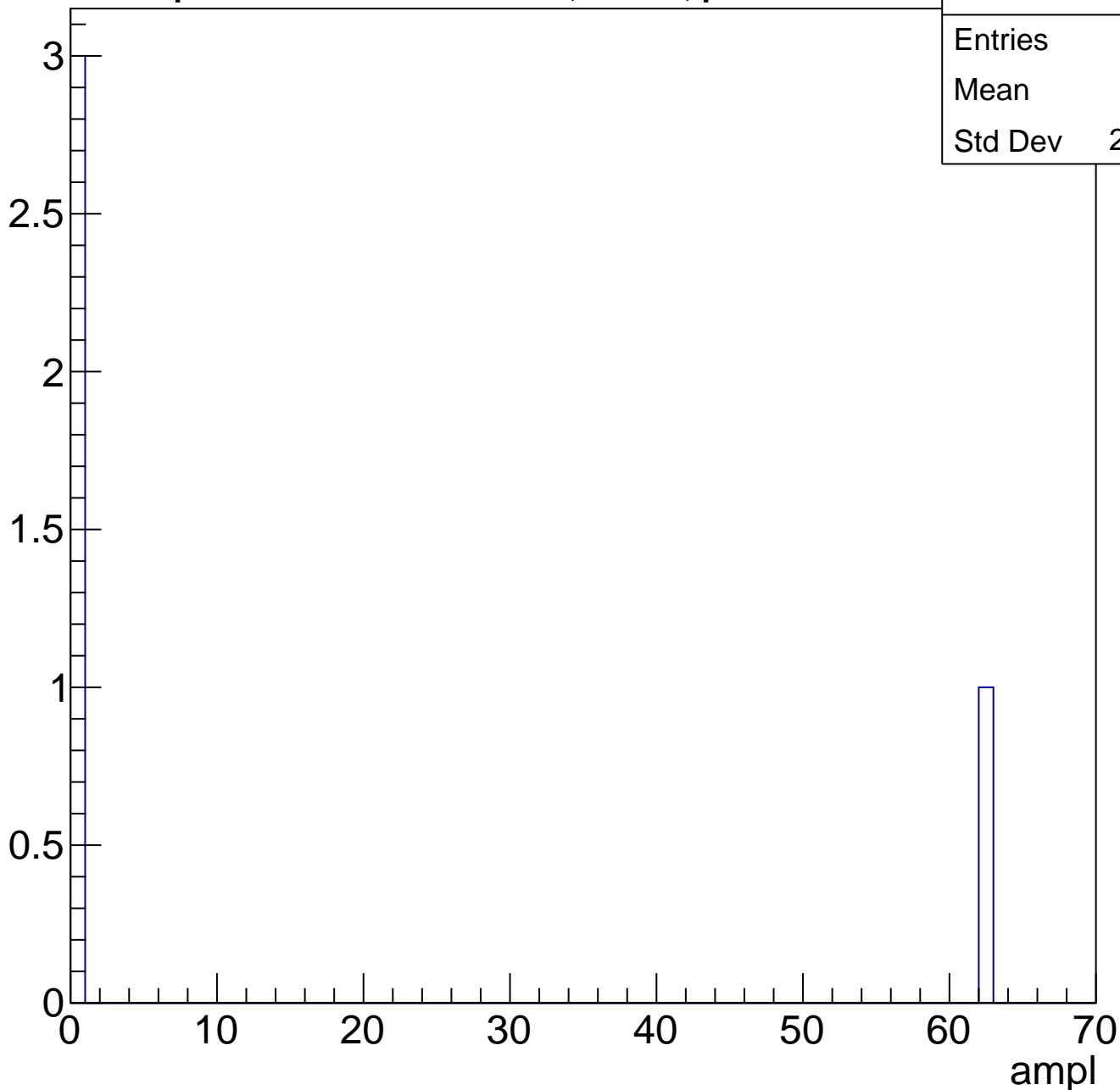




# B1L002S, U18-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch81, adc0

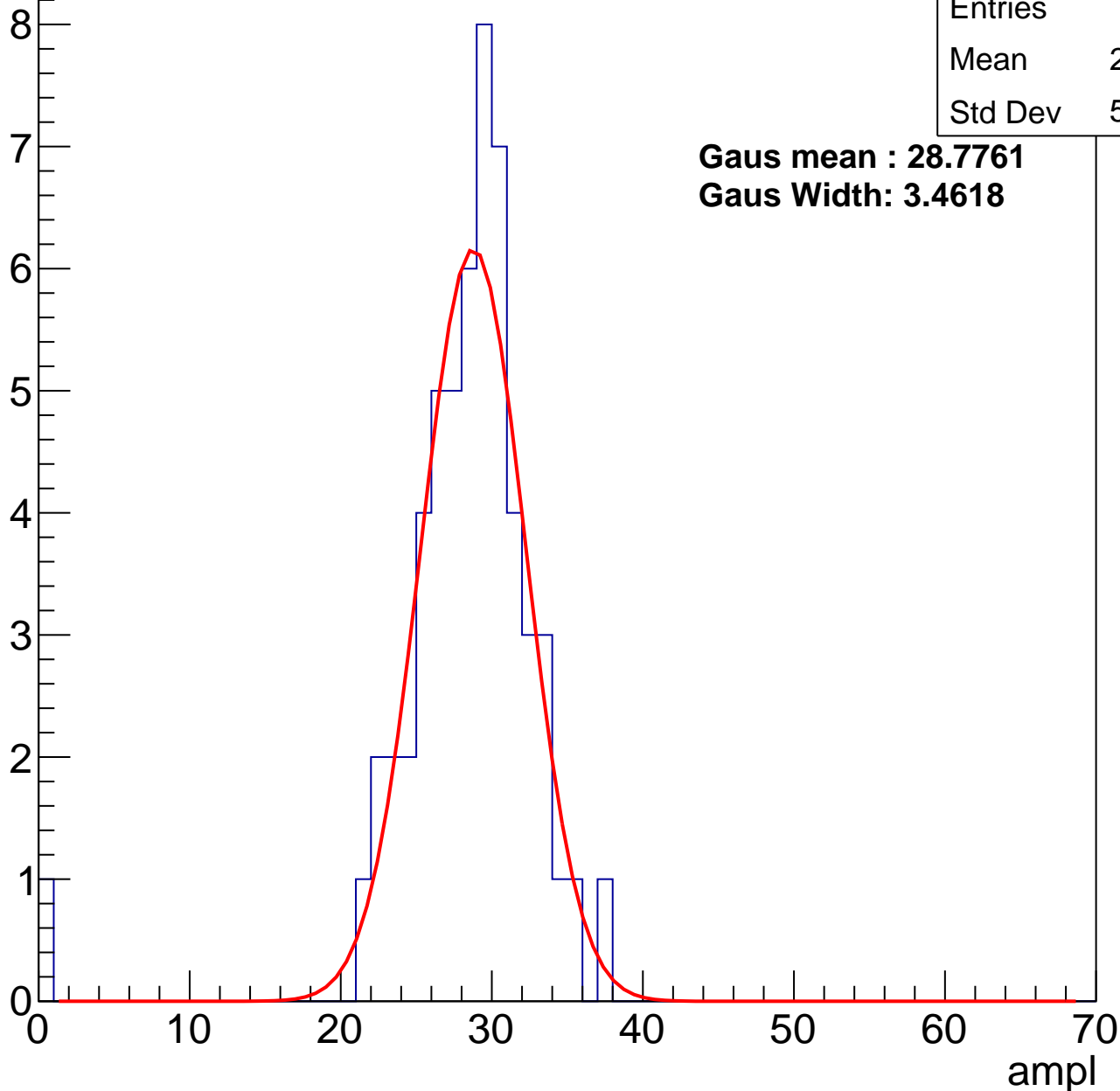
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	27.84
Std Dev	5.013

**Gaus mean : 28.7761**

**Gaus Width: 3.4618**



# B1L002S, U18-ch81, adc1

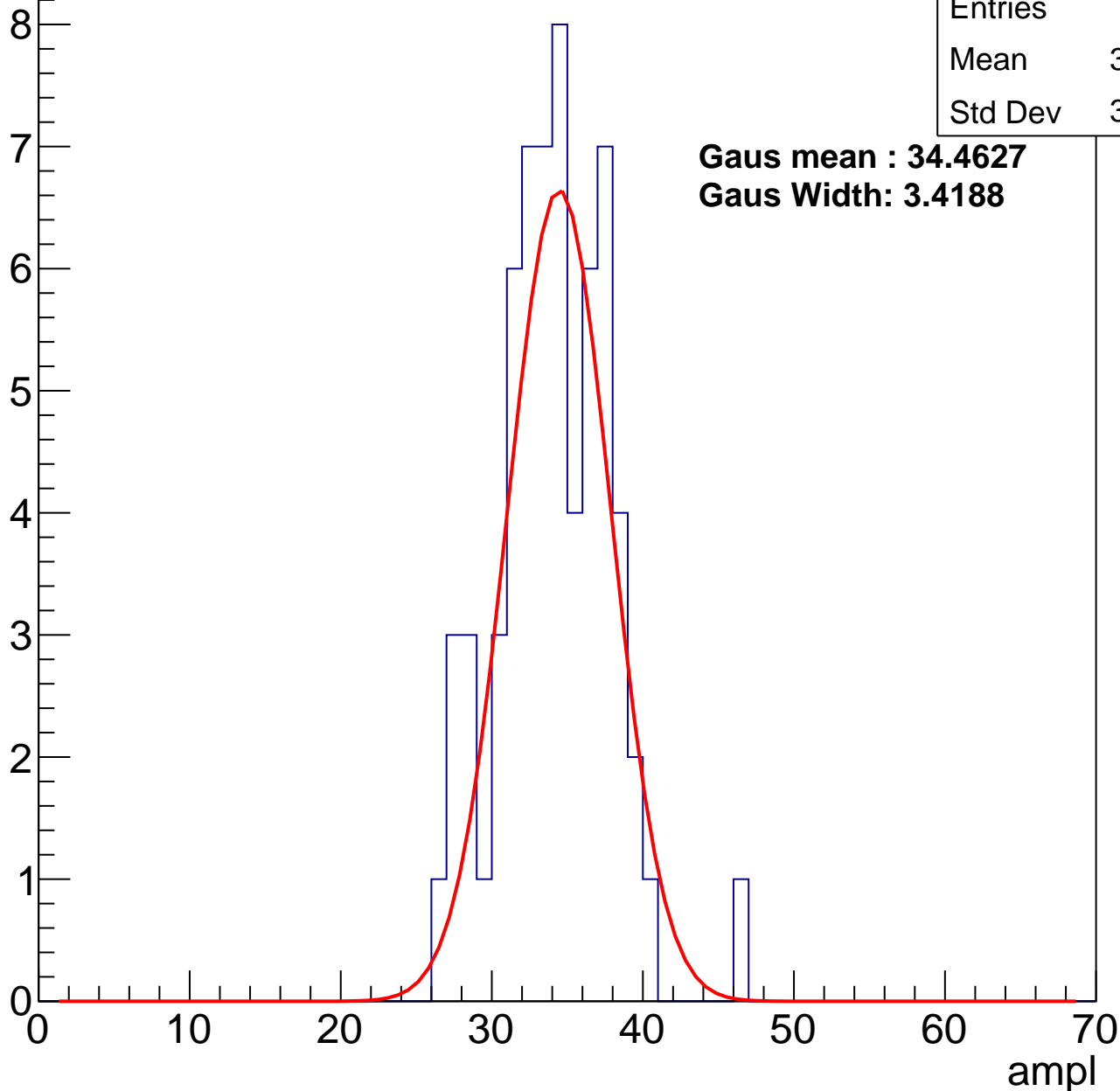
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.66
Std Dev	3.662

**Gaus mean : 34.4627**

**Gaus Width: 3.4188**



# B1L002S, U18-ch81, adc2

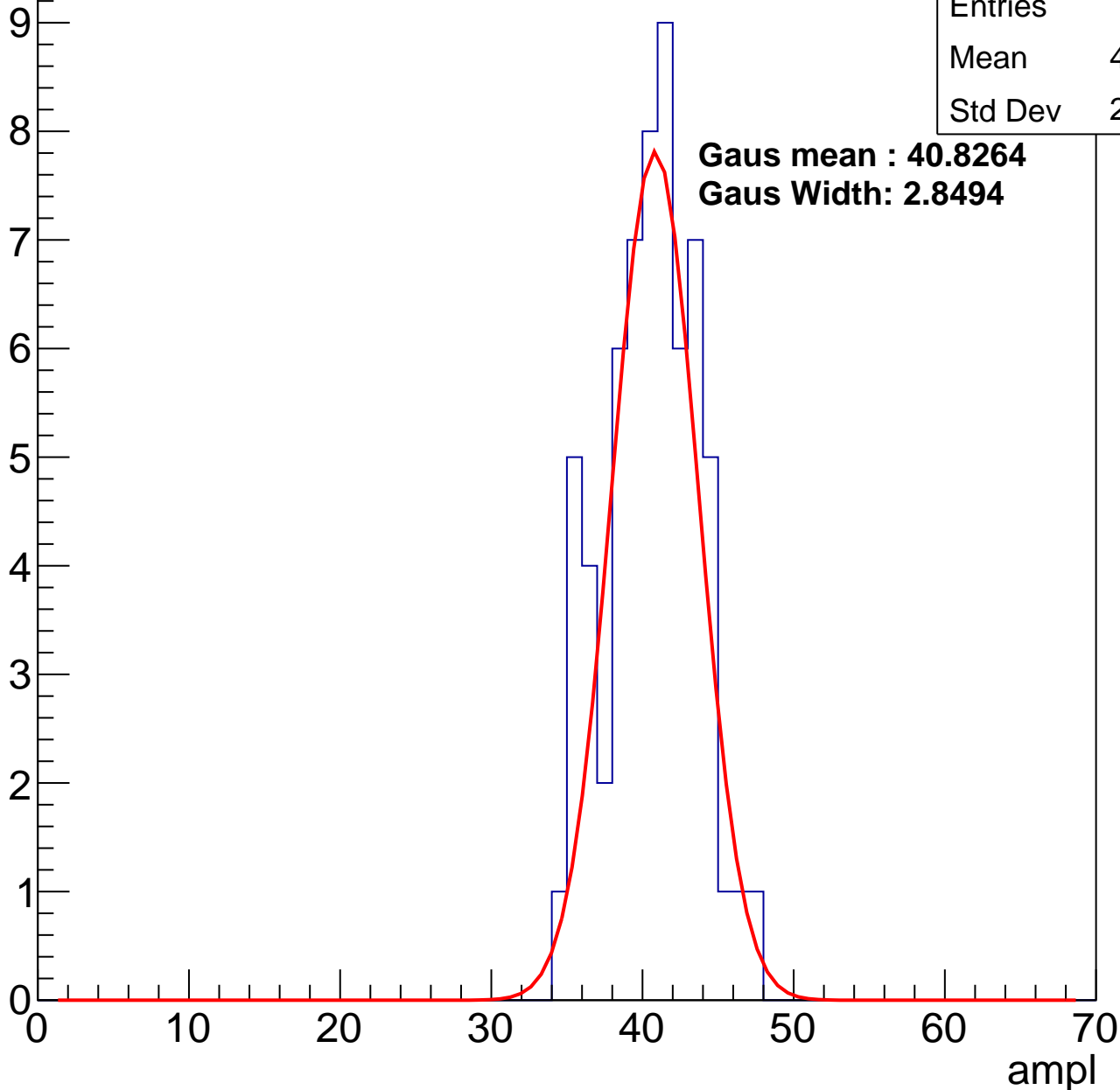
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.13
Std Dev	2.979

**Gaus mean : 40.8264**

**Gaus Width: 2.8494**

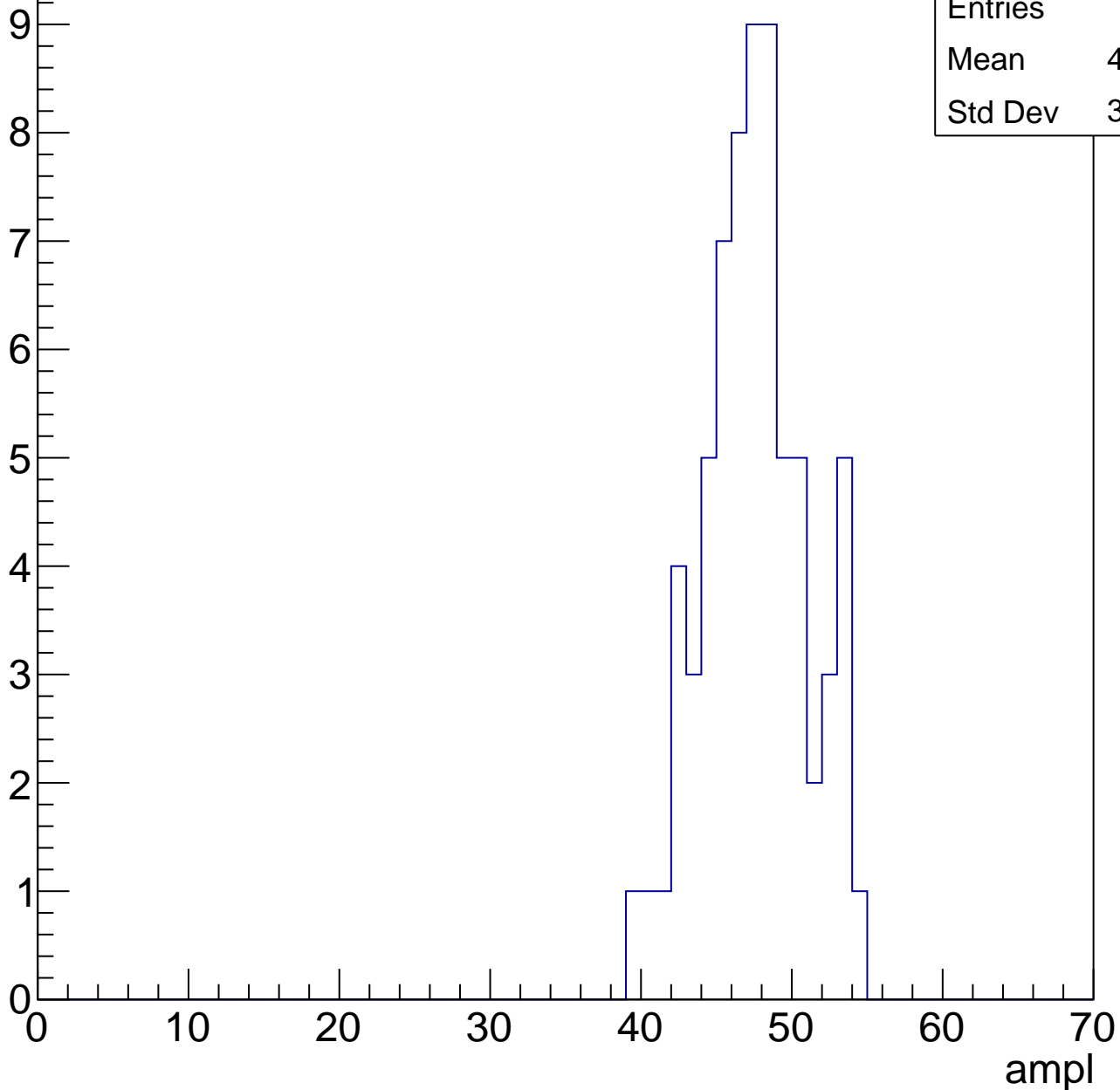


# B1L002S, U18-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	47.06
Std Dev	3.379

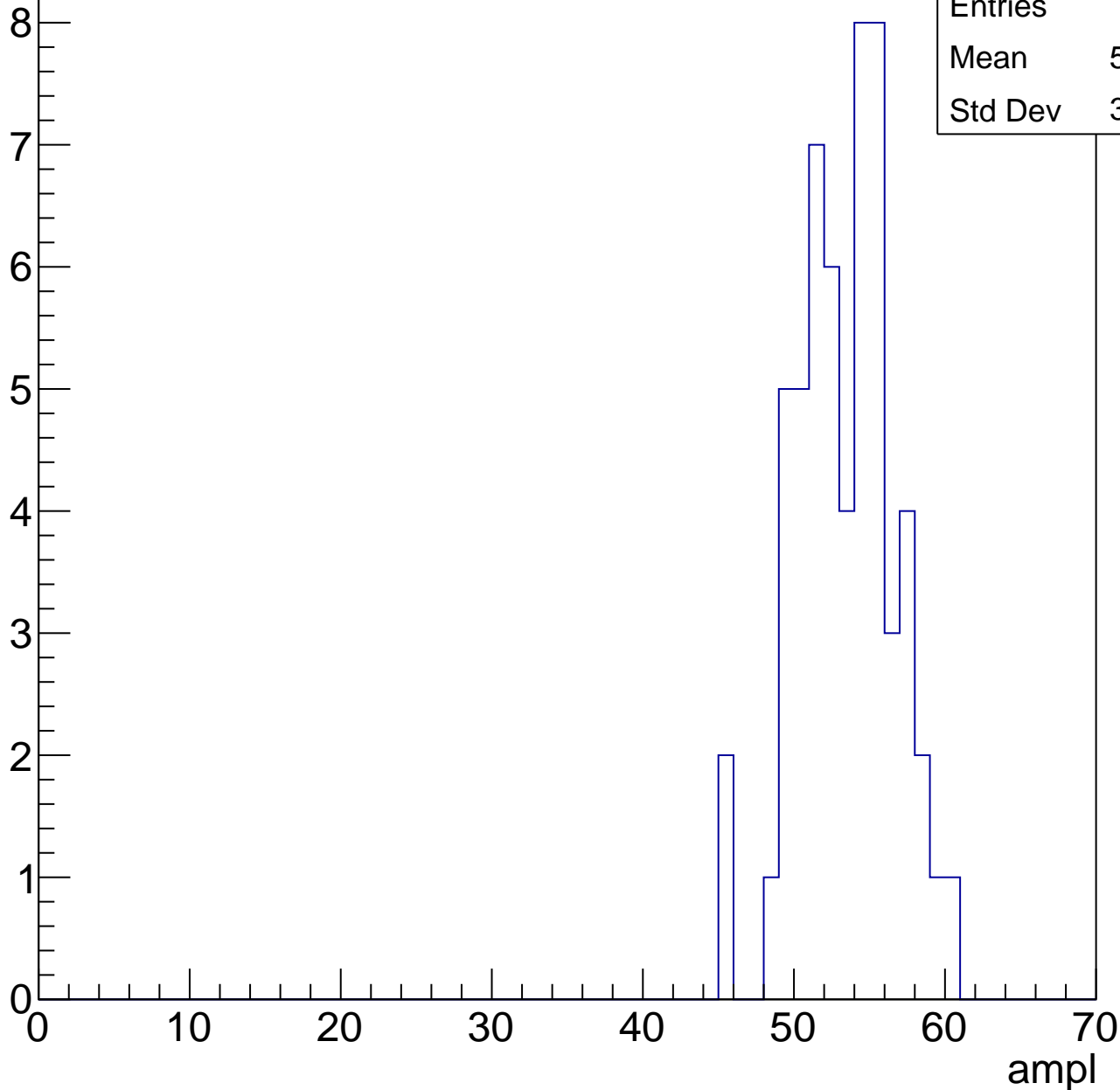


# B1L002S, U18-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

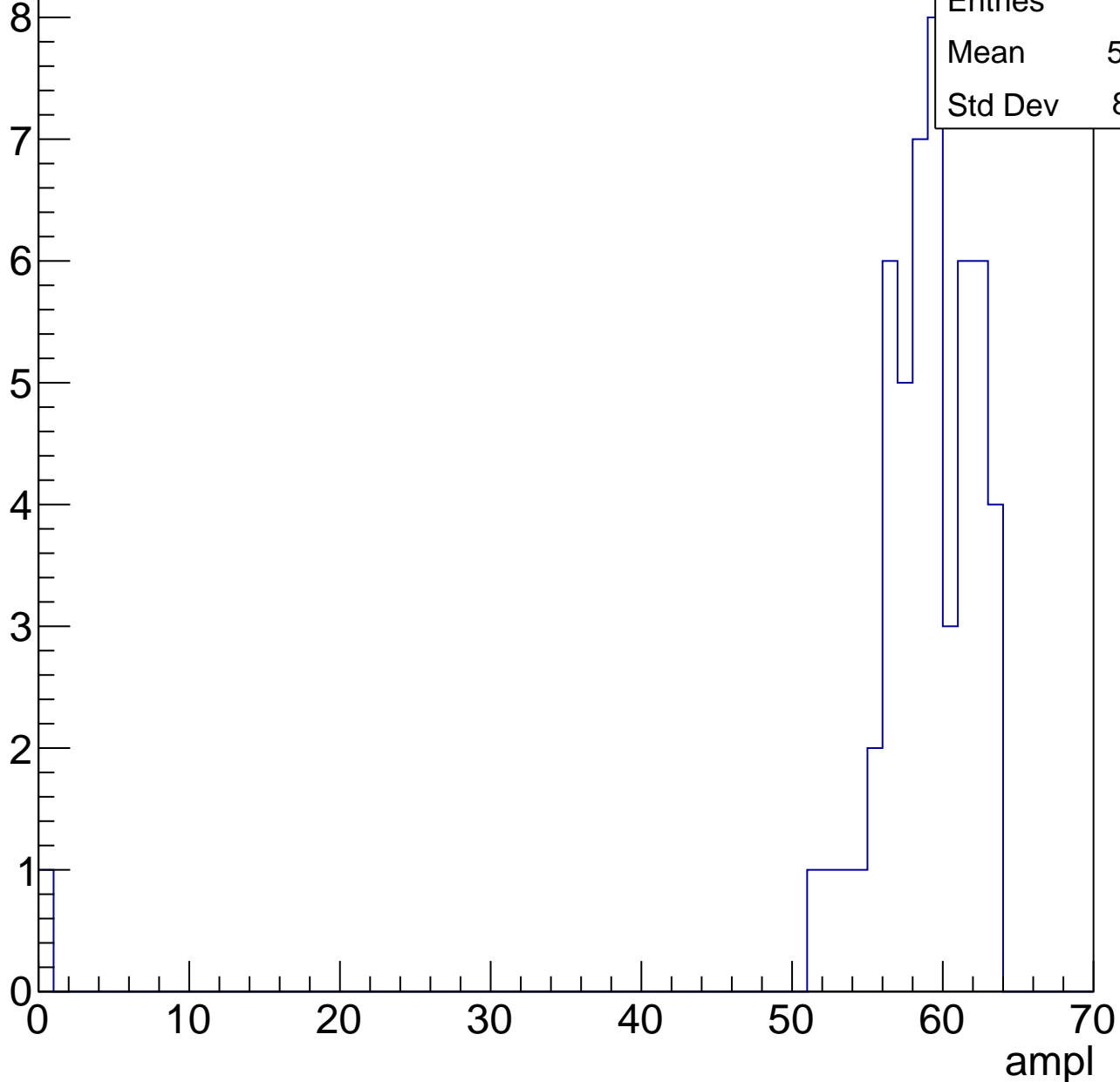
Entries	57
Mean	52.93
Std Dev	3.184



# B1L002S, U18-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

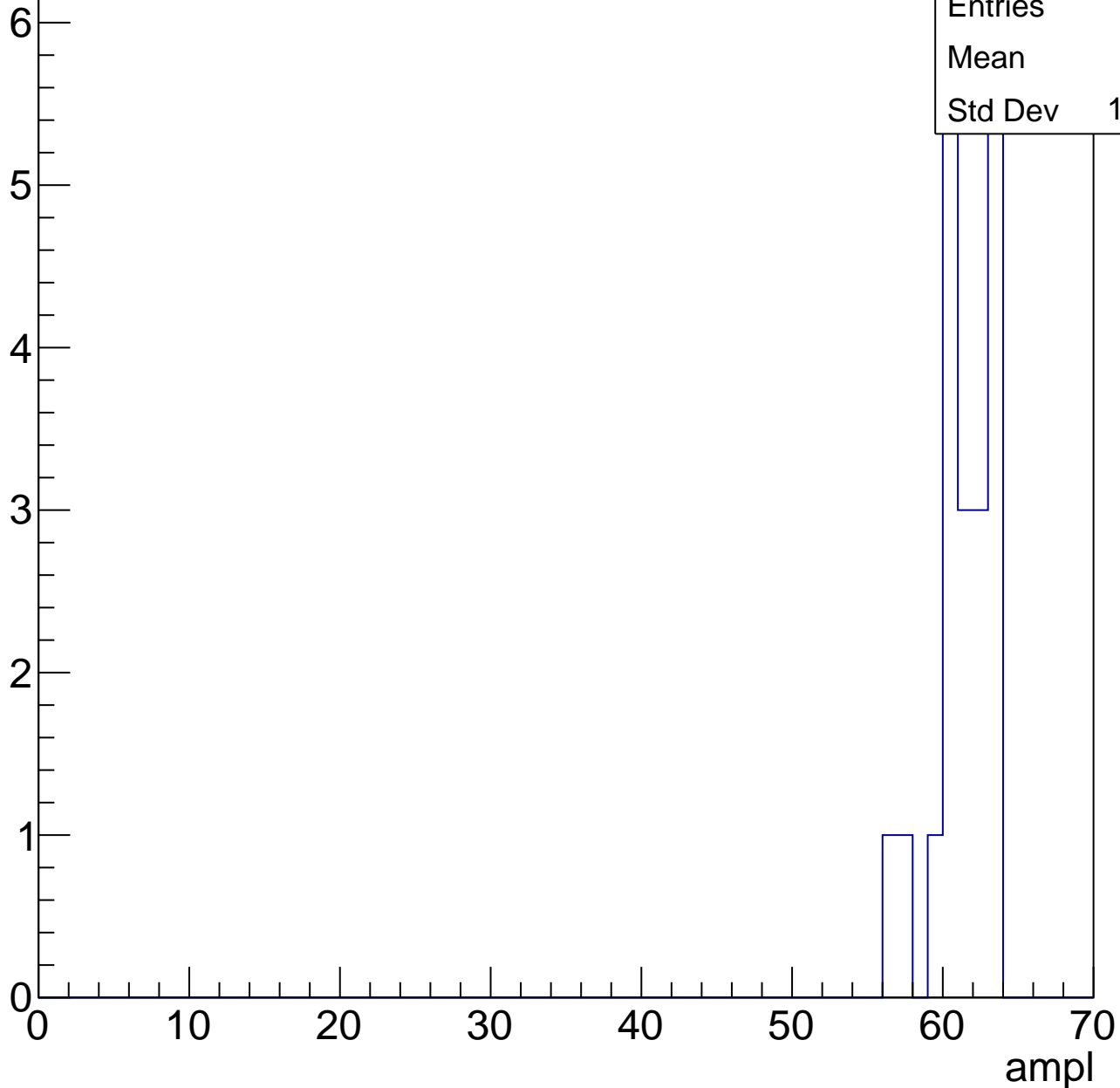


Entries	52
Mean	57.48
Std Dev	8.541

# B1L002S, U18-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

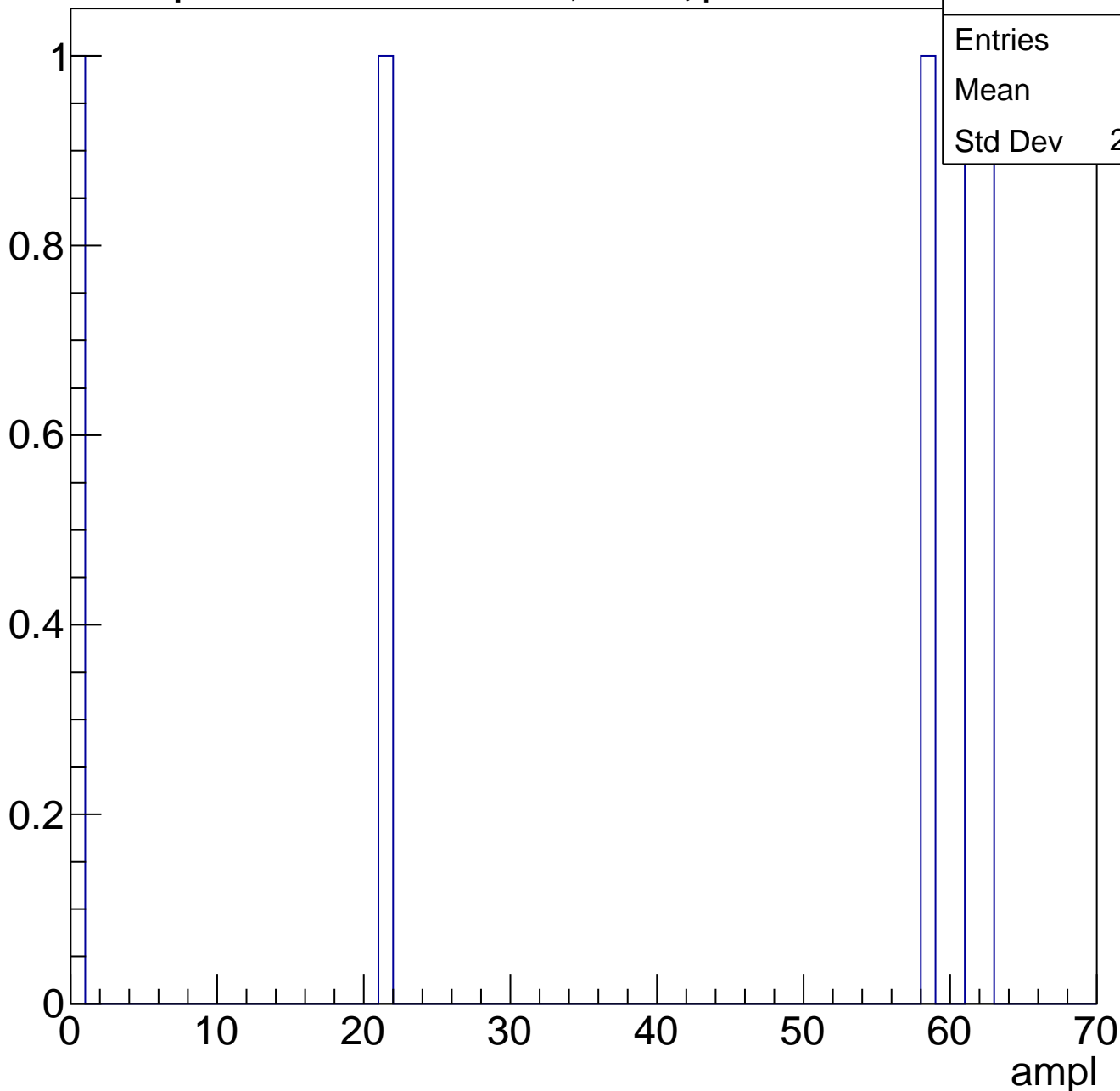




# B1L002S, U18-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch82, adc0

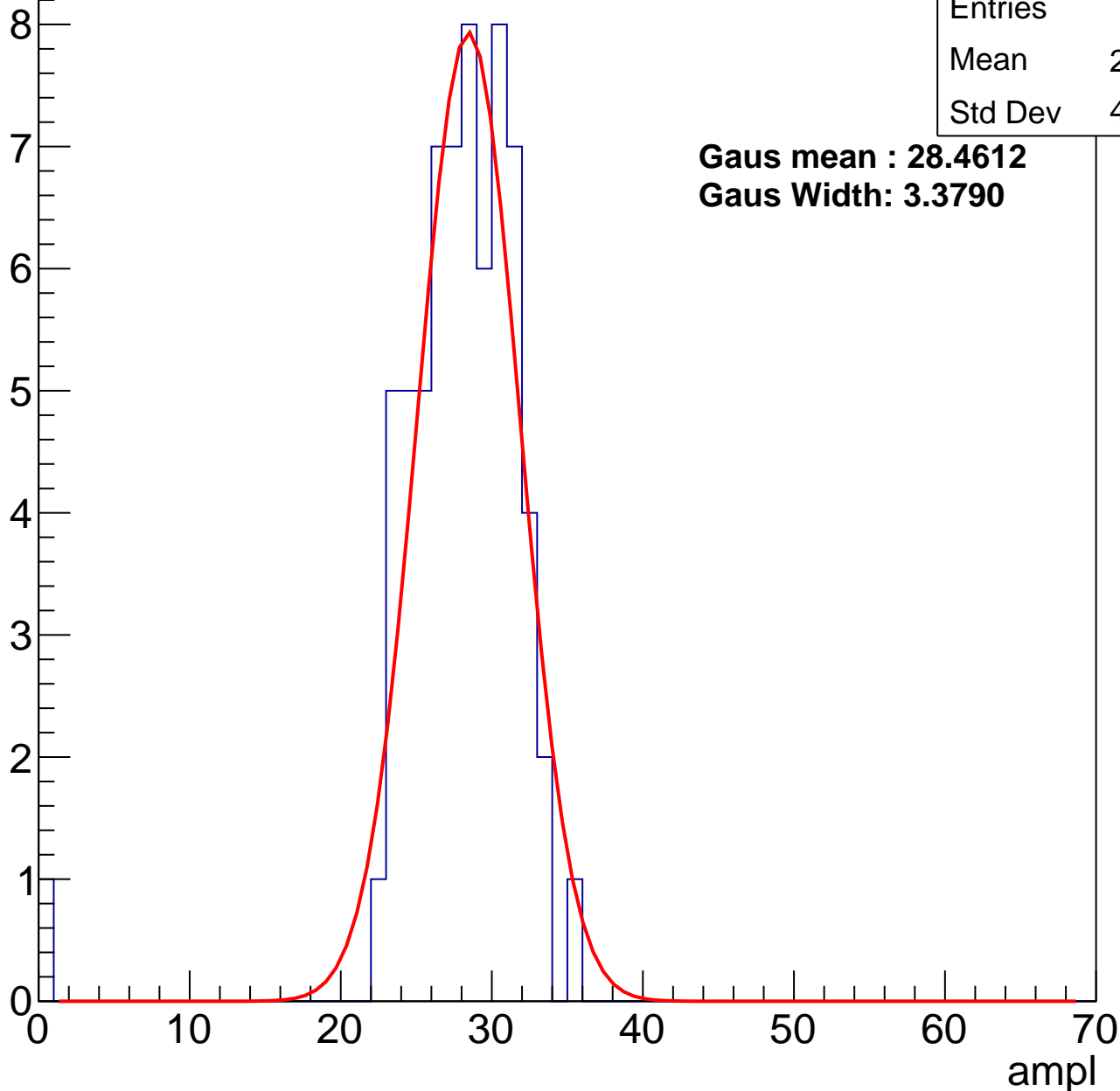
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.42
Std Dev	4.483

**Gaus mean : 28.4612**

**Gaus Width: 3.3790**



# B1L002S, U18-ch82, adc1

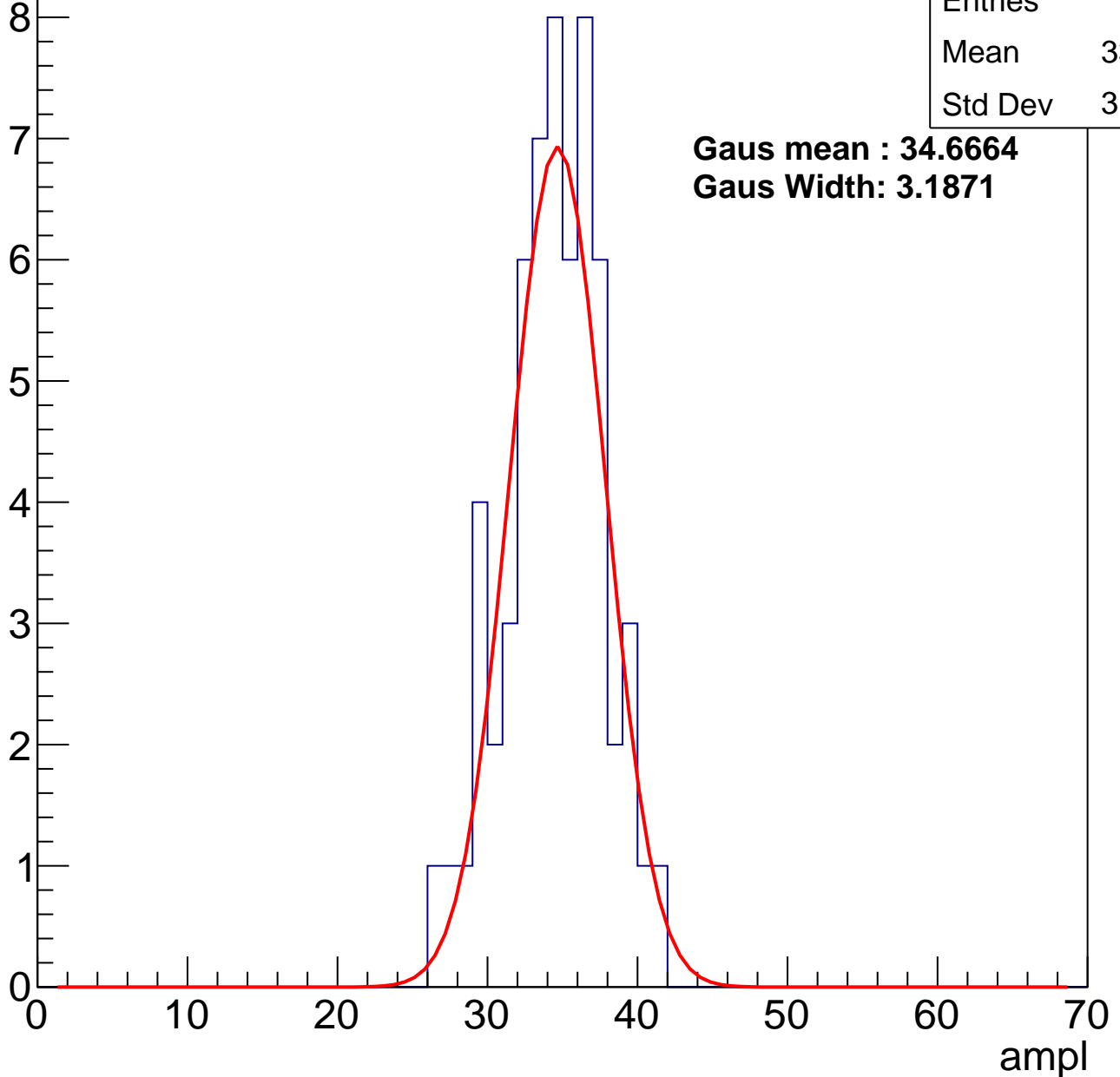
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.98
Std Dev	3.222

**Gaus mean : 34.6664**

**Gaus Width: 3.1871**



# B1L002S, U18-ch82, adc2

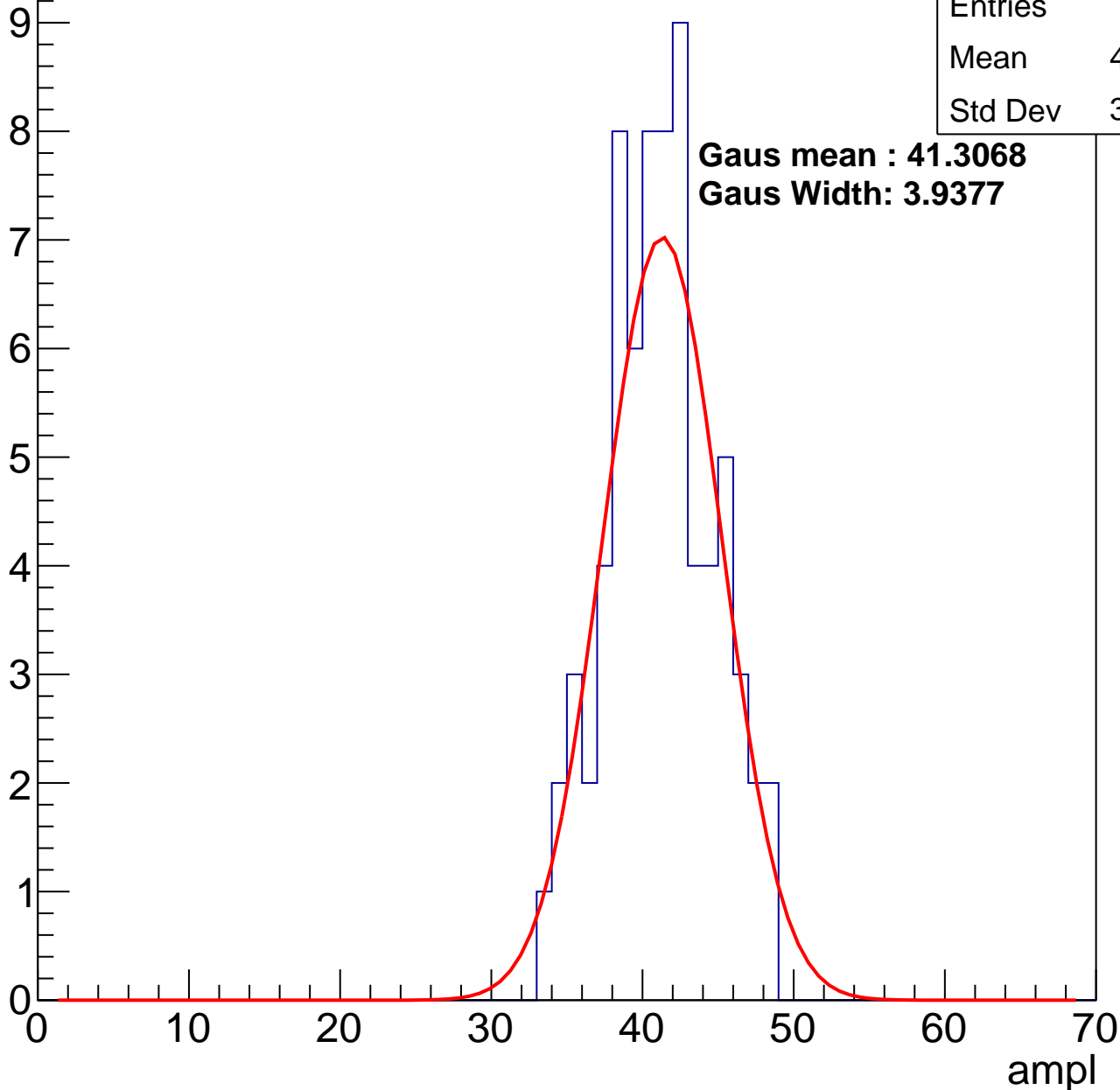
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.72
Std Dev	3.497

**Gaus mean : 41.3068**

**Gaus Width: 3.9377**



# B1L002S, U18-ch82, adc3

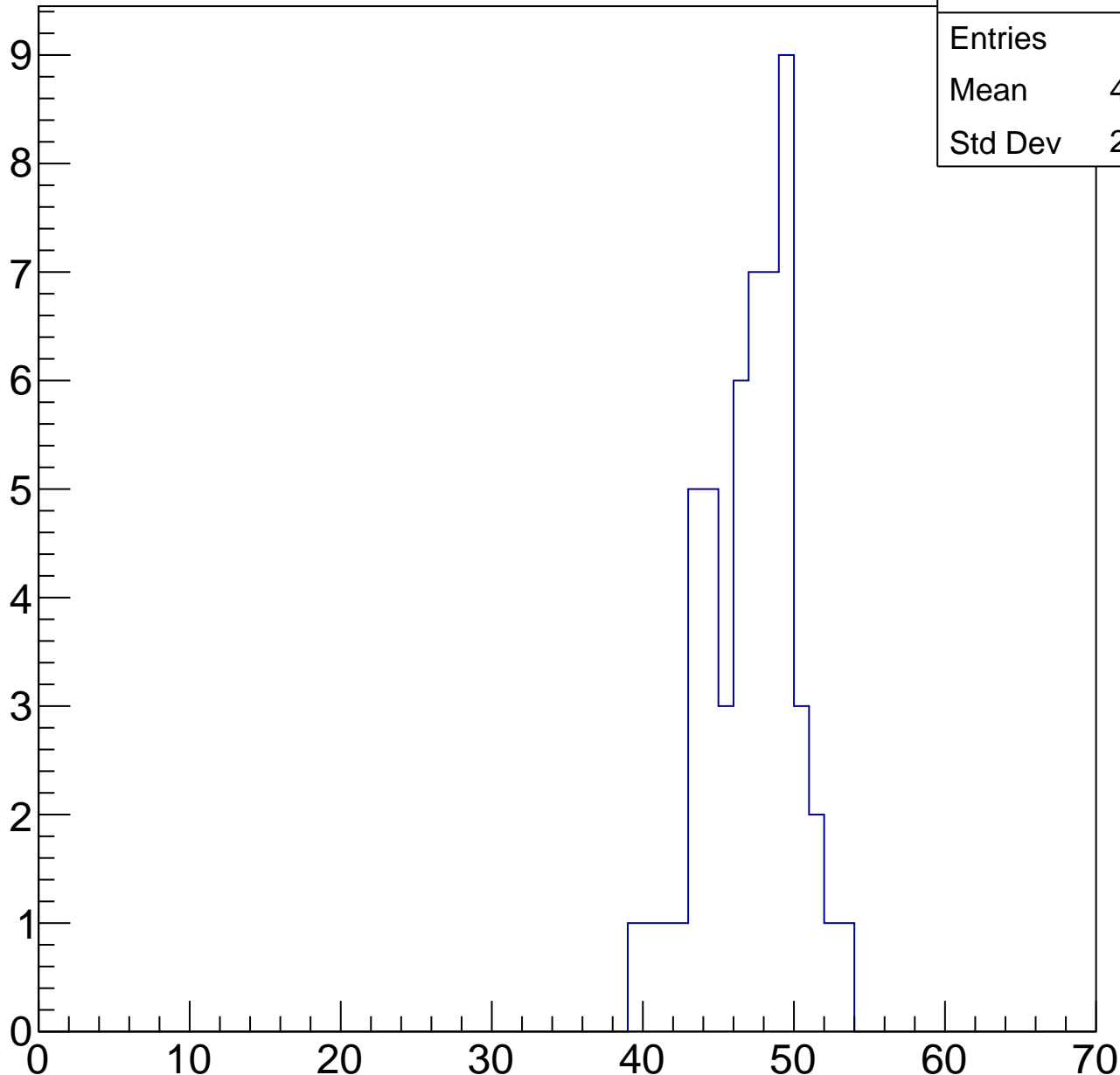
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	46.62
Std Dev	2.986

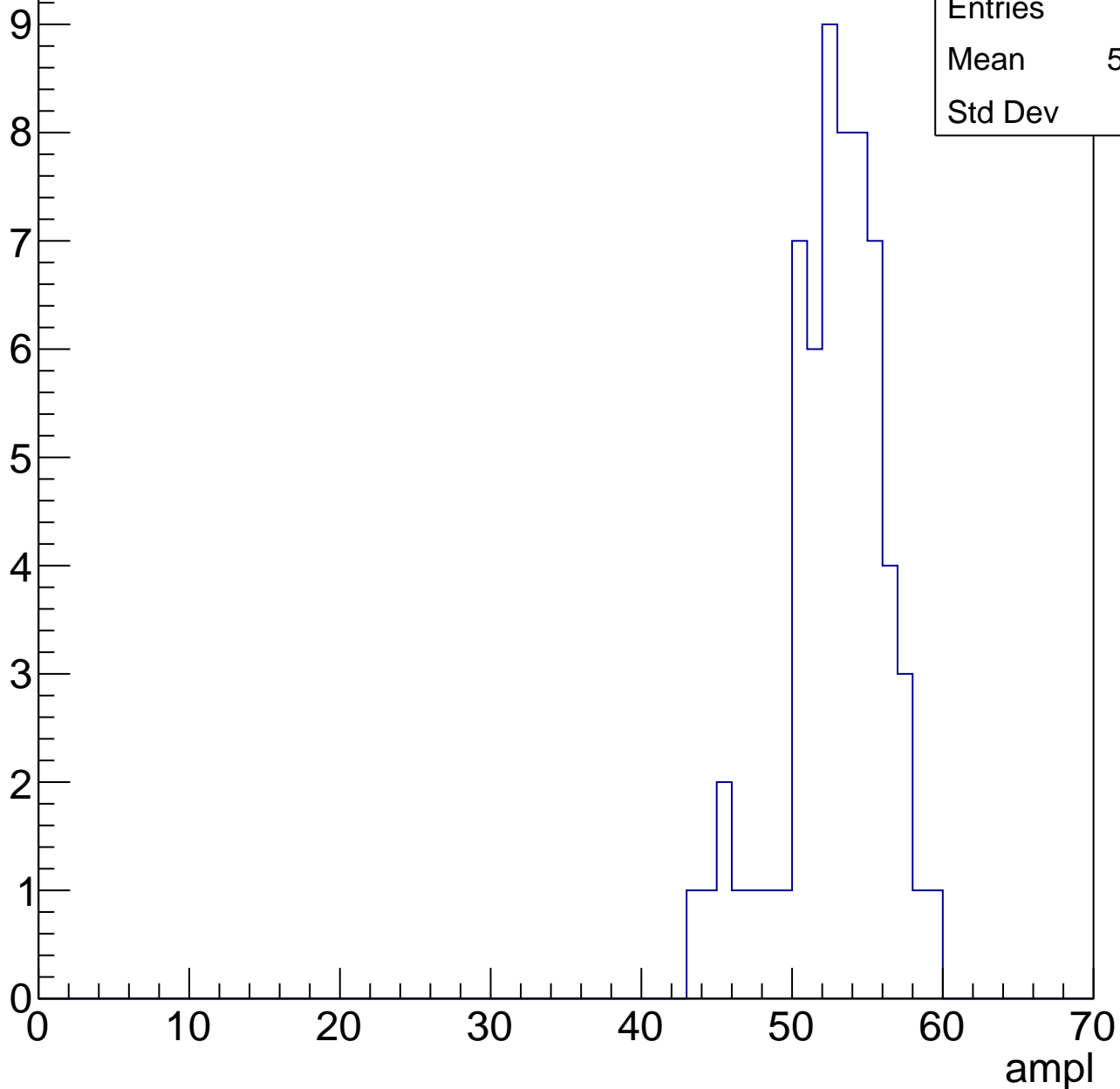
ampl



# B1L002S, U18-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

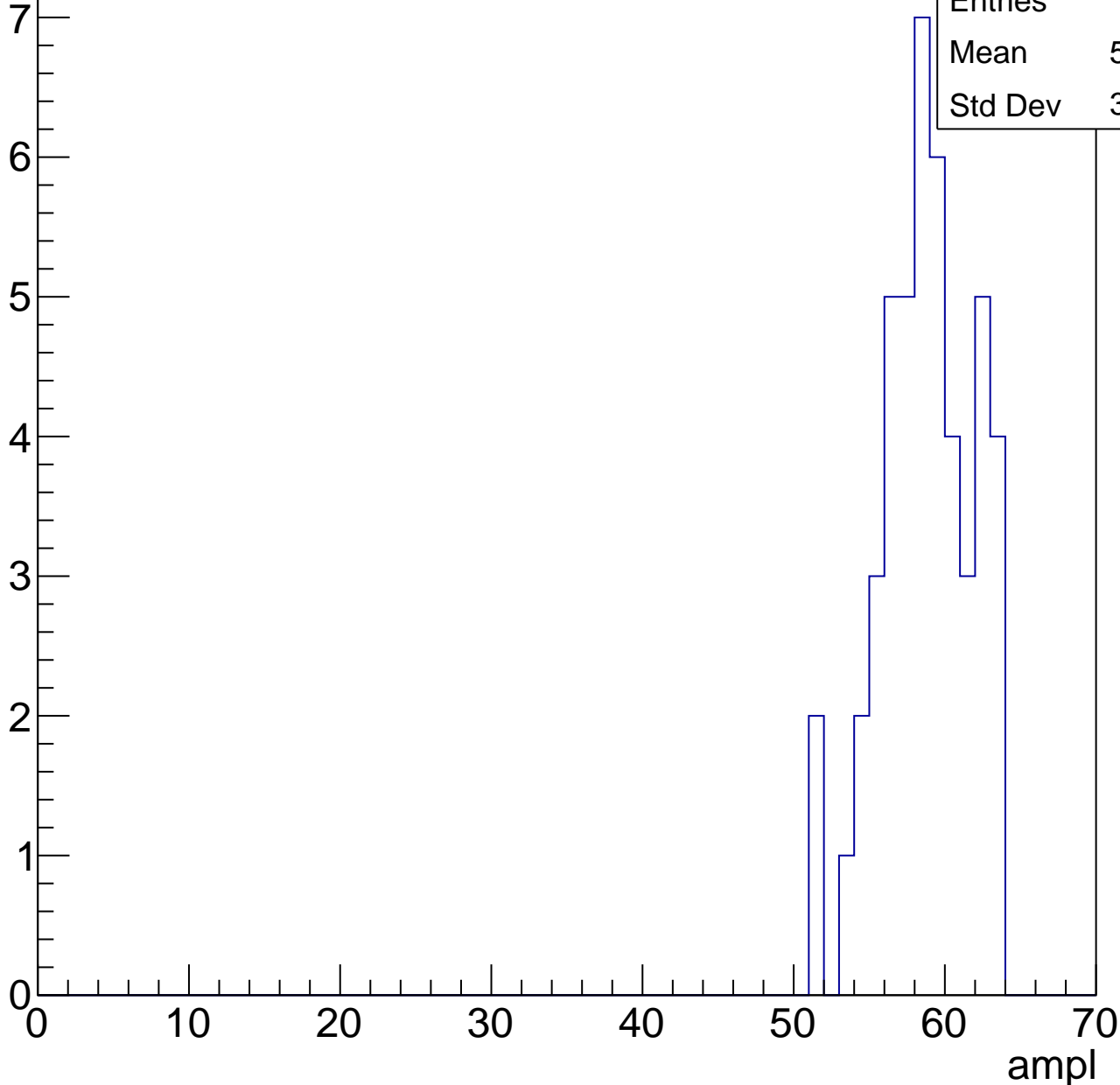


# B1L002S, U18-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

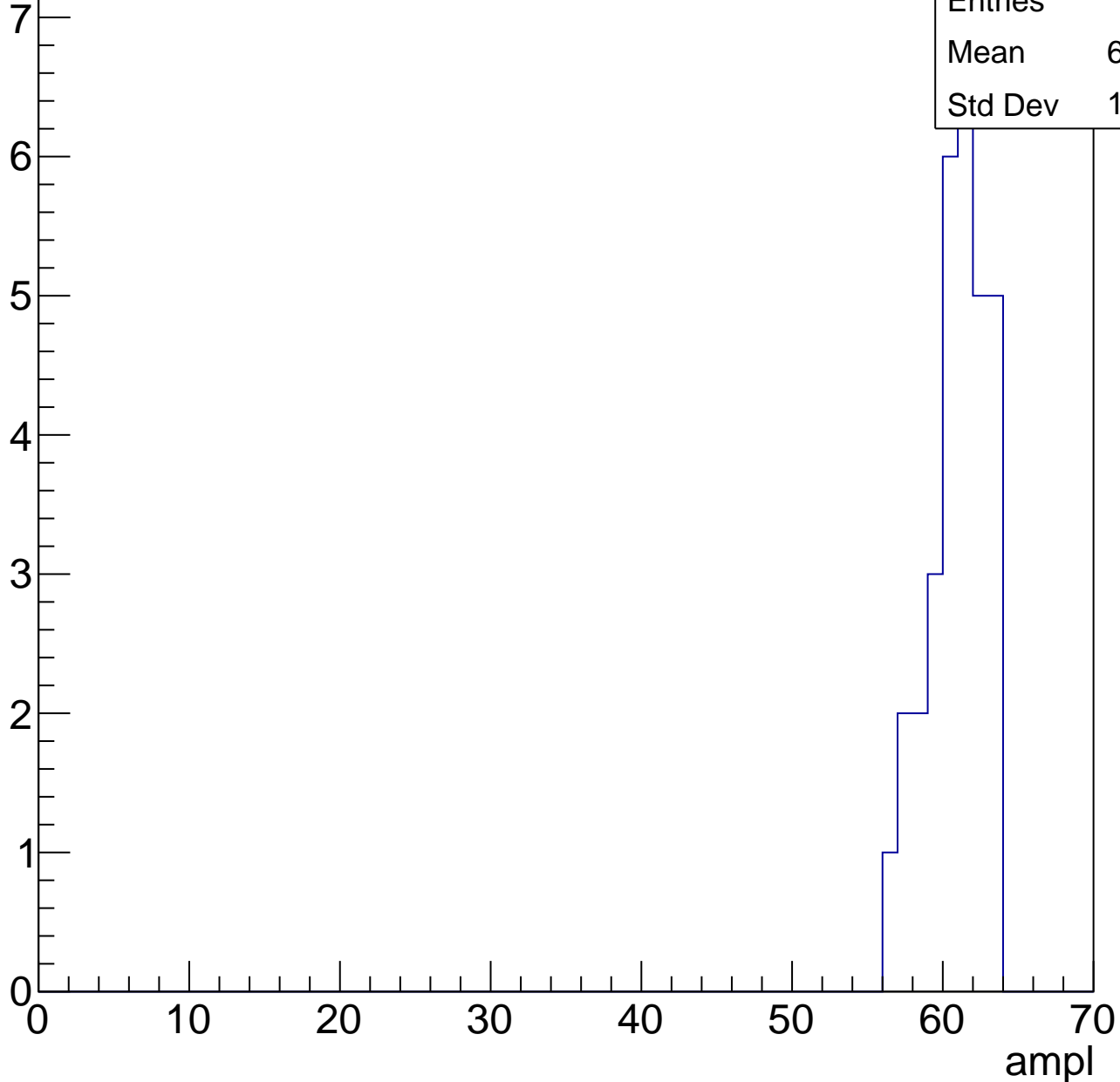
Entries	47
Mean	58.26
Std Dev	3.035



# B1L002S, U18-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	31
Mean	60.48
Std Dev	1.882



# B1L002S, U18-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U18-ch83, adc0

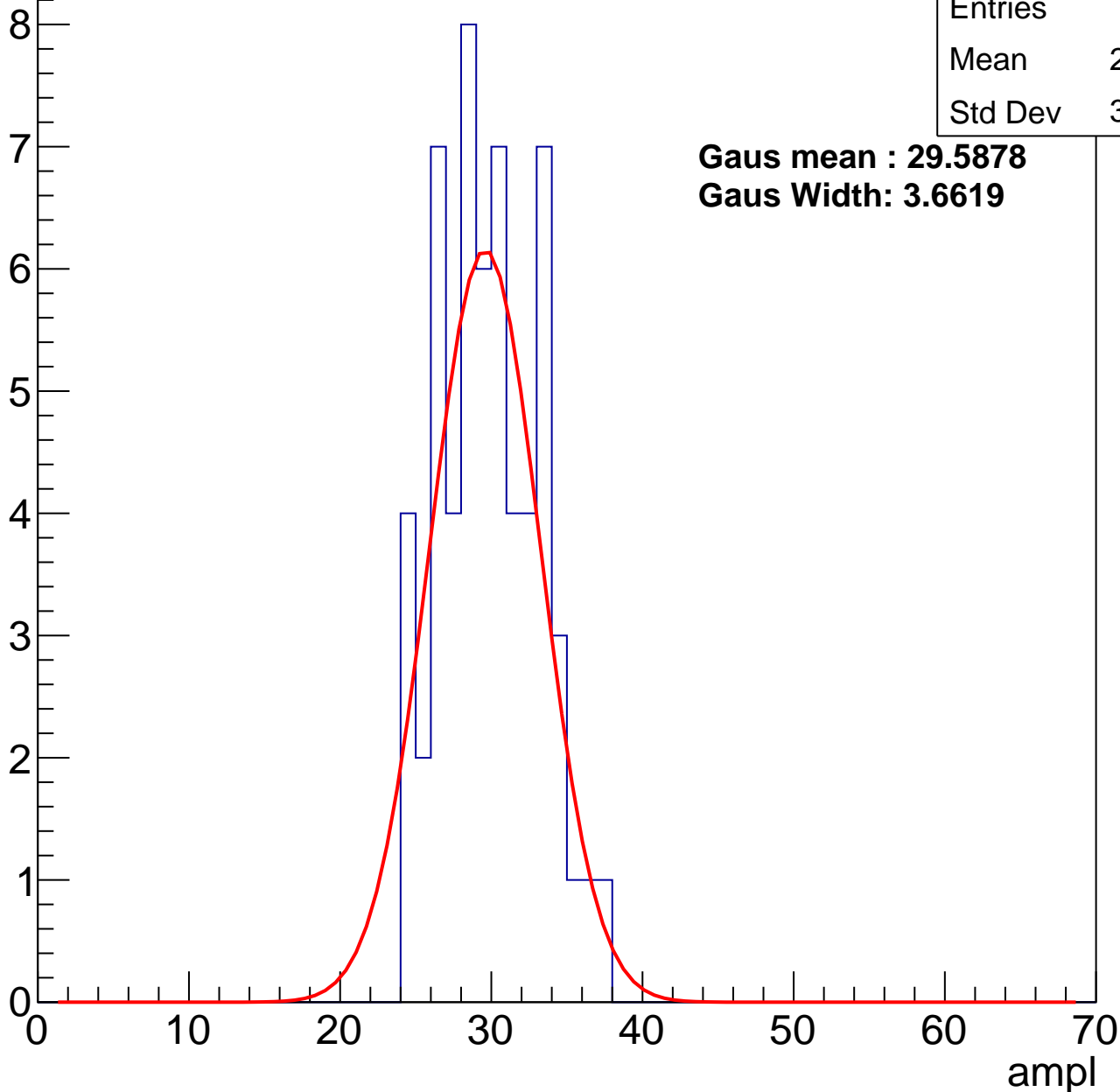
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	29.44
Std Dev	3.196

**Gaus mean : 29.5878**

**Gaus Width: 3.6619**



# B1L002S, U18-ch83, adc1

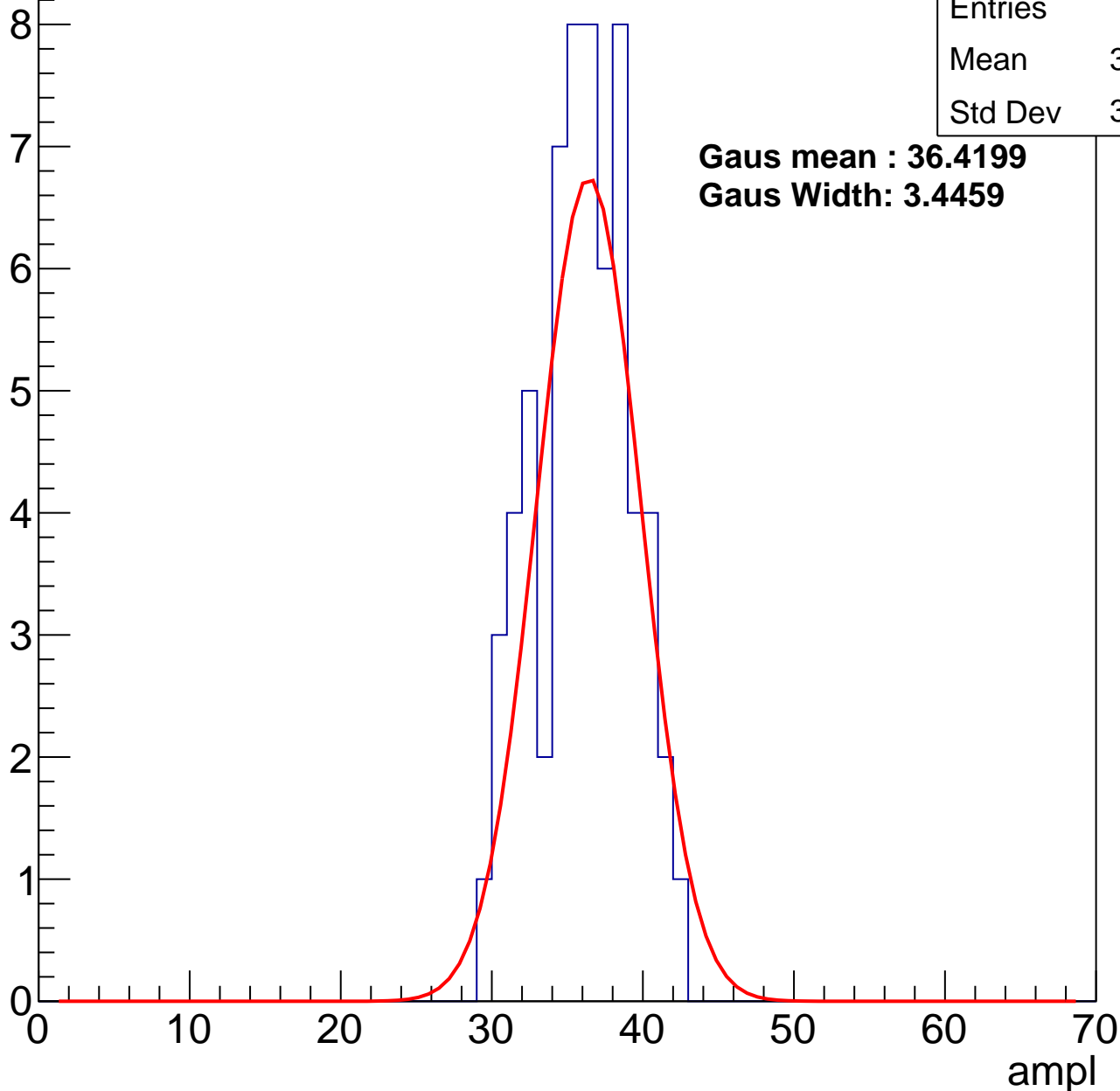
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.57
Std Dev	3.095

**Gaus mean : 36.4199**

**Gaus Width: 3.4459**



# B1L002S, U18-ch83, adc2

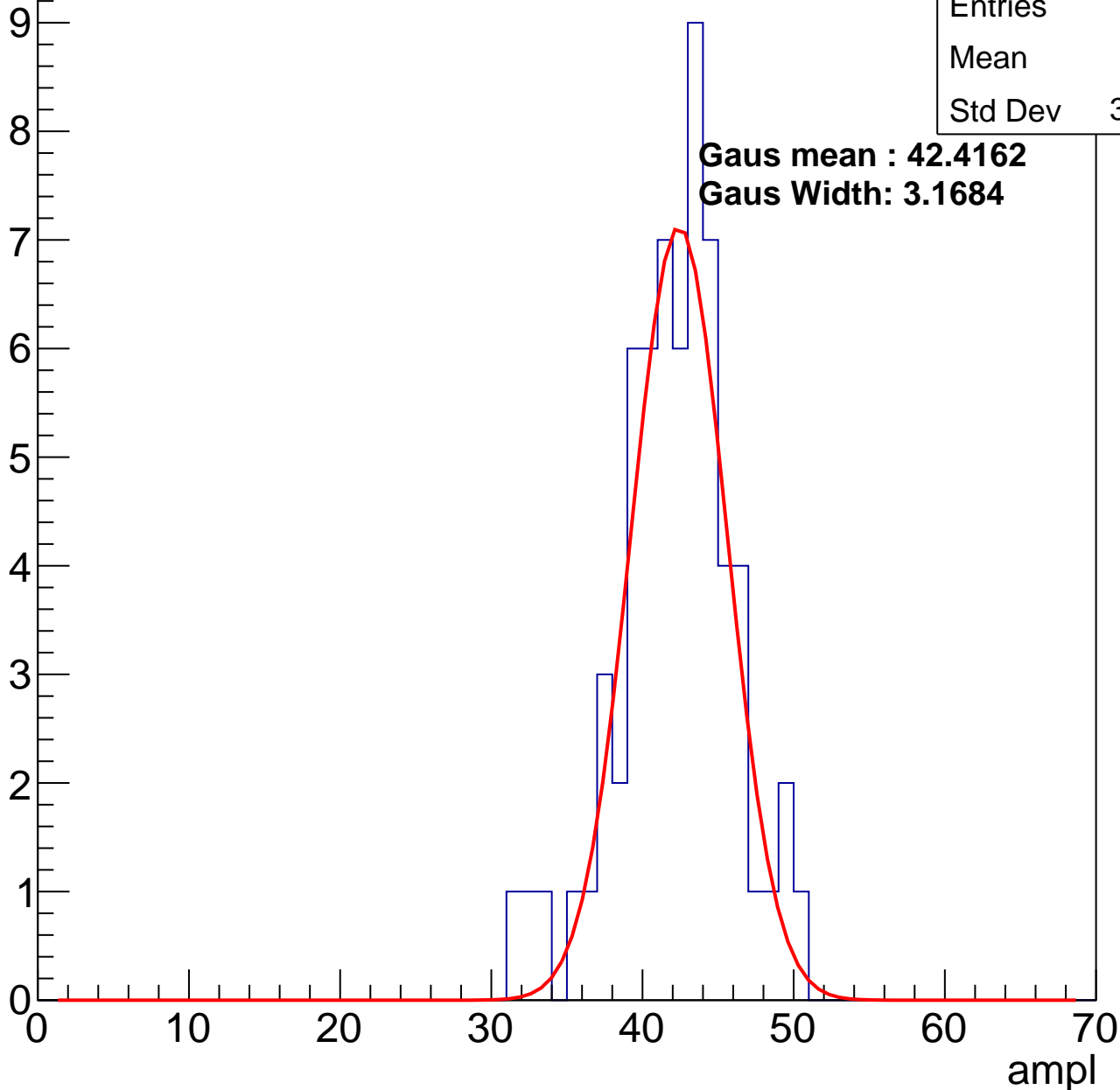
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.7
Std Dev	3.819

**Gaus mean : 42.4162**

**Gaus Width: 3.1684**

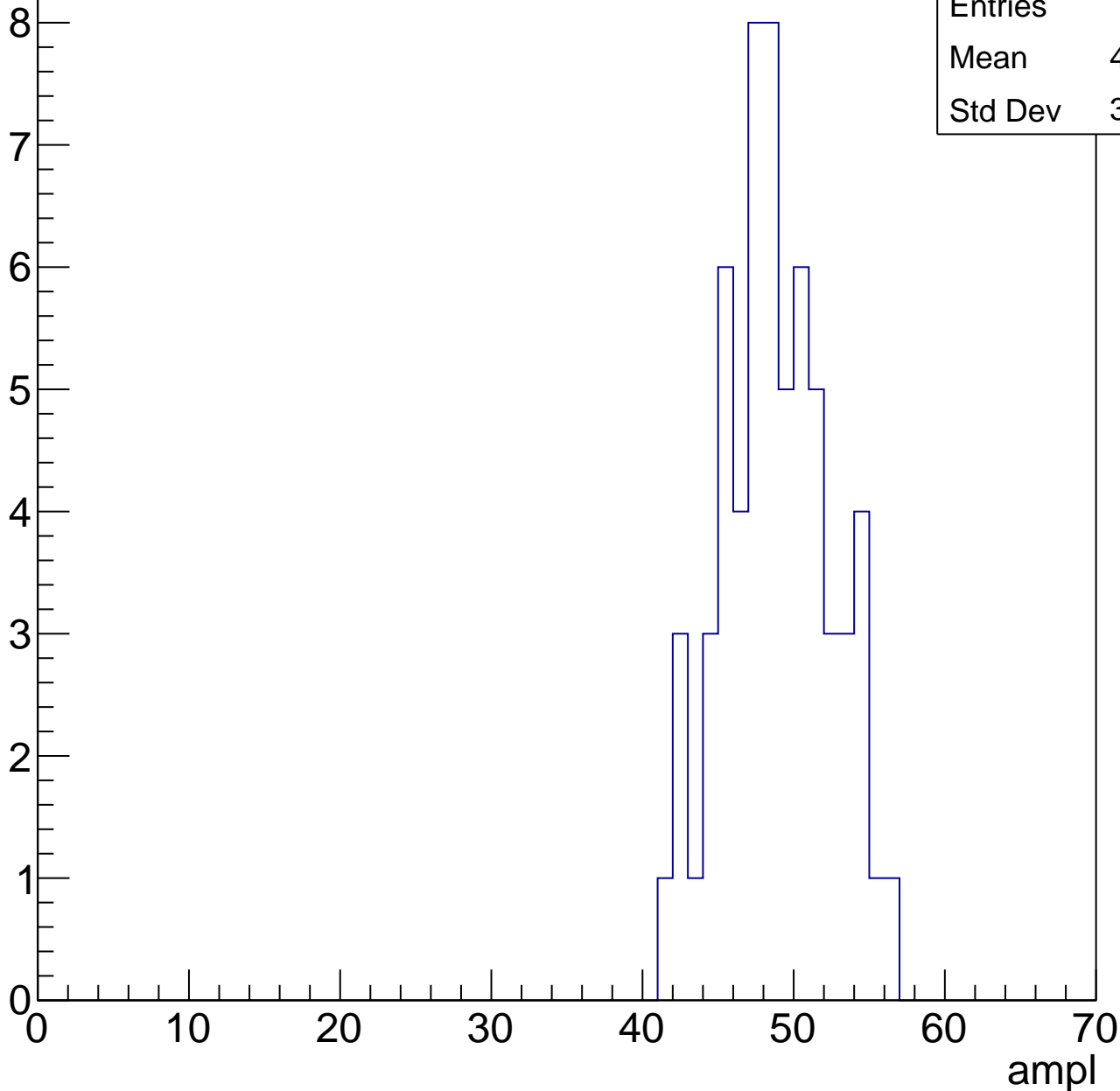


# B1L002S, U18-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	48.35
Std Dev	3.488

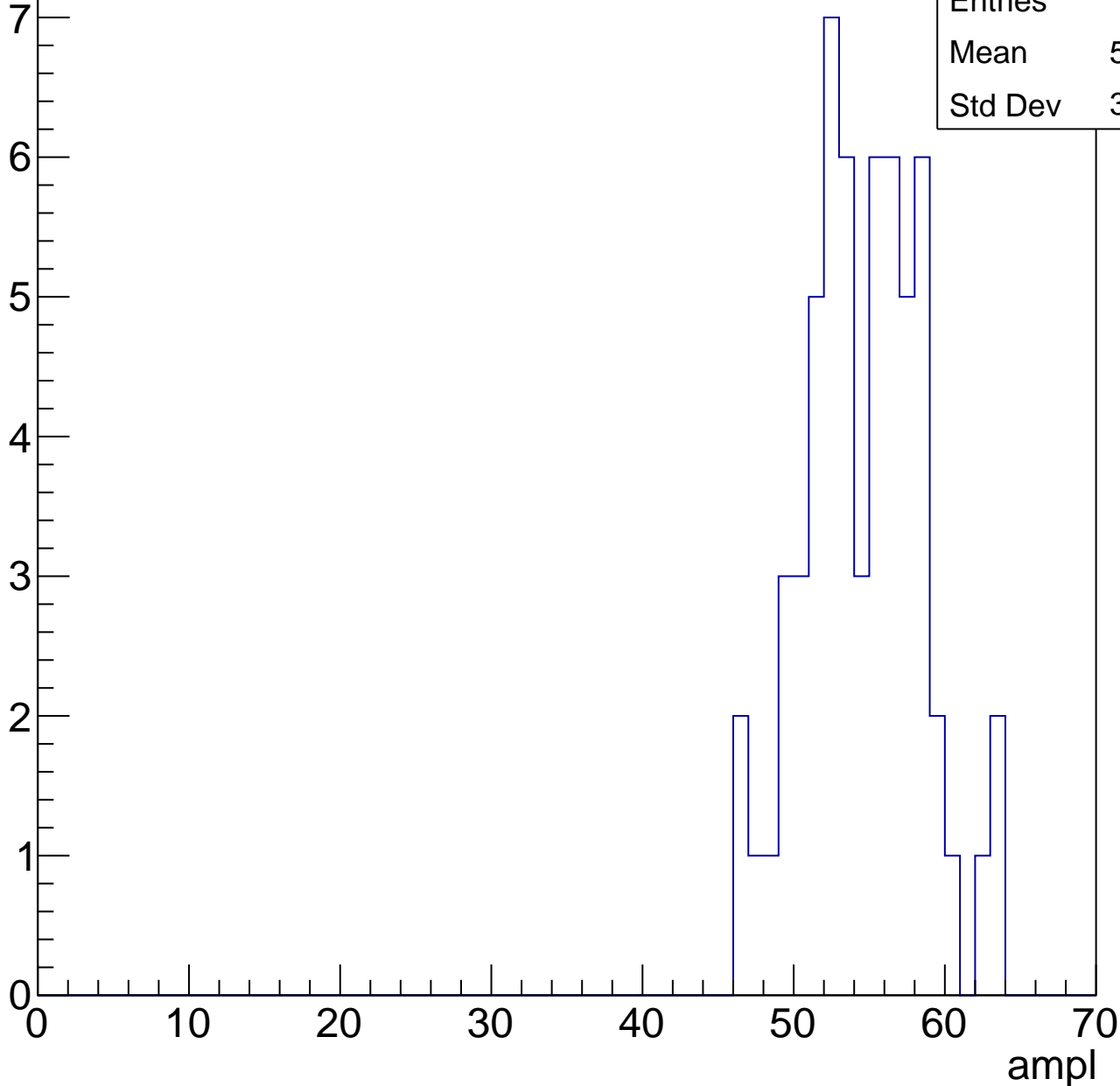


# B1L002S, U18-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	54.13
Std Dev	3.866



# B1L002S, U18-ch83, adc5

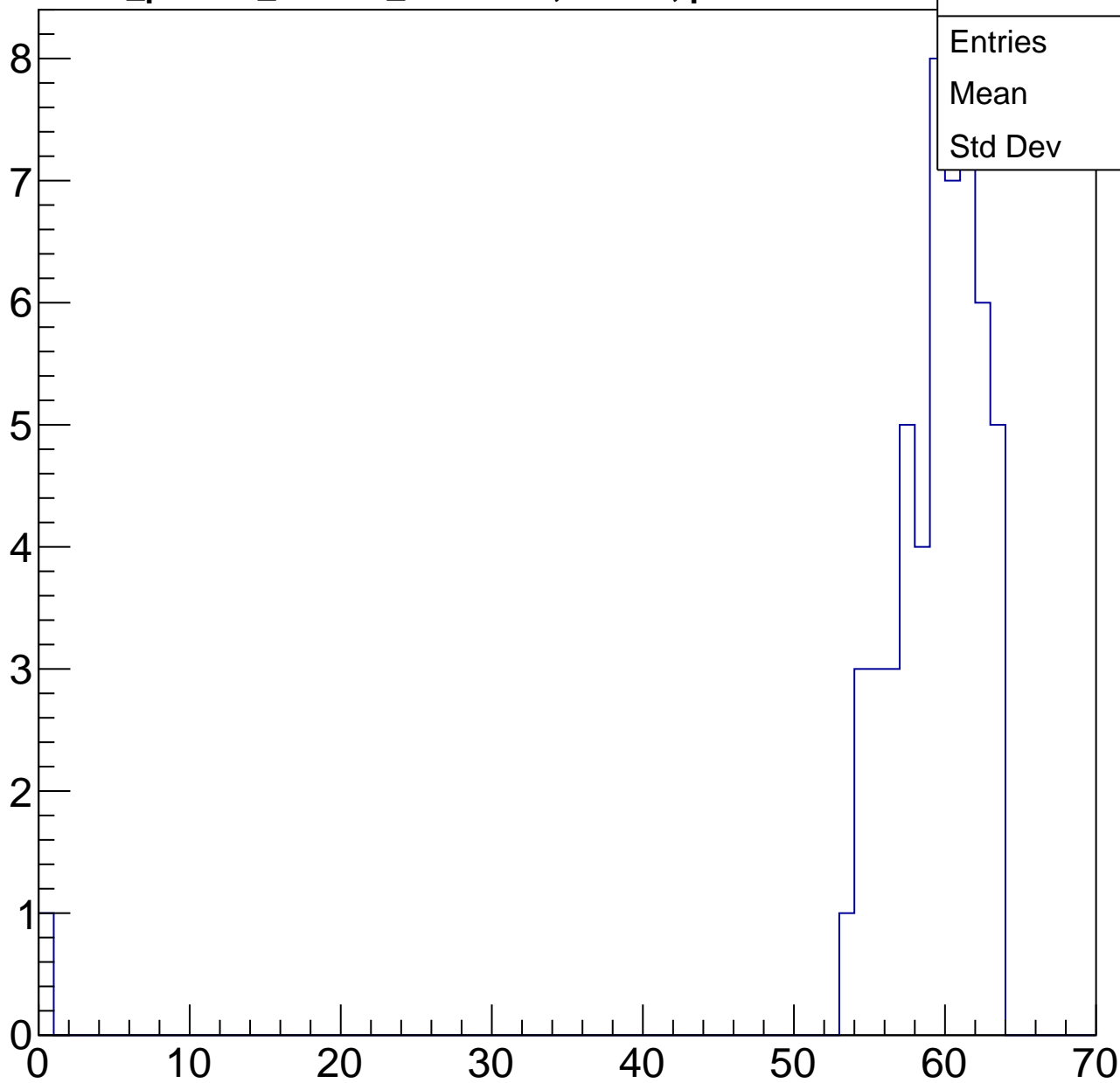
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58
Std Dev	8.4

ampl



# B1L002S, U18-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.7

Std Dev

1.1



# B1L002S, U18-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch84, adc0

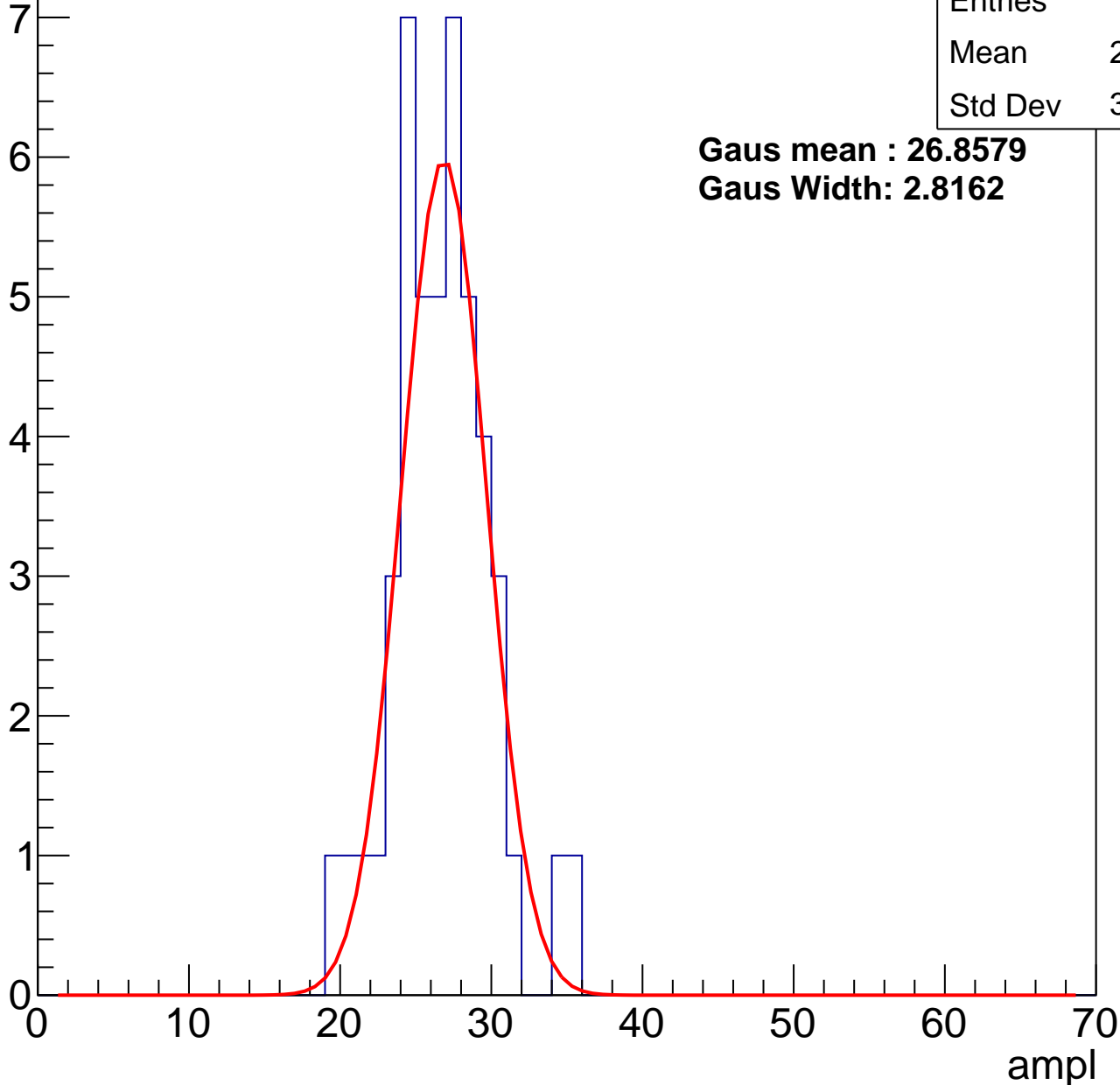
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	26.28
Std Dev	3.174

**Gaus mean : 26.8579**

**Gaus Width: 2.8162**



# B1L002S, U18-ch84, adc1

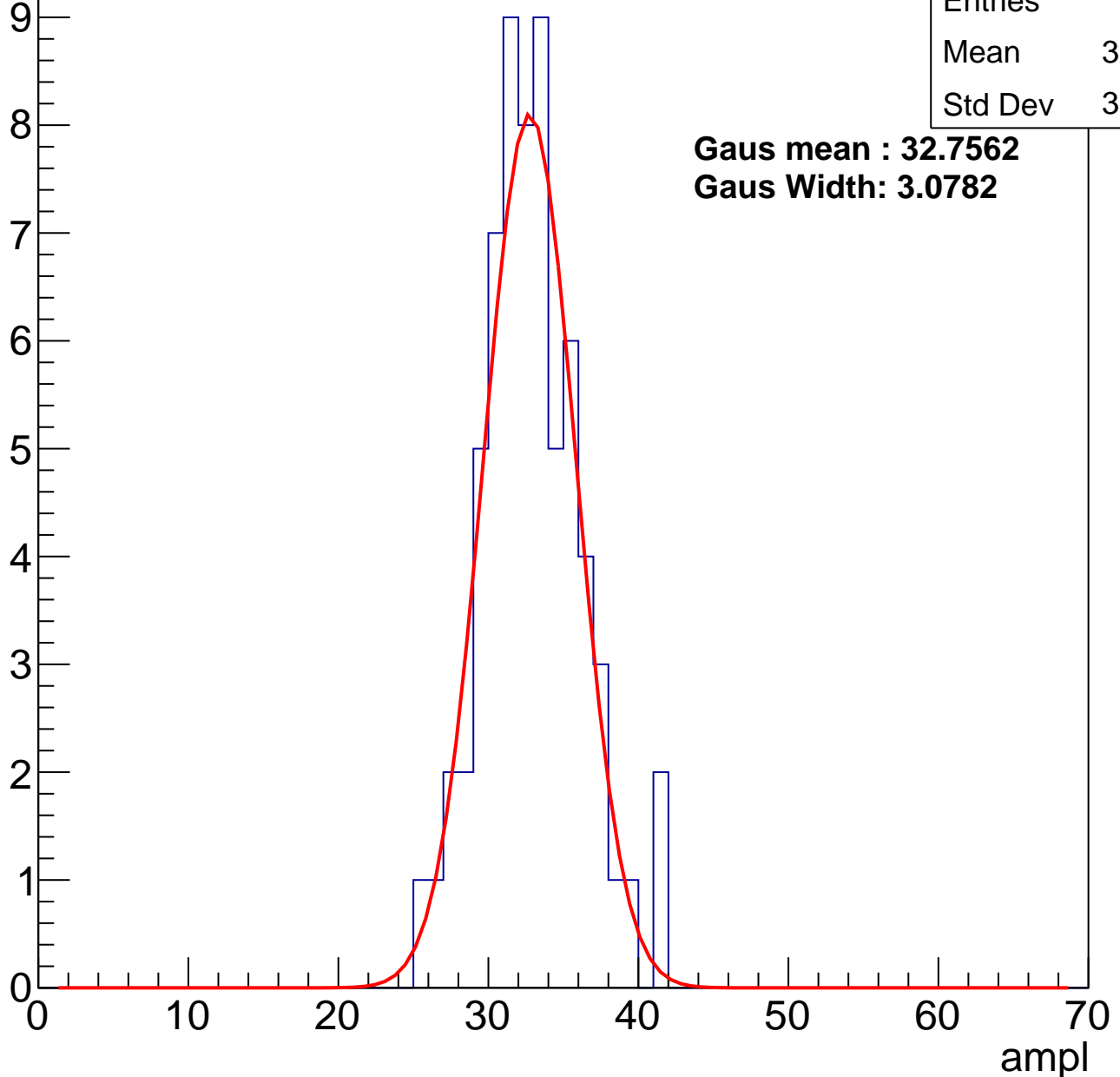
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	32.45
Std Dev	3.267

**Gaus mean : 32.7562**

**Gaus Width: 3.0782**



# B1L002S, U18-ch84, adc2

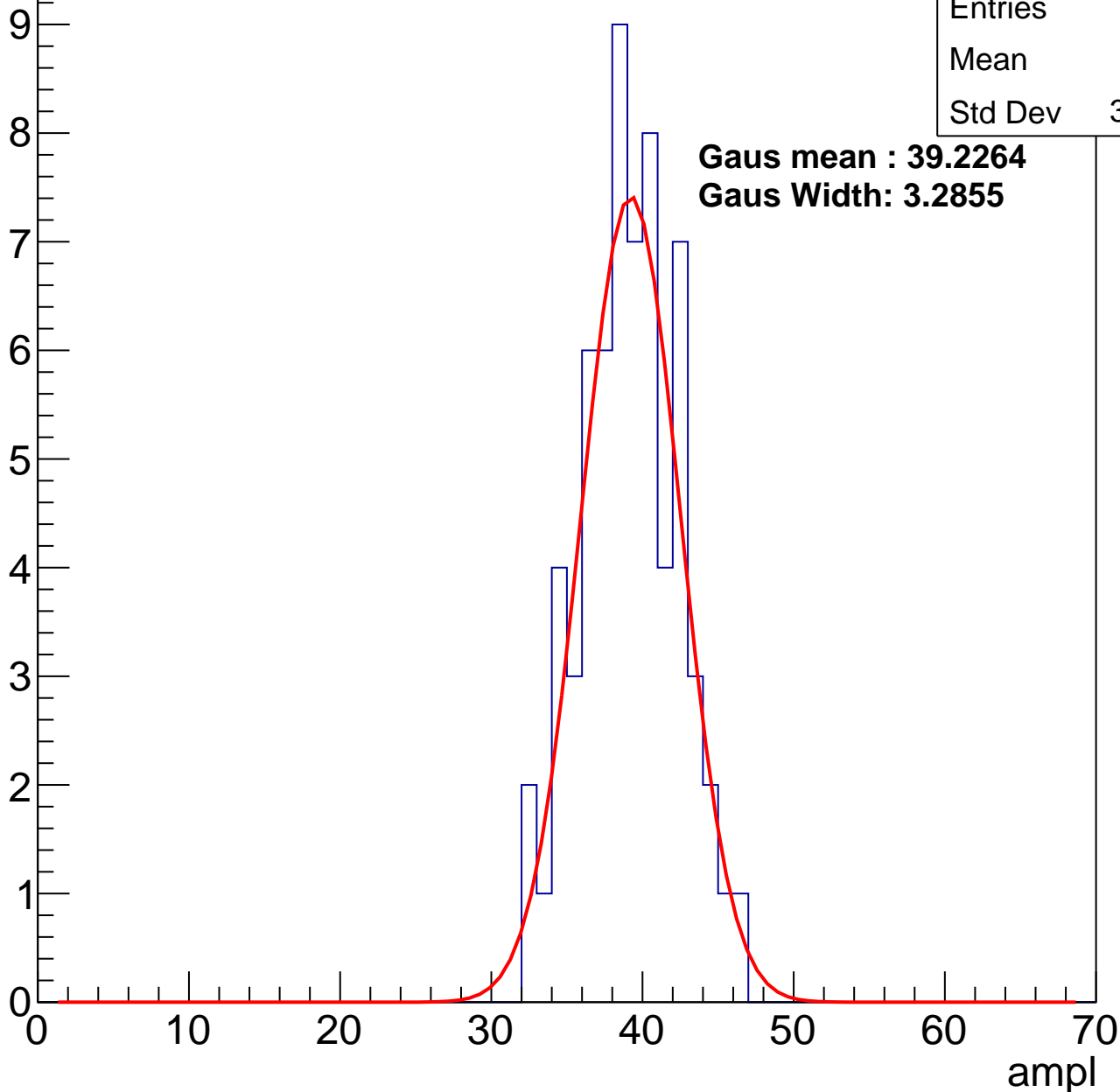
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	38.7
Std Dev	3.146

**Gaus mean : 39.2264**

**Gaus Width: 3.2855**

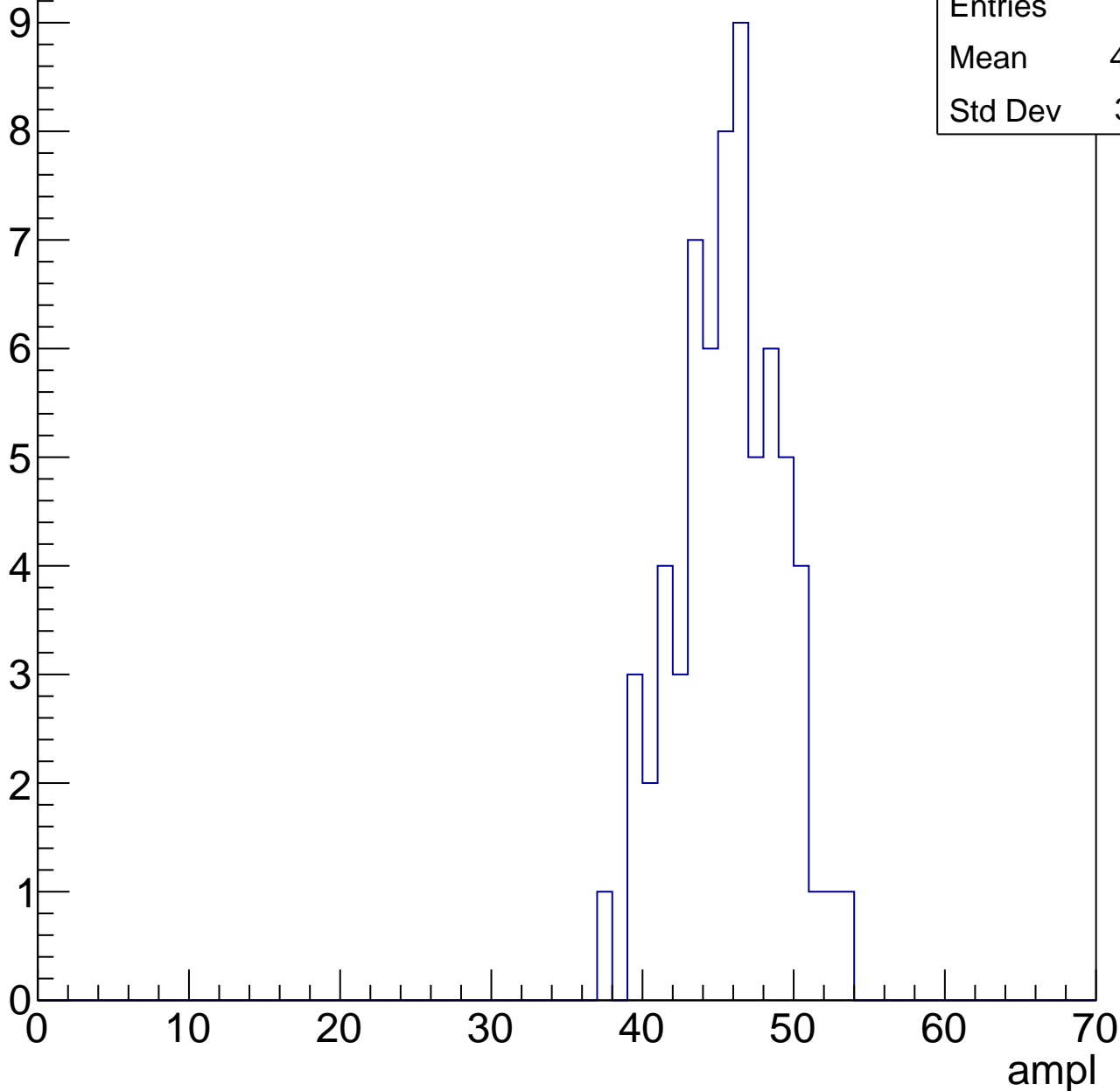


# B1L002S, U18-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	45.26
Std Dev	3.381

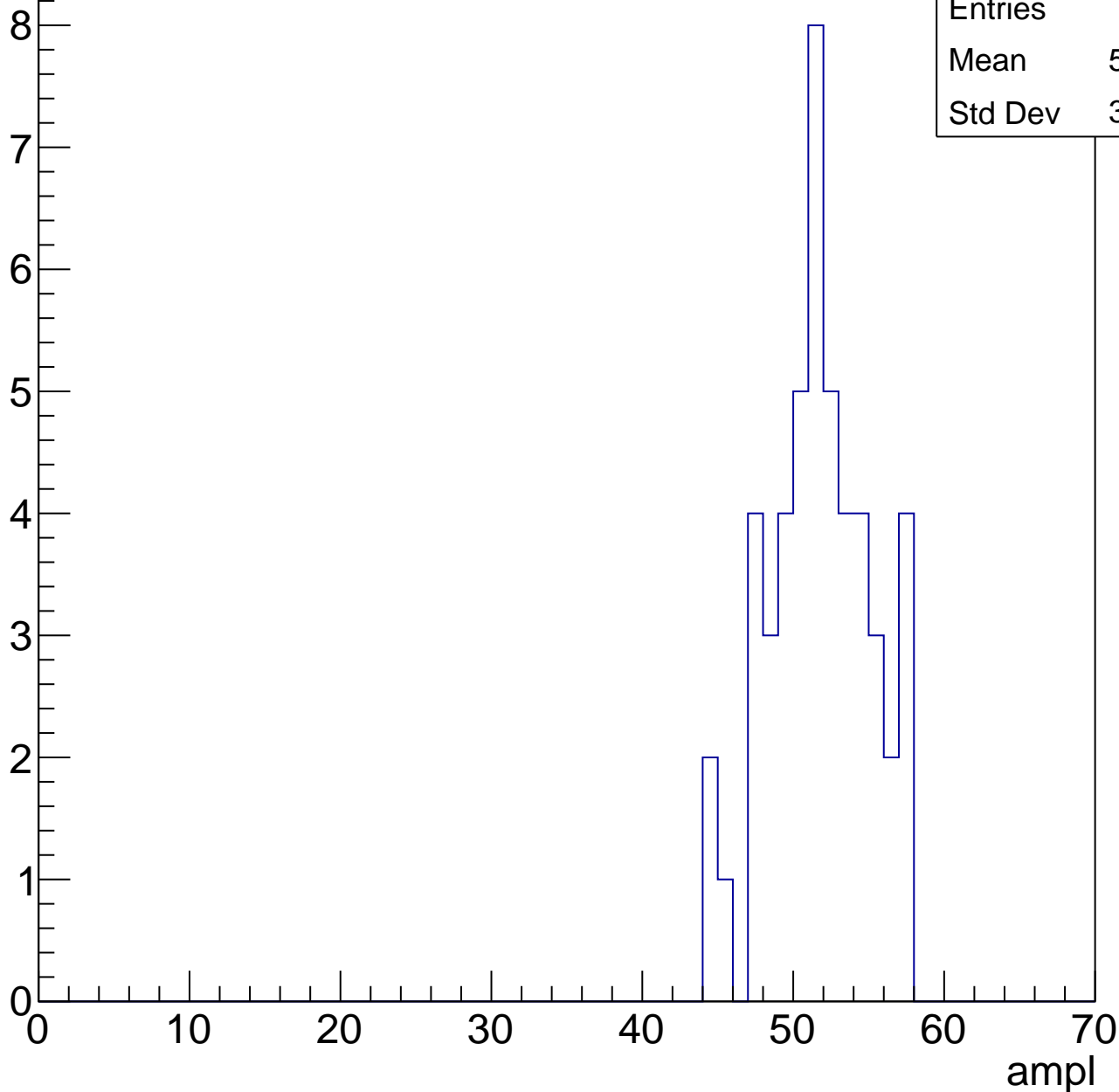


# B1L002S, U18-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	51.27
Std Dev	3.324

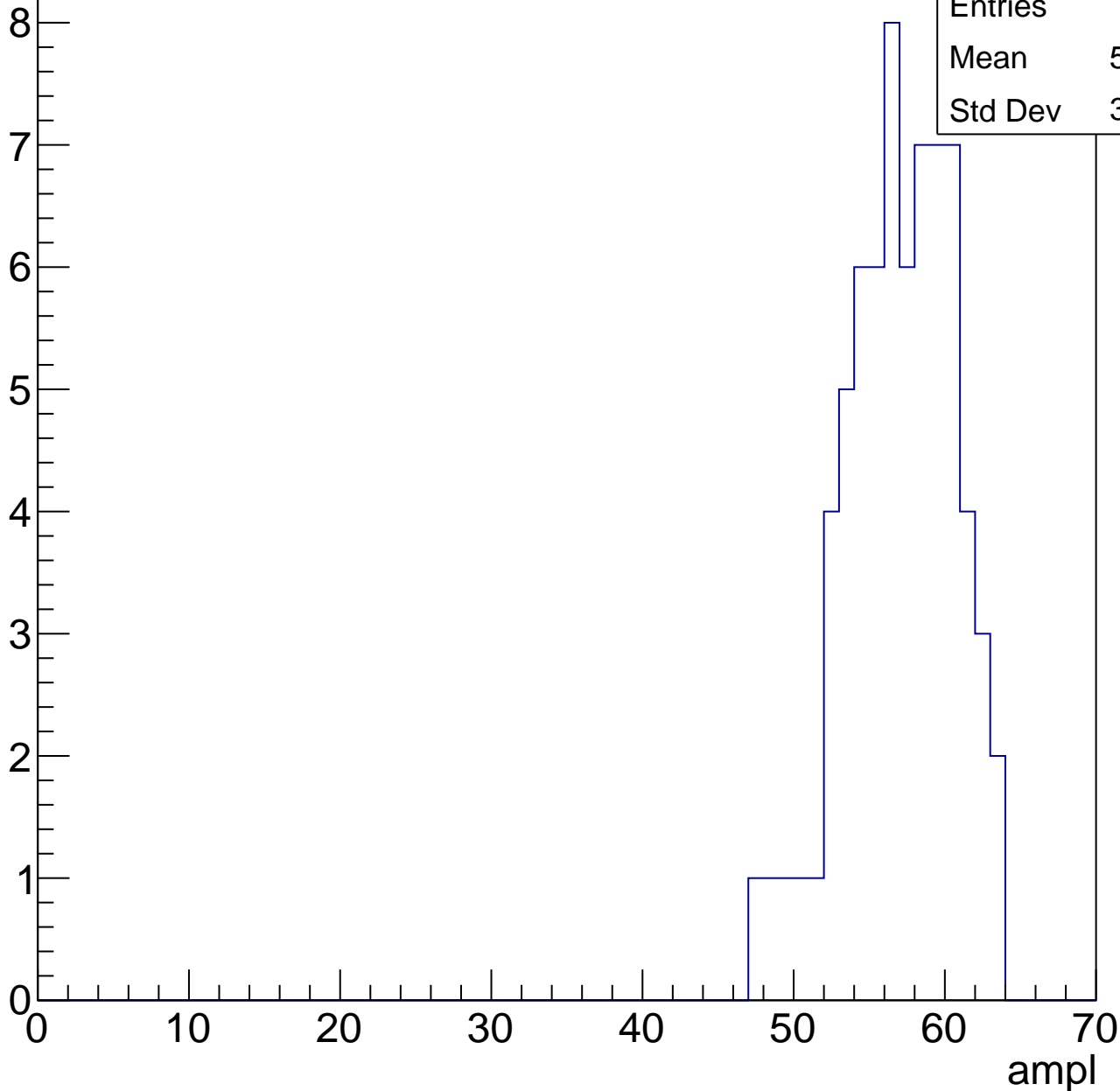


# B1L002S, U18-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	56.53
Std Dev	3.565

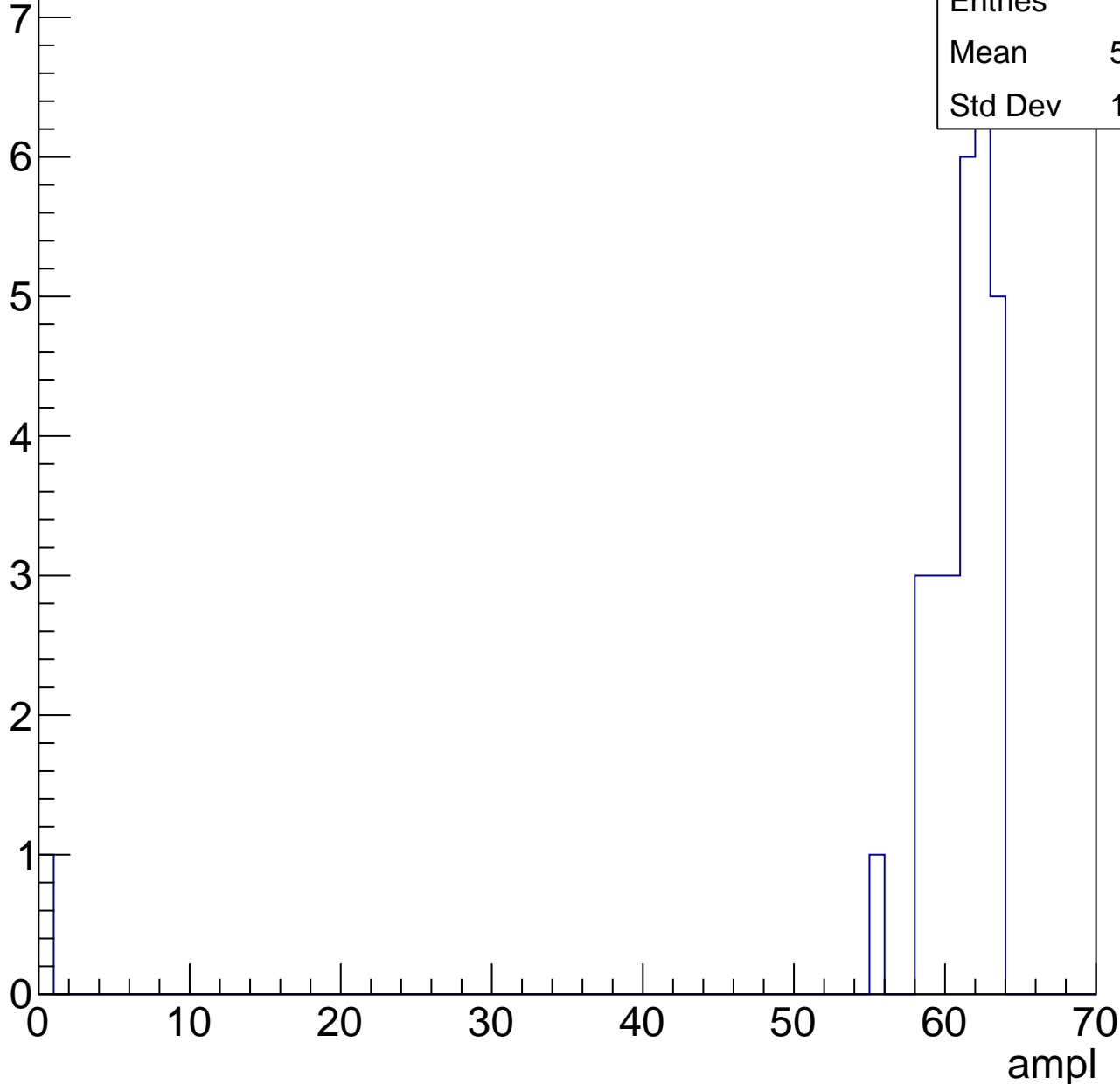


# B1L002S, U18-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.66
Std Dev	11.24

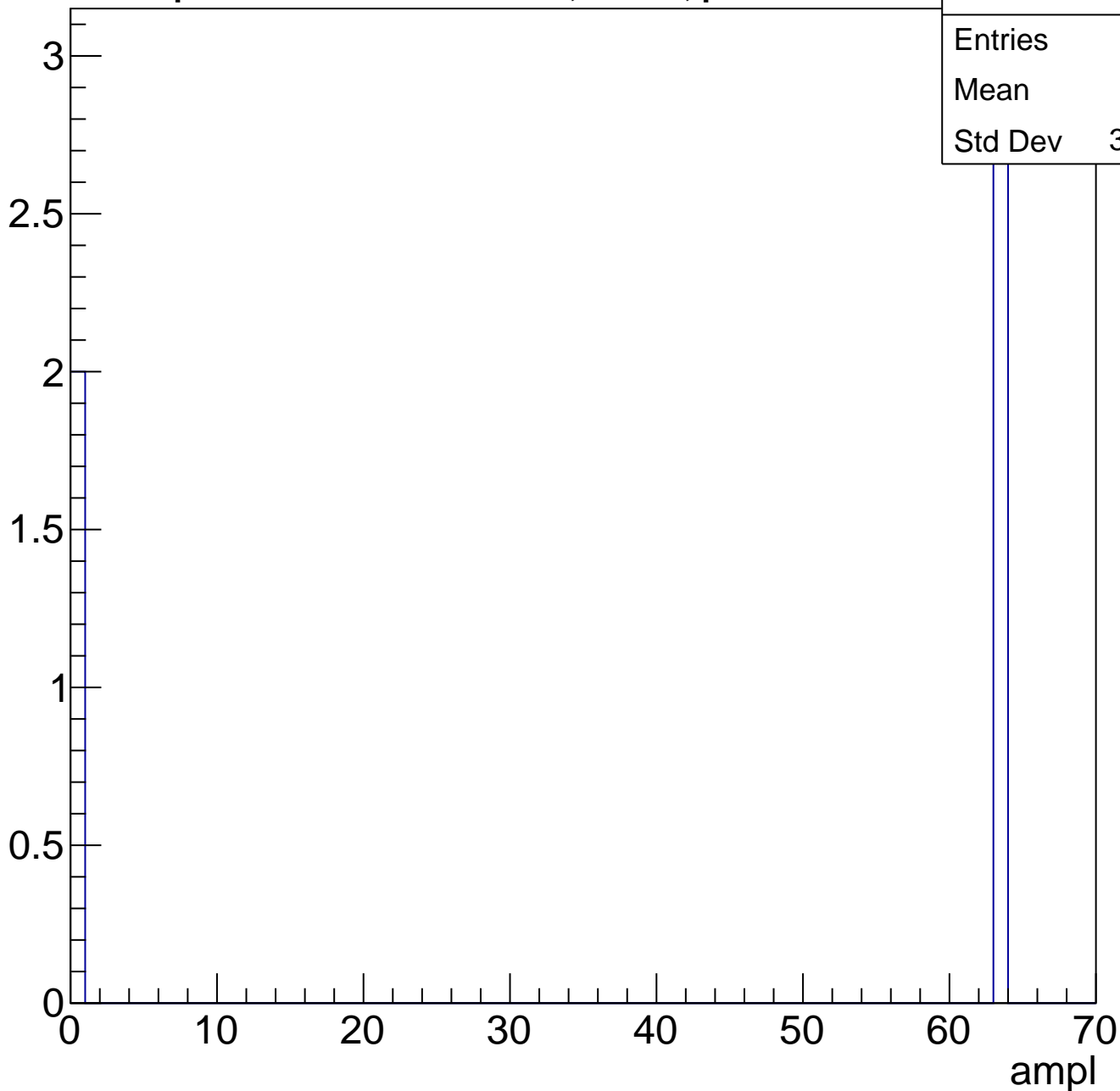




# B1L002S, U18-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch85, adc0

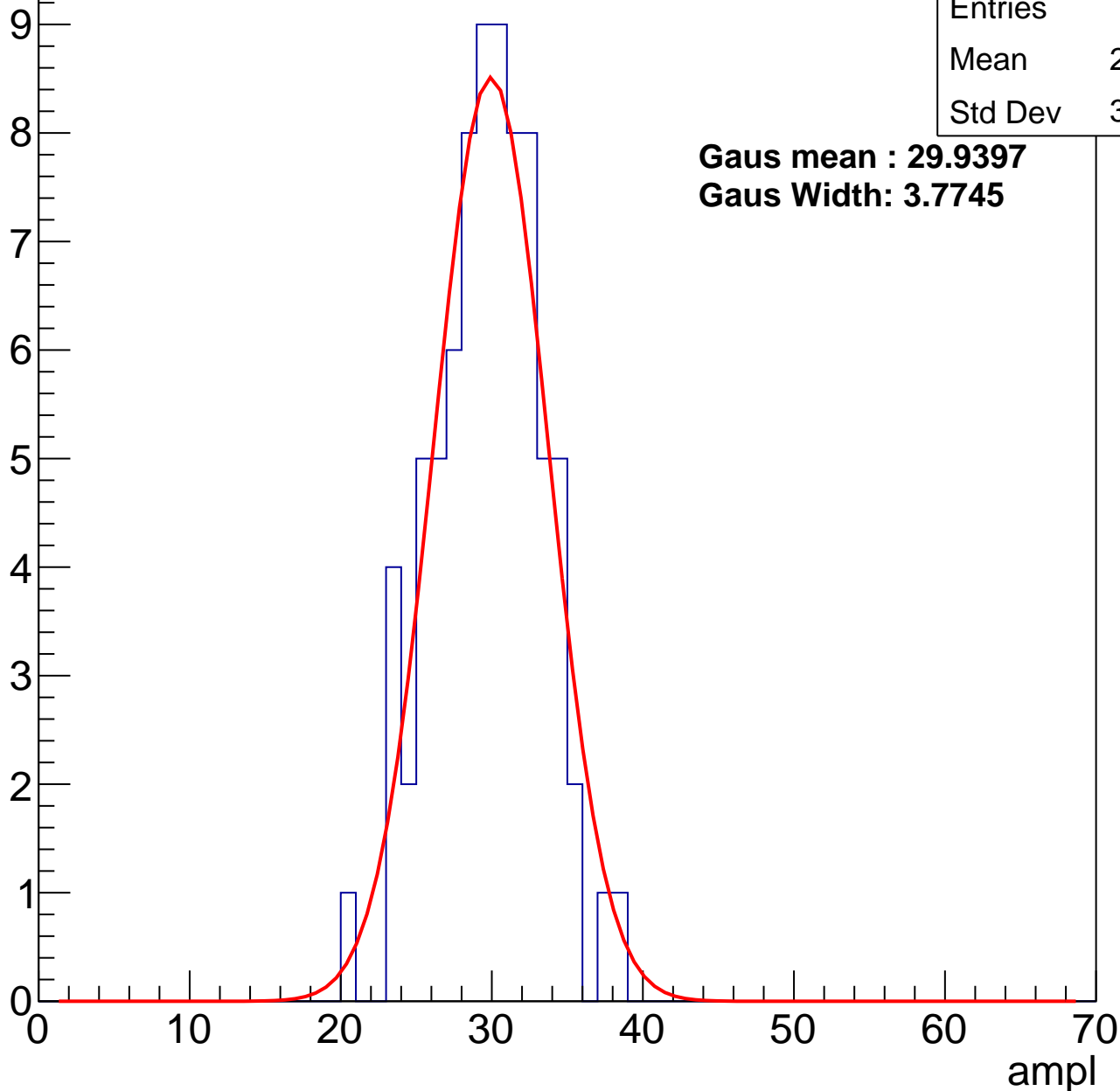
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	29.32
Std Dev	3.484

**Gaus mean : 29.9397**

**Gaus Width: 3.7745**



# B1L002S, U18-ch85, adc1

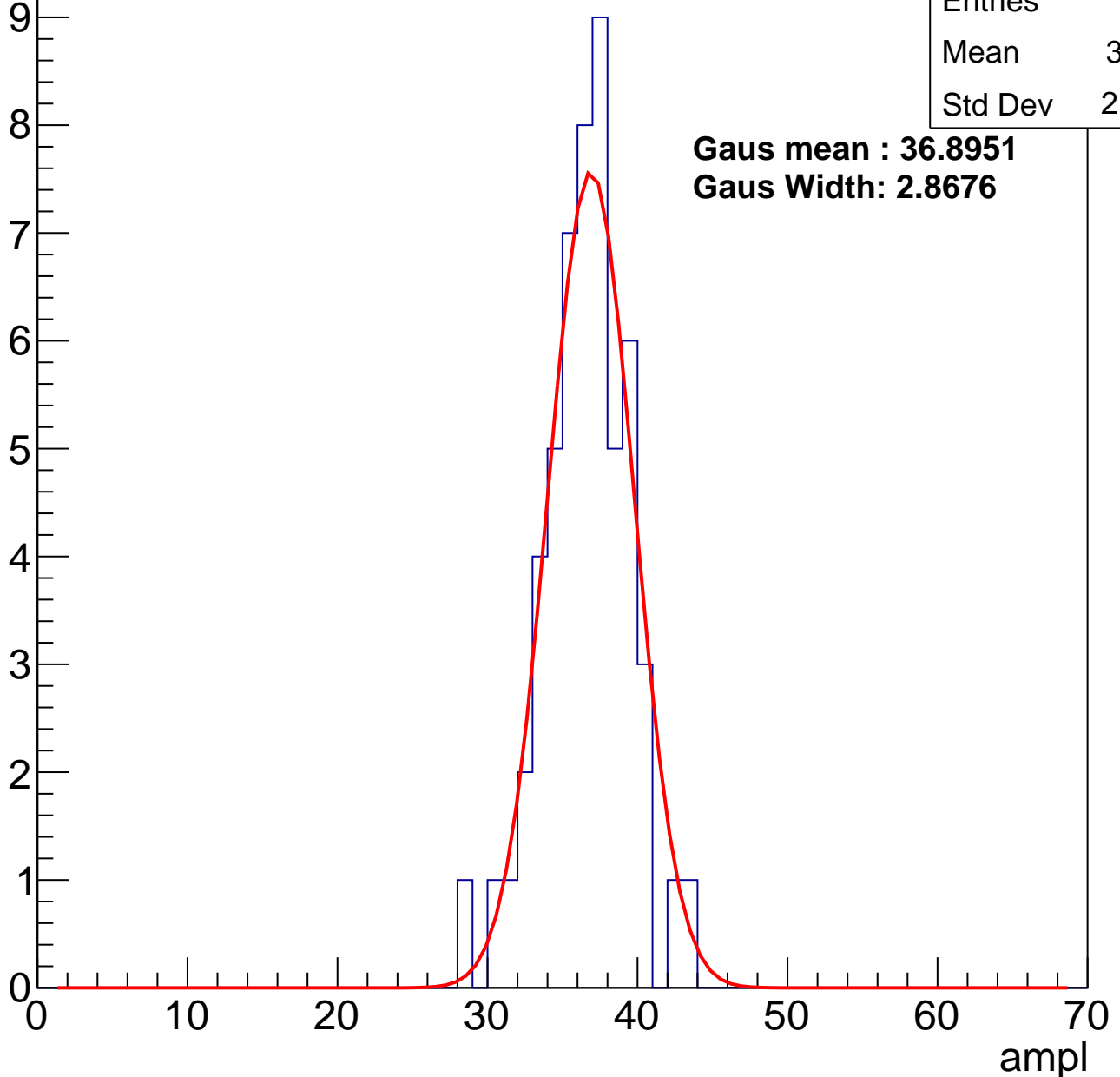
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	36.11
Std Dev	2.839

**Gaus mean : 36.8951**

**Gaus Width: 2.8676**



# B1L002S, U18-ch85, adc2

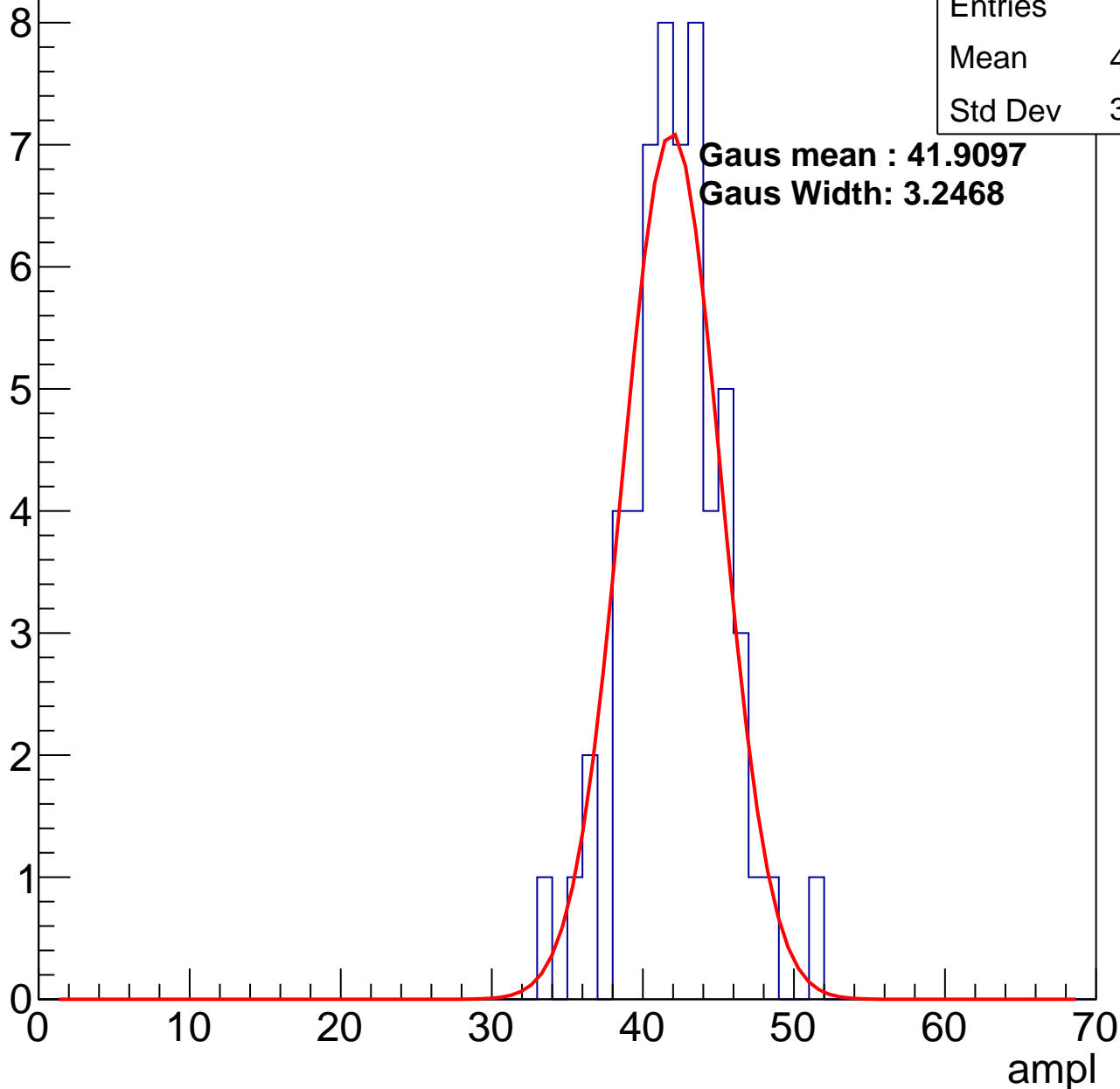
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.74
Std Dev	3.226

**Gaus mean : 41.9097**

**Gaus Width: 3.2468**

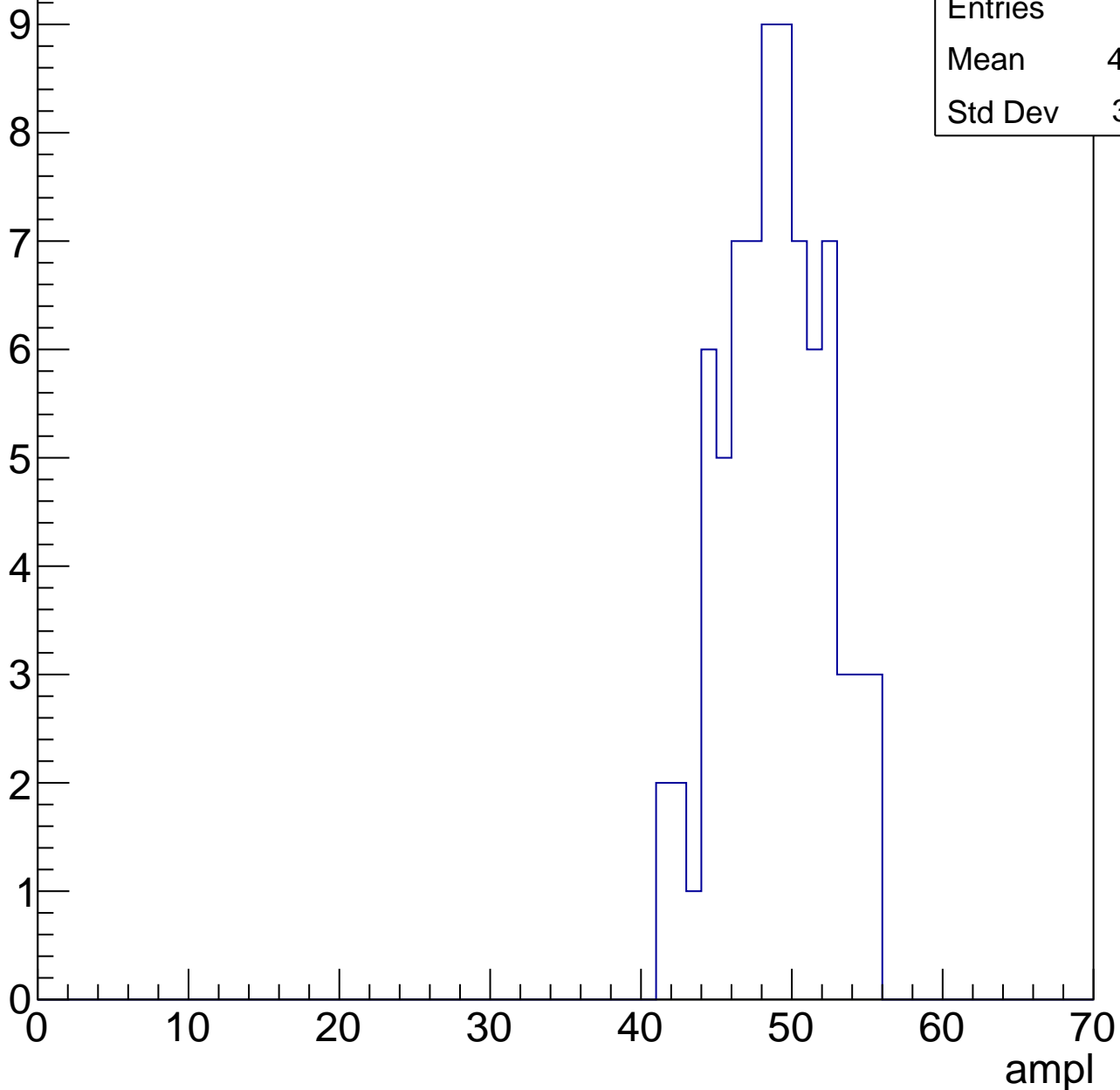


# B1L002S, U18-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	48.42
Std Dev	3.401

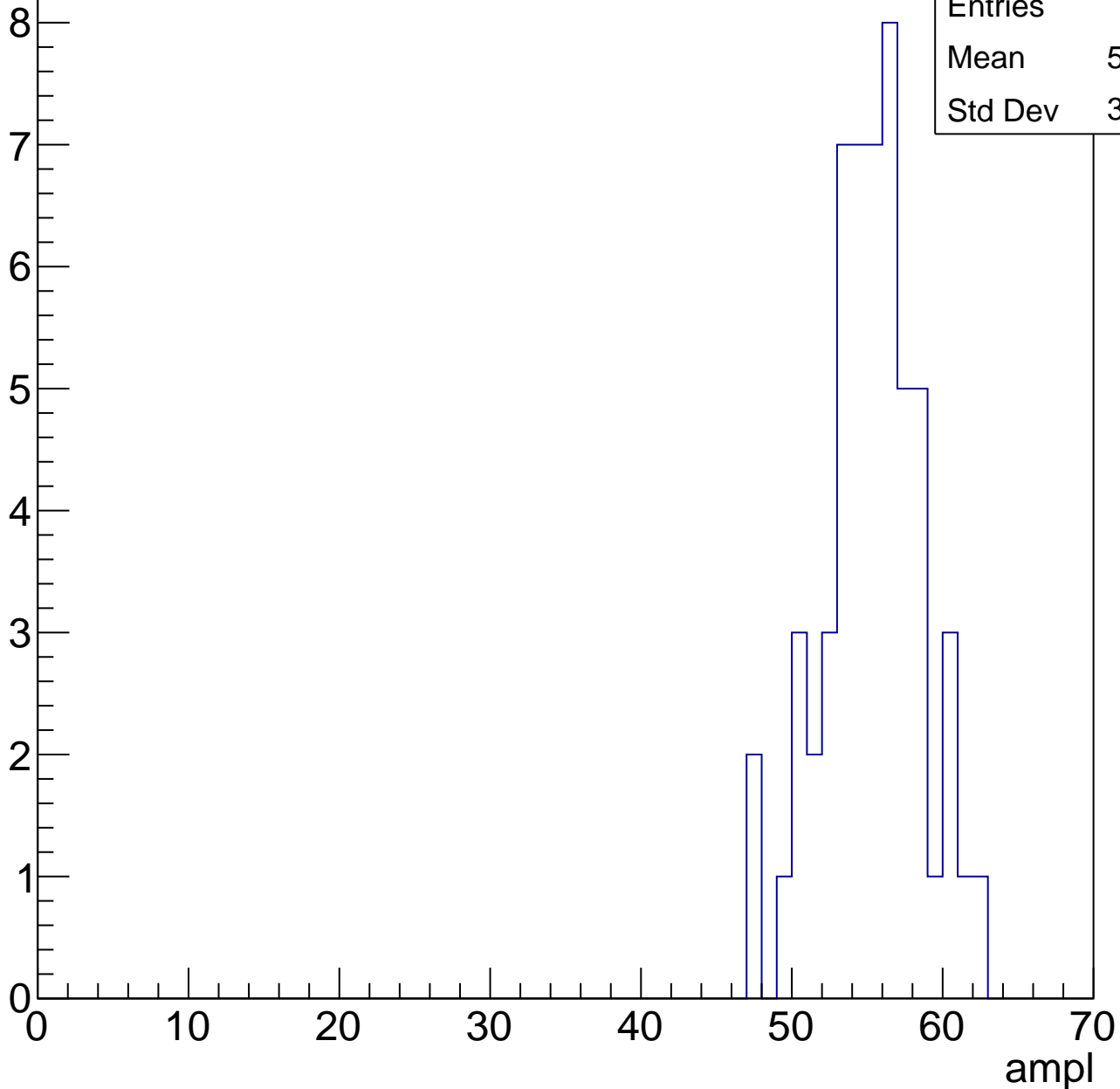


# B1L002S, U18-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	54.82
Std Dev	3.219

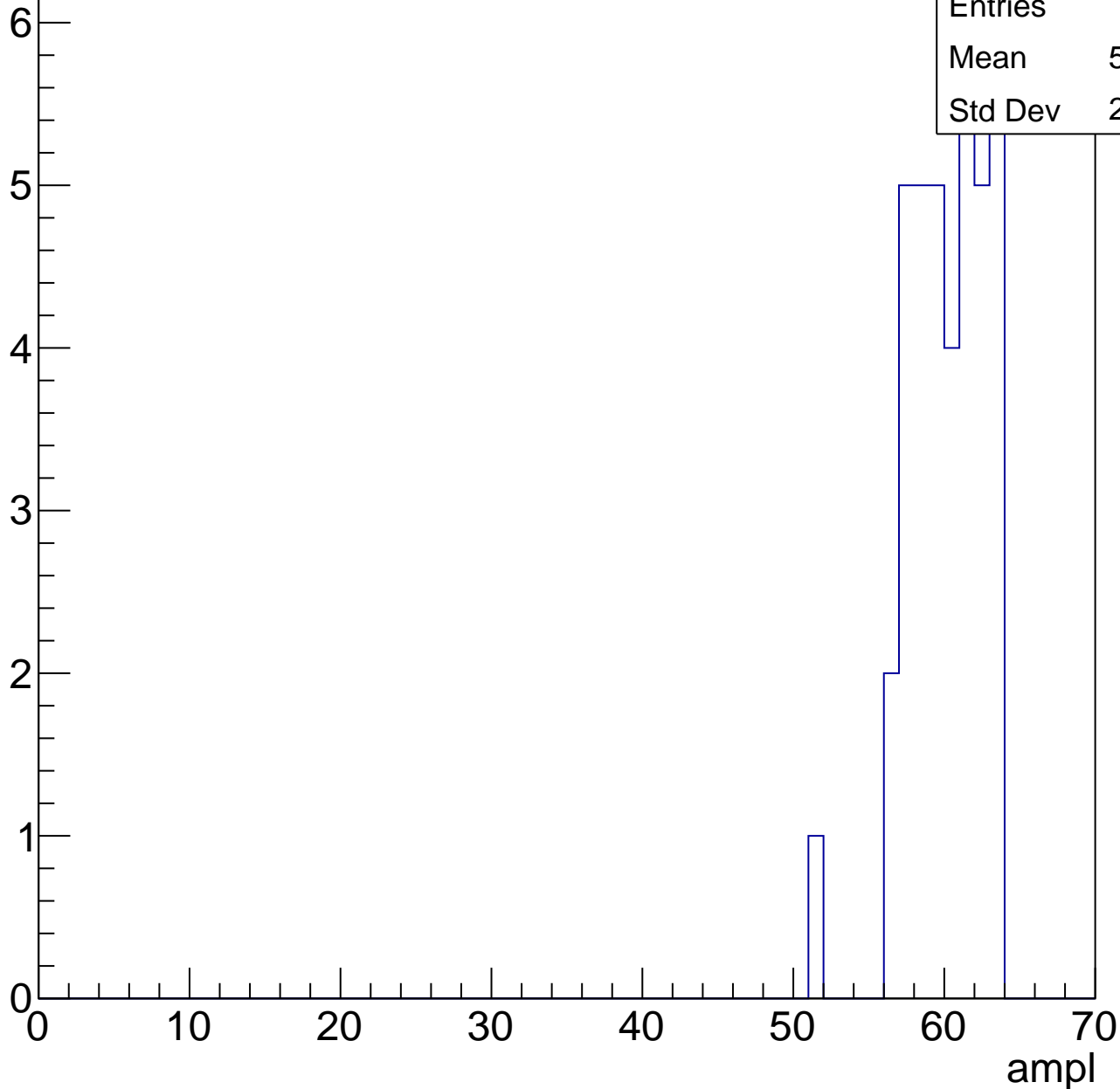


# B1L002S, U18-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

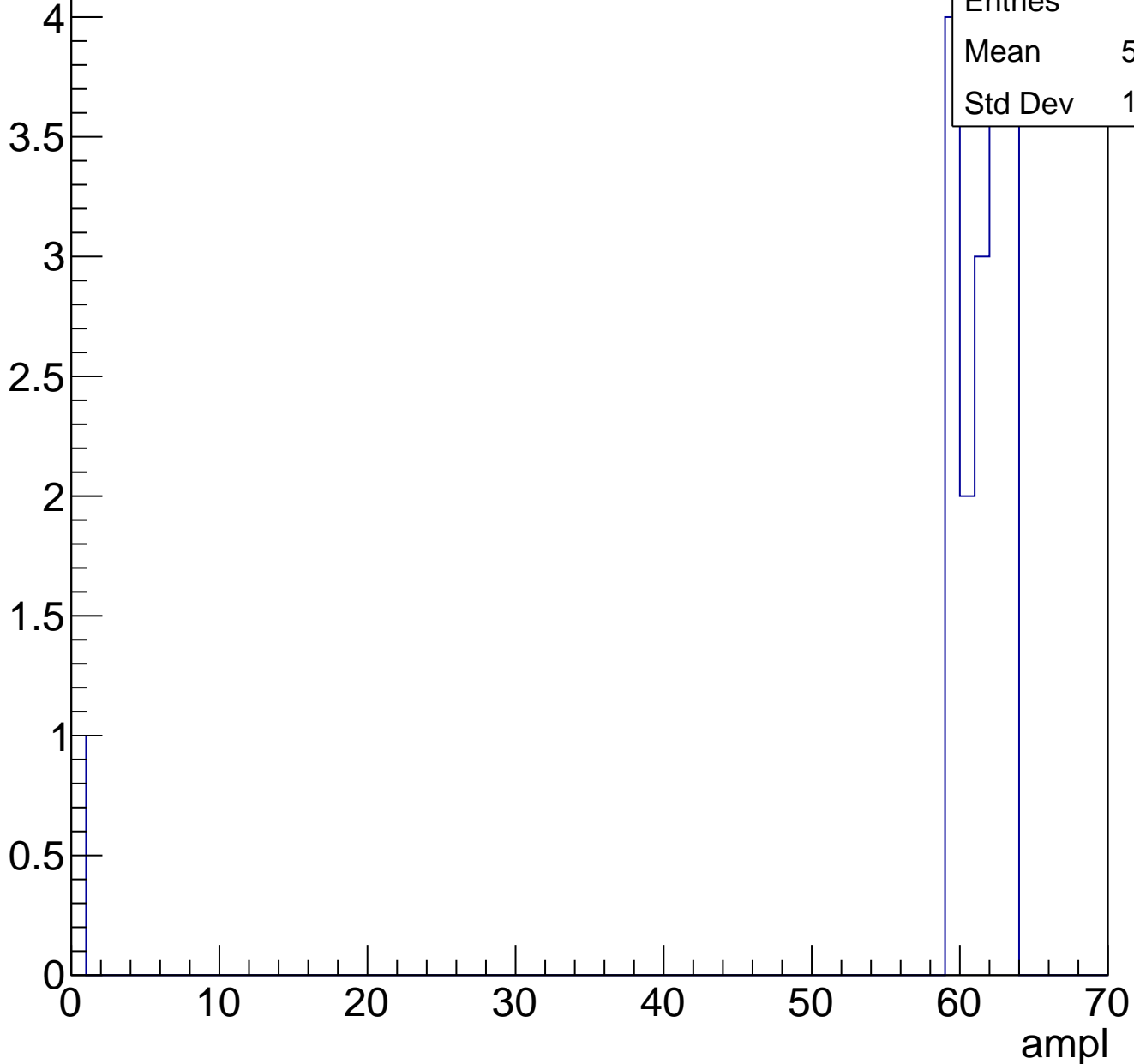
Entries	39
Mean	59.67
Std Dev	2.575



# B1L002S, U18-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

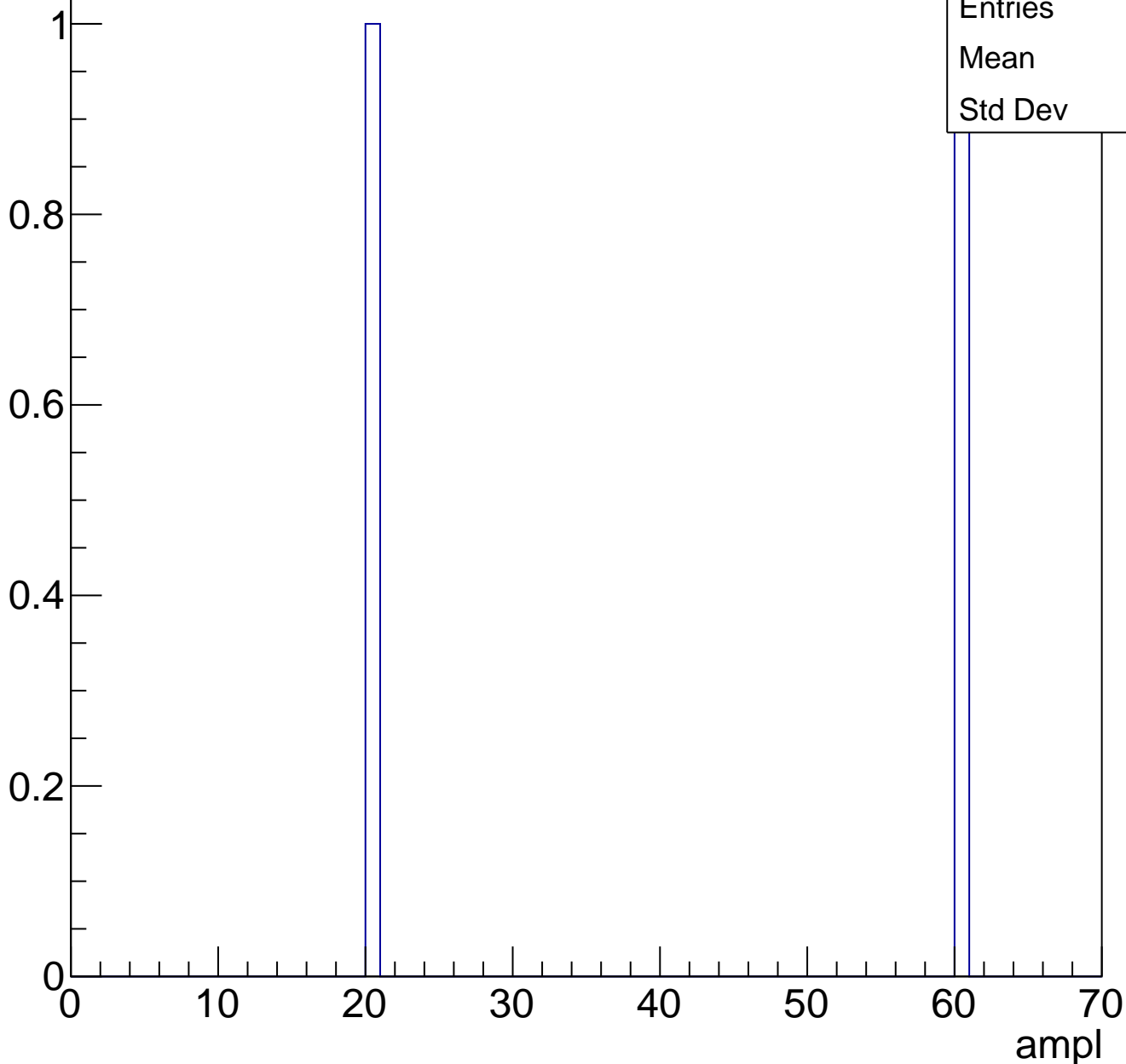




# B1L002S, U18-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	40
Std Dev	20

# B1L002S, U18-ch86, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	25.56
Std Dev	4.394

**Gaus mean : 26.4076**

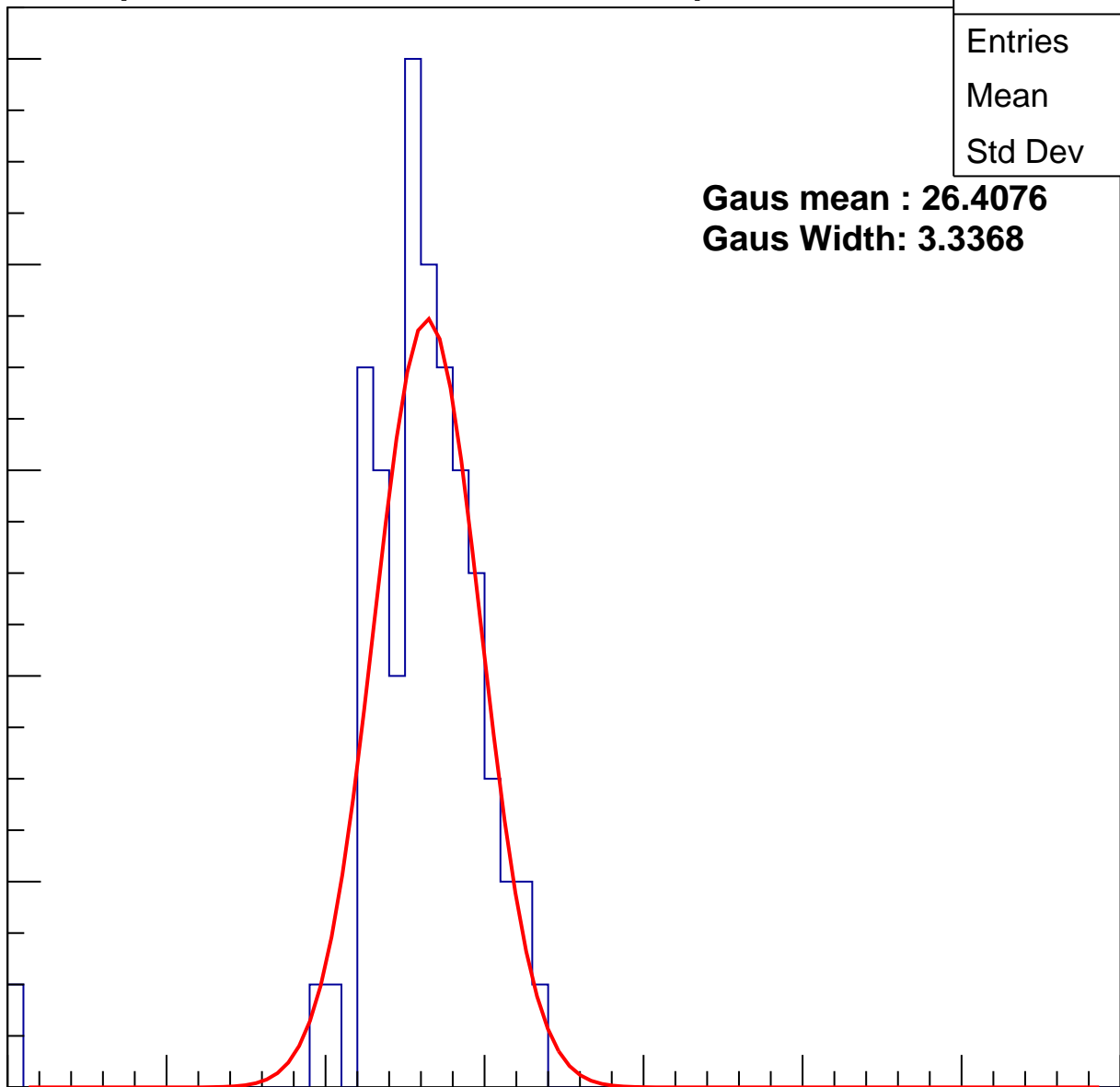
**Gaus Width: 3.3368**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



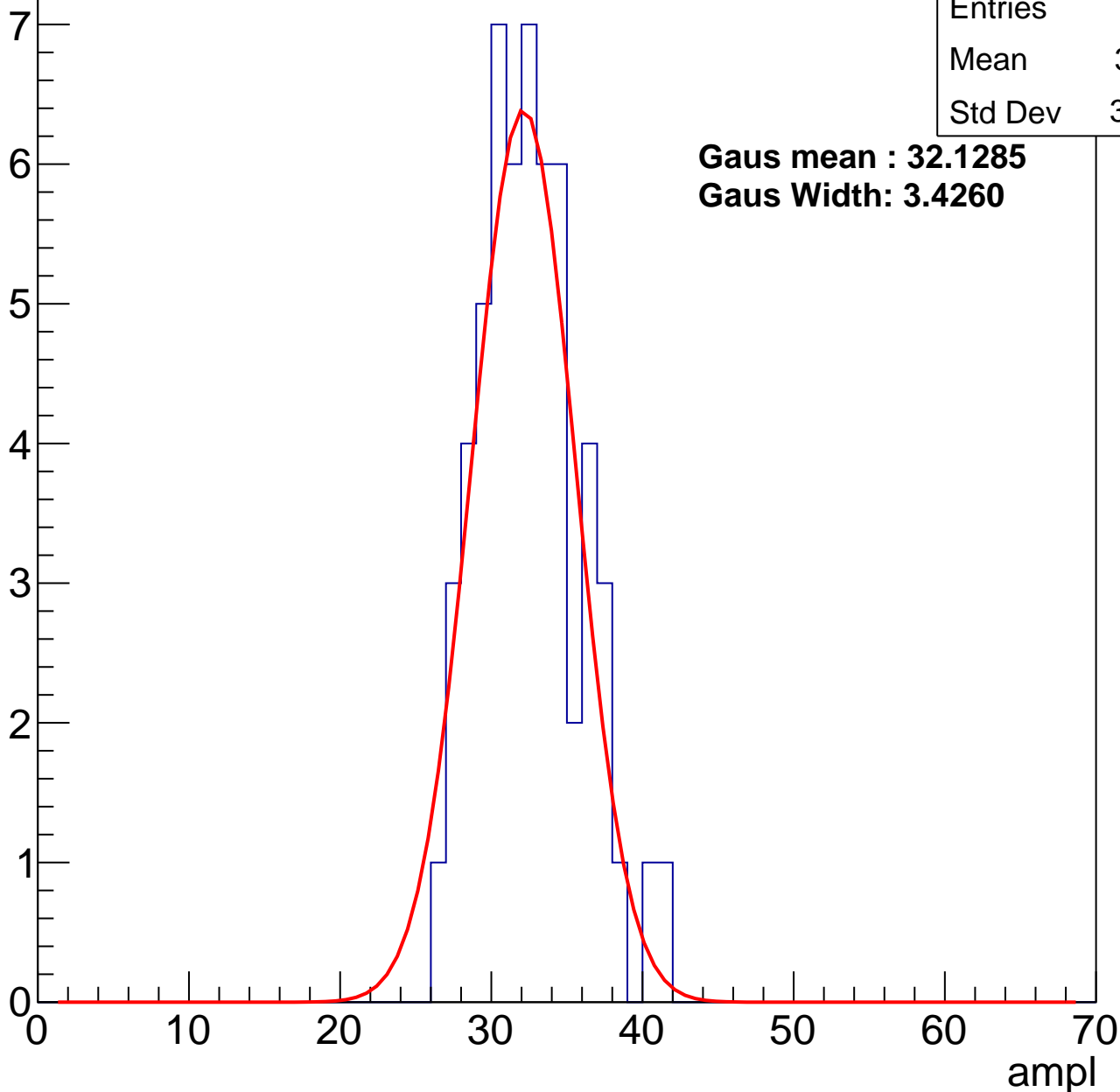
# B1L002S, U18-ch86, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	32.11
Std Dev	3.302

**Gaus mean : 32.1285**  
**Gaus Width: 3.4260**



# B1L002S, U18-ch86, adc2

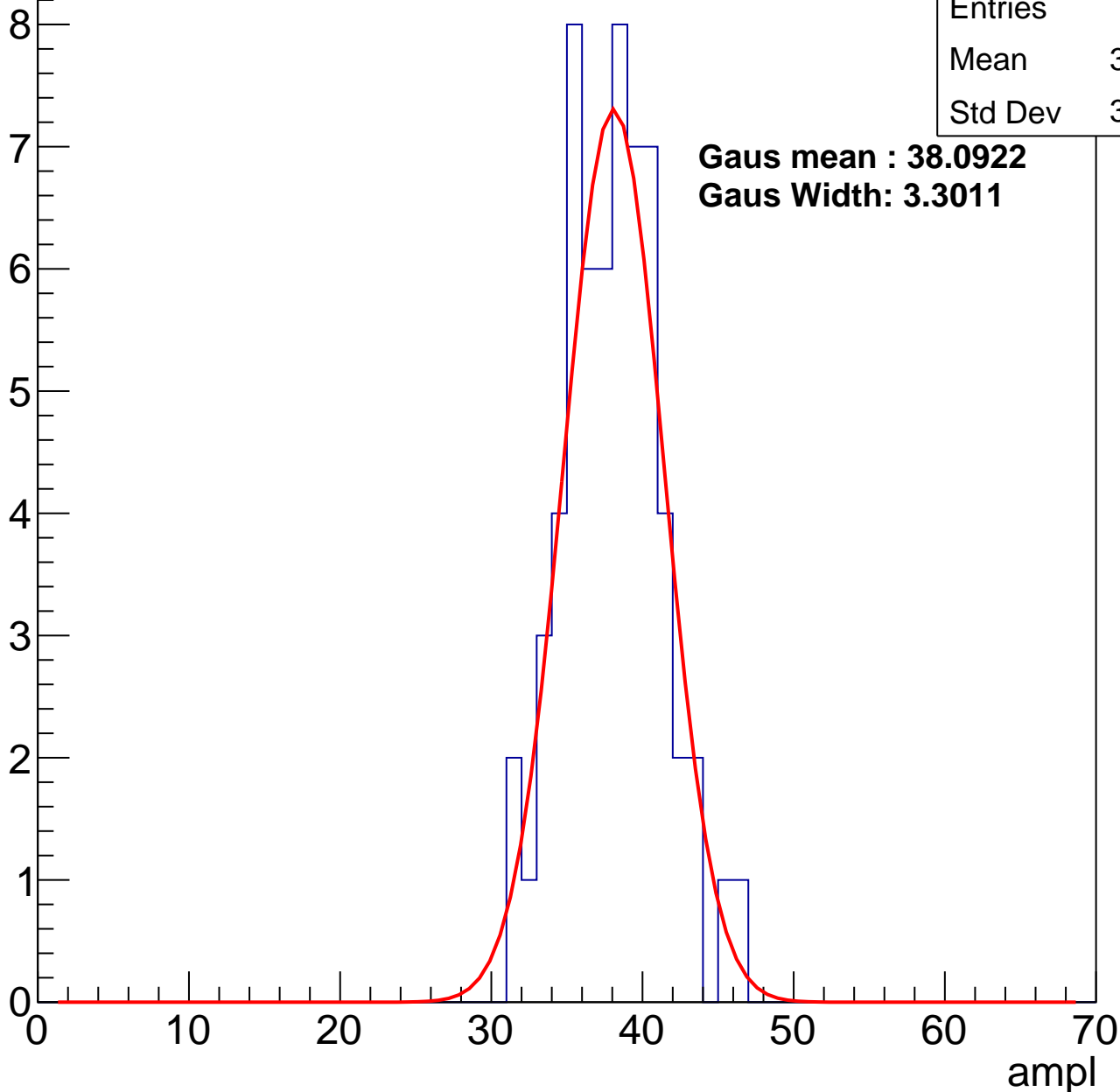
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	37.56
Std Dev	3.186

**Gaus mean : 38.0922**

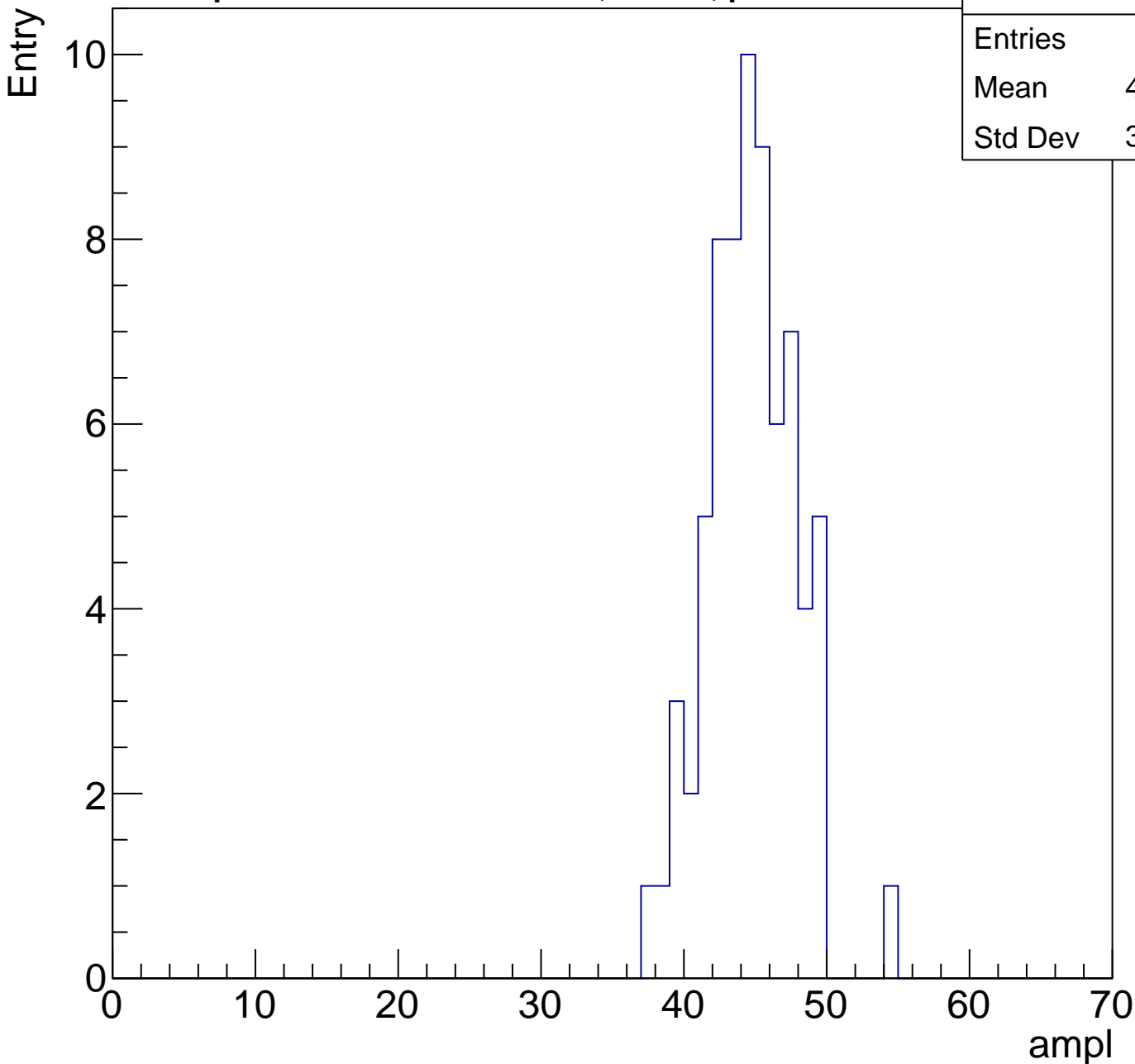
**Gaus Width: 3.3011**



# B1L002S, U18-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	44.26
Std Dev	3.064

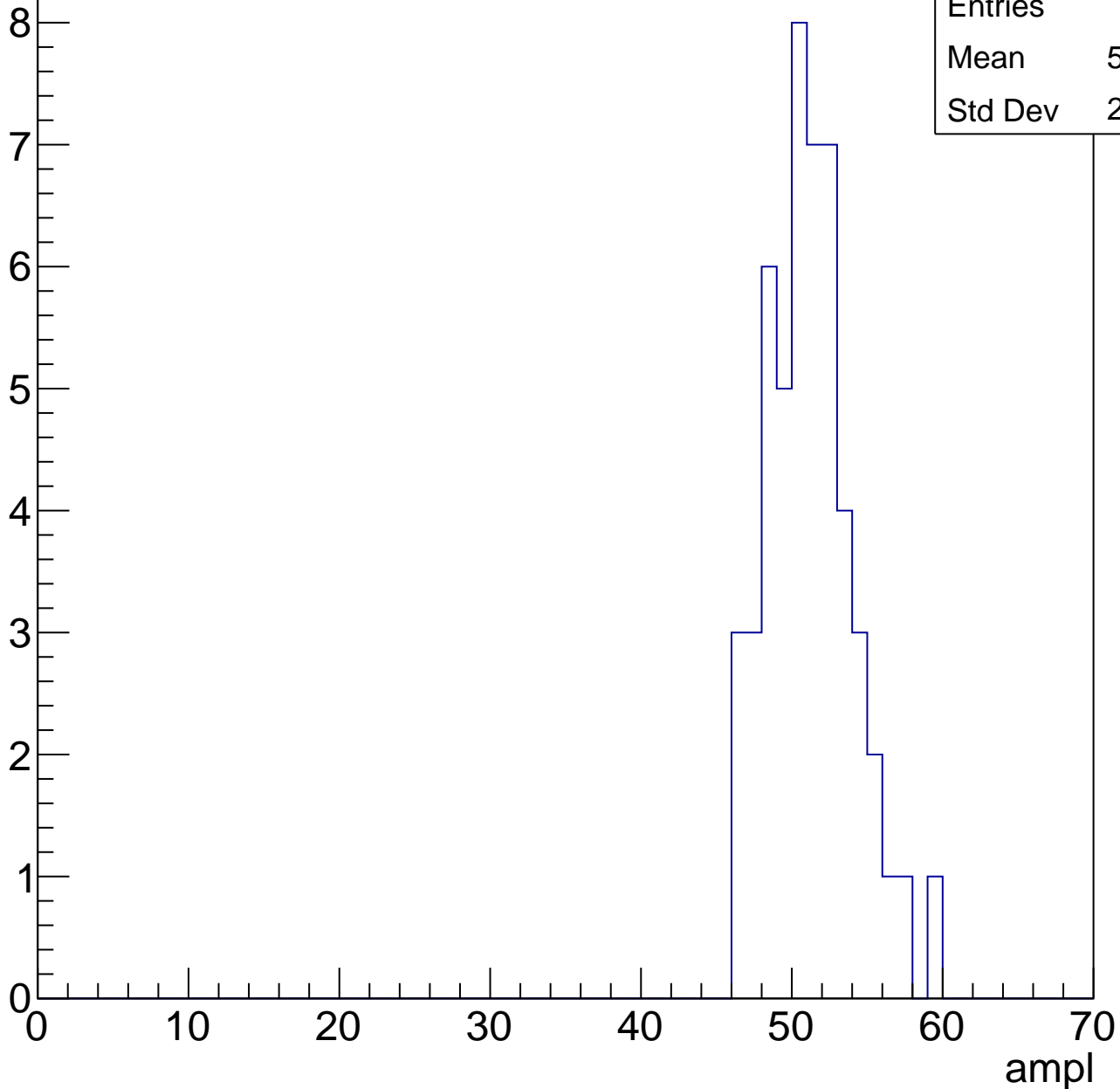


# B1L002S, U18-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	50.76
Std Dev	2.833

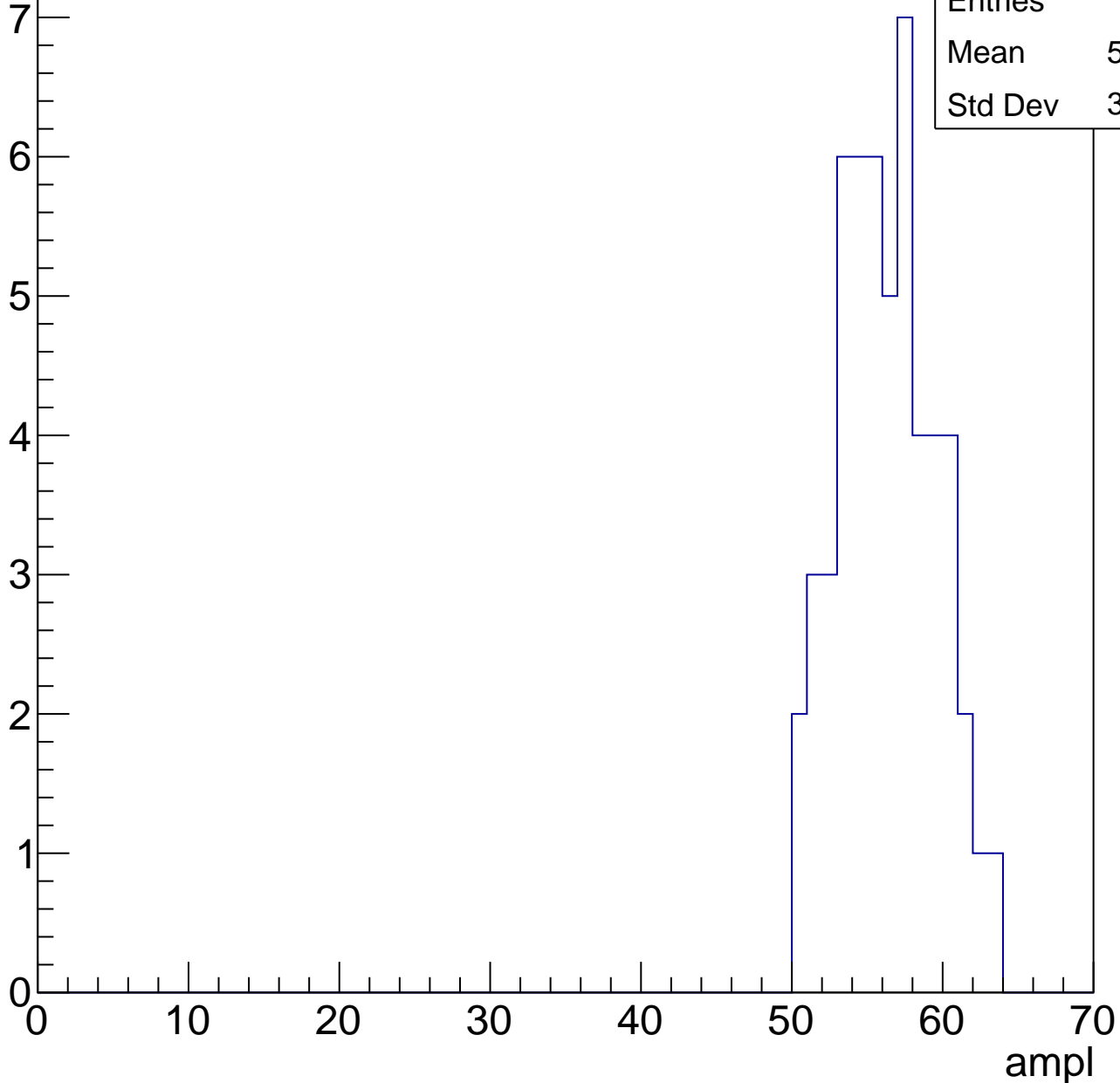


# B1L002S, U18-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	55.83
Std Dev	3.149

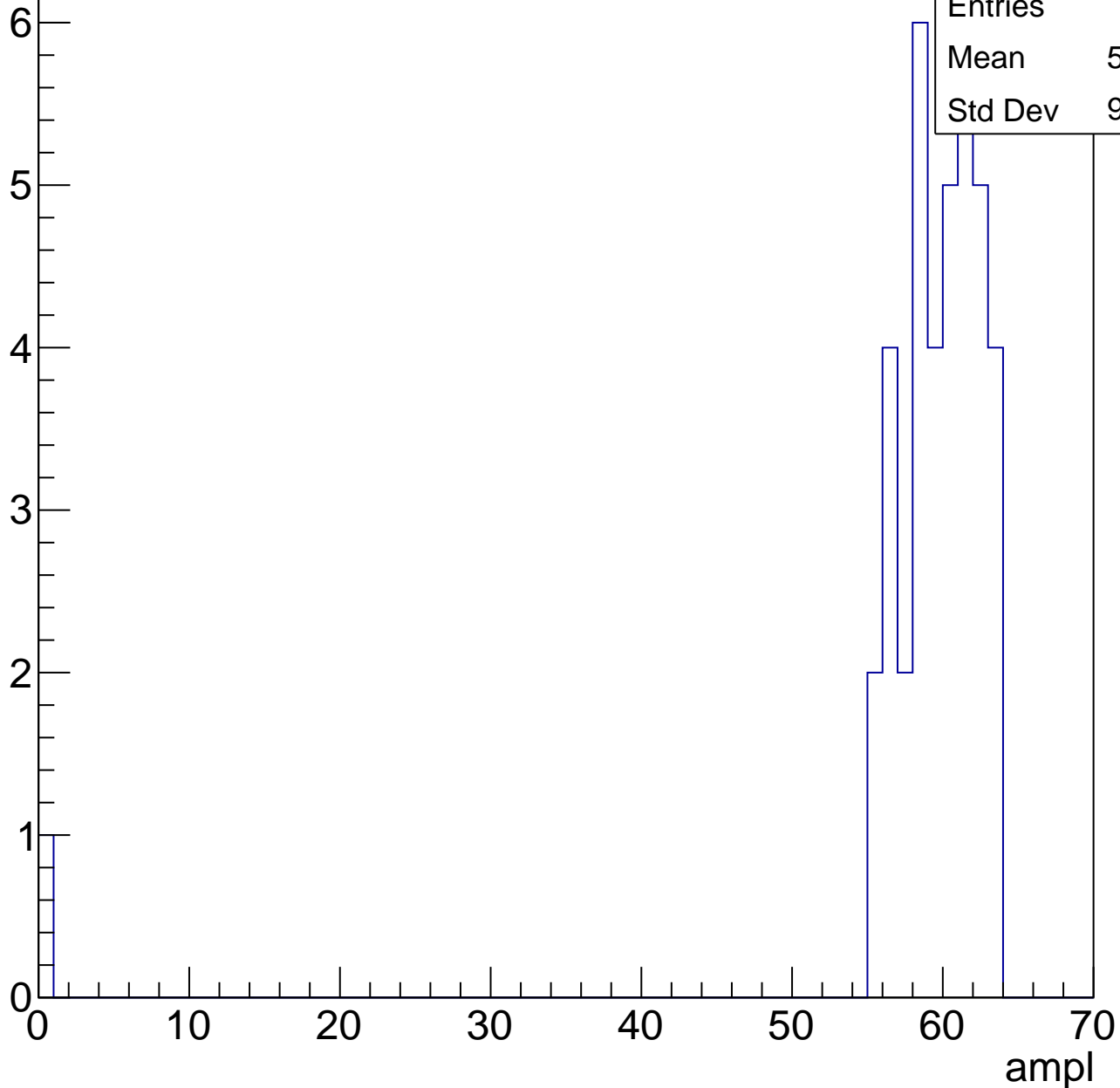


# B1L002S, U18-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	57.95
Std Dev	9.685



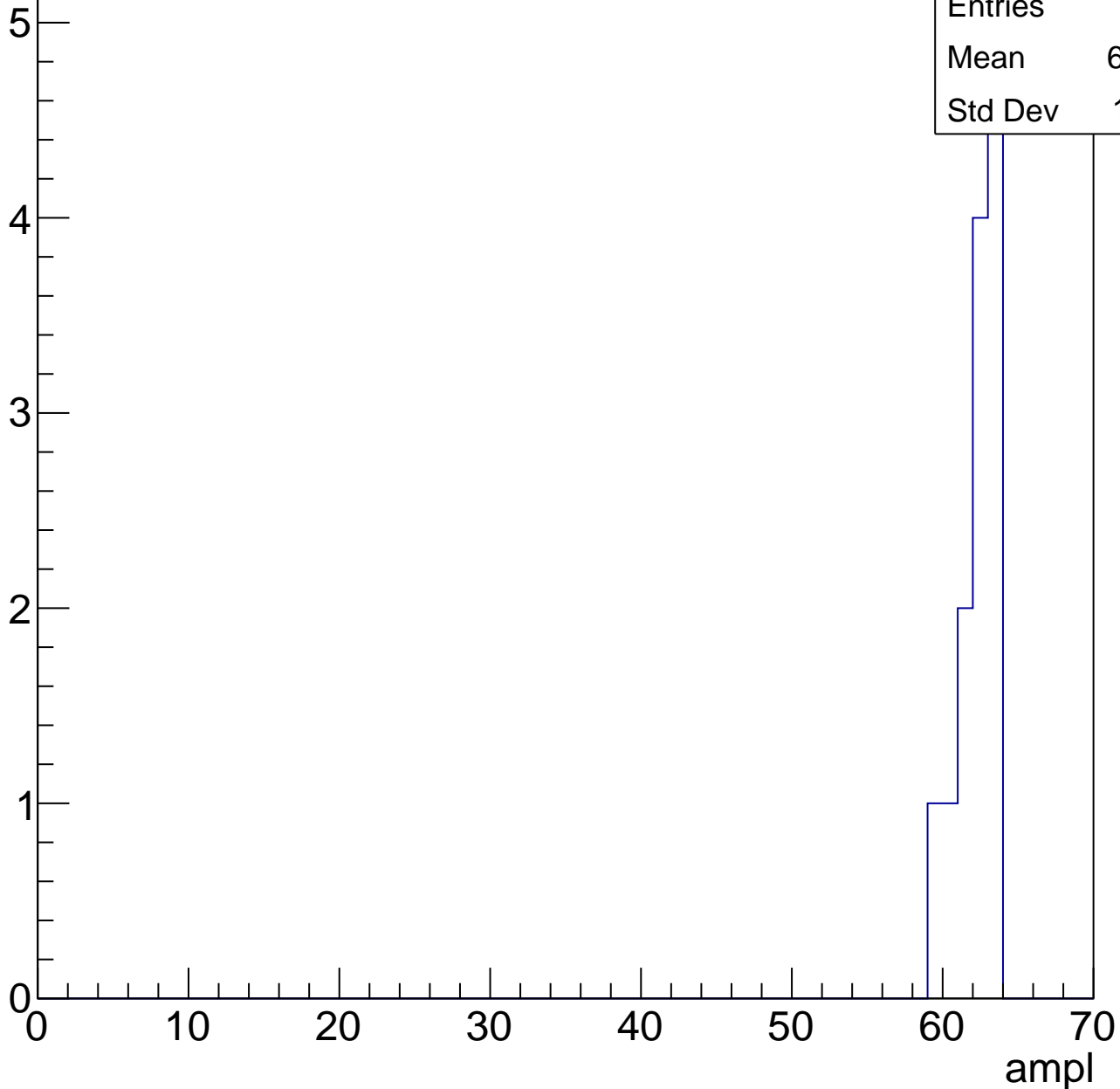


# B1L002S, U18-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.85
Std Dev	1.231



# B1L002S, U18-ch87, adc0

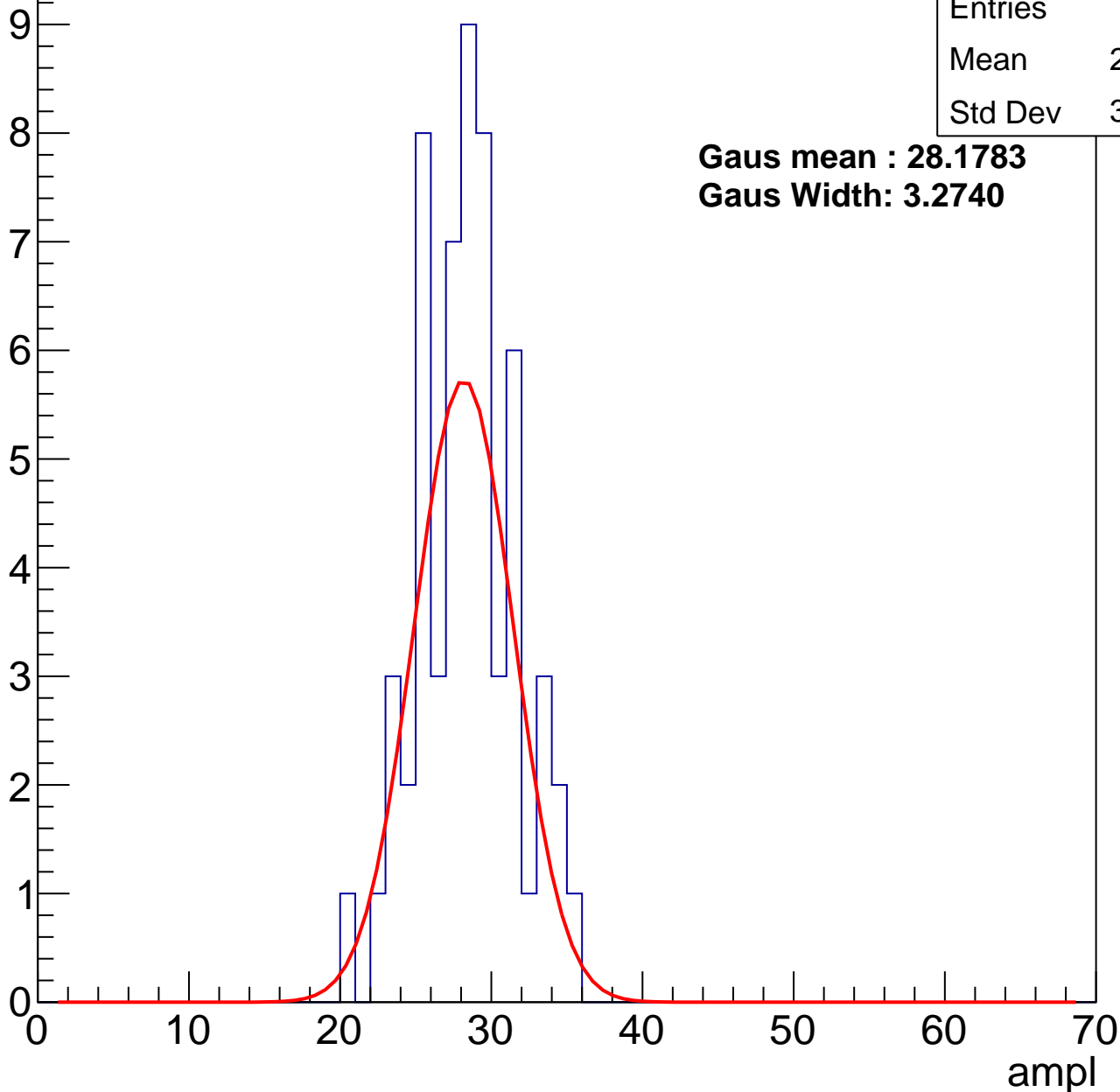
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.93
Std Dev	3.172

**Gaus mean : 28.1783**

**Gaus Width: 3.2740**



# B1L002S, U18-ch87, adc1

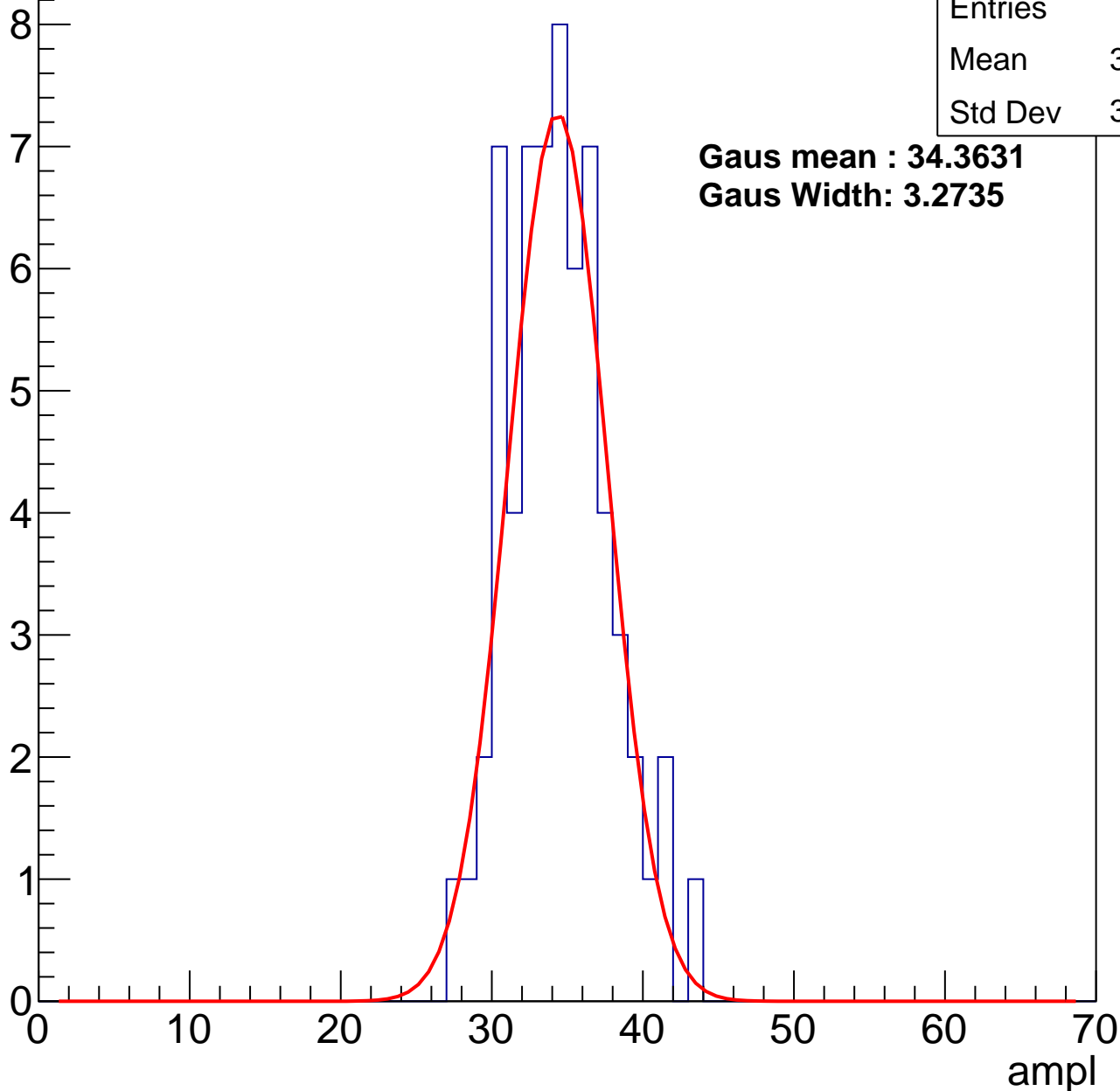
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.98
Std Dev	3.336

**Gaus mean : 34.3631**

**Gaus Width: 3.2735**



# B1L002S, U18-ch87, adc2

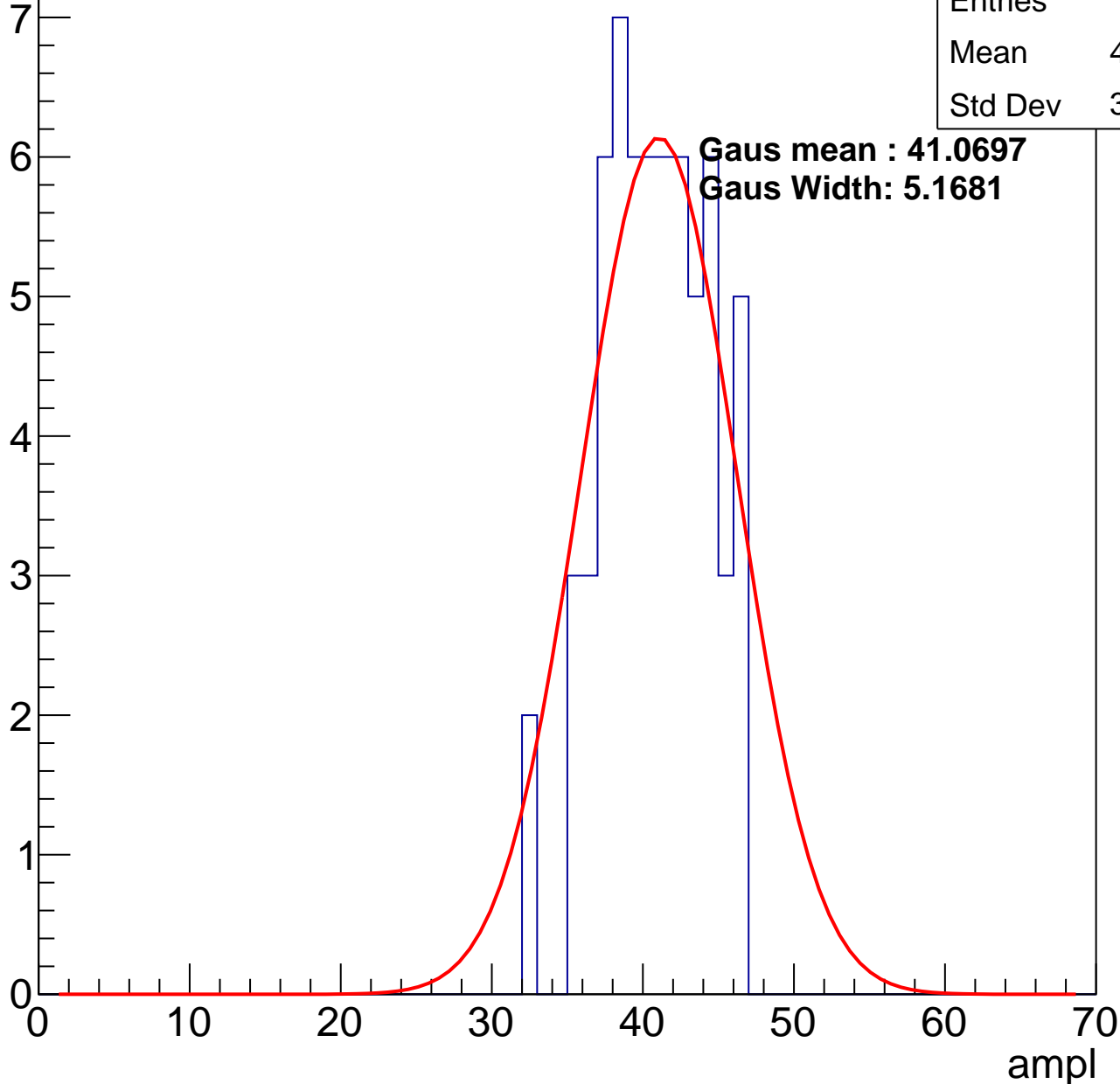
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.33
Std Dev	3.442

**Gaus mean : 41.0697**

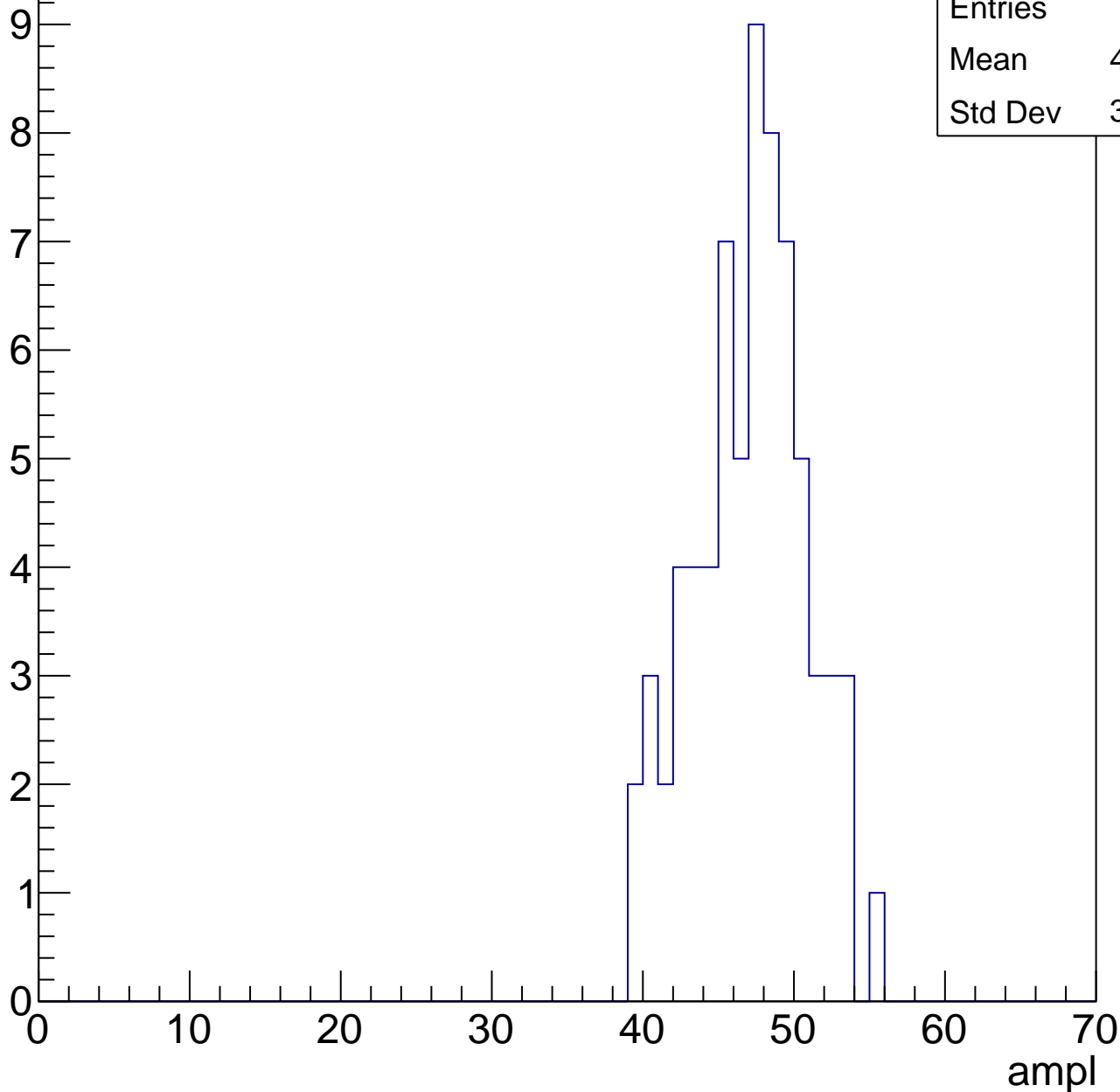
**Gaus Width: 5.1681**



# B1L002S, U18-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

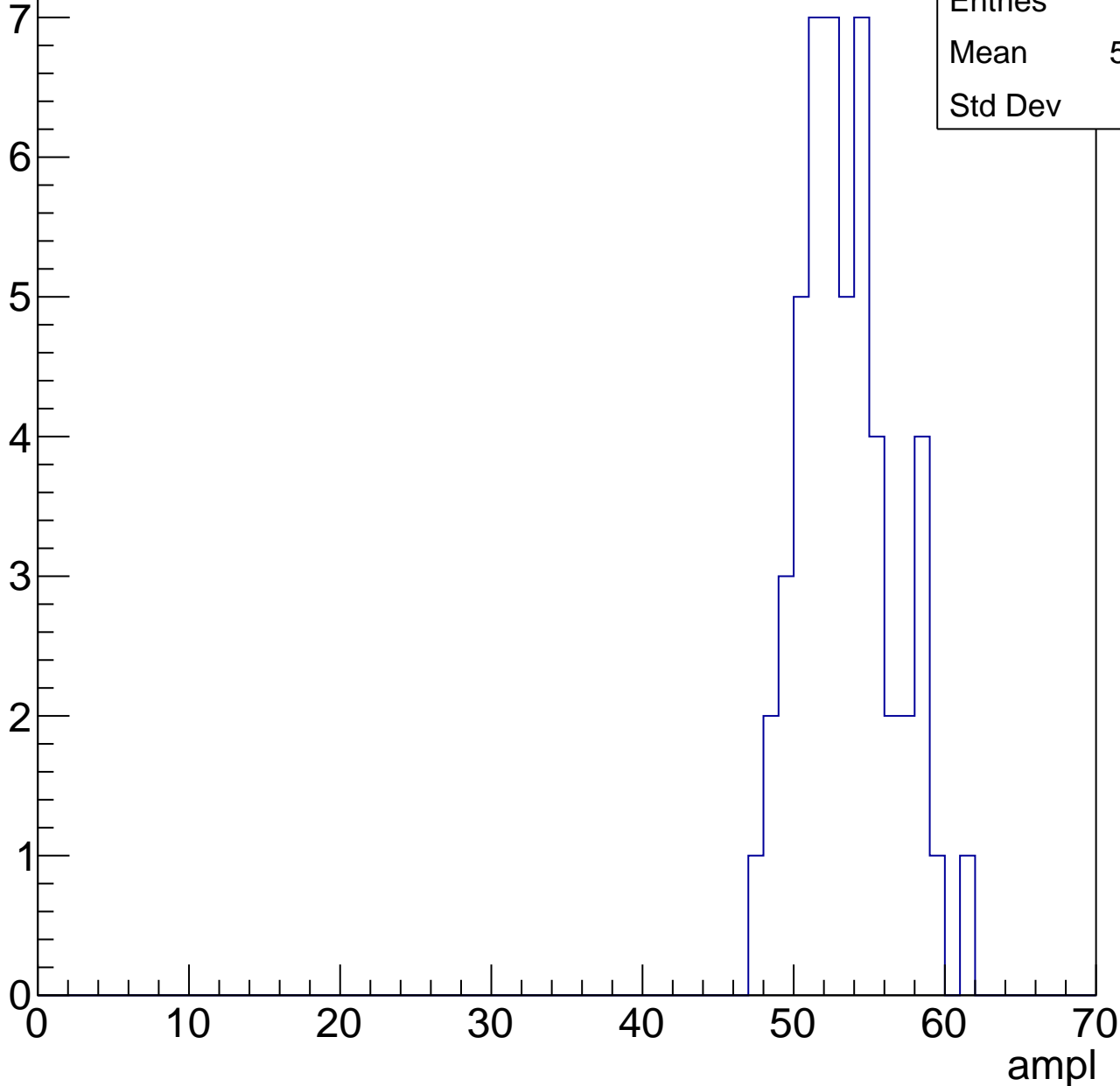


# B1L002S, U18-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

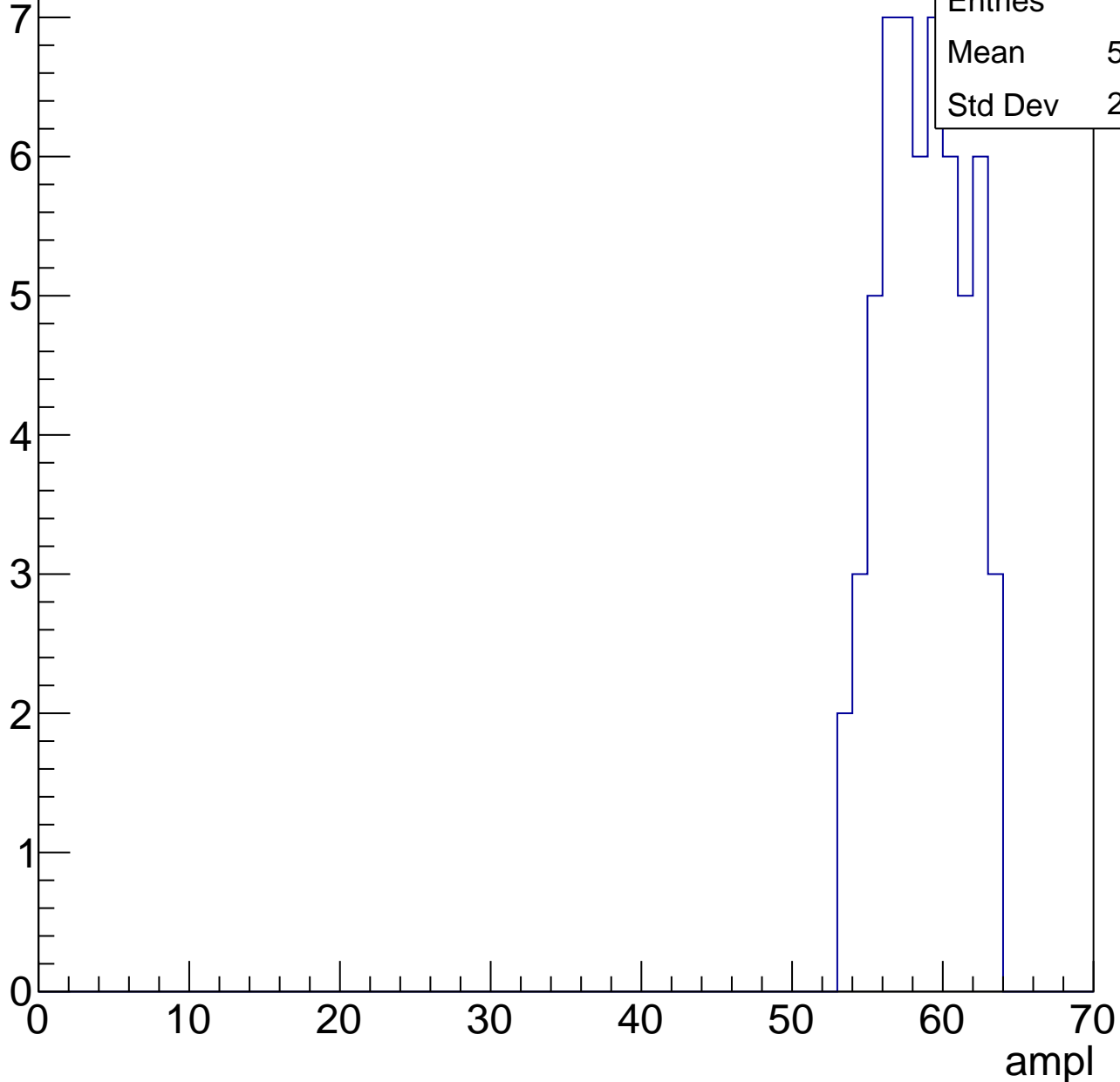
Entries	51
Mean	52.98
Std Dev	3.09



# B1L002S, U18-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



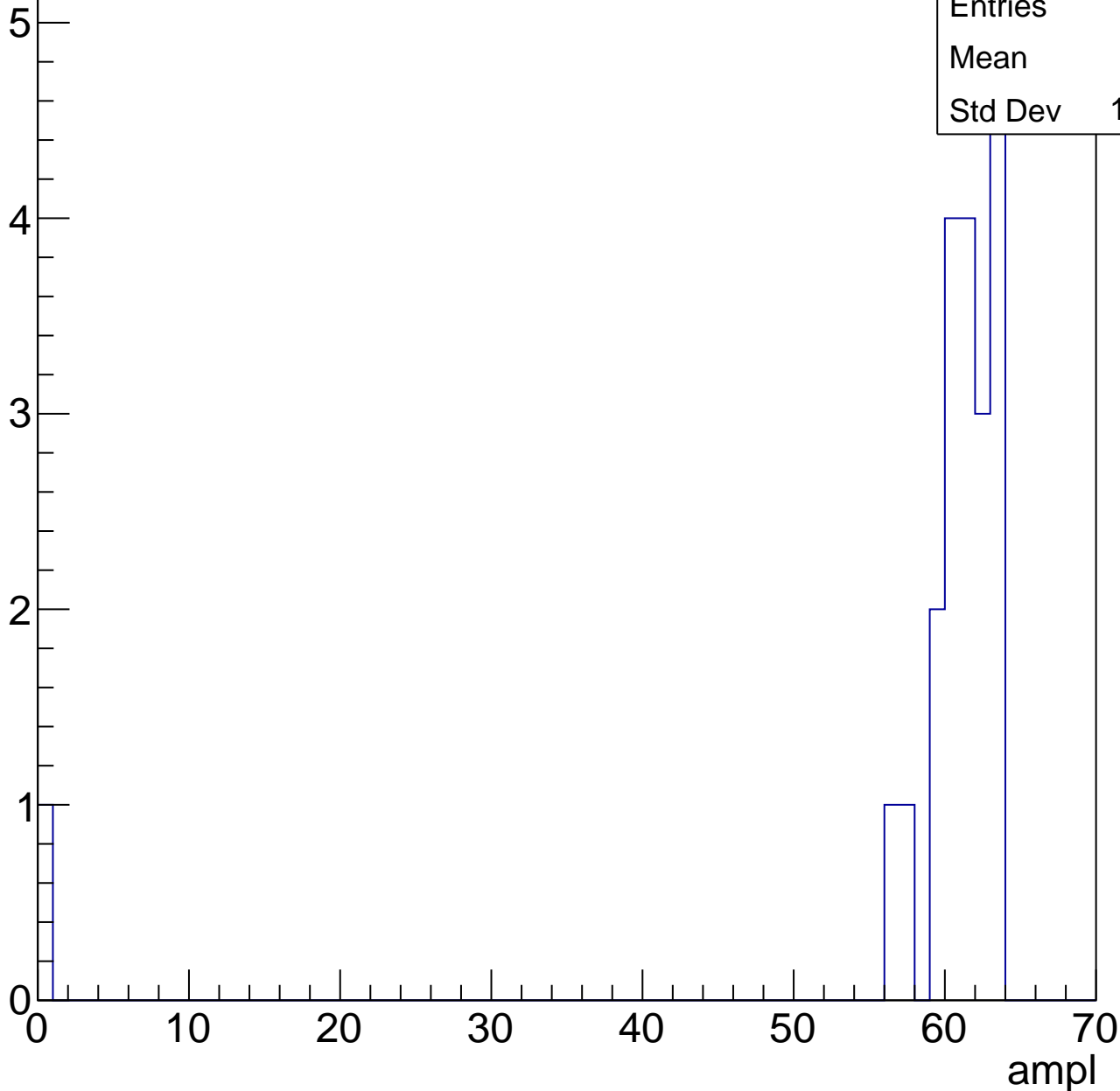
Entries	57
Mean	58.26
Std Dev	2.718

# B1L002S, U18-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	57.9
Std Dev	13.09

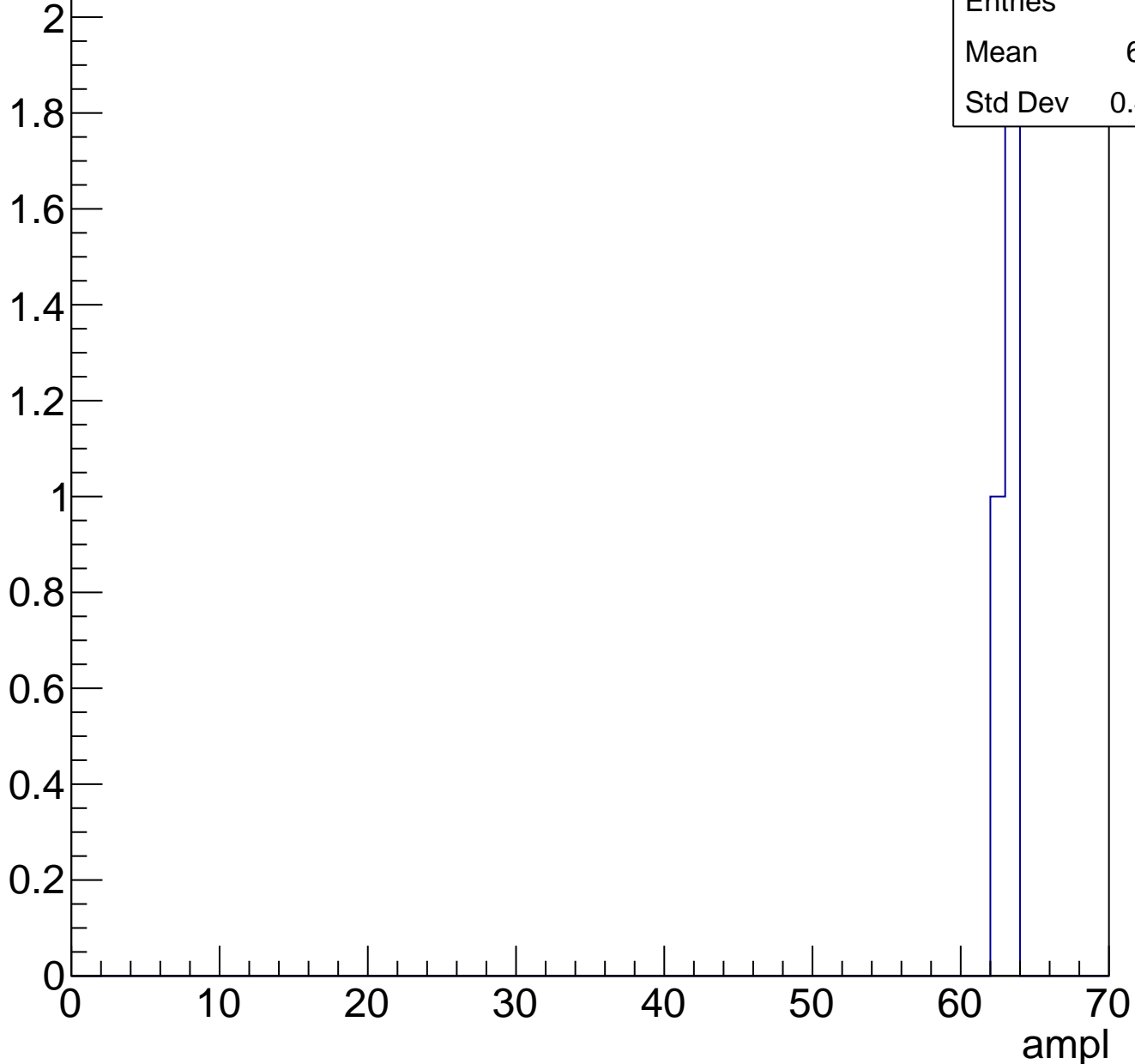




# B1L002S, U18-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch88, adc0

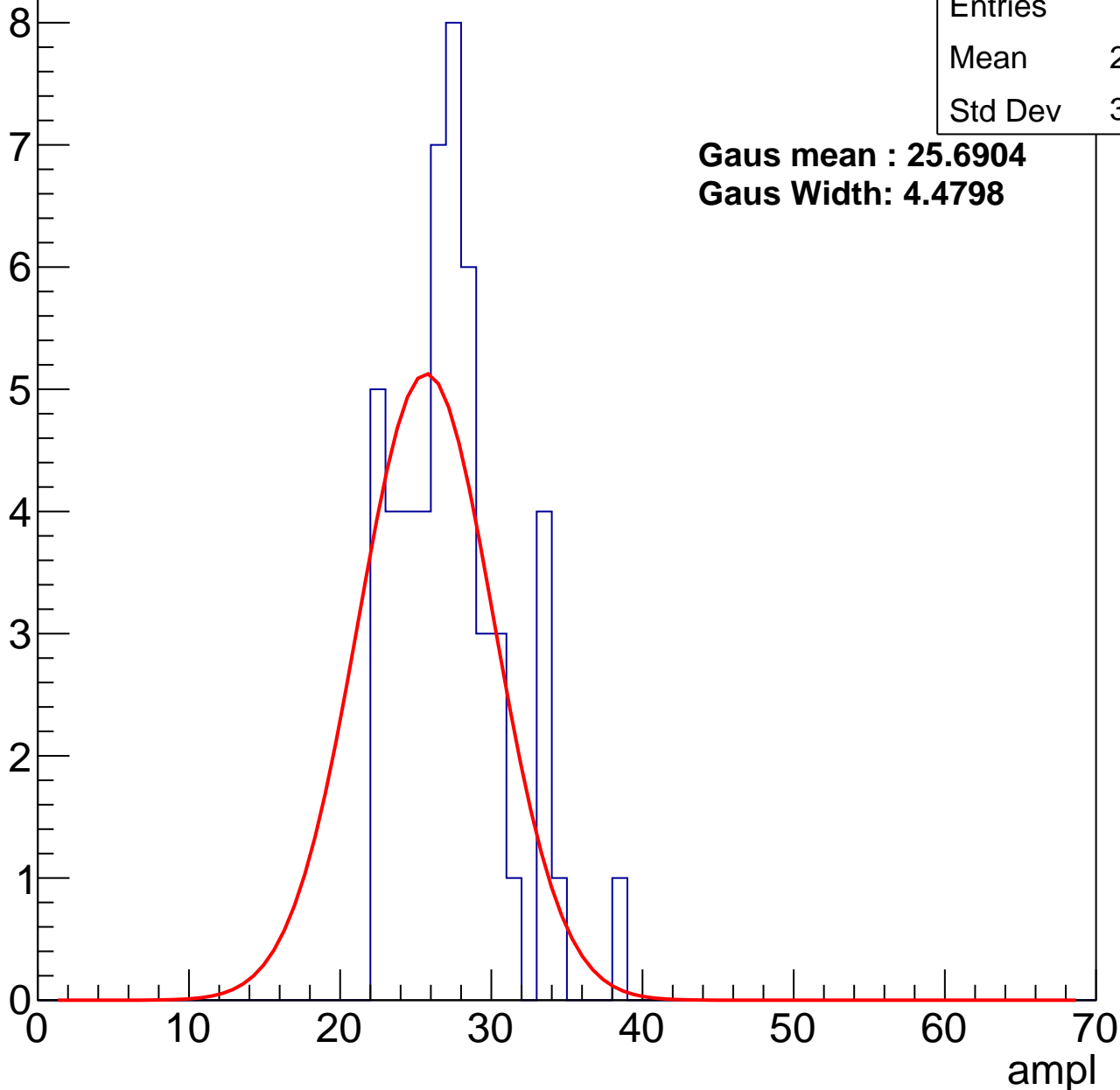
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	26.98
Std Dev	3.495

**Gaus mean : 25.6904**

**Gaus Width: 4.4798**



# B1L002S, U18-ch88, adc1

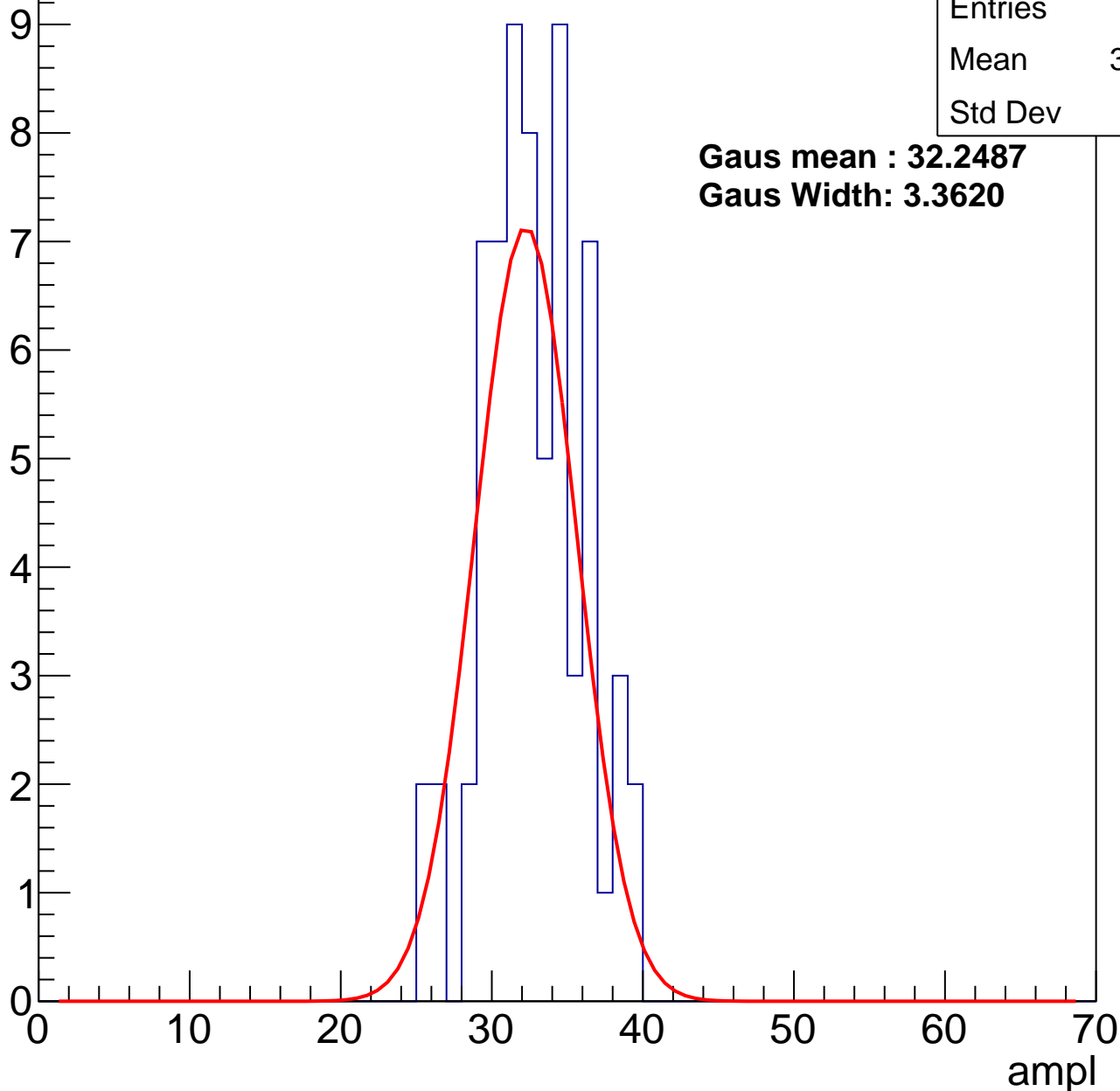
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	32.28
Std Dev	3.25

**Gaus mean : 32.2487**

**Gaus Width: 3.3620**



# B1L002S, U18-ch88, adc2

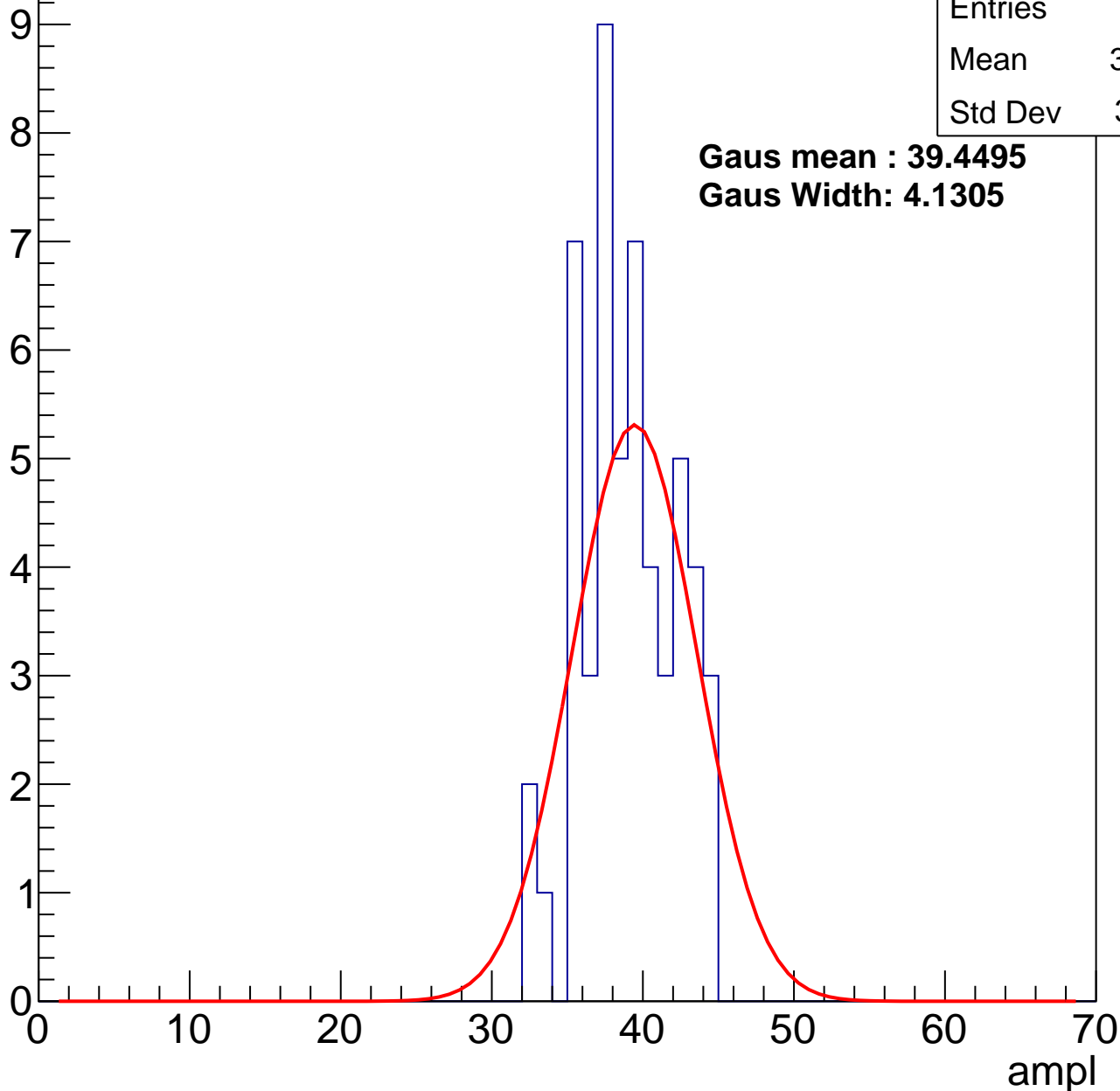
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	38.55
Std Dev	3.081

**Gaus mean : 39.4495**

**Gaus Width: 4.1305**

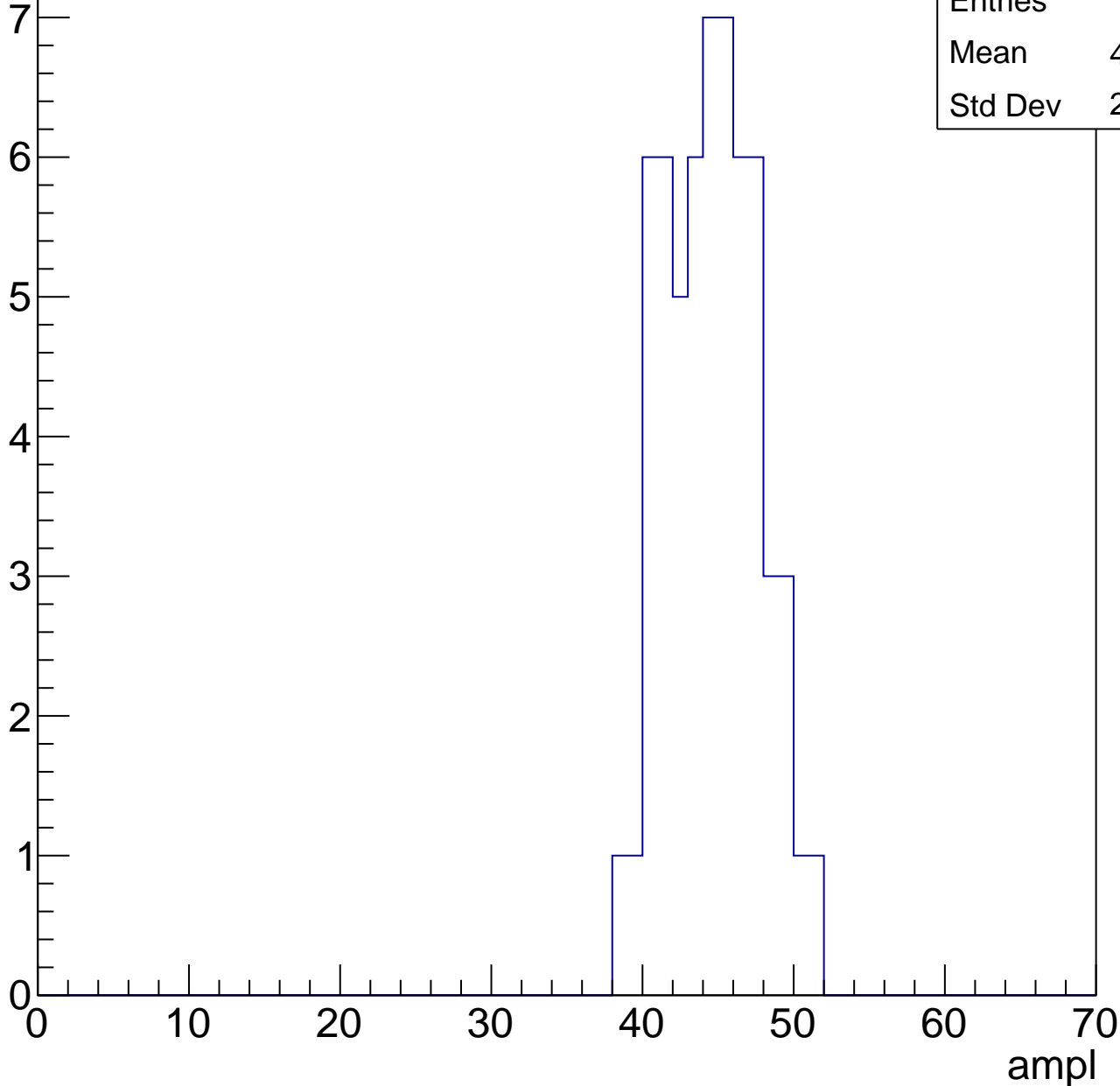


# B1L002S, U18-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	44.14
Std Dev	2.994

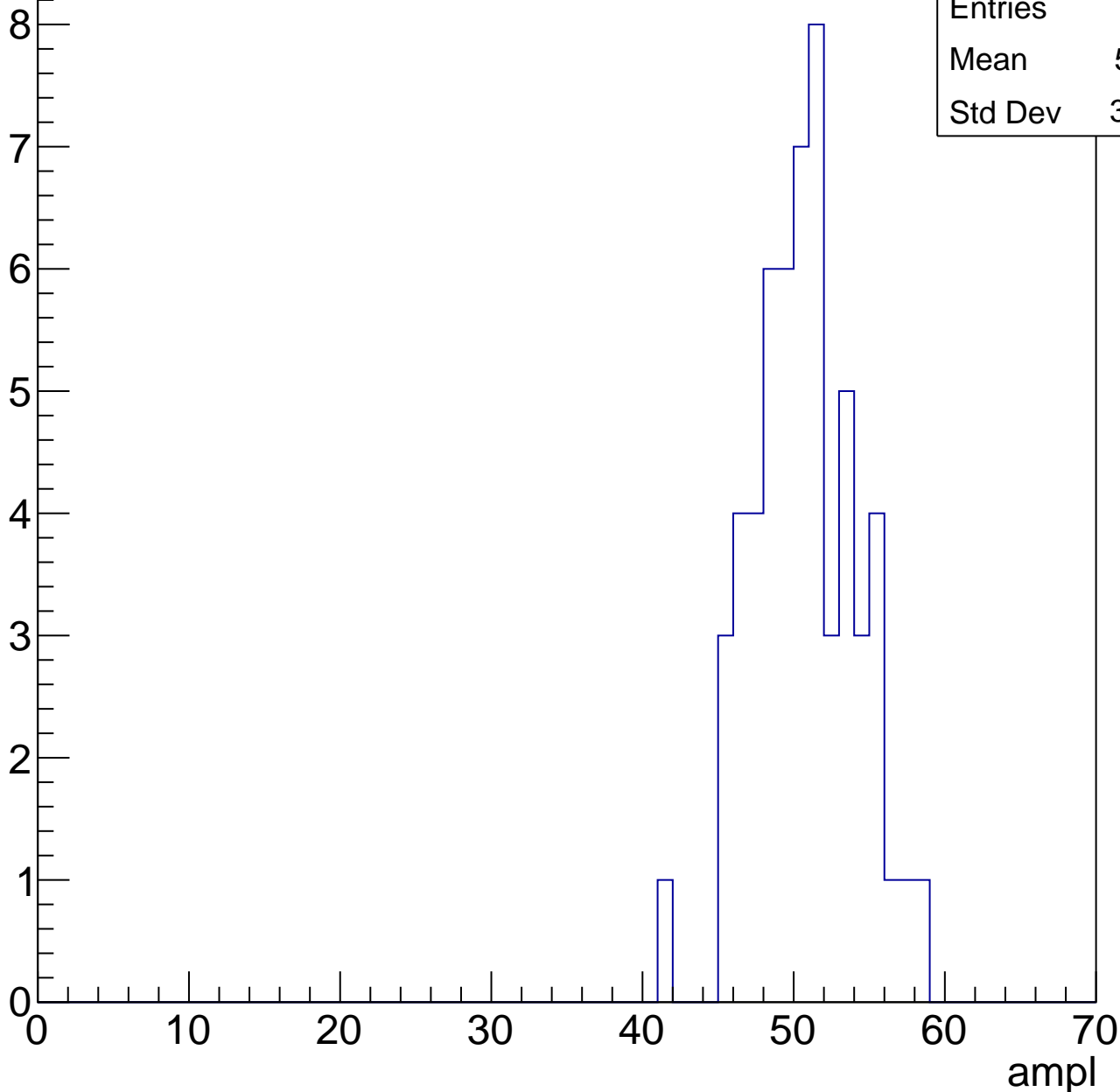


# B1L002S, U18-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

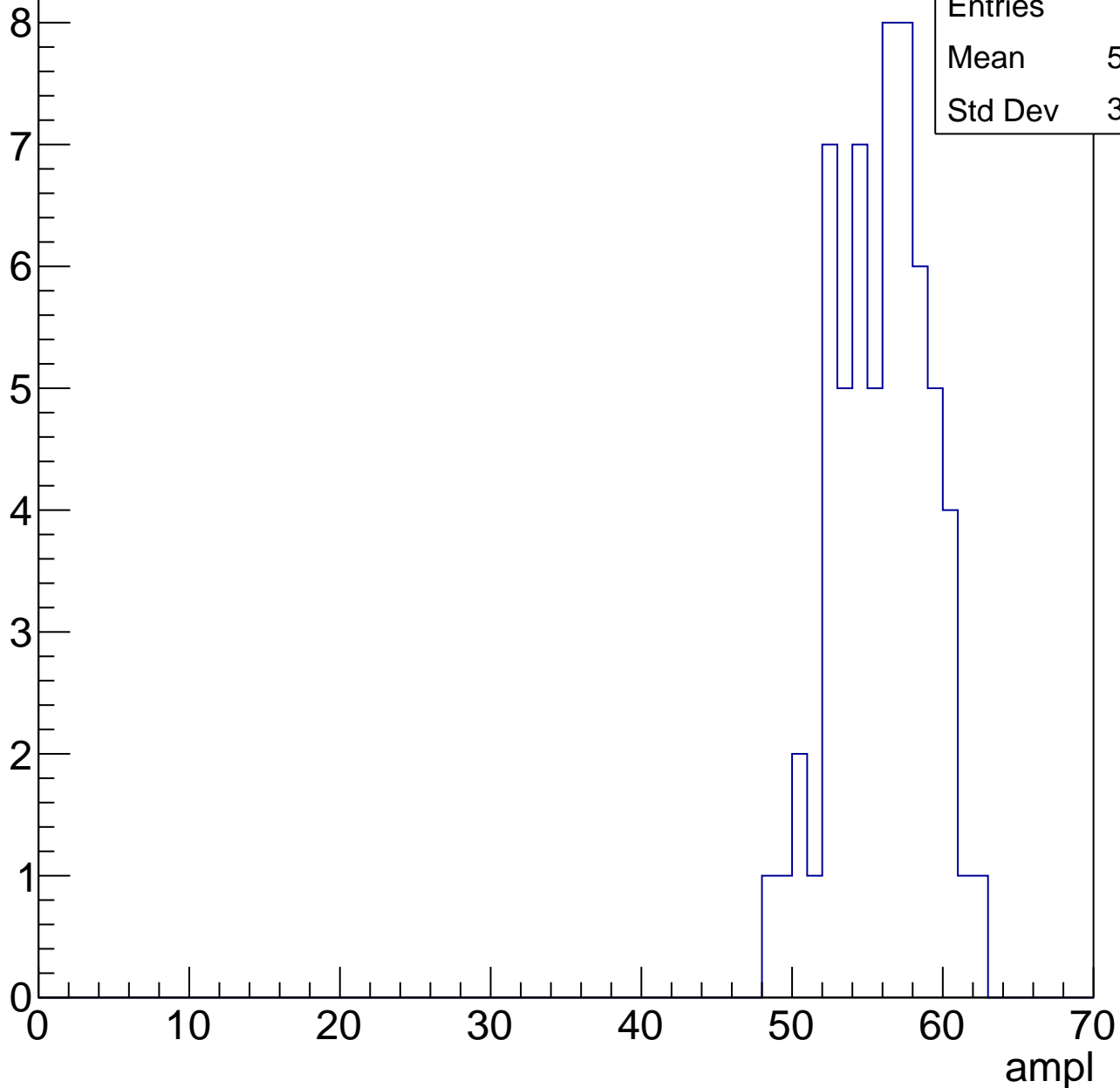
Entries	57
Mean	50.21
Std Dev	3.365



# B1L002S, U18-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

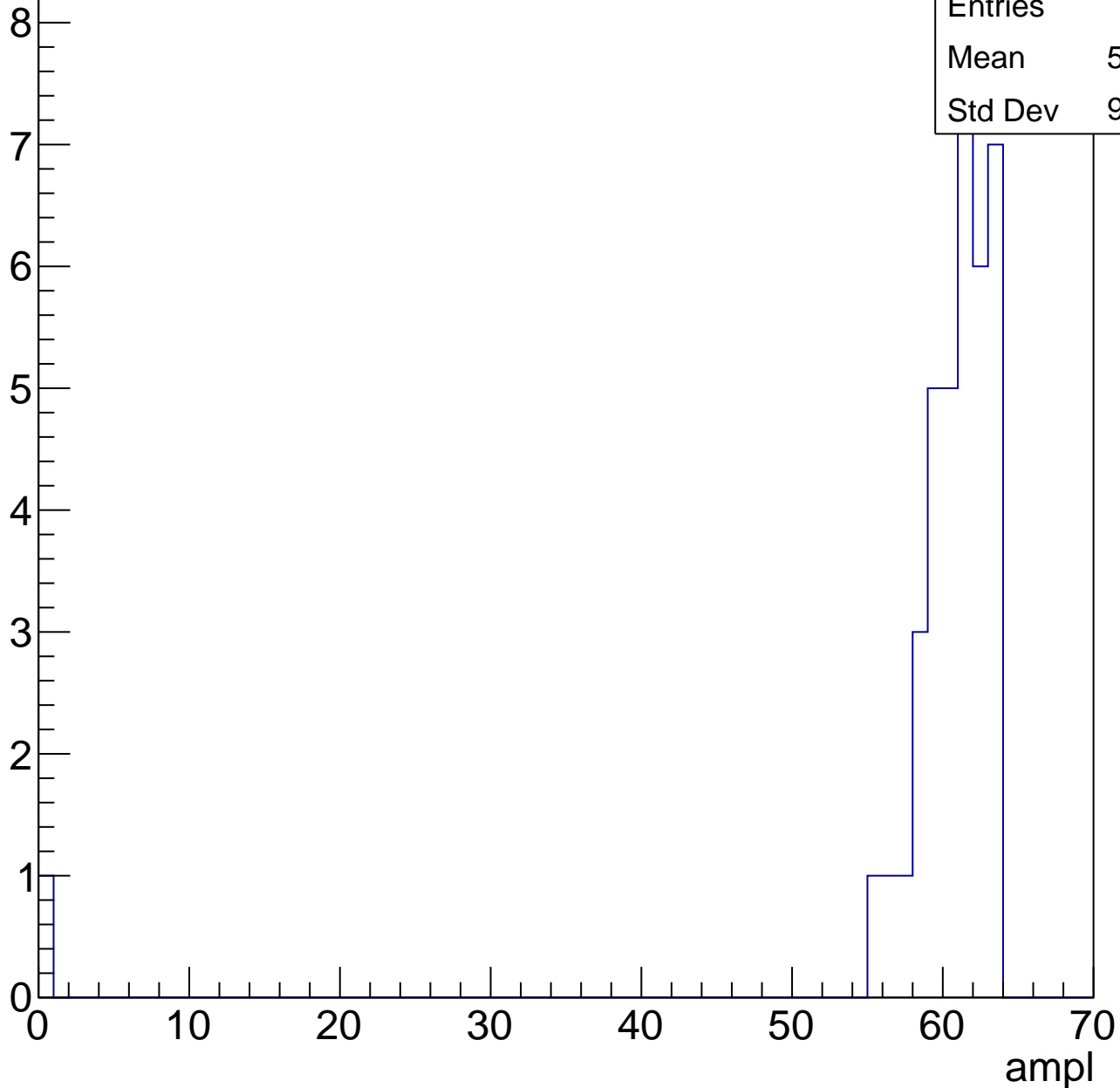


# B1L002S, U18-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	58.89
Std Dev	9.888

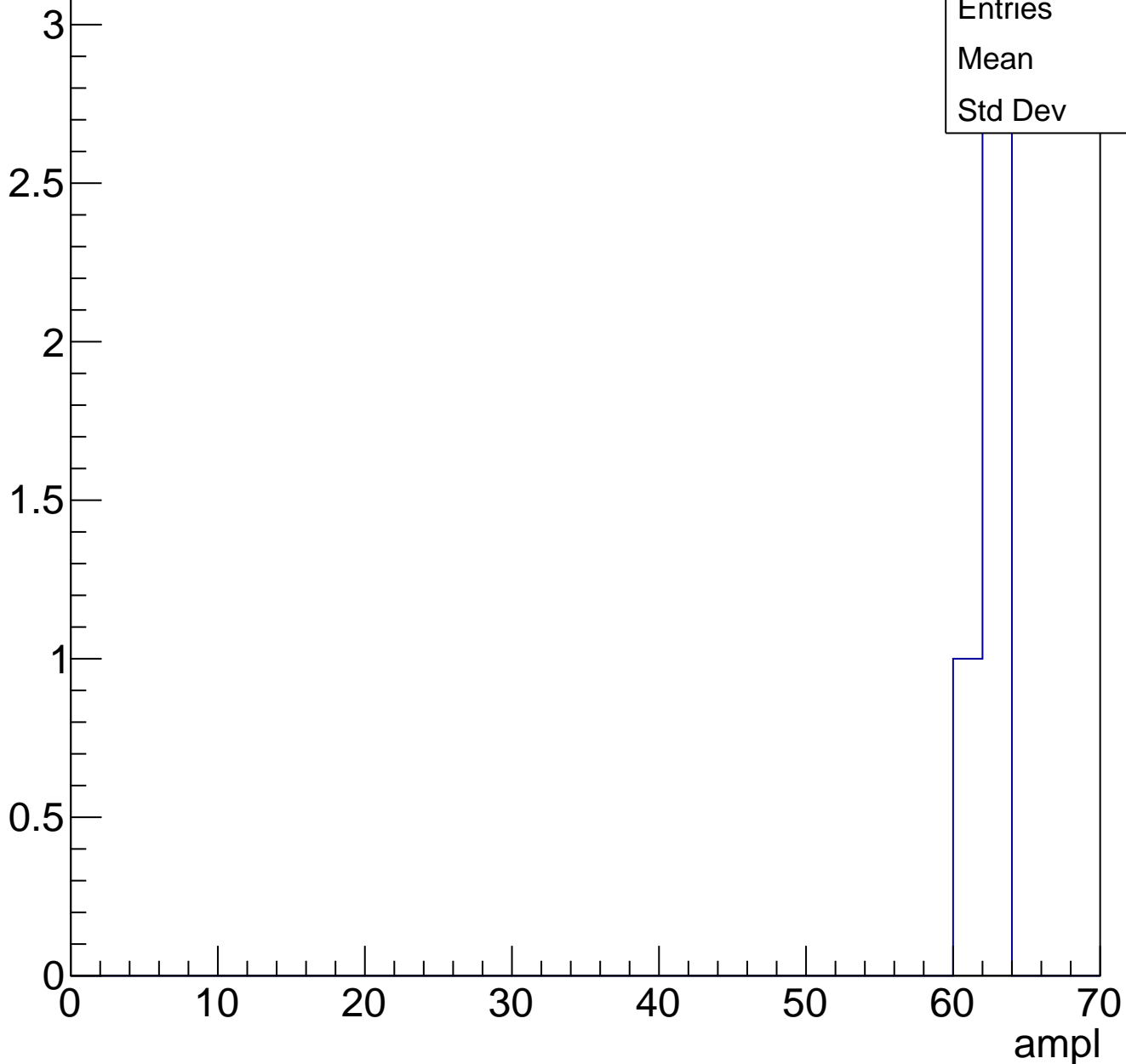




# B1L002S, U18-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch89, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	25.92
Std Dev	5.671

**Gaus mean : 27.2341**

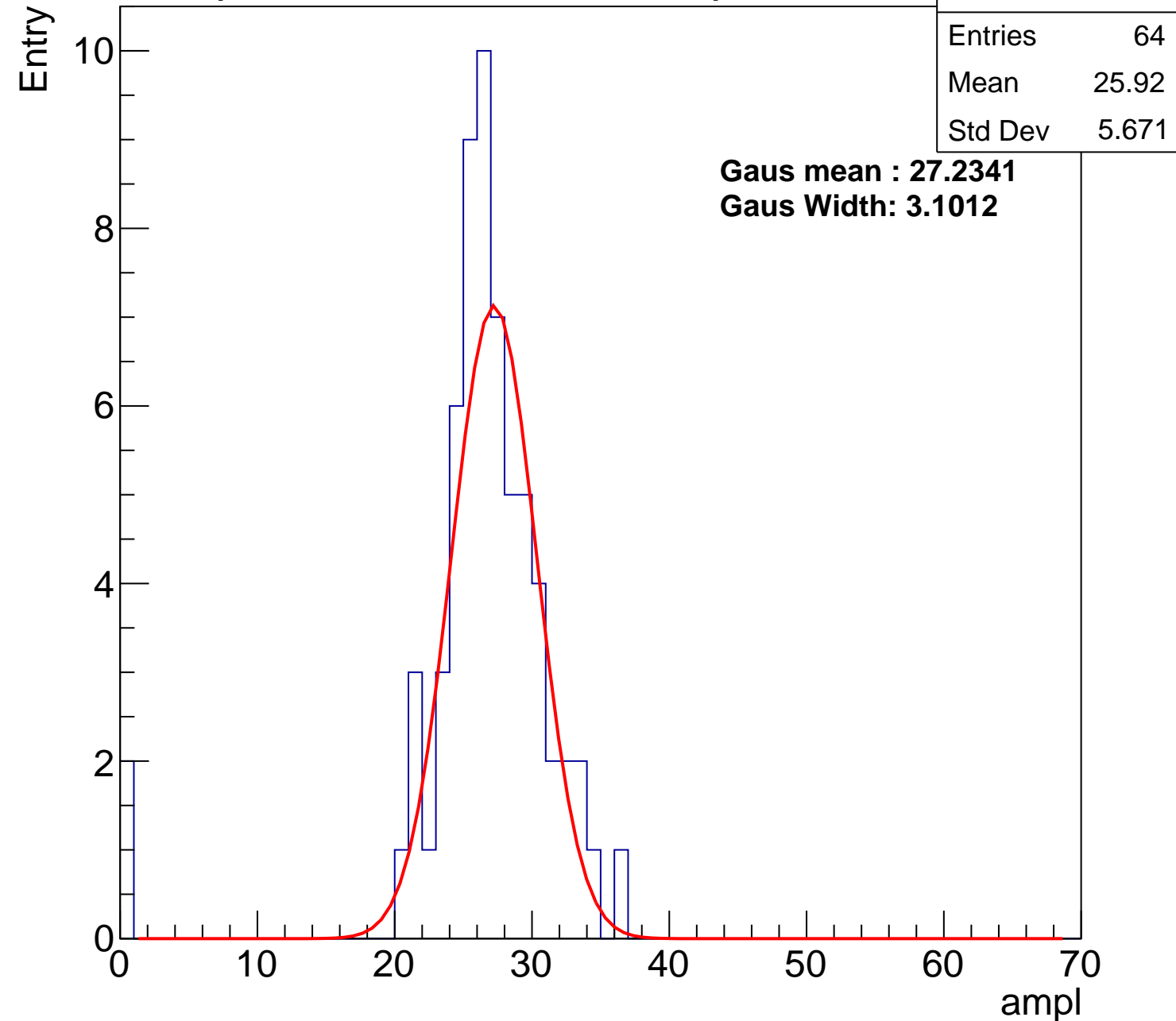
**Gaus Width: 3.1012**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch89, adc1

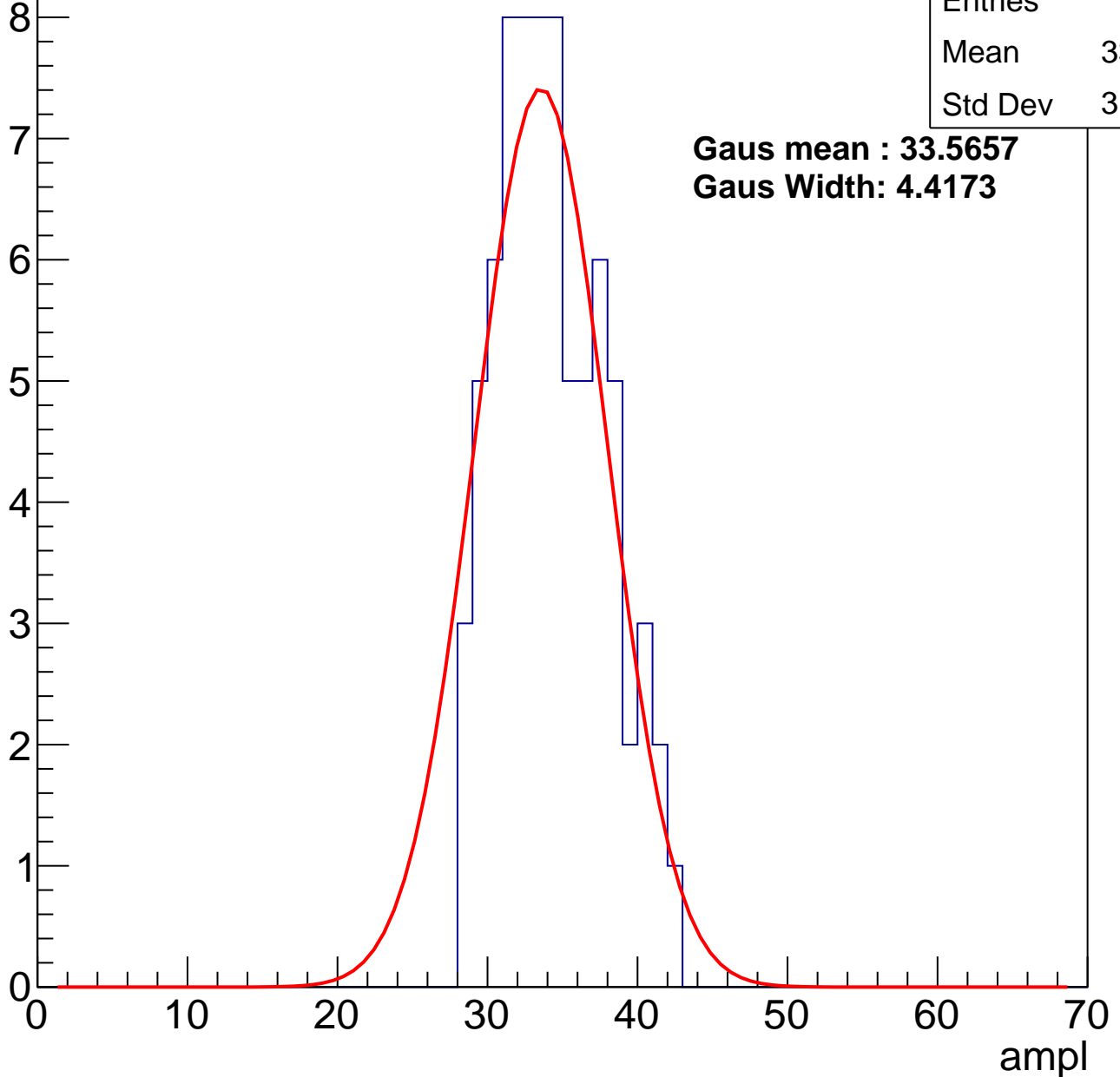
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	33.84
Std Dev	3.499

**Gaus mean : 33.5657**

**Gaus Width: 4.4173**



# B1L002S, U18-ch89, adc2

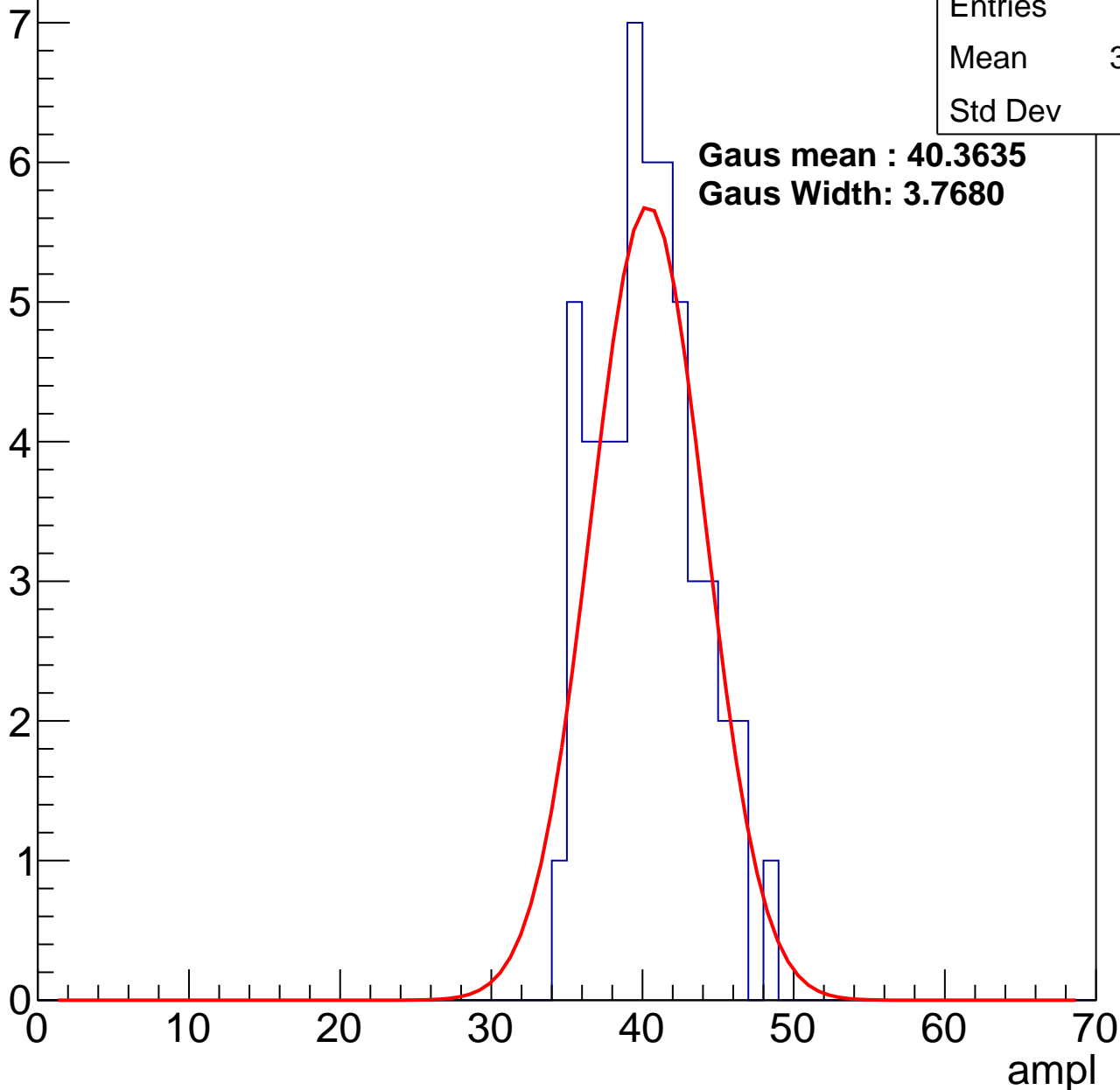
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	39.87
Std Dev	3.28

**Gaus mean : 40.3635**

**Gaus Width: 3.7680**

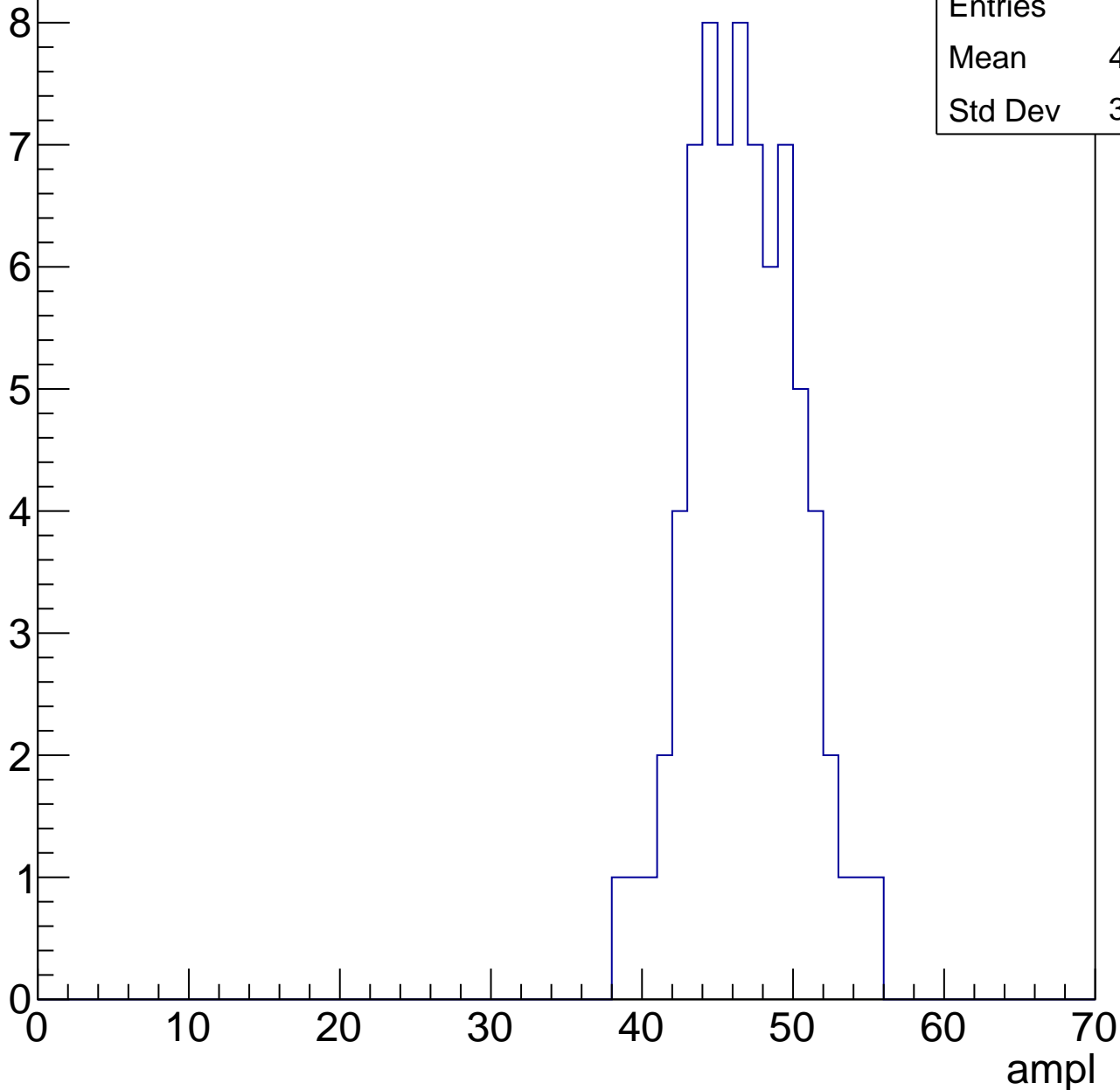


# B1L002S, U18-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

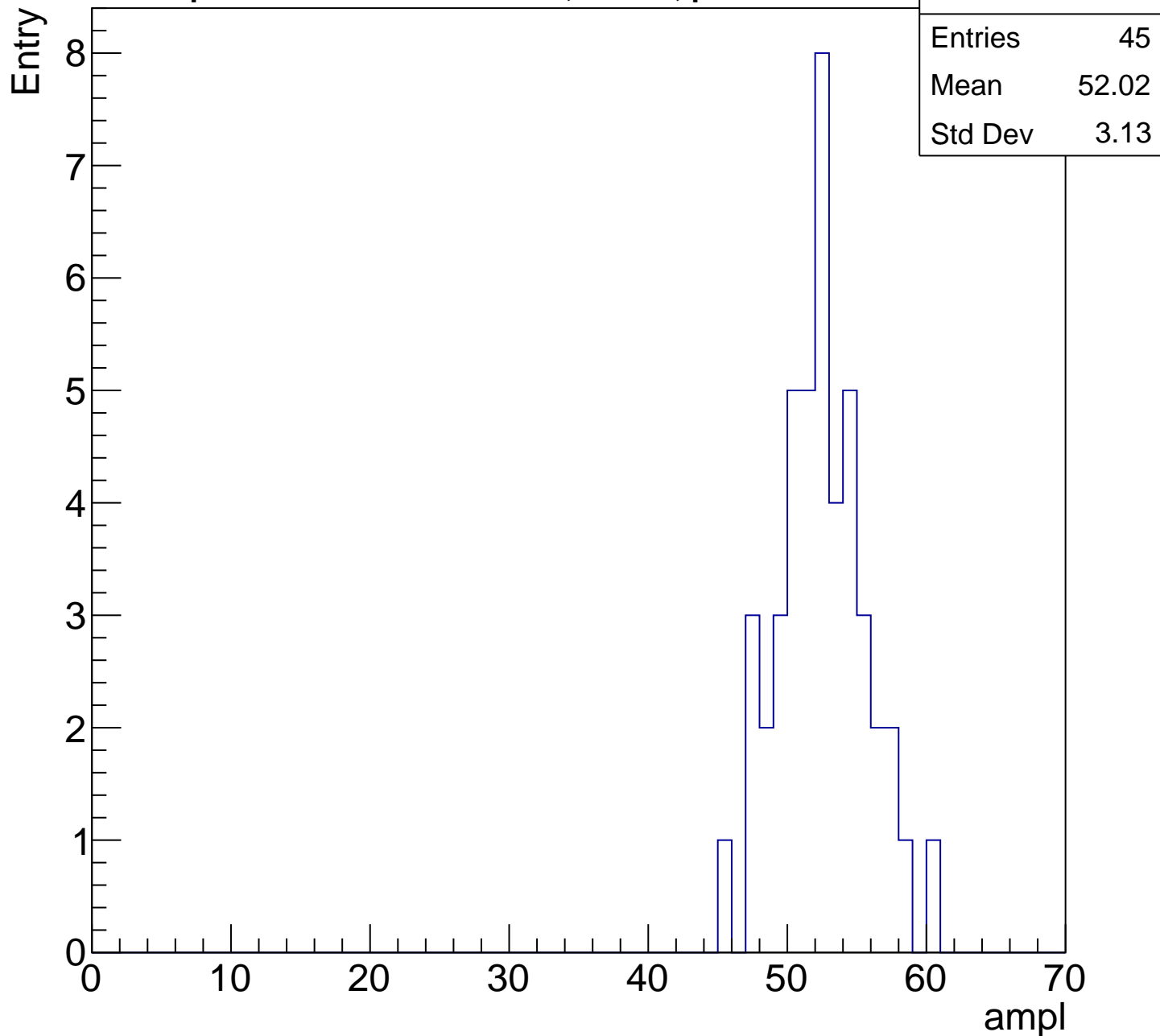
Entry

Entries	73
Mean	46.34
Std Dev	3.504



# B1L002S, U18-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

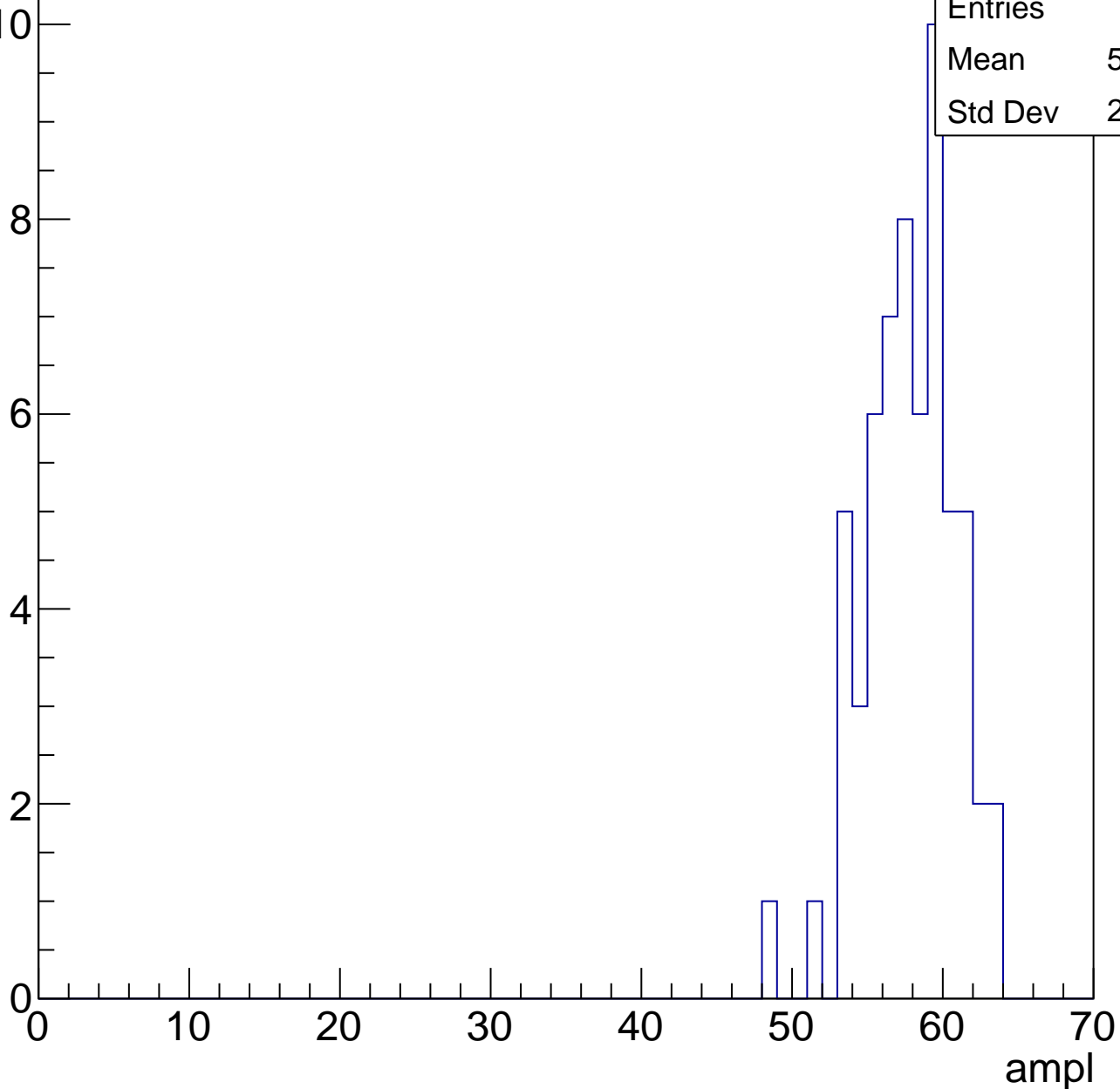


# B1L002S, U18-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.33
Std Dev	2.979

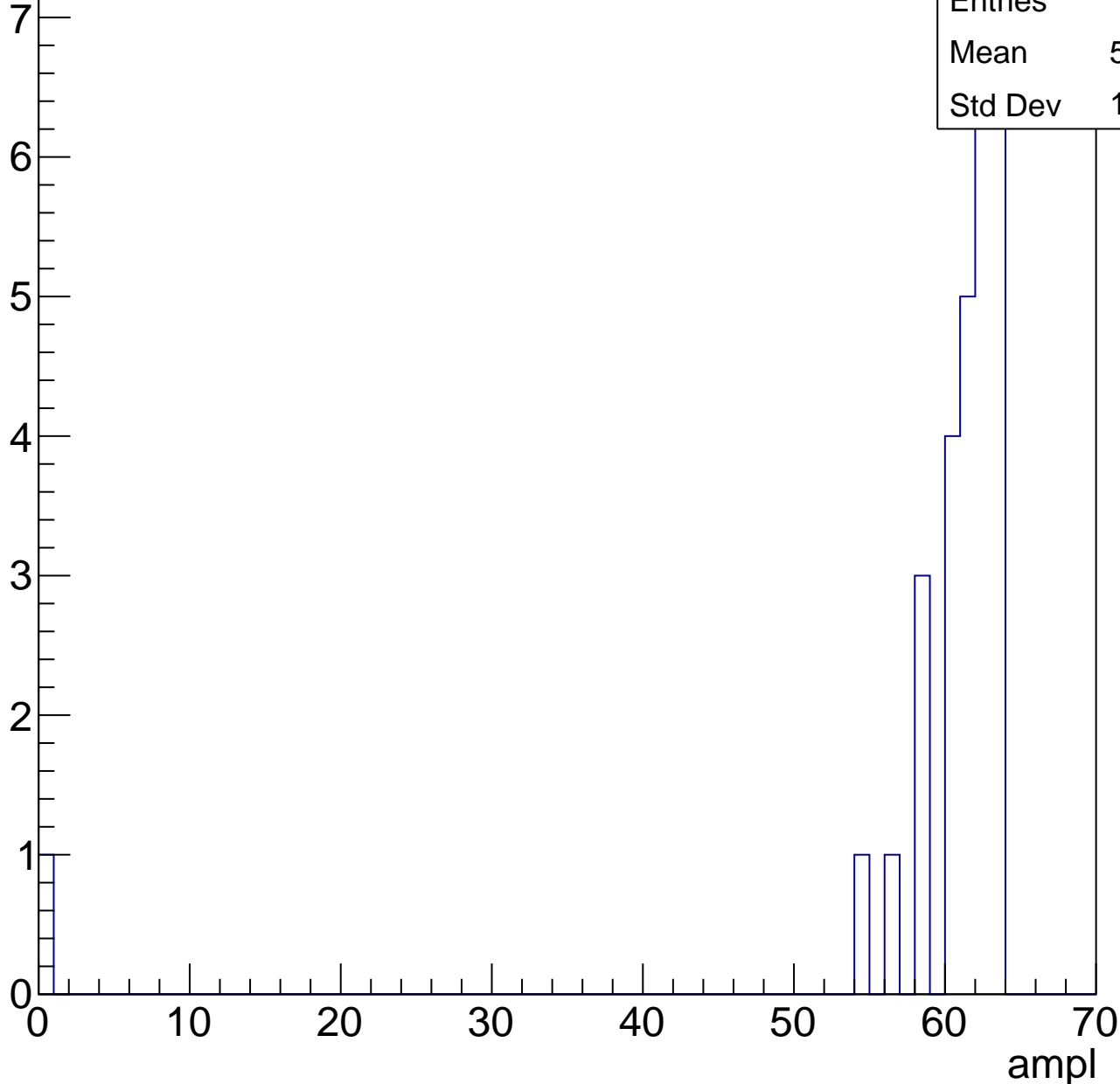


# B1L002S, U18-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.76
Std Dev	11.32

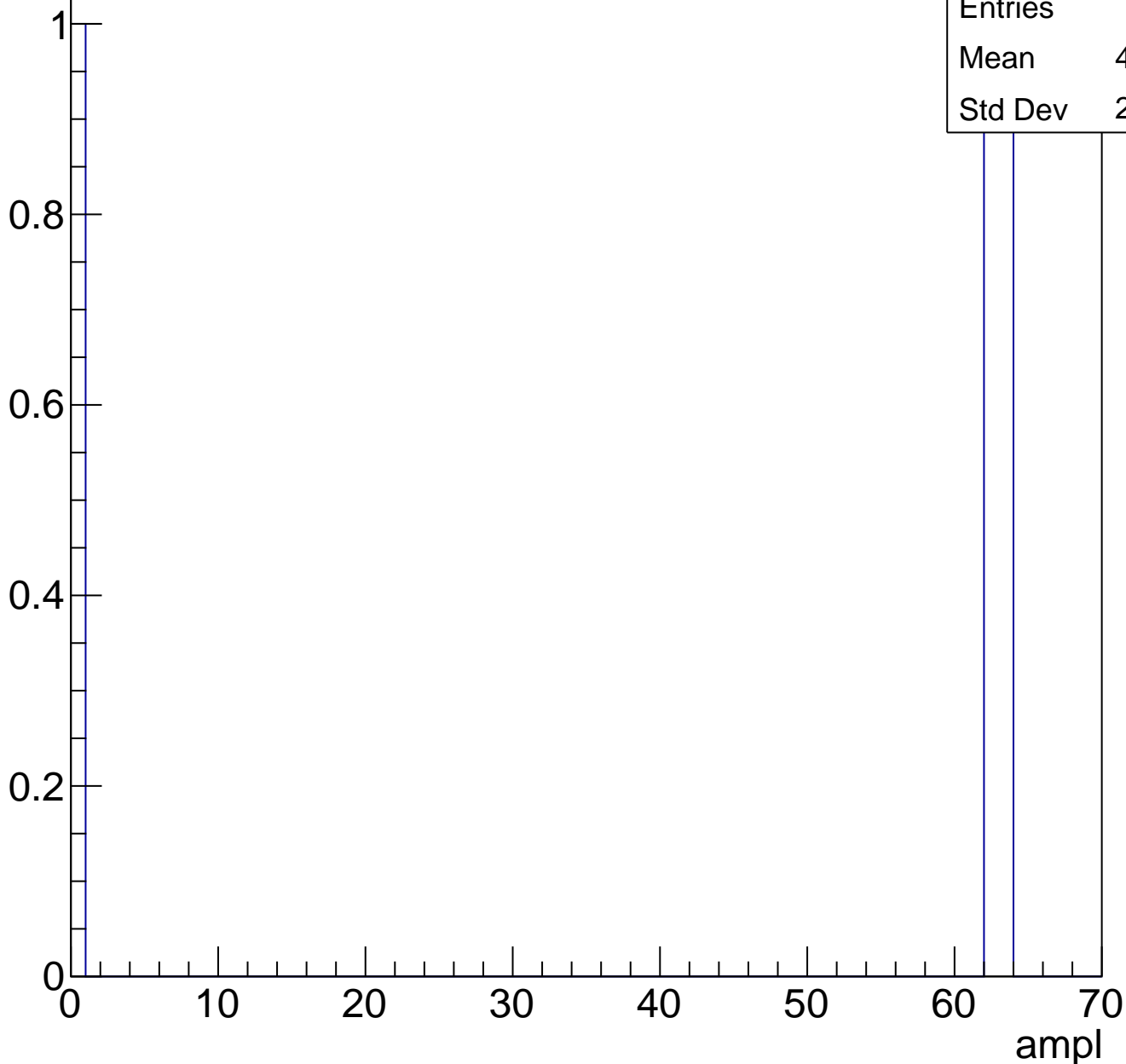




# B1L002S, U18-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch90, adc0

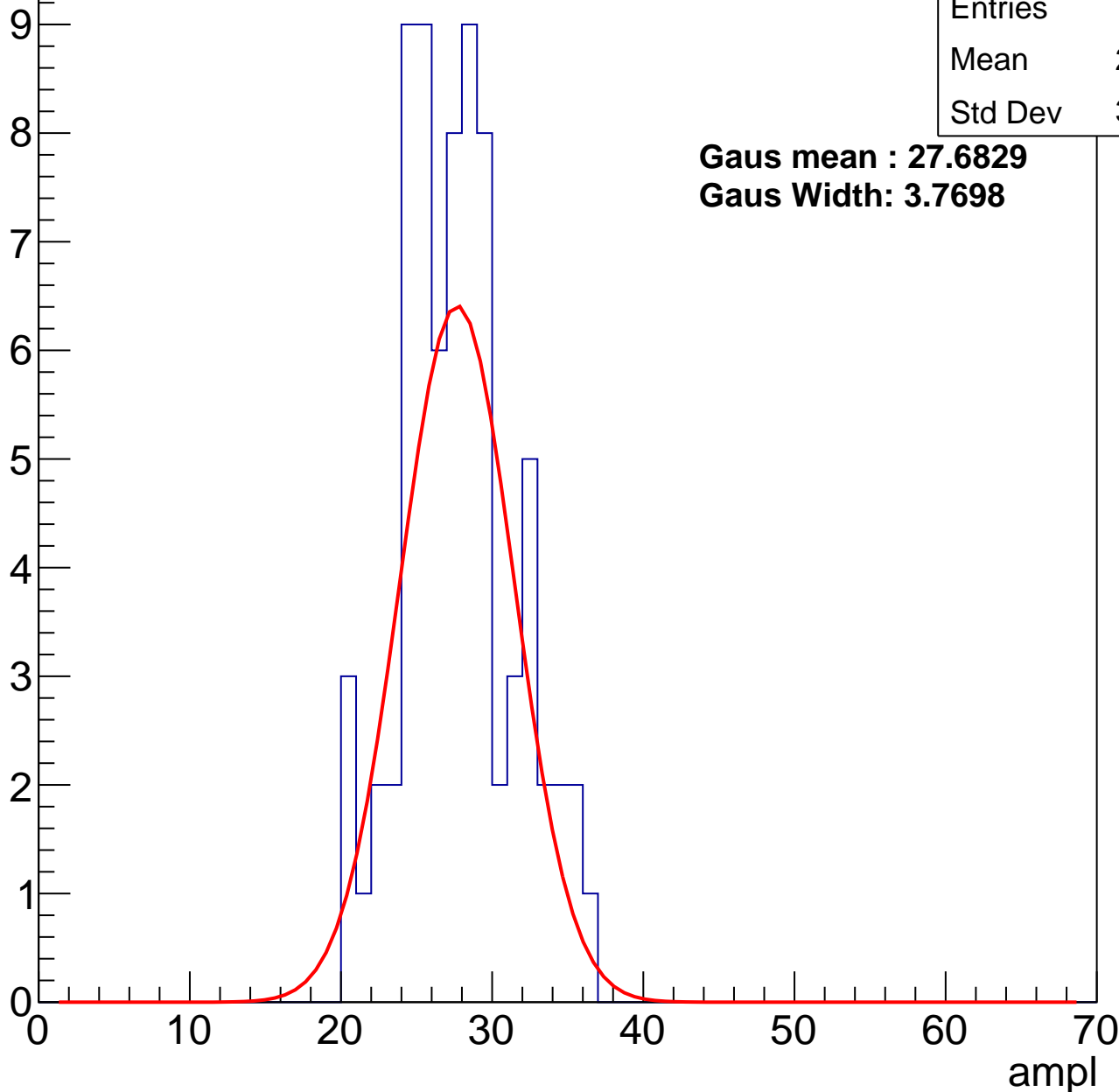
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	27.31
Std Dev	3.661

**Gaus mean : 27.6829**

**Gaus Width: 3.7698**



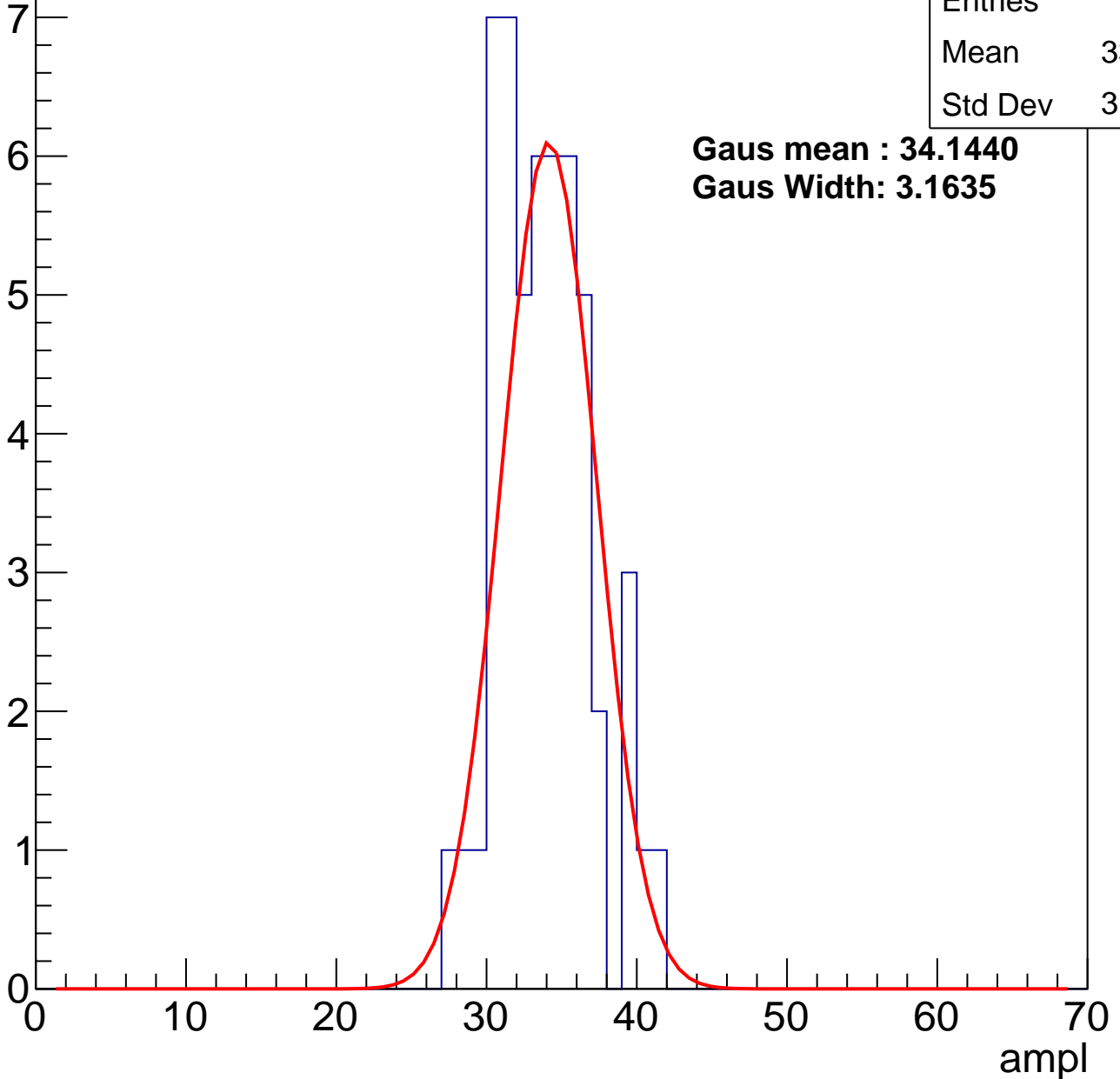
# B1L002S, U18-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.37
Std Dev	3.082

**Gaus mean : 34.1440**  
**Gaus Width: 3.1635**



# B1L002S, U18-ch90, adc2

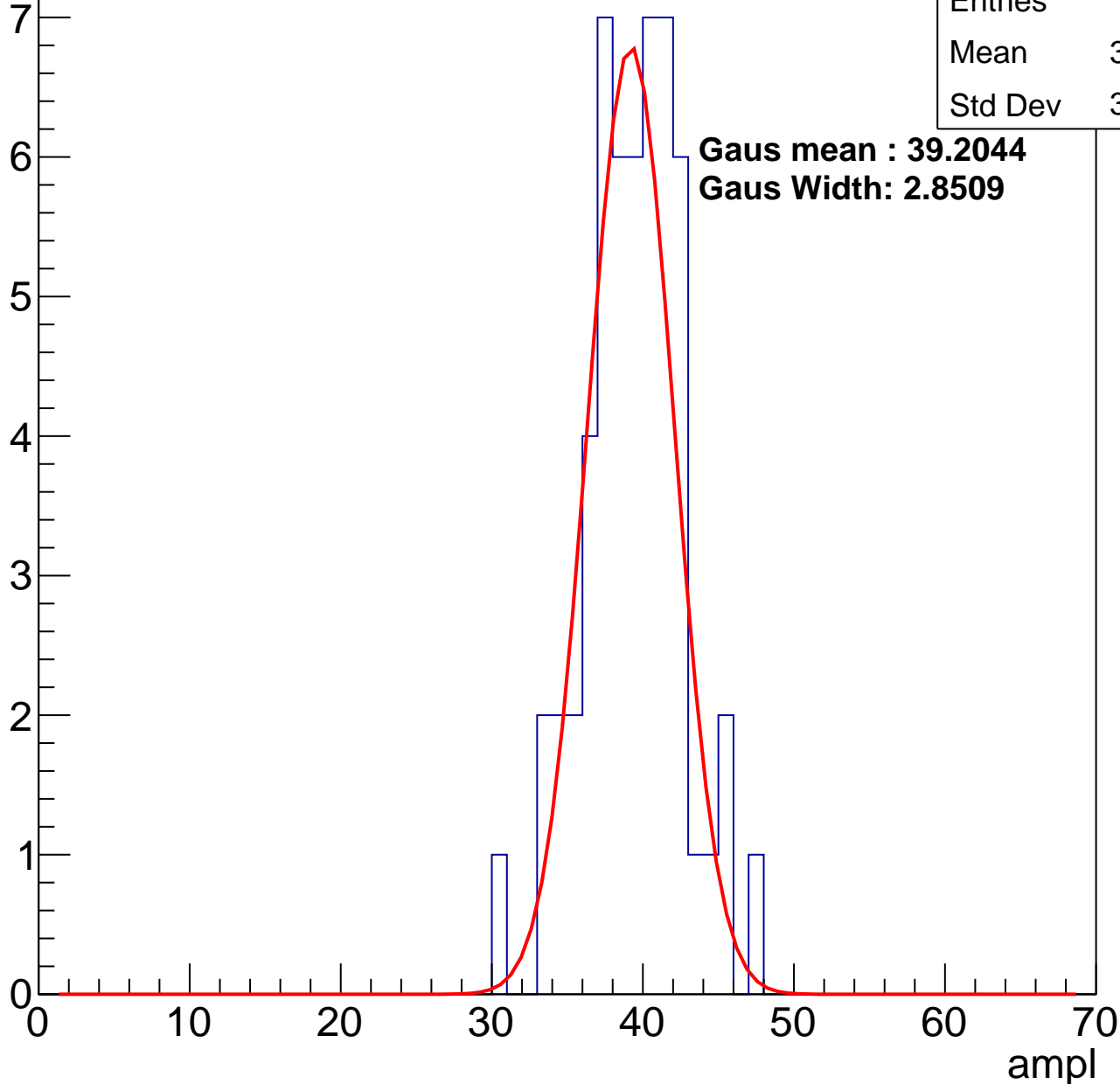
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	38.95
Std Dev	3.222

**Gaus mean : 39.2044**

**Gaus Width: 2.8509**

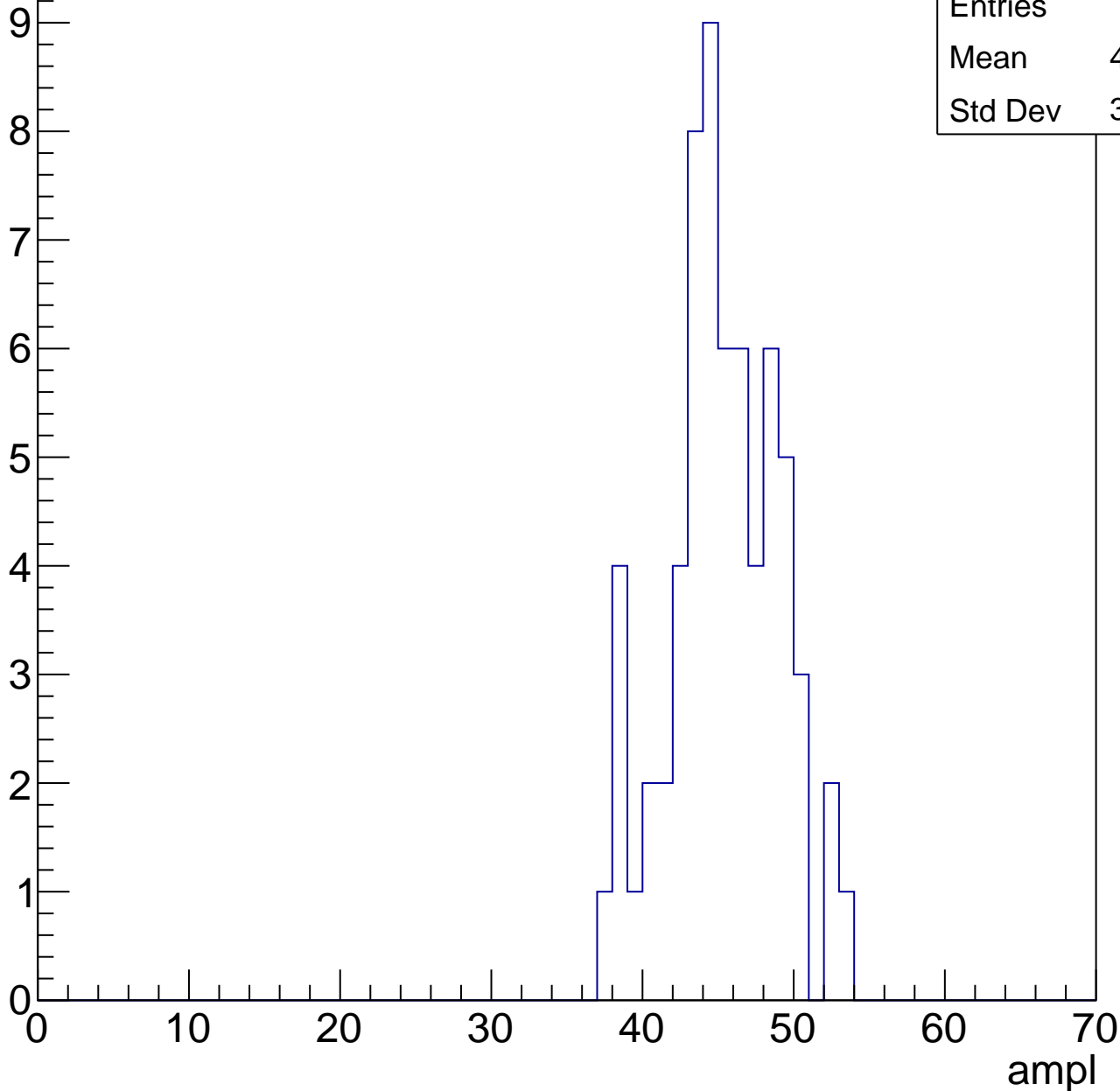


# B1L002S, U18-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	44.88
Std Dev	3.638

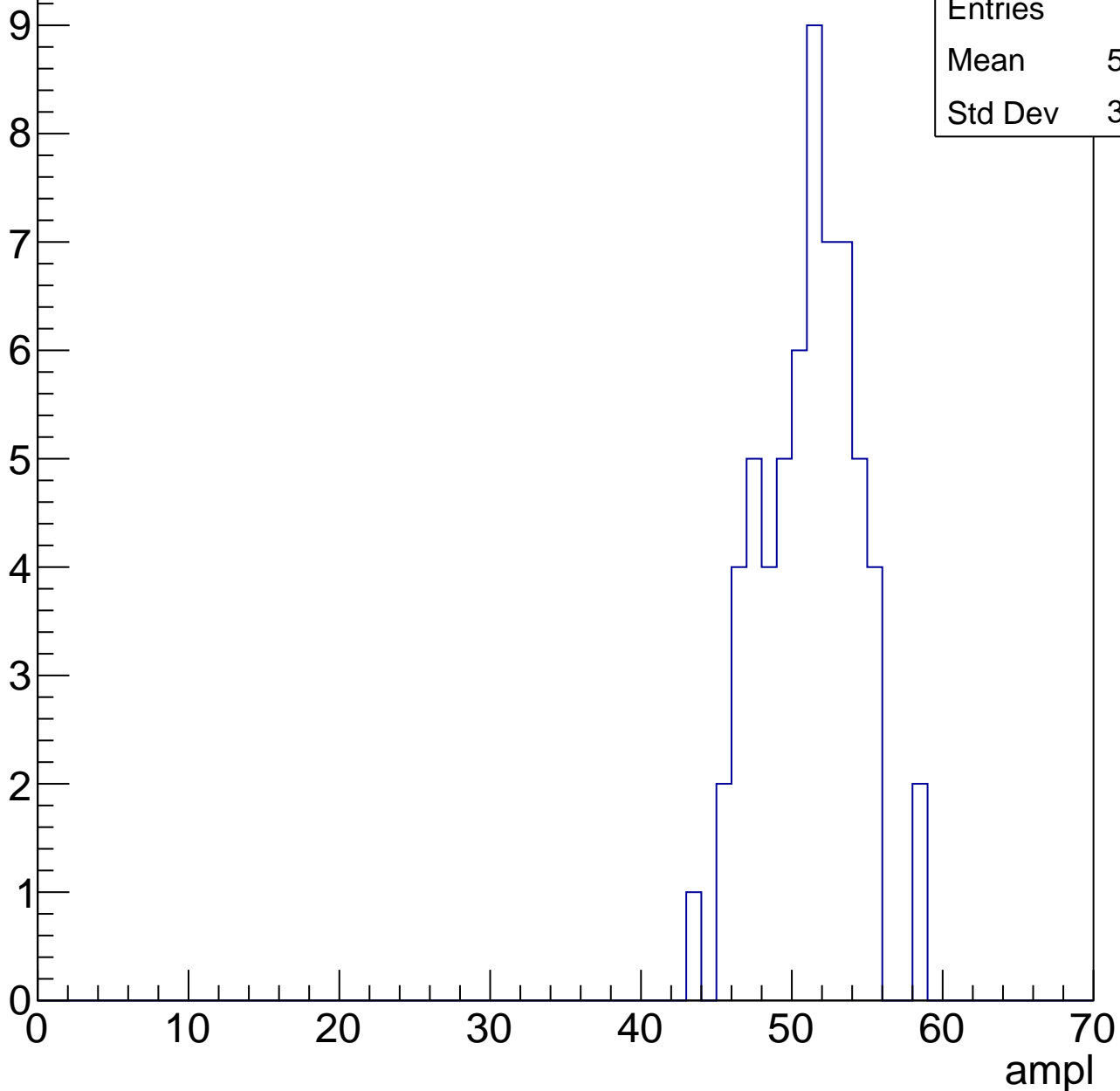


# B1L002S, U18-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

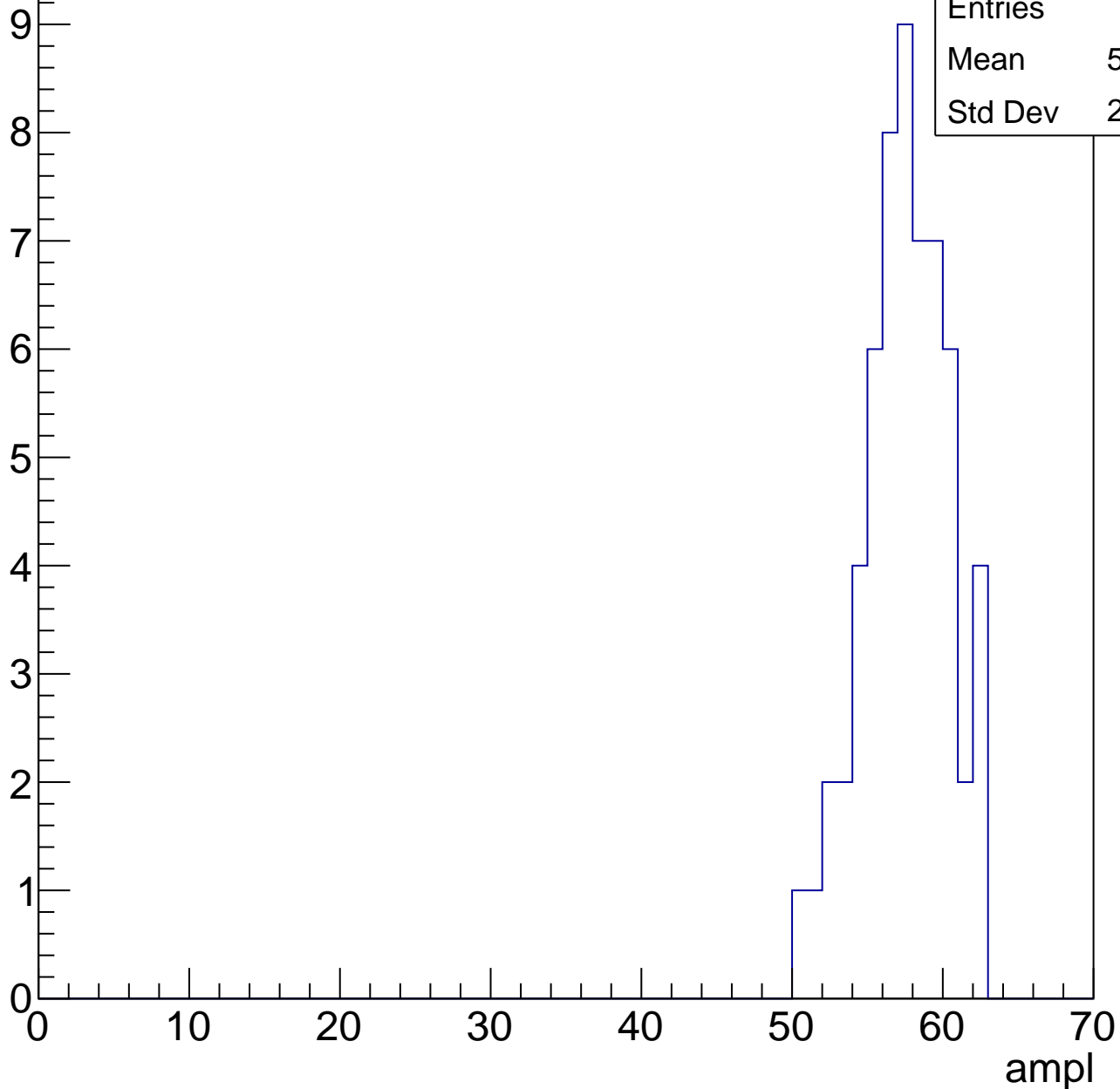
Entries	61
Mean	50.64
Std Dev	3.168



# B1L002S, U18-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



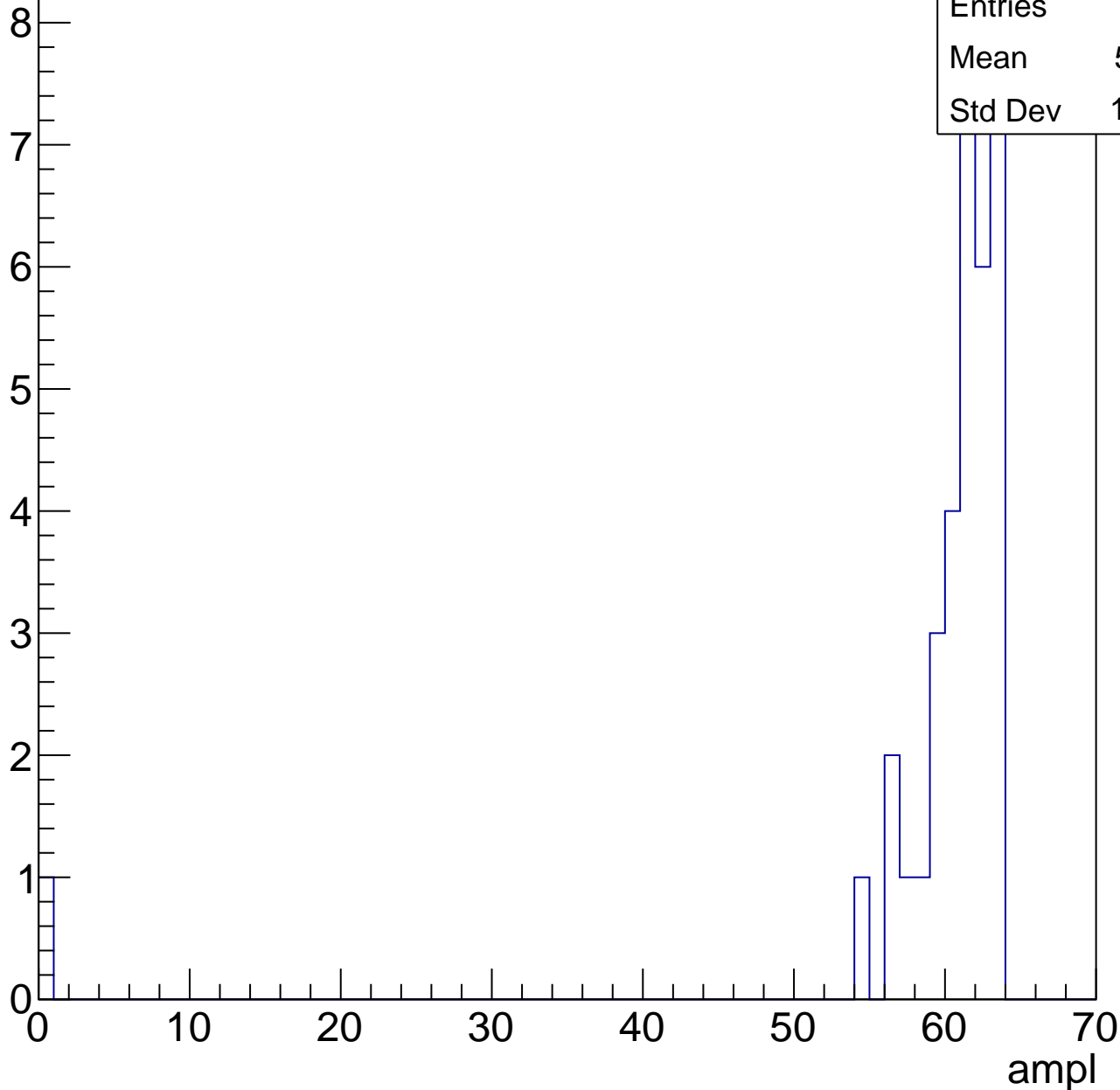
Entries	59
Mean	57.07
Std Dev	2.779

# B1L002S, U18-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.91
Std Dev	10.35





# B1L002S, U18-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch91, adc0

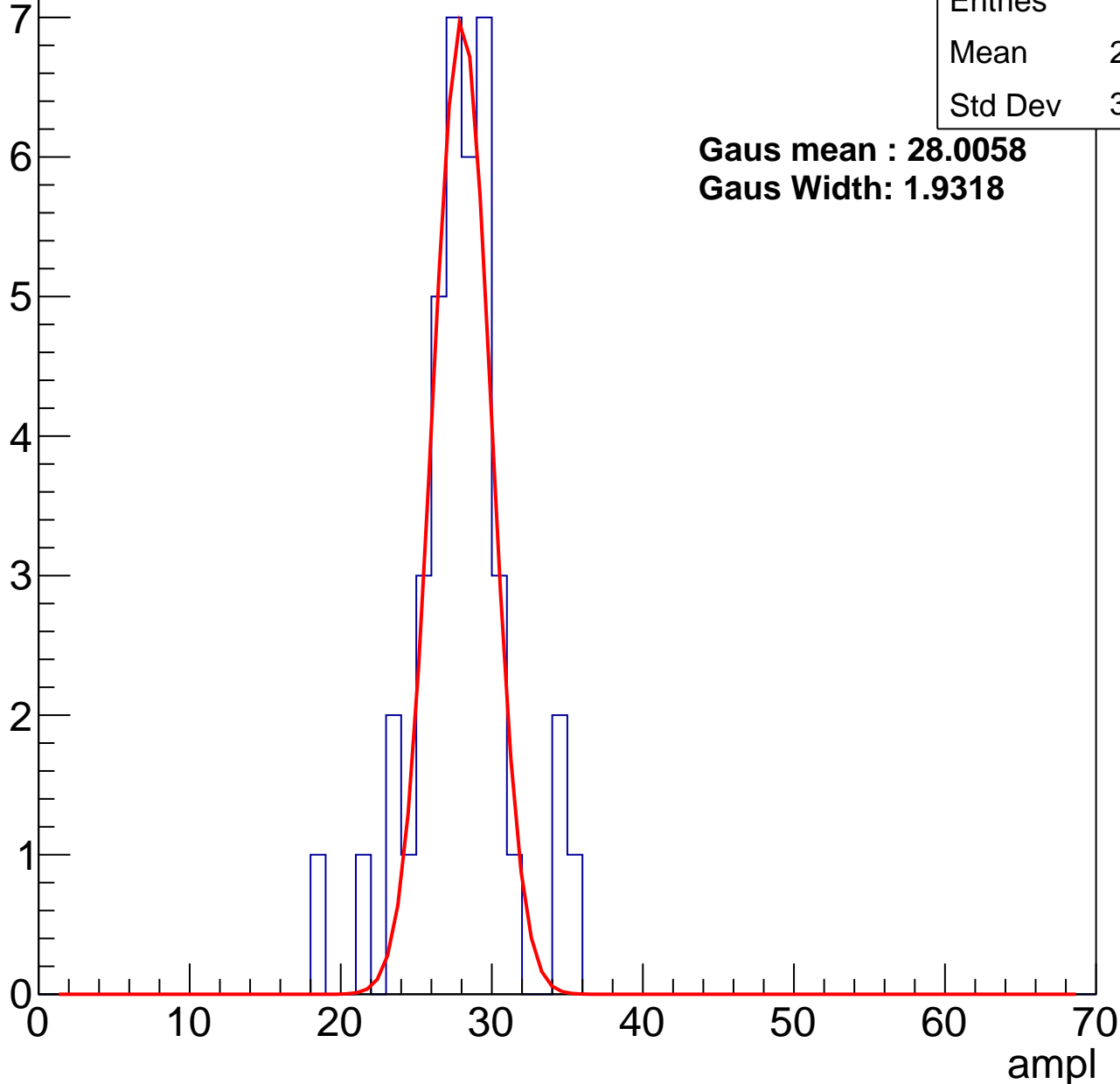
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	27.45
Std Dev	3.186

**Gaus mean : 28.0058**

**Gaus Width: 1.9318**



# B1L002S, U18-ch91, adc1

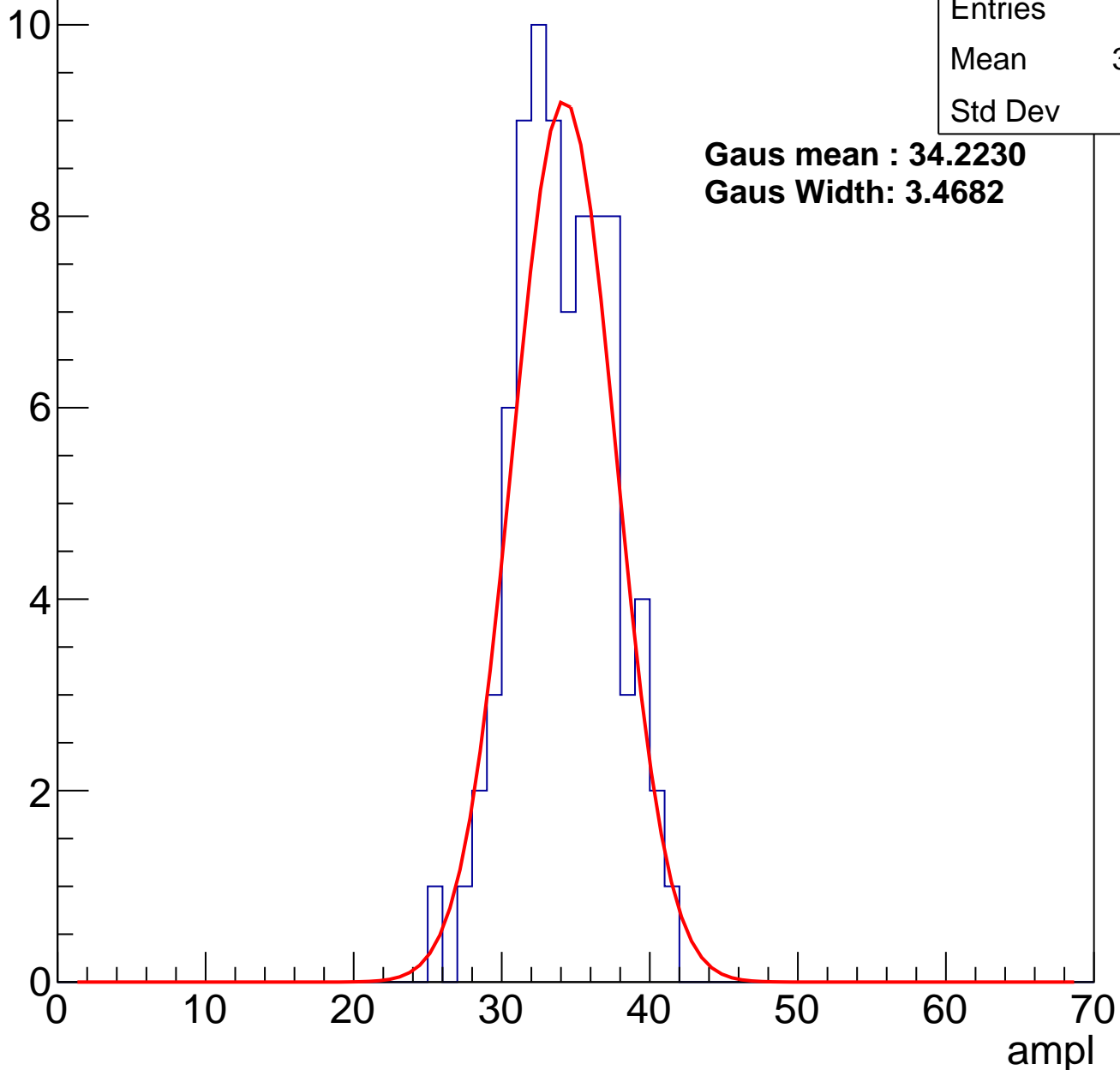
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	33.71
Std Dev	3.27

**Gaus mean : 34.2230**

**Gaus Width: 3.4682**

Entry



# B1L002S, U18-ch91, adc2

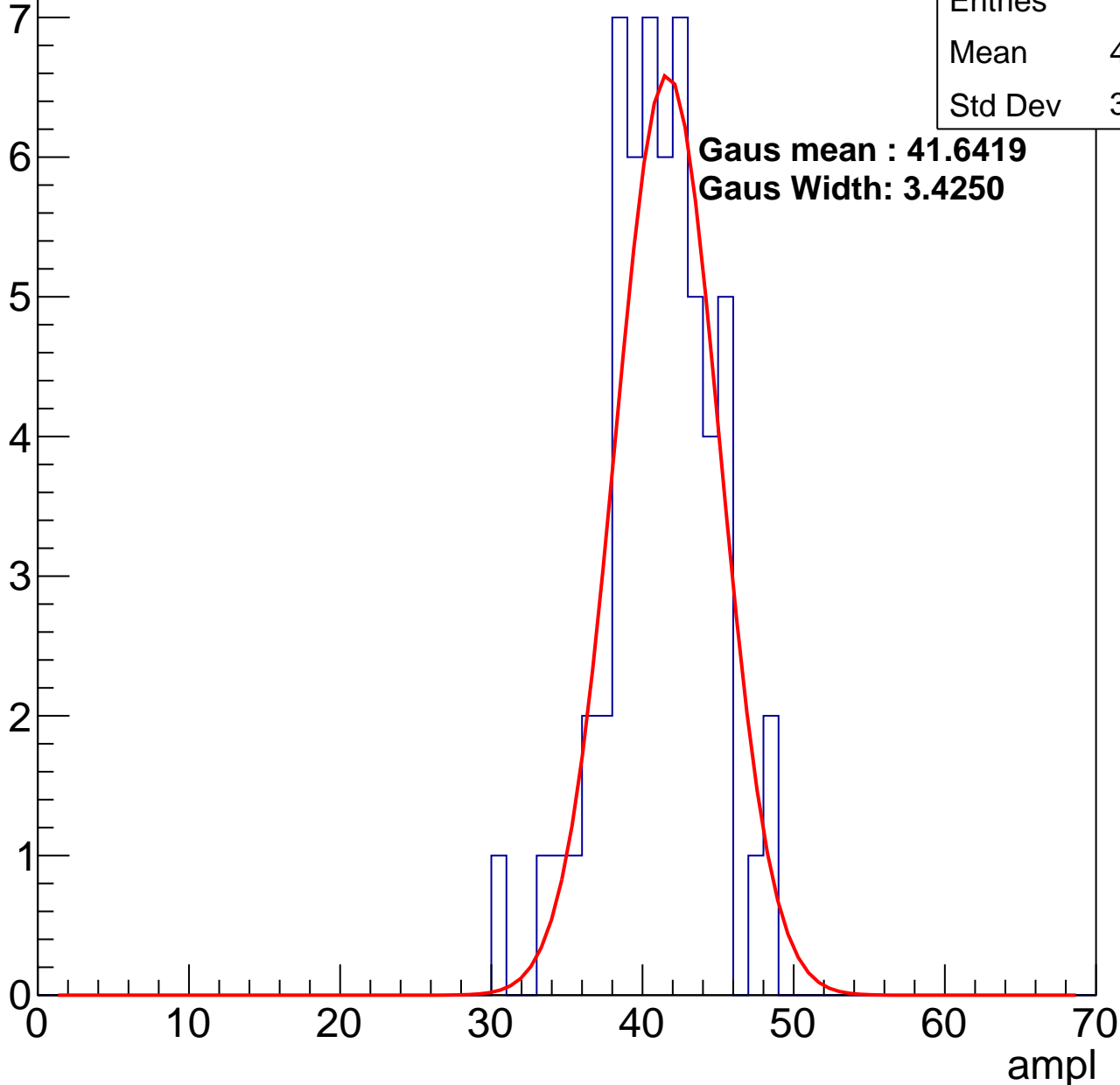
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.64
Std Dev	3.512

**Gaus mean : 41.6419**

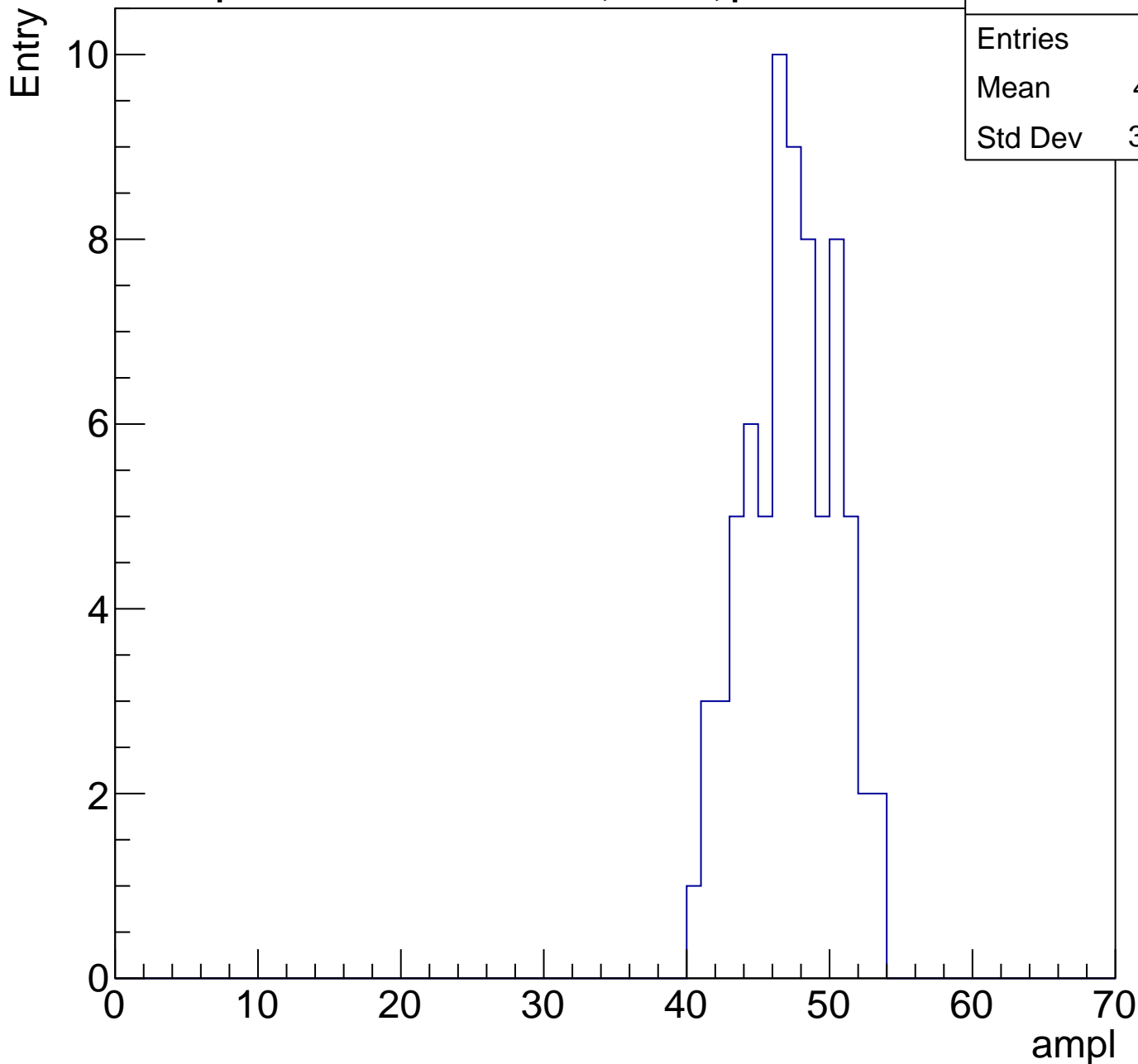
**Gaus Width: 3.4250**



# B1L002S, U18-ch91, adc3

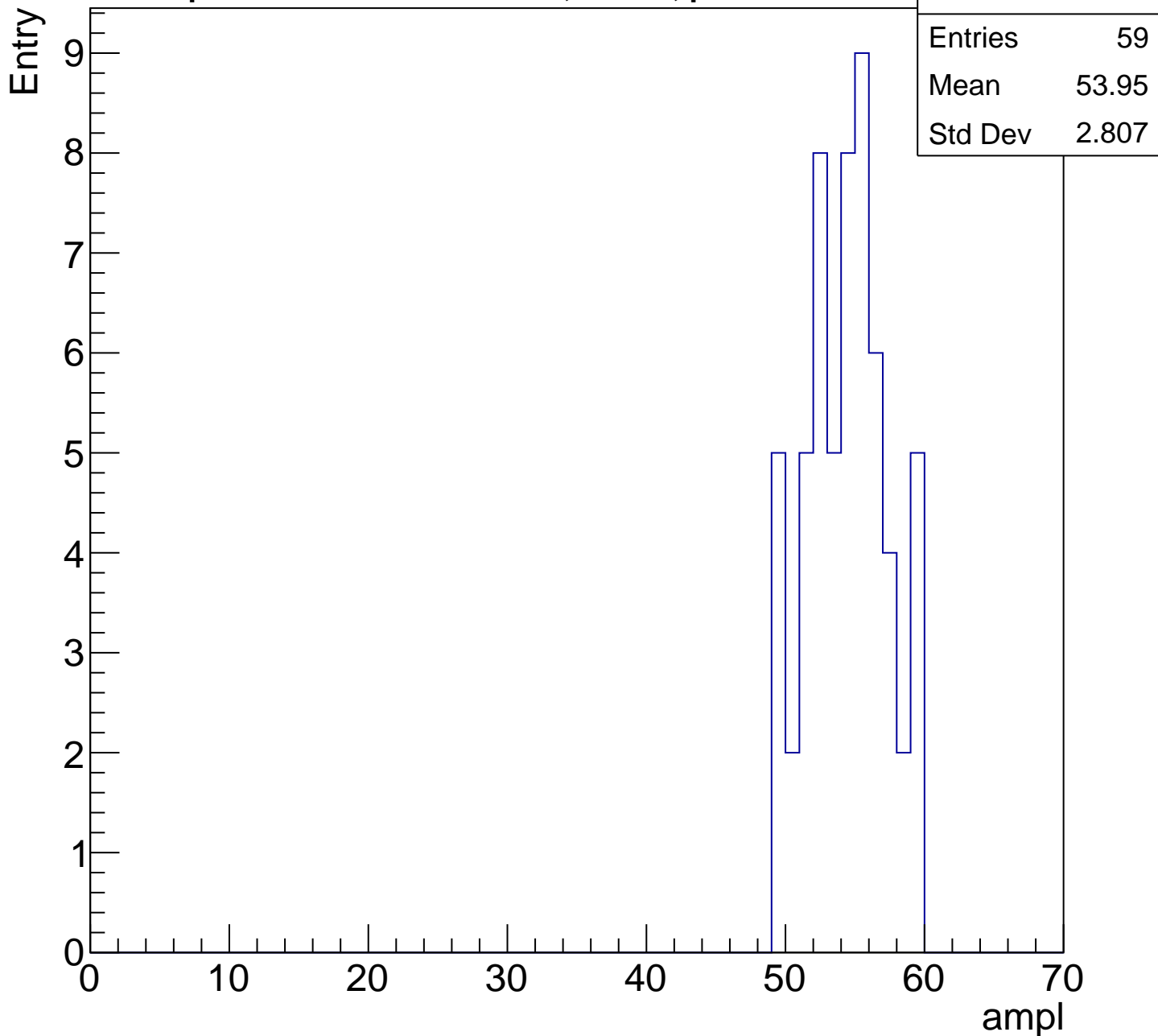
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	46.81
Std Dev	3.108



# B1L002S, U18-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

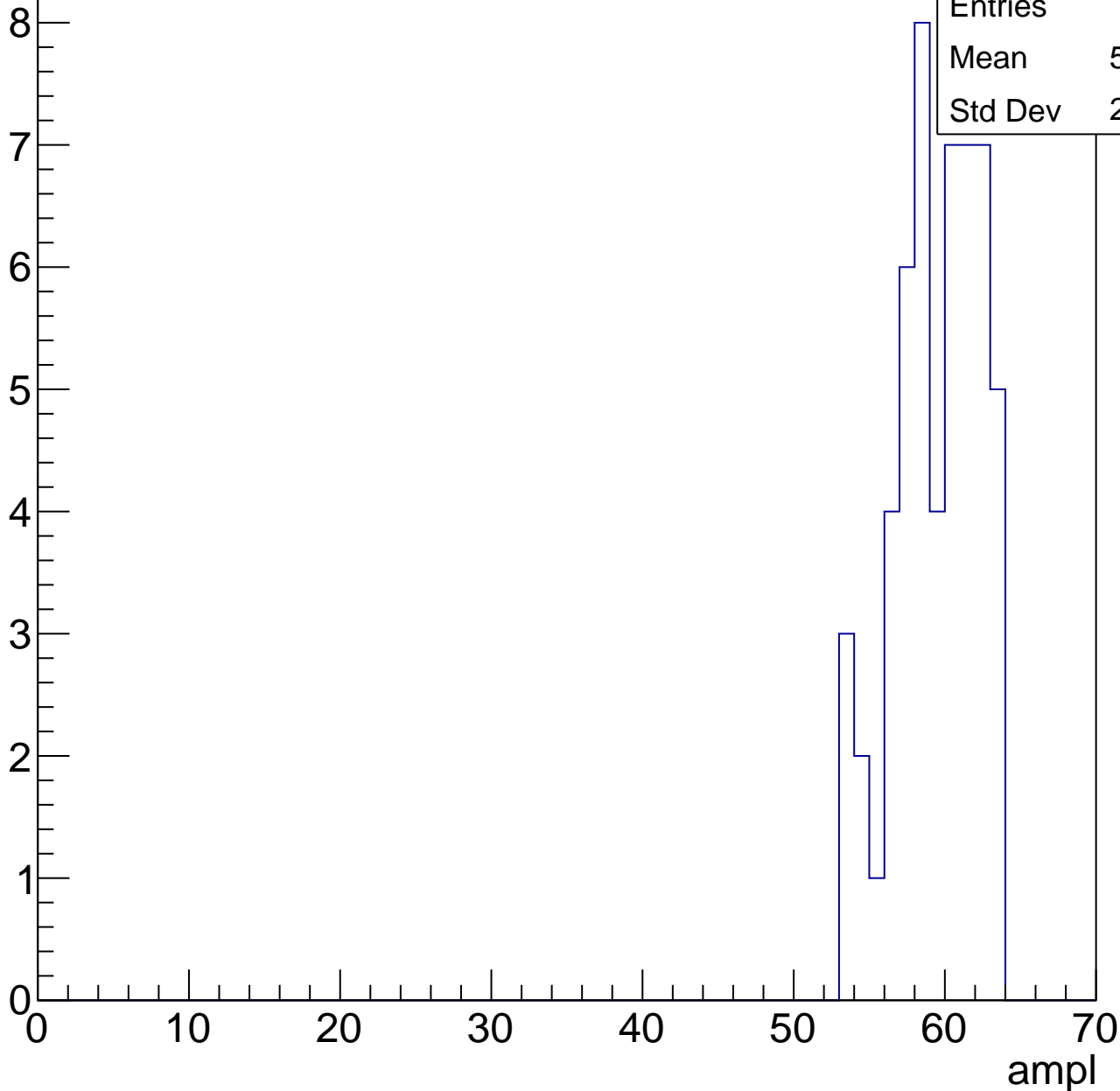


# B1L002S, U18-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.96
Std Dev	2.789



# B1L002S, U18-ch91, adc6

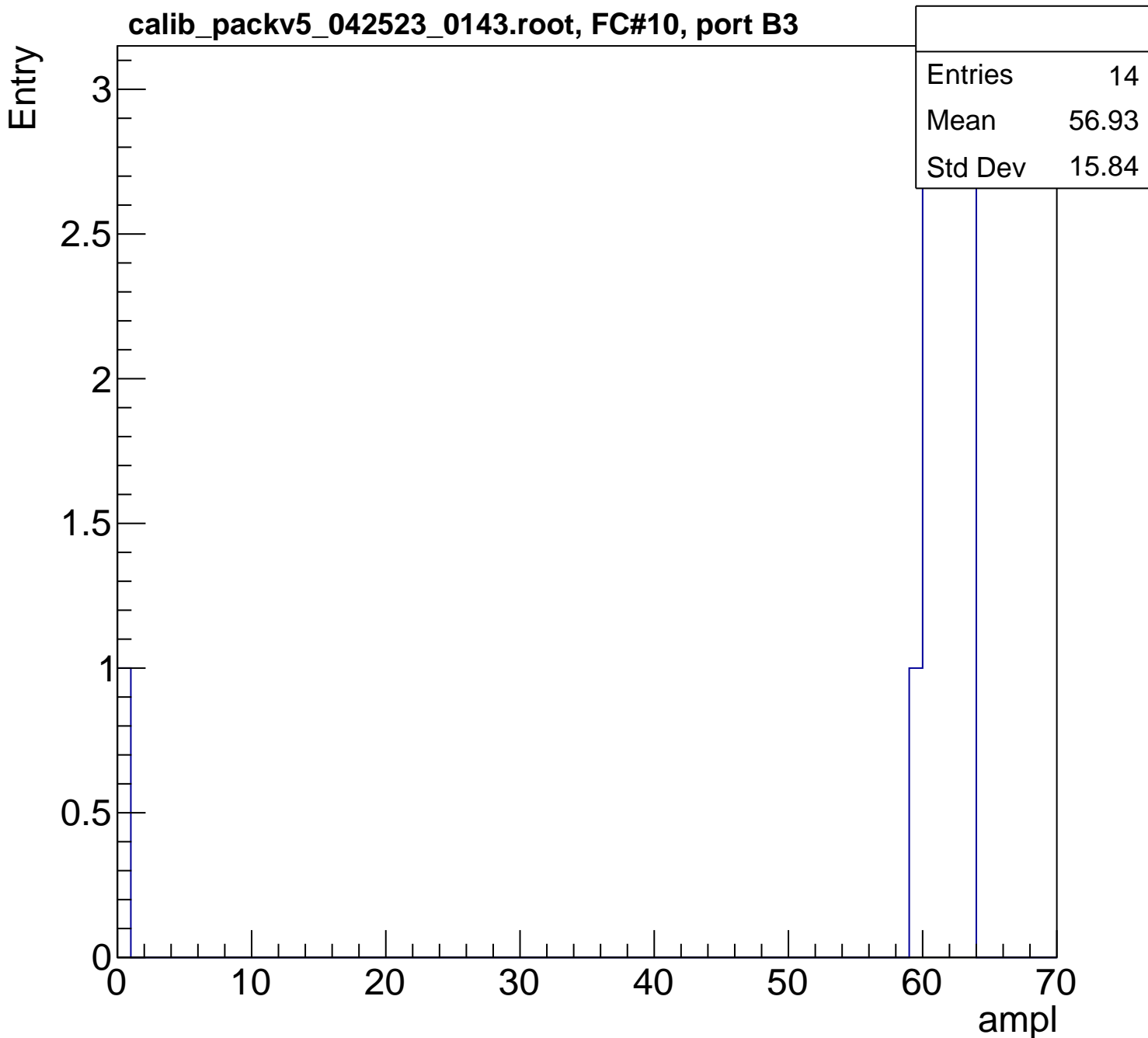
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	14
Mean	56.93
Std Dev	15.84

ampl





# B1L002S, U18-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch92, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	27.85
Std Dev	4.606

**Gaus mean : 28.5023**

**Gaus Width: 2.9332**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

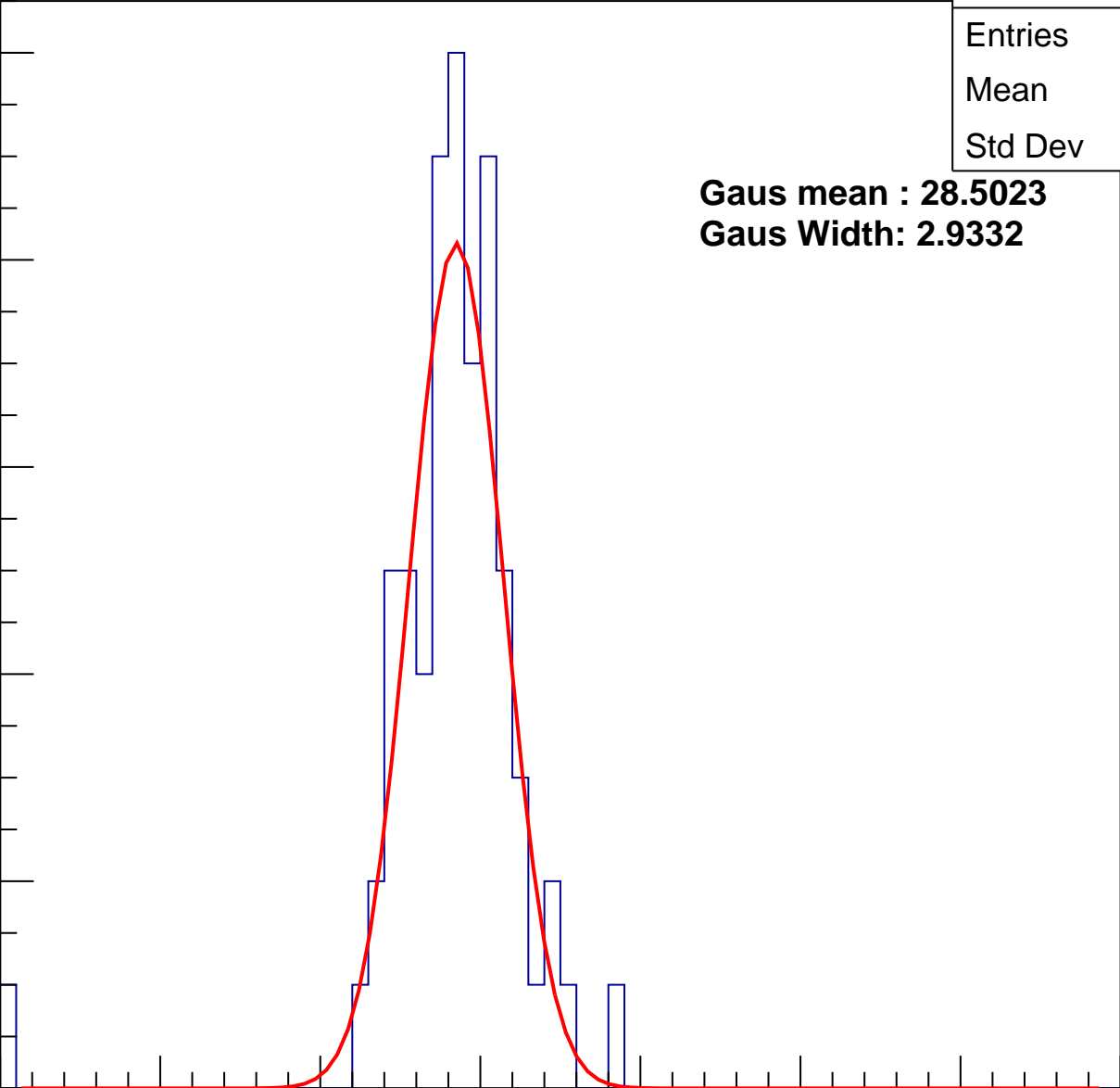
30

40

50

60

70



# B1L002S, U18-ch92, adc1

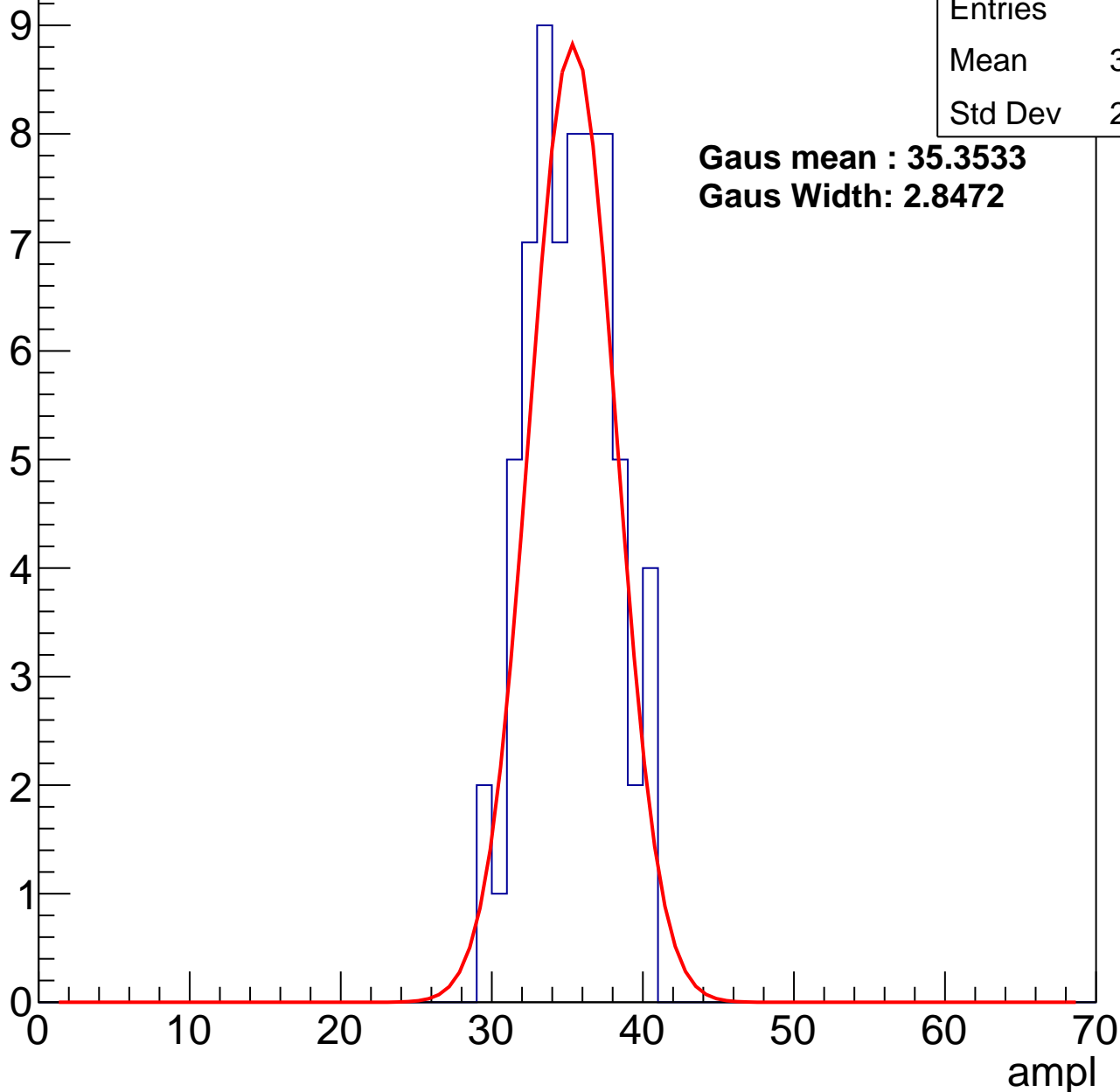
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.76
Std Dev	2.742

**Gaus mean : 35.3533**

**Gaus Width: 2.8472**



# B1L002S, U18-ch92, adc2

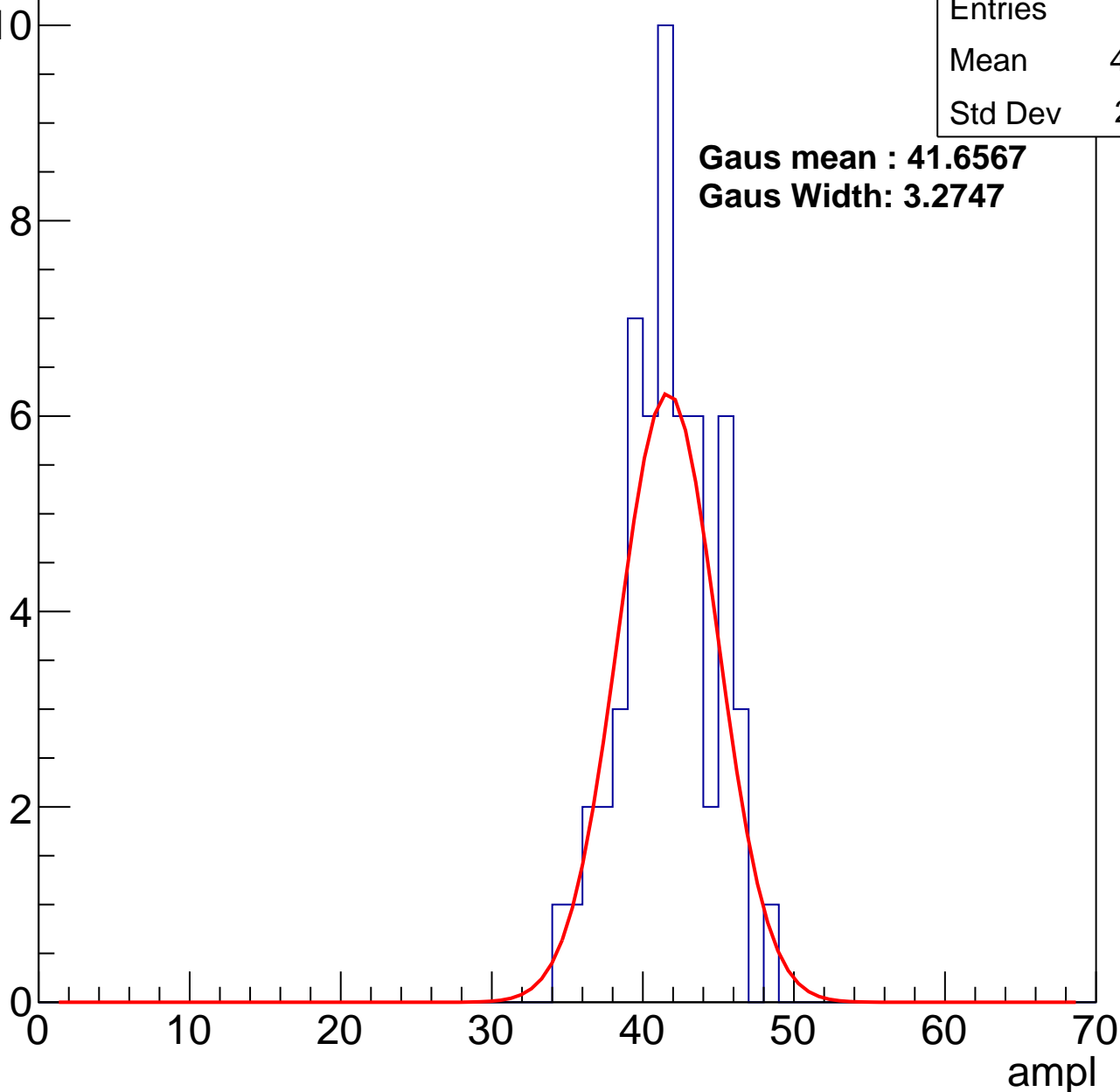
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.18
Std Dev	2.971

**Gaus mean : 41.6567**

**Gaus Width: 3.2747**

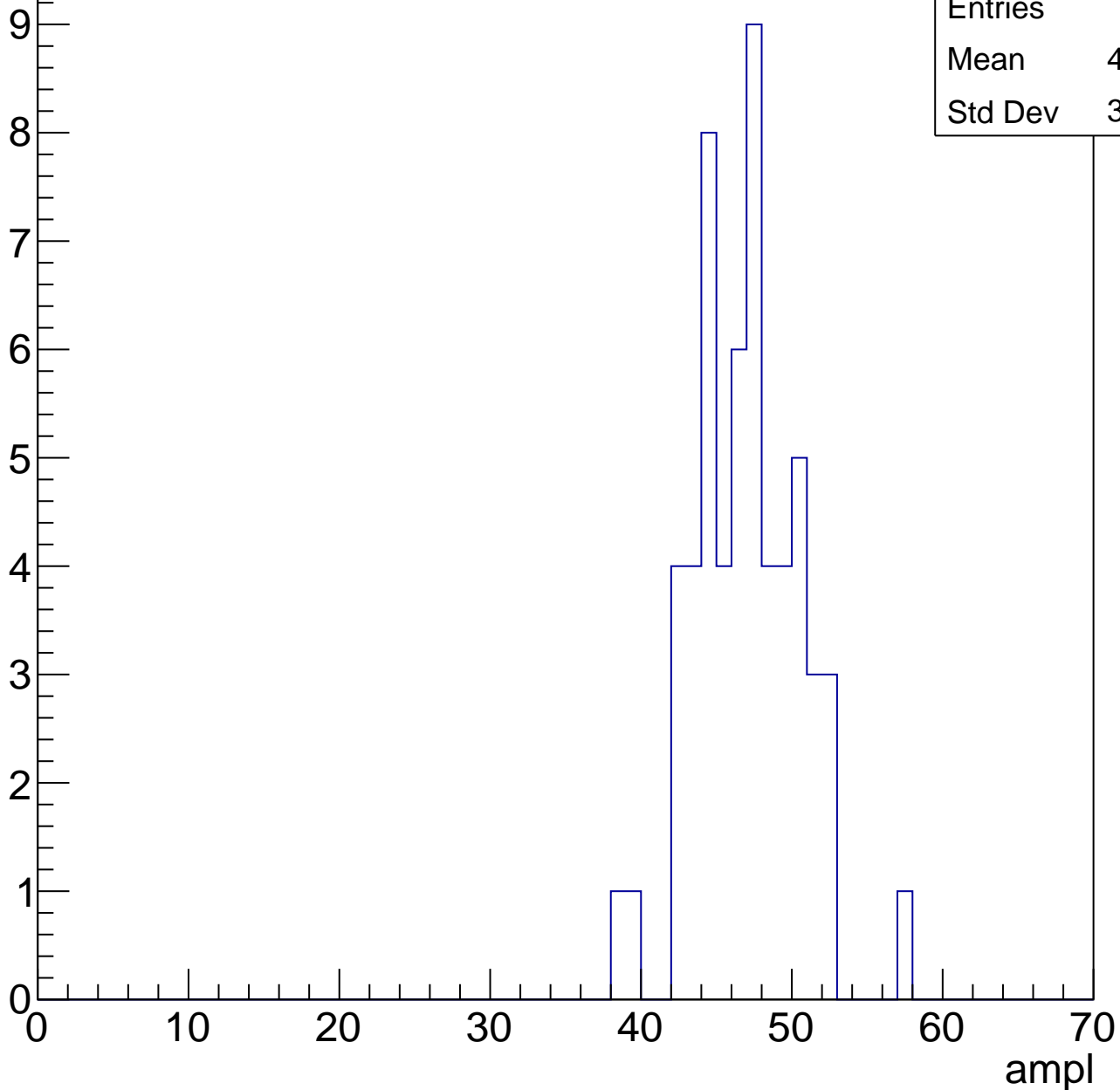


# B1L002S, U18-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.53
Std Dev	3.449

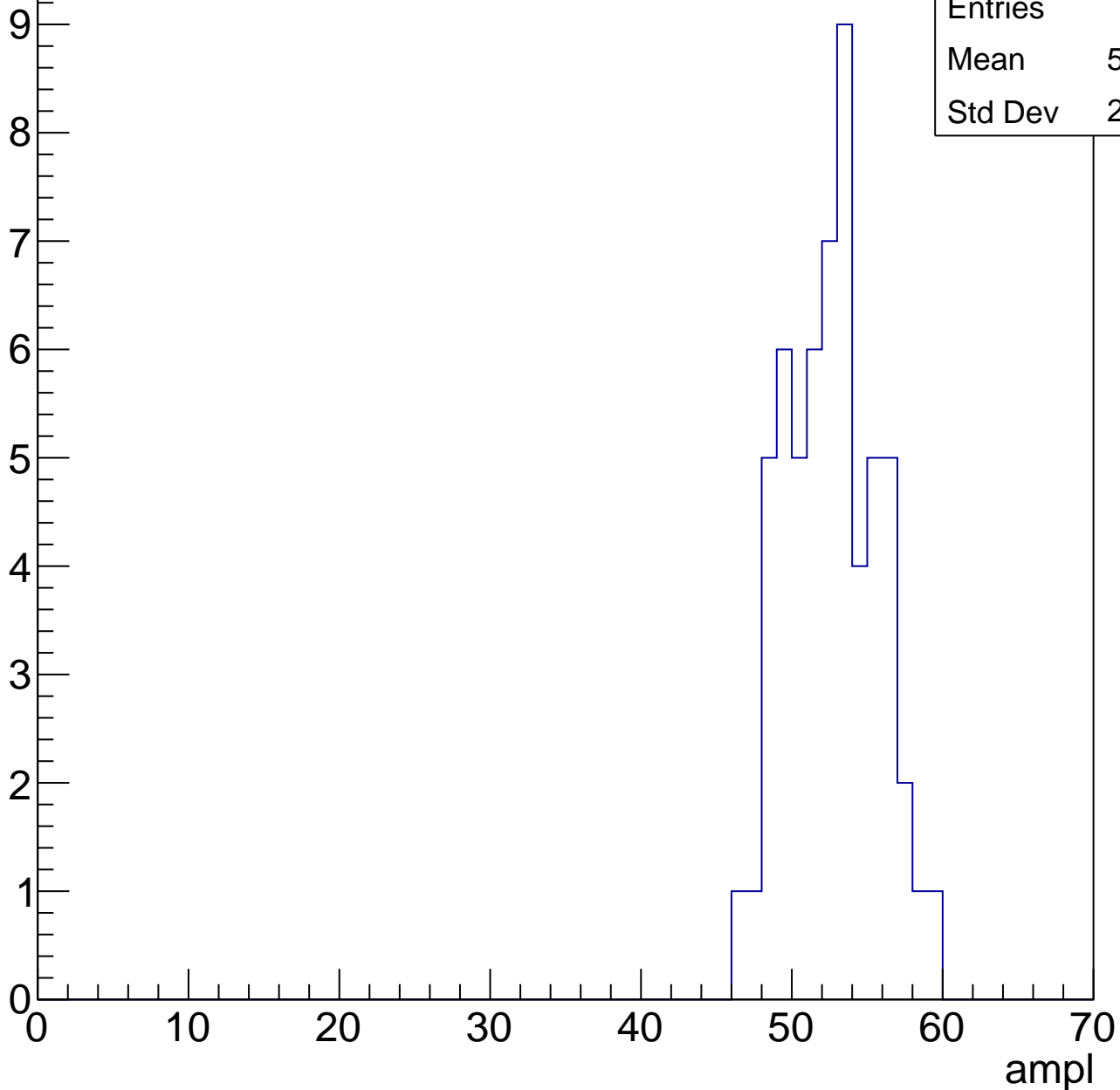


# B1L002S, U18-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.17
Std Dev	2.949

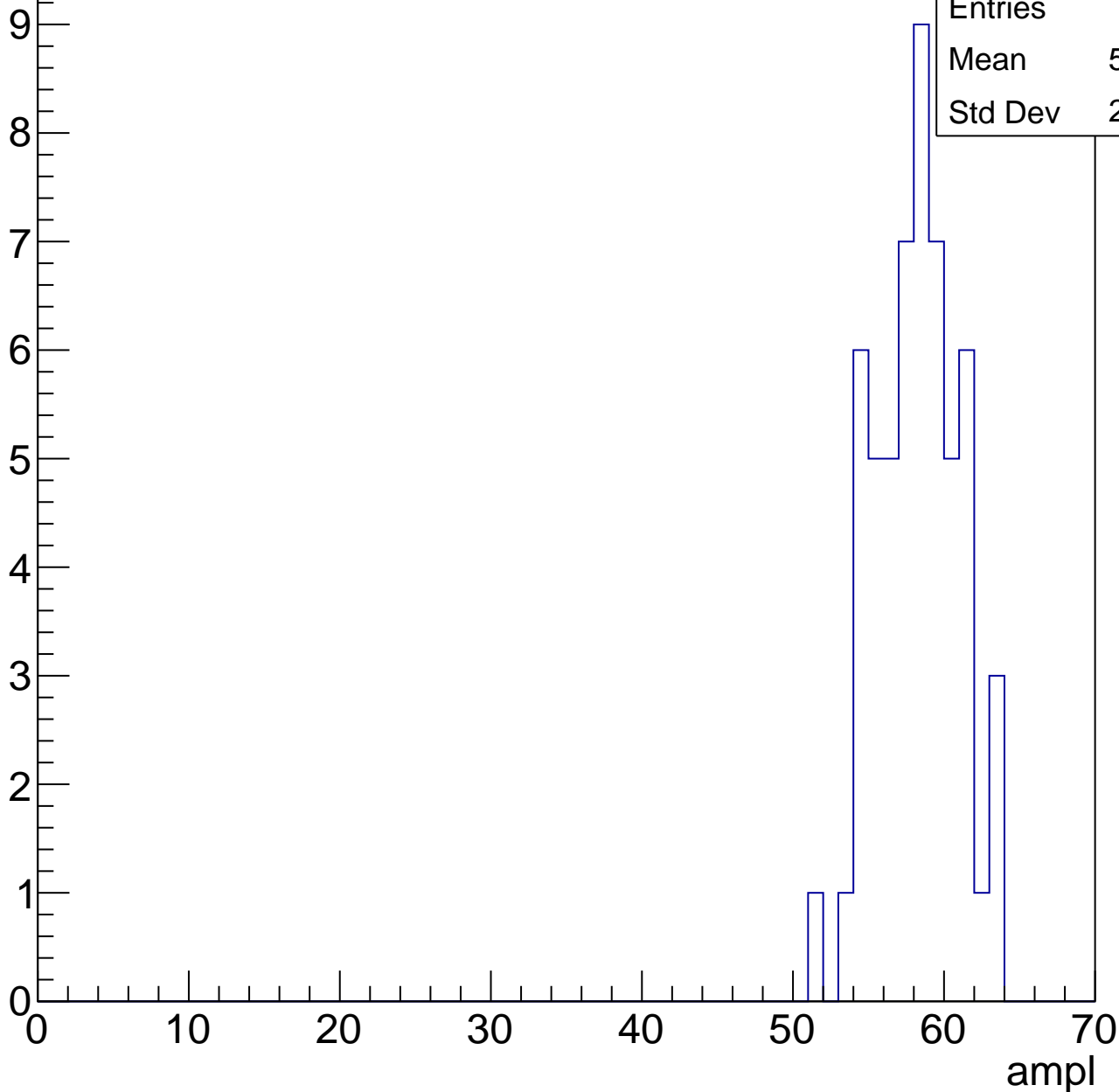


# B1L002S, U18-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.75
Std Dev	2.707

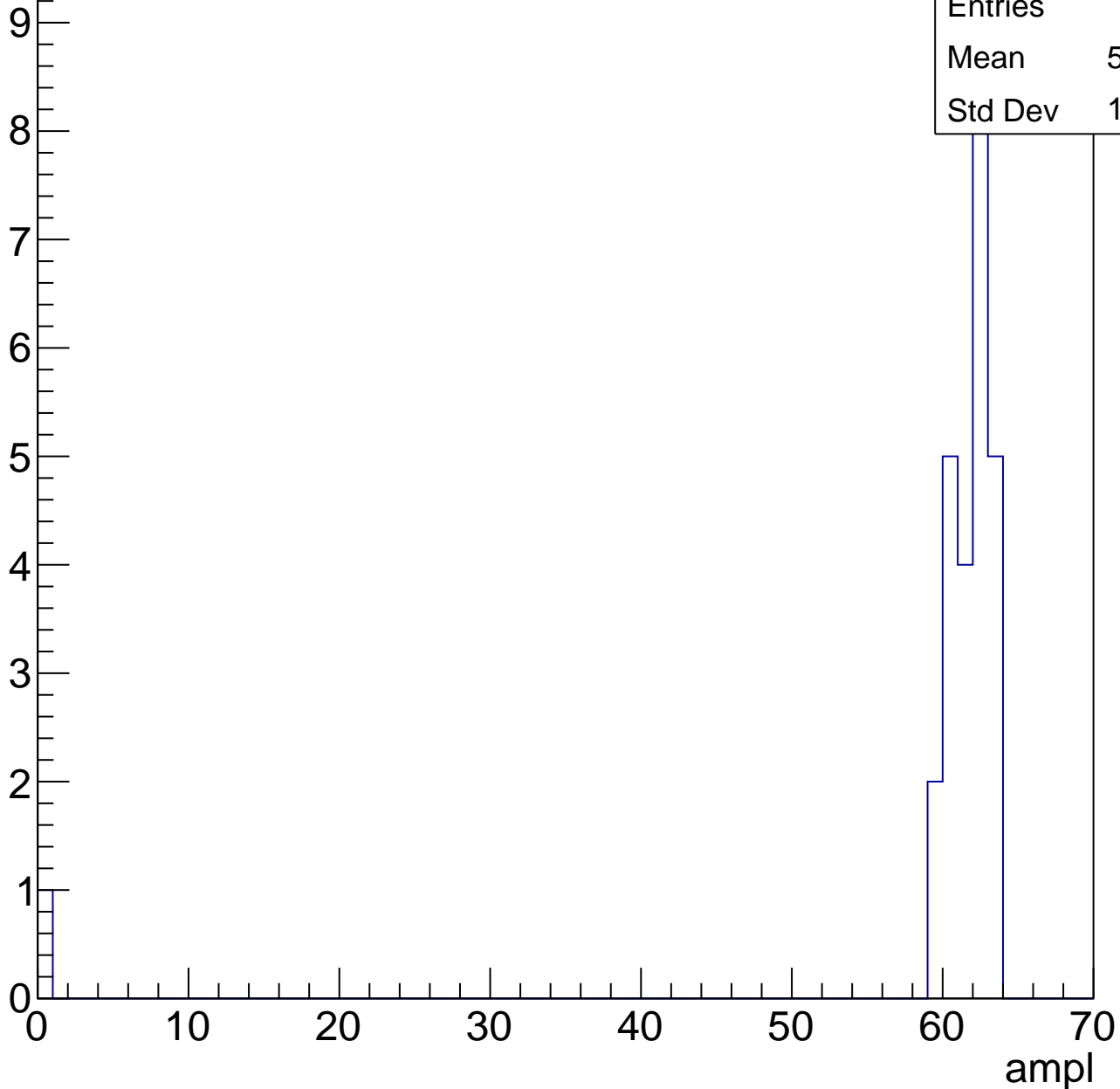


# B1L002S, U18-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59.04
Std Dev	11.87





# B1L002S, U18-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	63
Std Dev	0

# B1L002S, U18-ch93, adc0

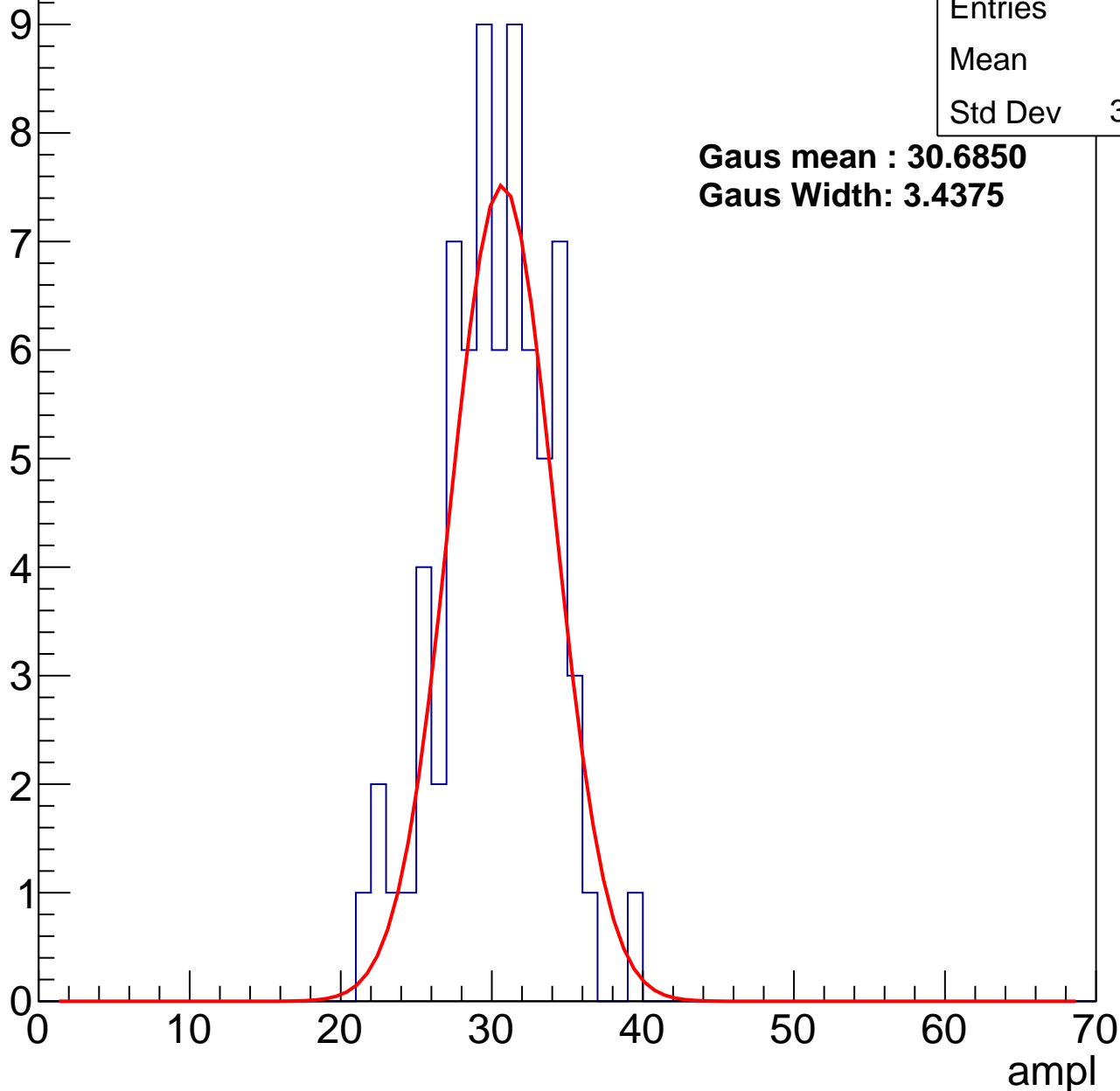
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	29.8
Std Dev	3.563

**Gaus mean : 30.6850**

**Gaus Width: 3.4375**



# B1L002S, U18-ch93, adc1

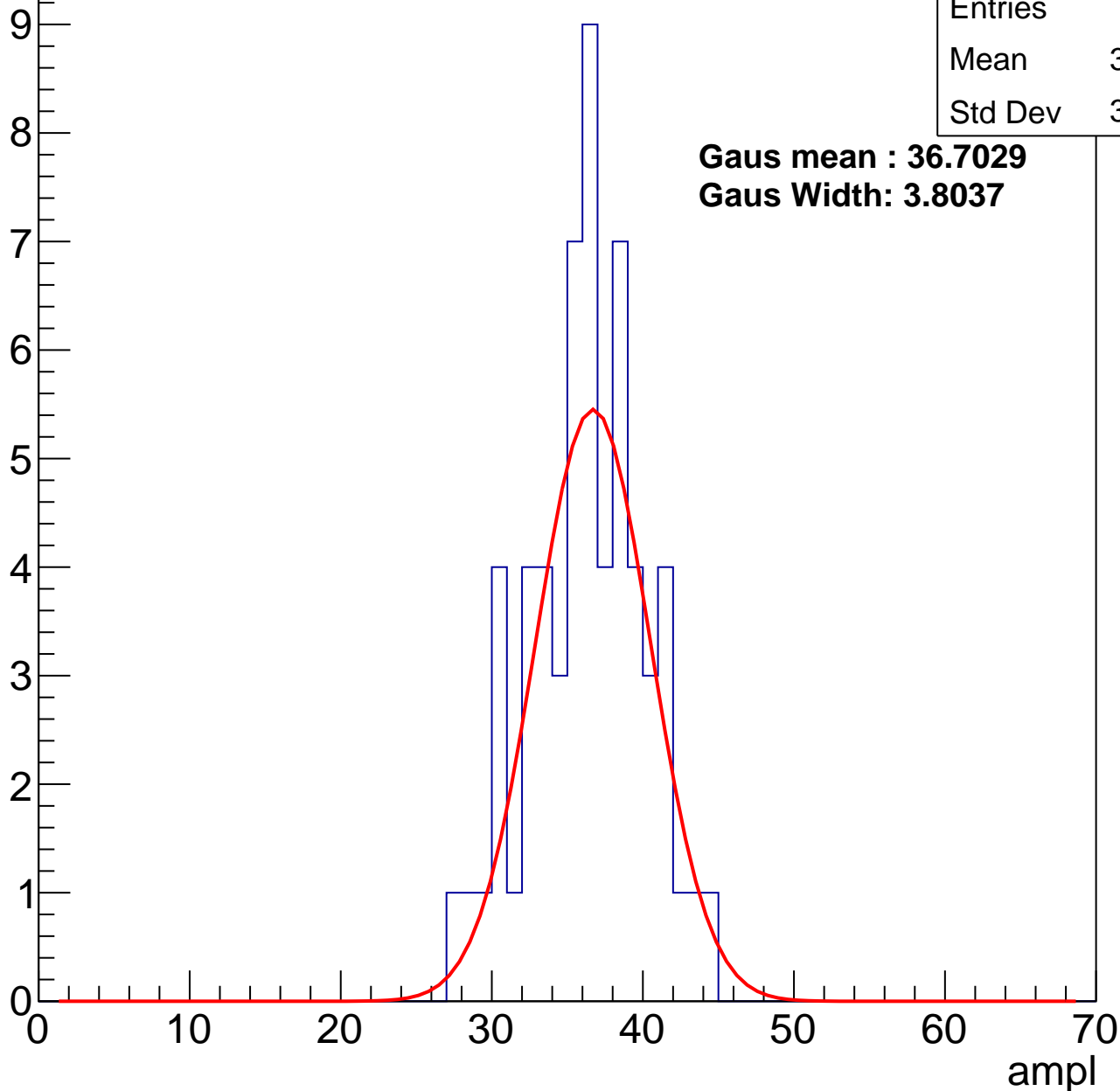
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.82
Std Dev	3.753

**Gaus mean : 36.7029**

**Gaus Width: 3.8037**



# B1L002S, U18-ch93, adc2

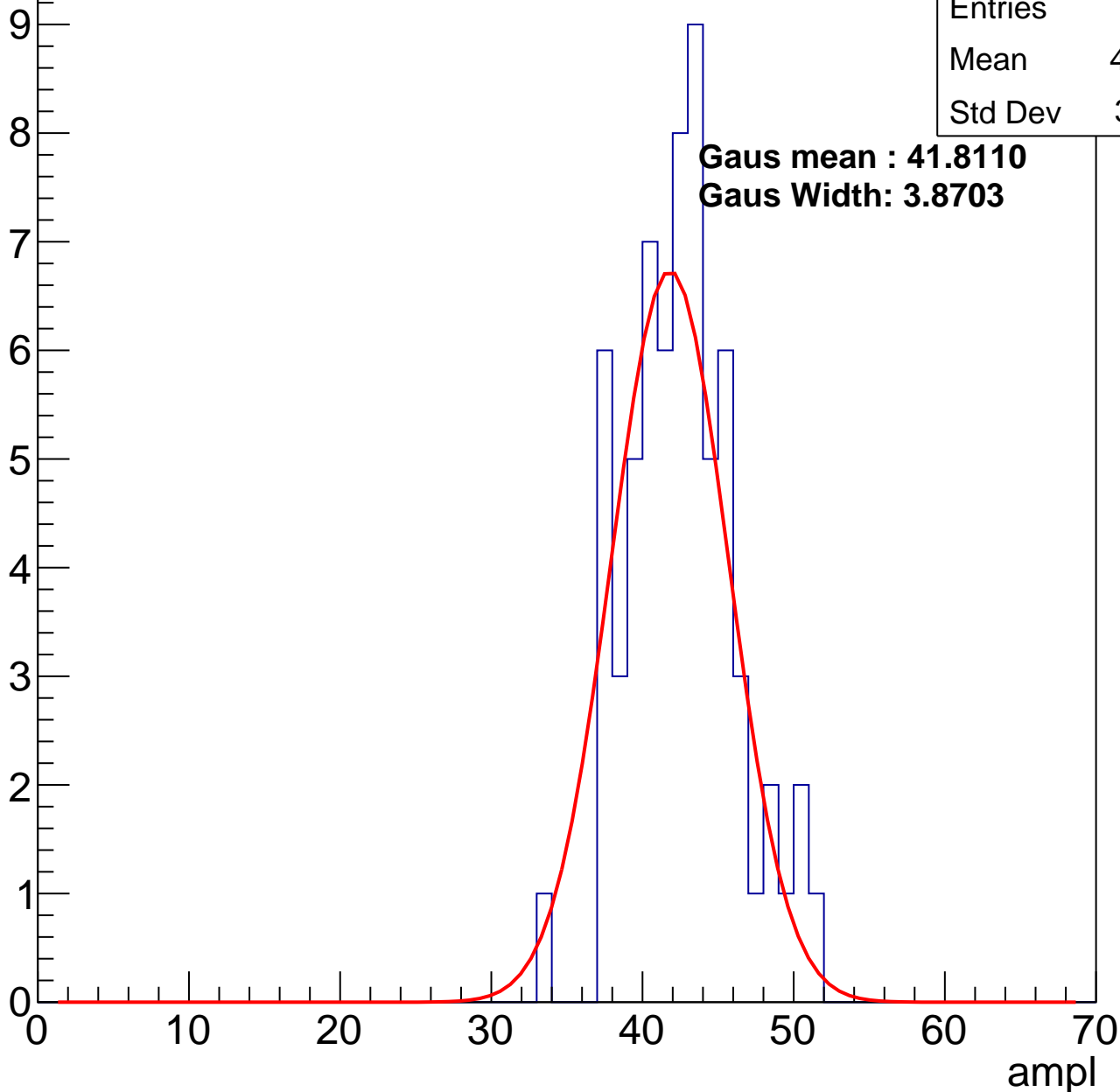
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	42.18
Std Dev	3.571

**Gaus mean : 41.8110**

**Gaus Width: 3.8703**

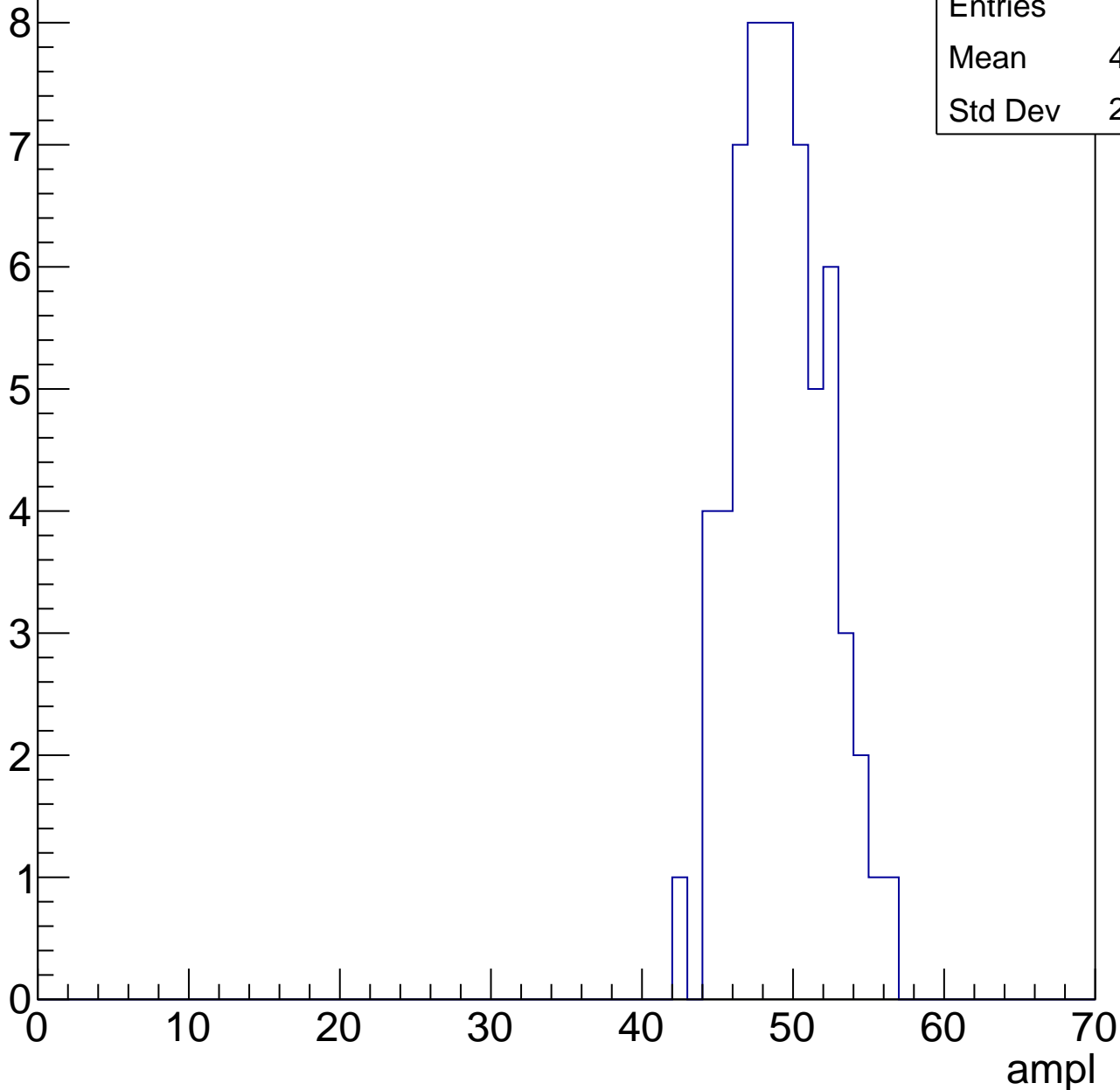


# B1L002S, U18-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

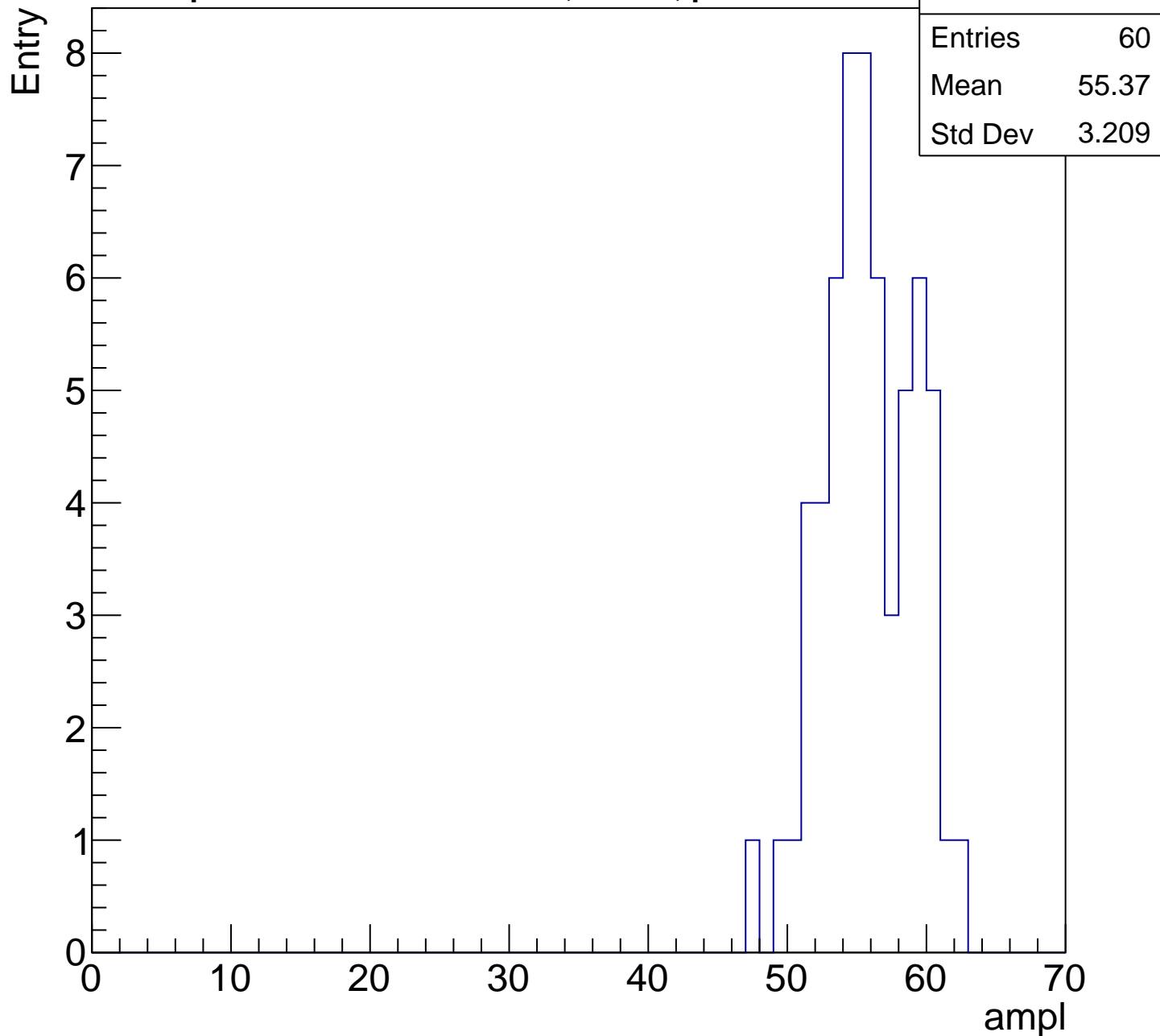
Entry

Entries	65
Mean	48.72
Std Dev	2.974



# B1L002S, U18-ch93, adc4

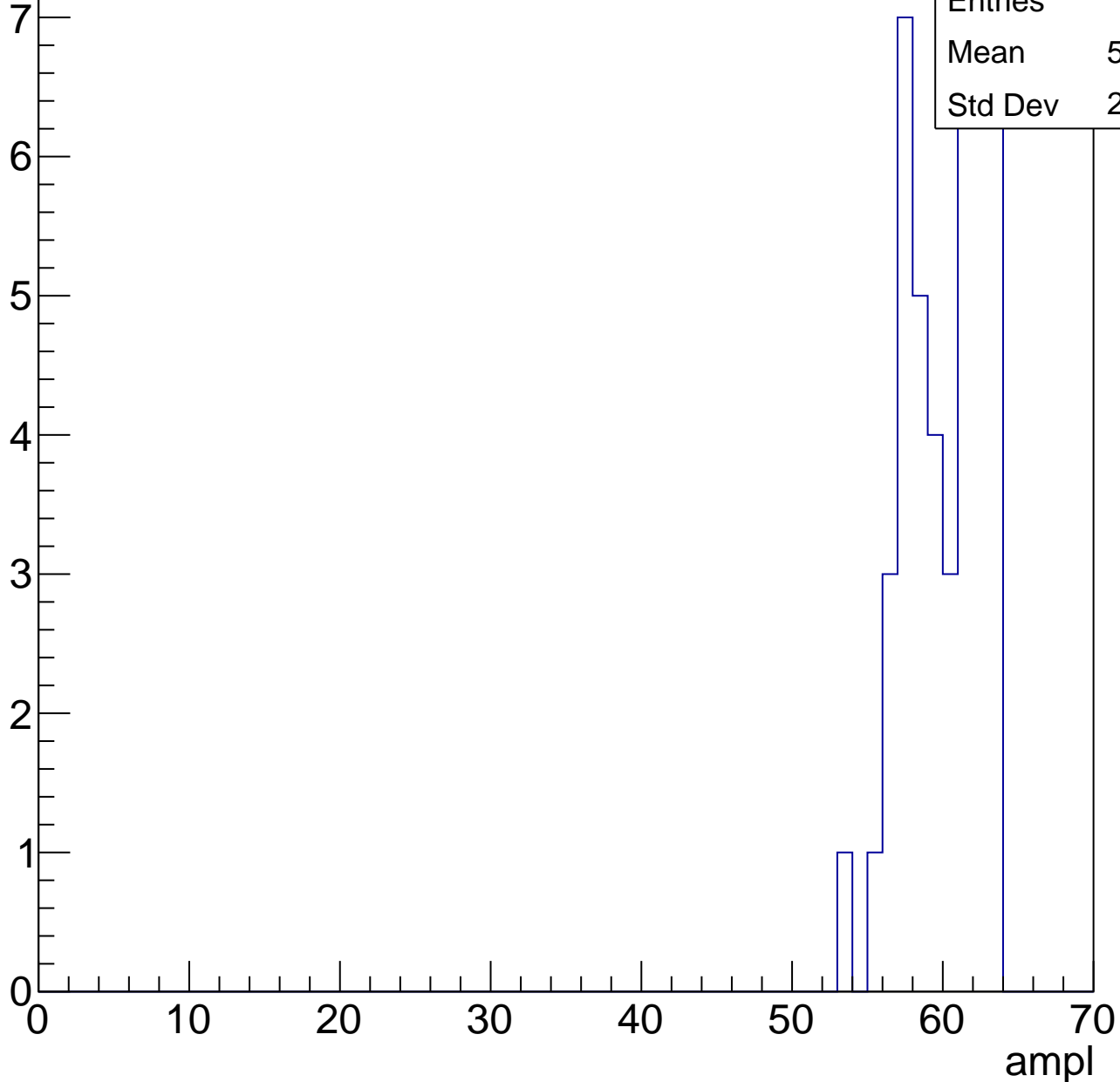
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

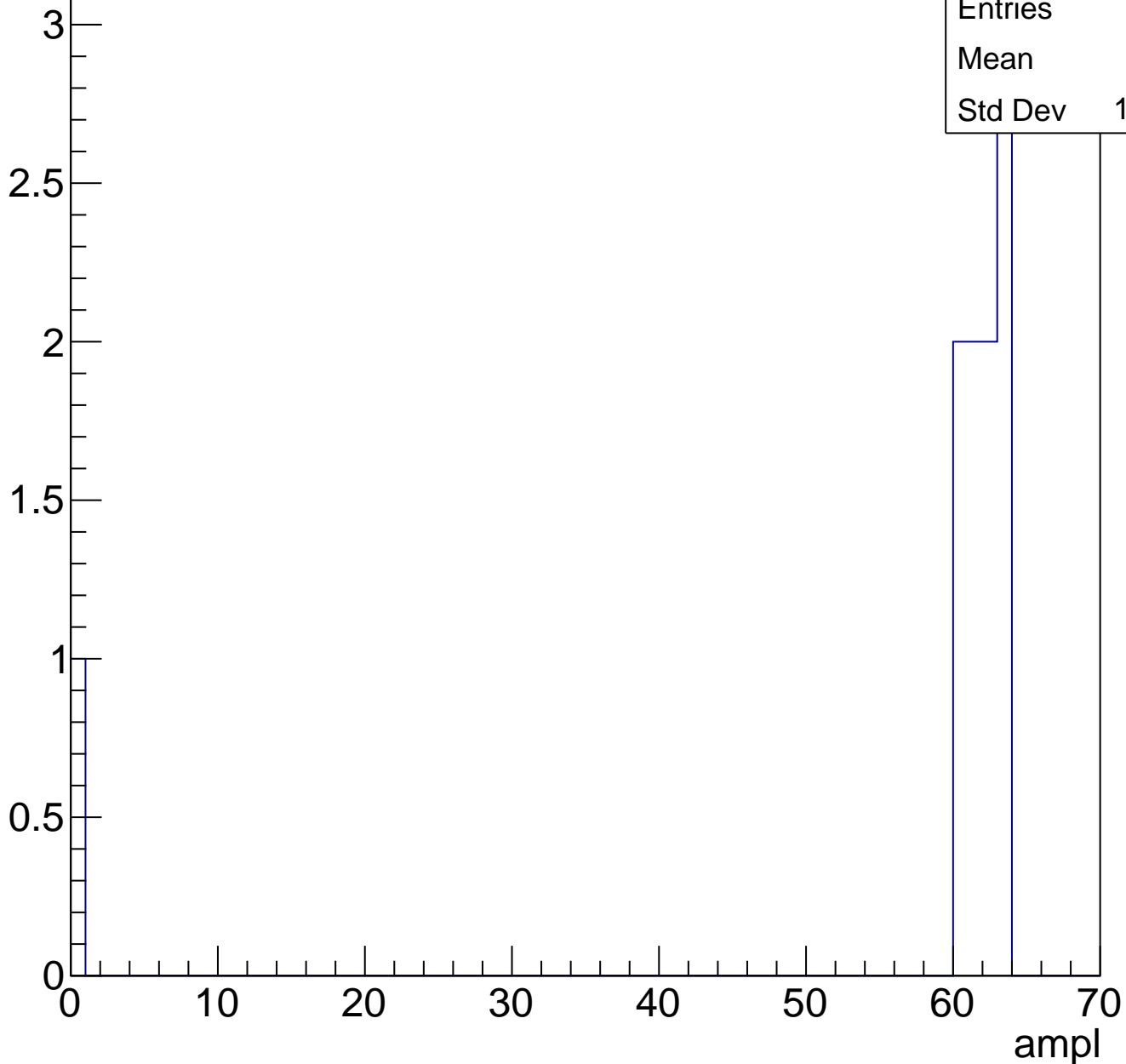


Entries	45
Mean	59.62
Std Dev	2.584

# B1L002S, U18-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U18-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch94, adc0

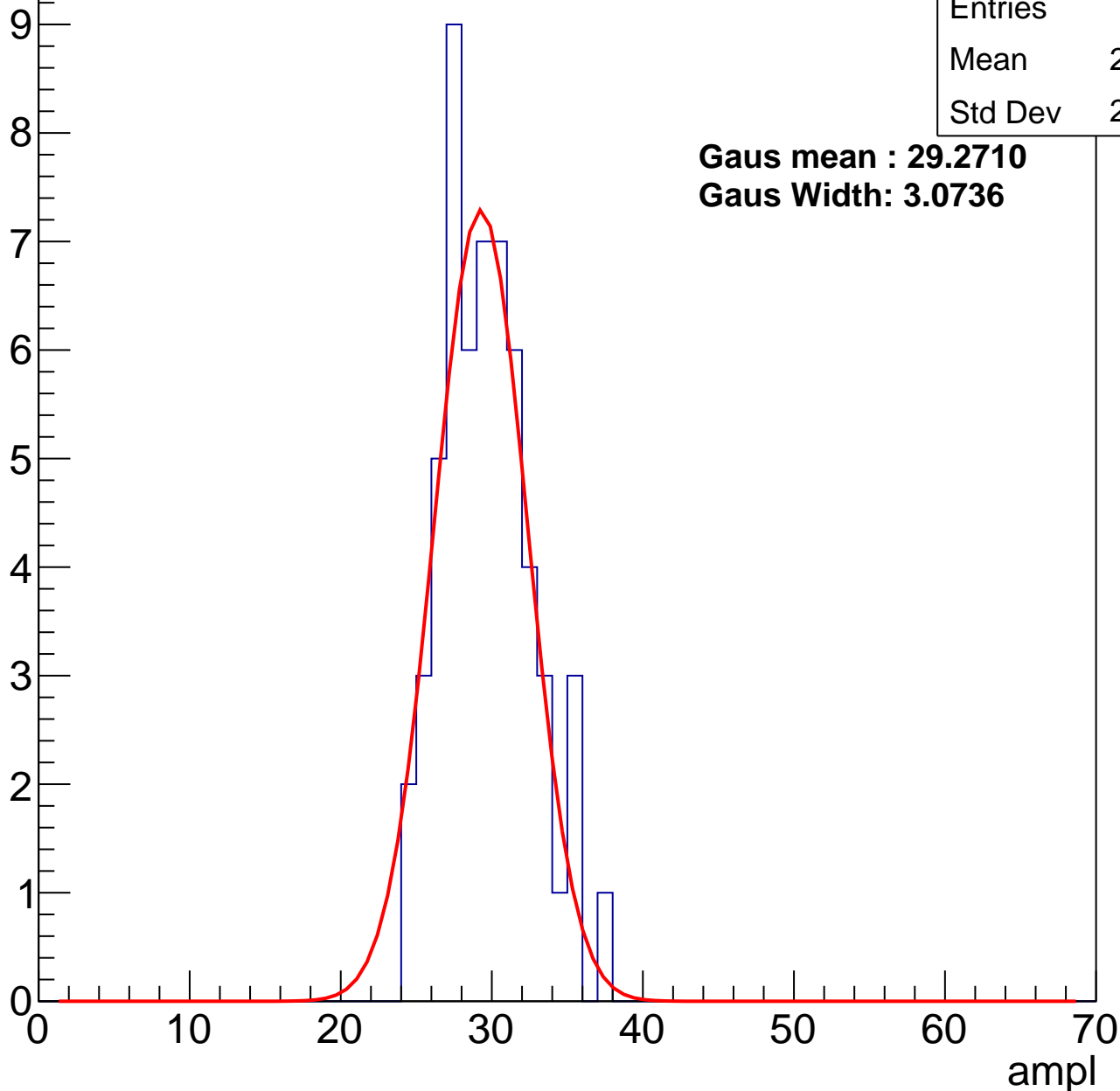
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	29.23
Std Dev	2.944

**Gaus mean : 29.2710**

**Gaus Width: 3.0736**



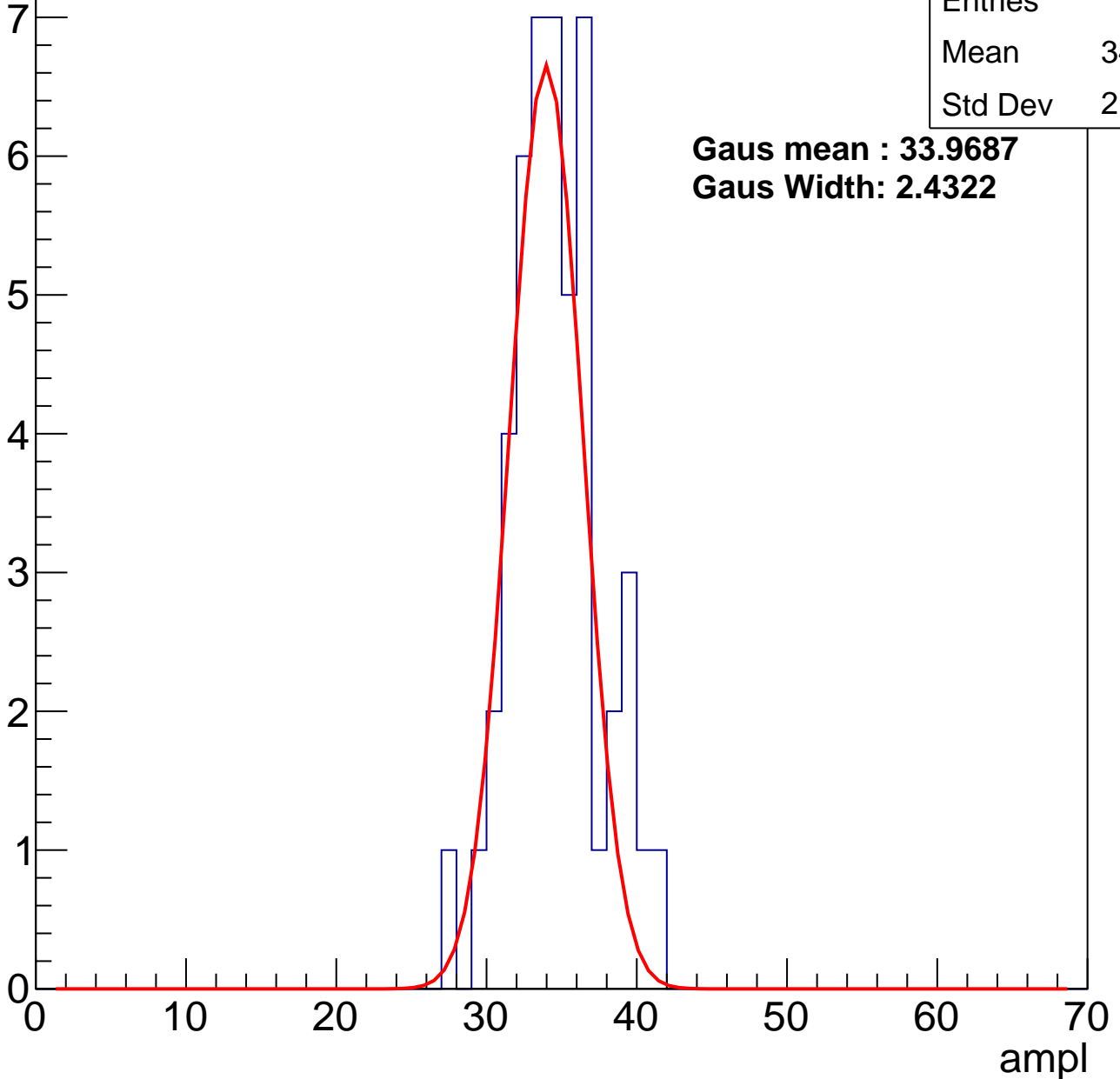
# B1L002S, U18-ch94, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	34.15
Std Dev	2.908

**Gaus mean : 33.9687**  
**Gaus Width: 2.4322**



# B1L002S, U18-ch94, adc2

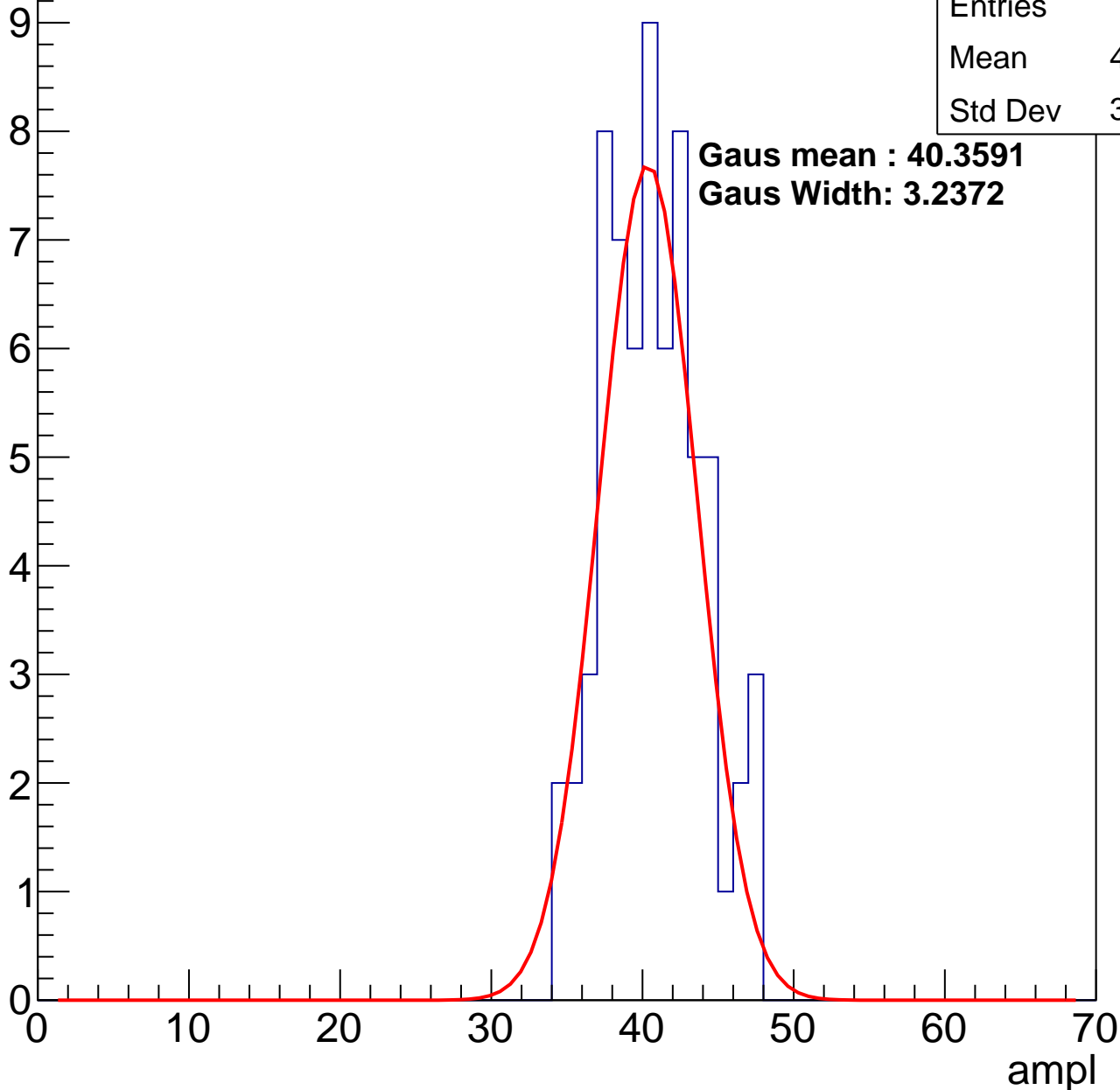
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	40.25
Std Dev	3.183

**Gaus mean : 40.3591**

**Gaus Width: 3.2372**

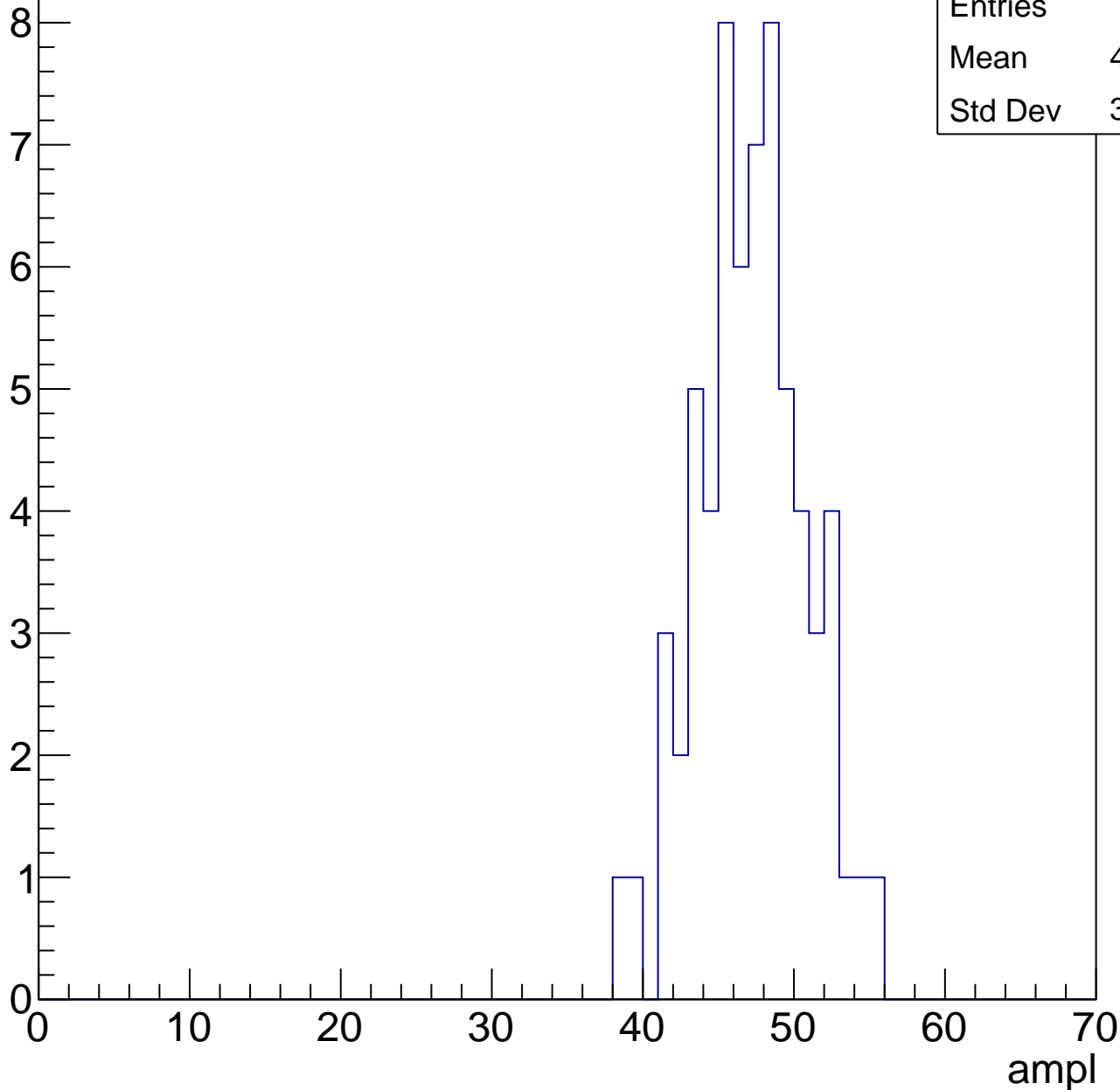


# B1L002S, U18-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.75
Std Dev	3.566

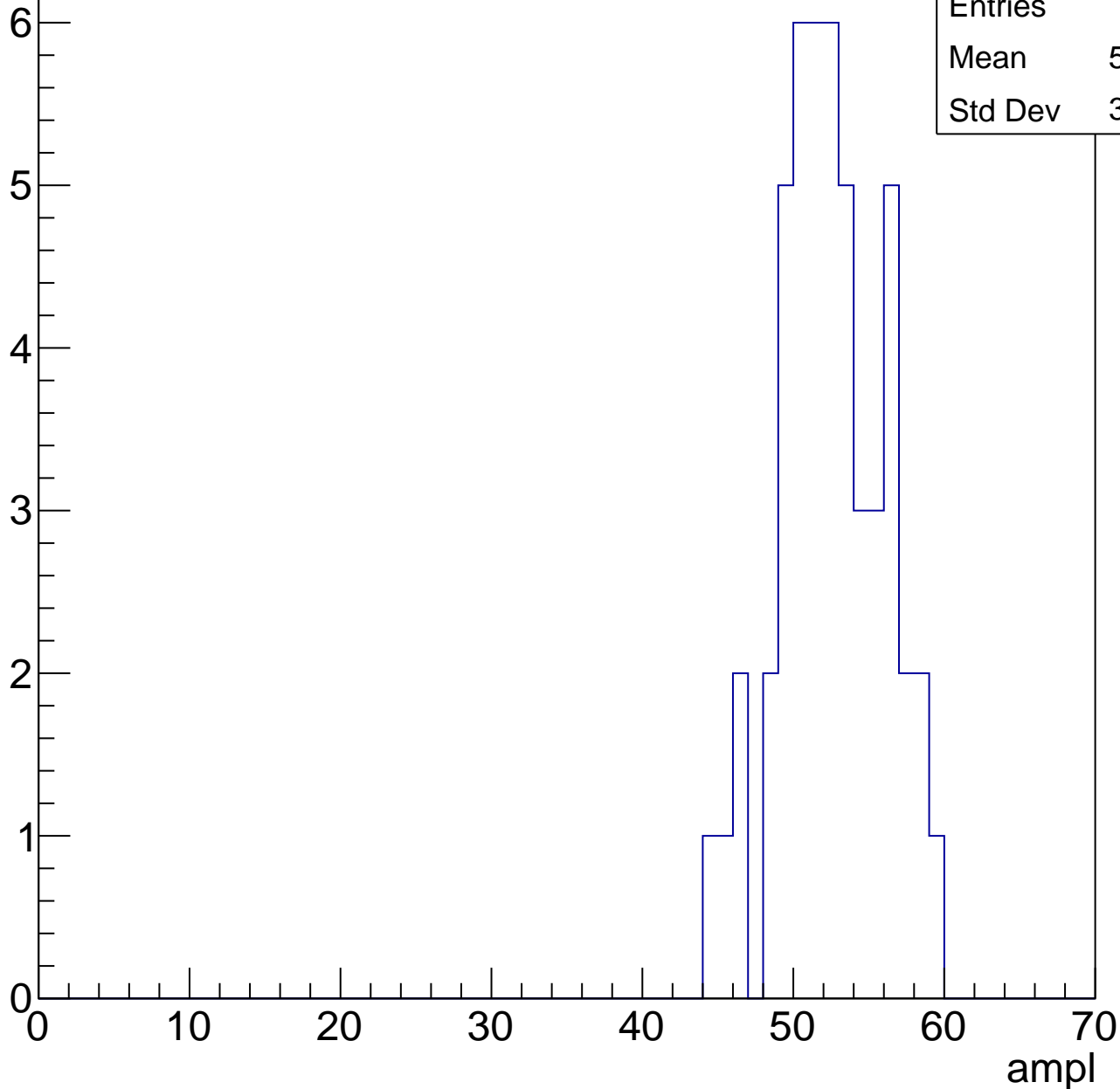


# B1L002S, U18-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	52.02
Std Dev	3.426

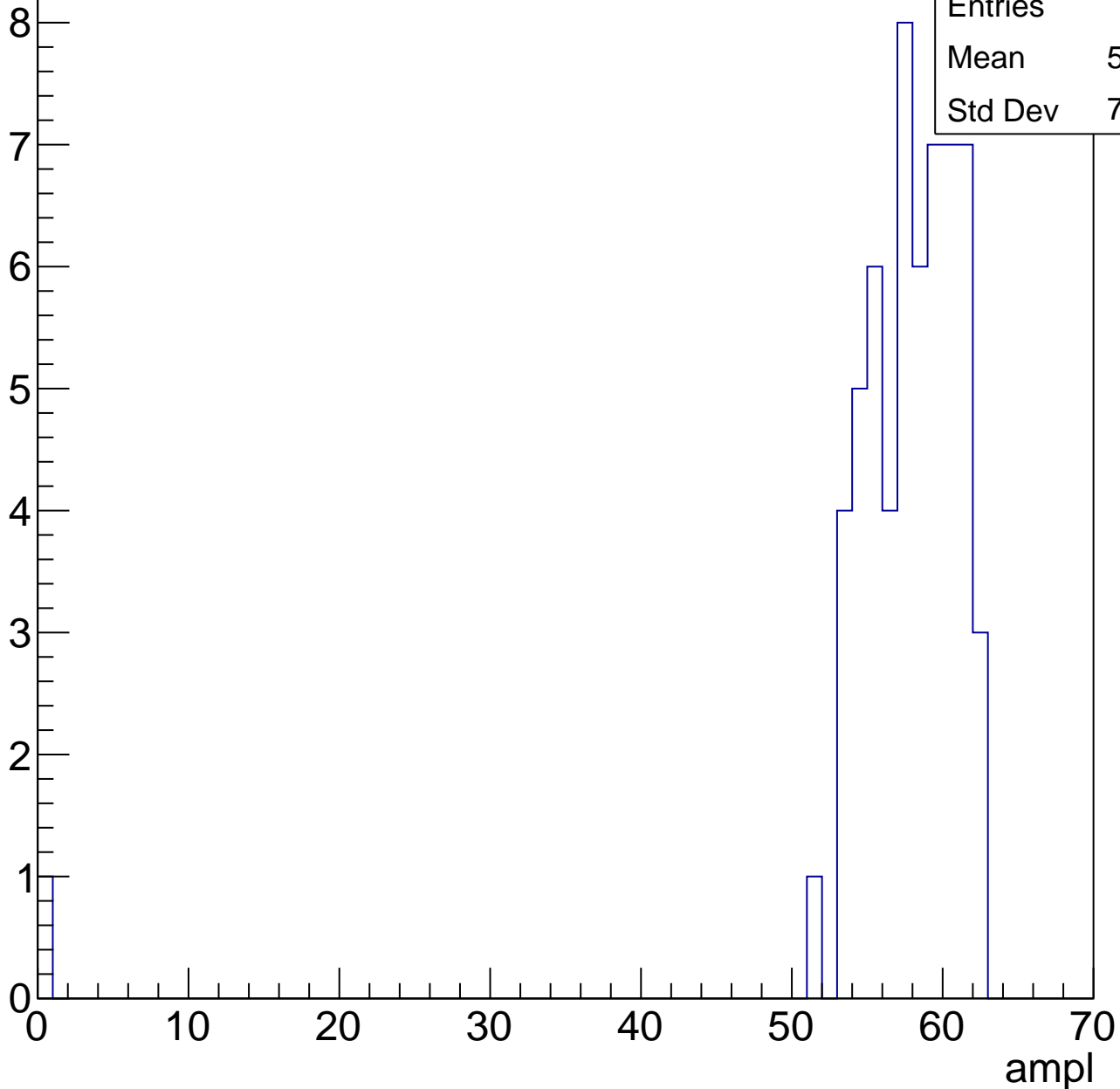


# B1L002S, U18-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	56.56
Std Dev	7.913

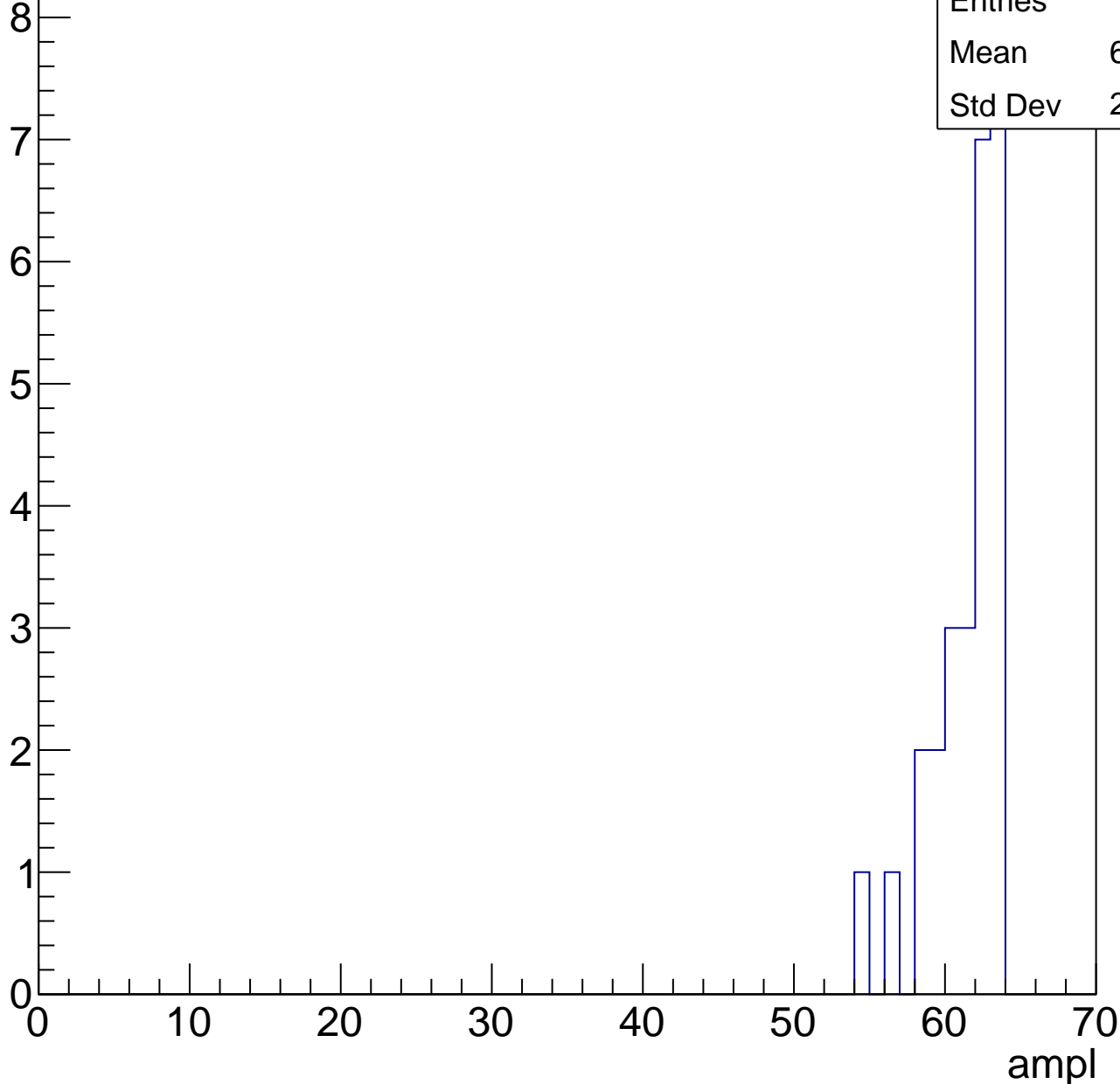


# B1L002S, U18-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.93
Std Dev	2.292

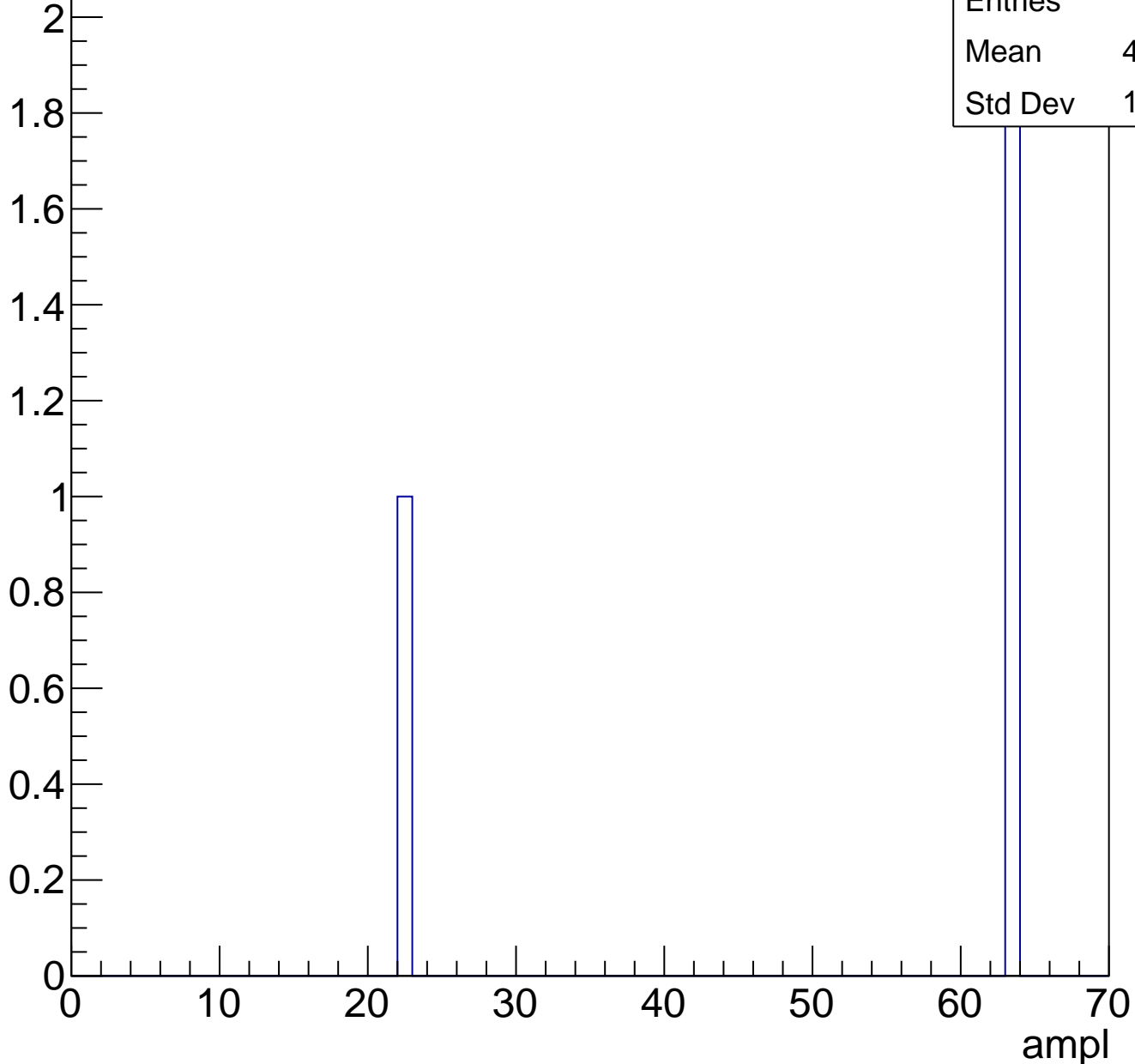




# B1L002S, U18-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	49.33
Std Dev	19.33

# B1L002S, U18-ch95, adc0

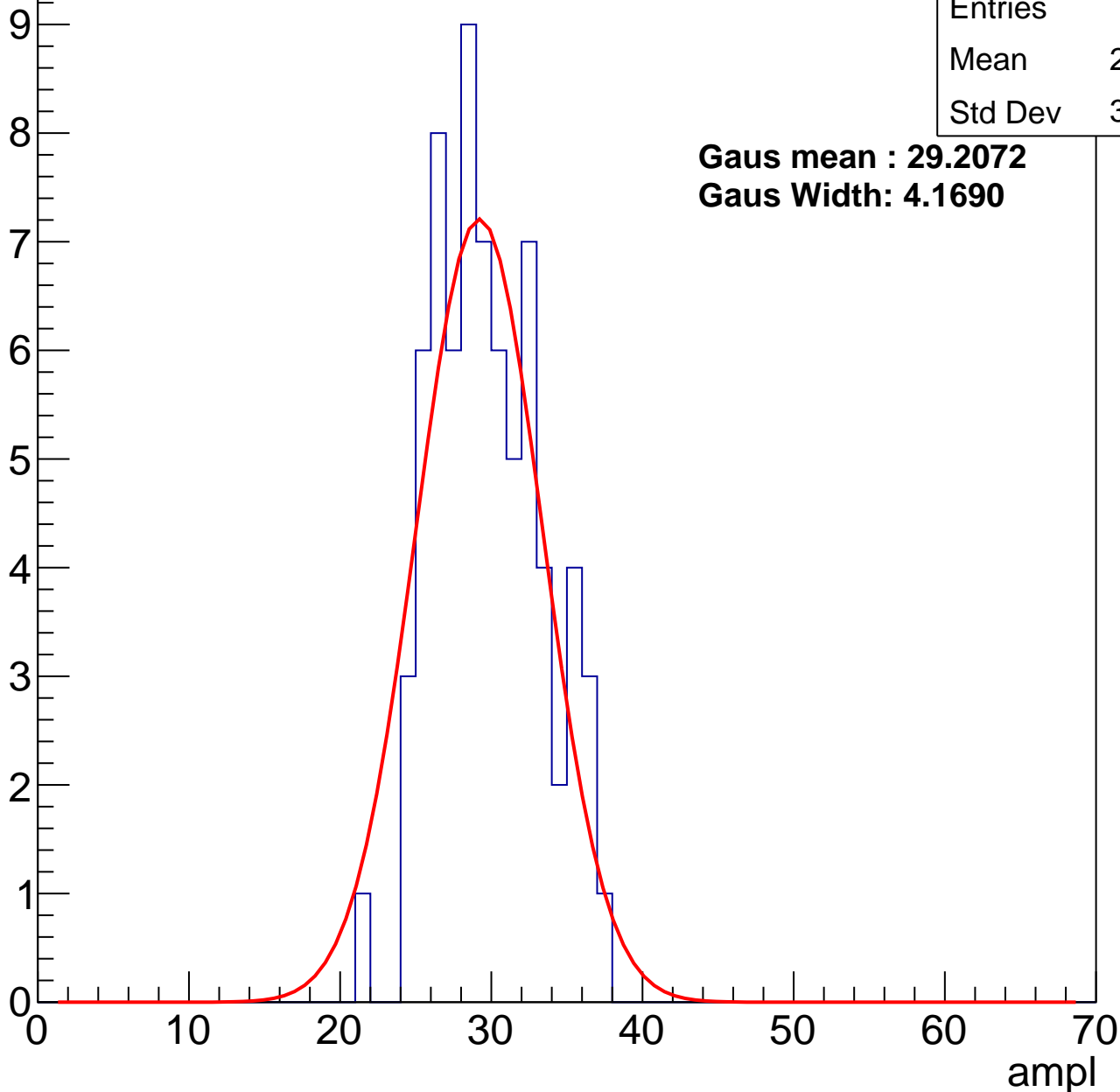
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	29.33
Std Dev	3.516

**Gaus mean : 29.2072**

**Gaus Width: 4.1690**



# B1L002S, U18-ch95, adc1

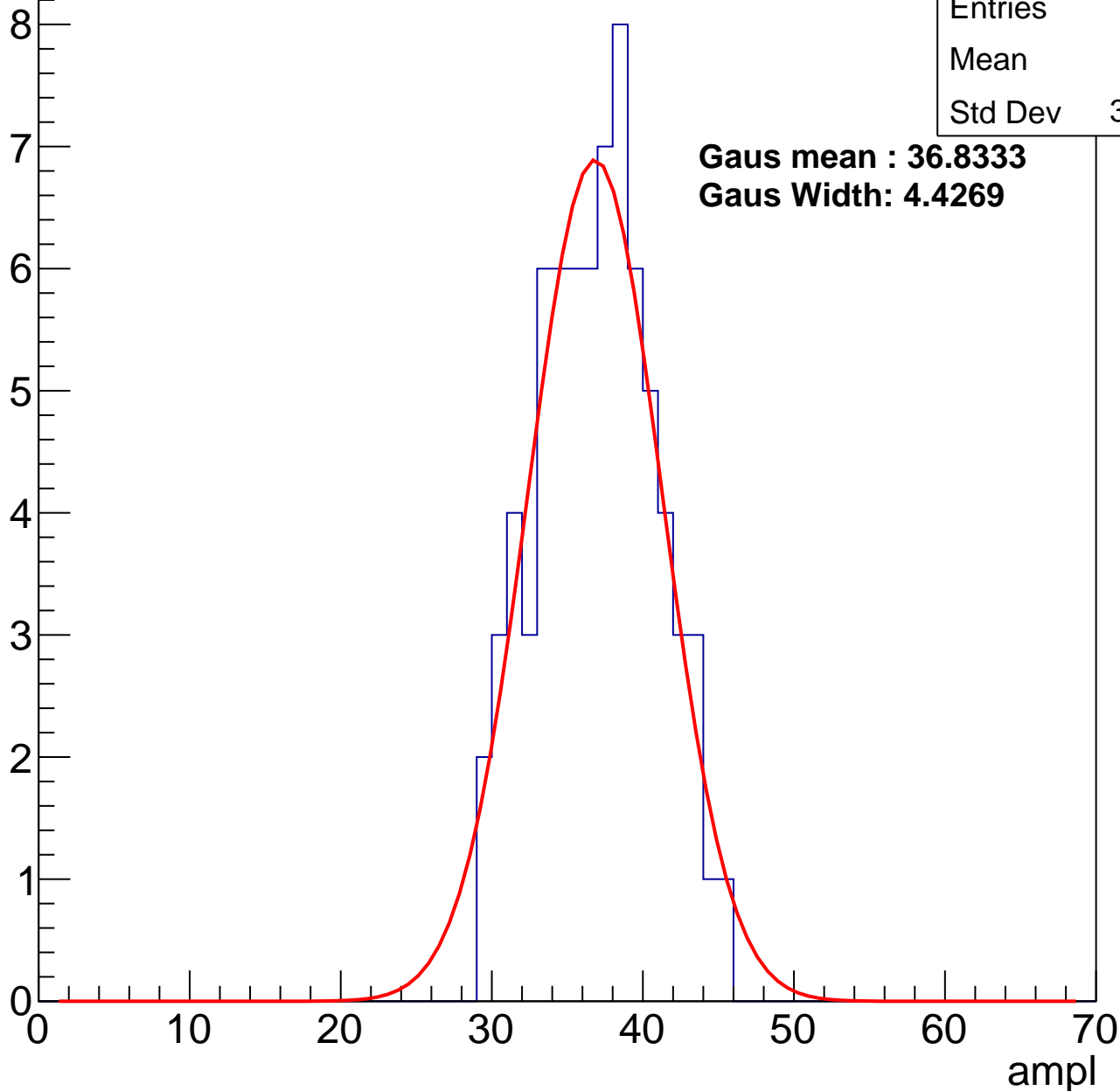
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	36.5
Std Dev	3.842

**Gaus mean : 36.8333**

**Gaus Width: 4.4269**



# B1L002S, U18-ch95, adc2

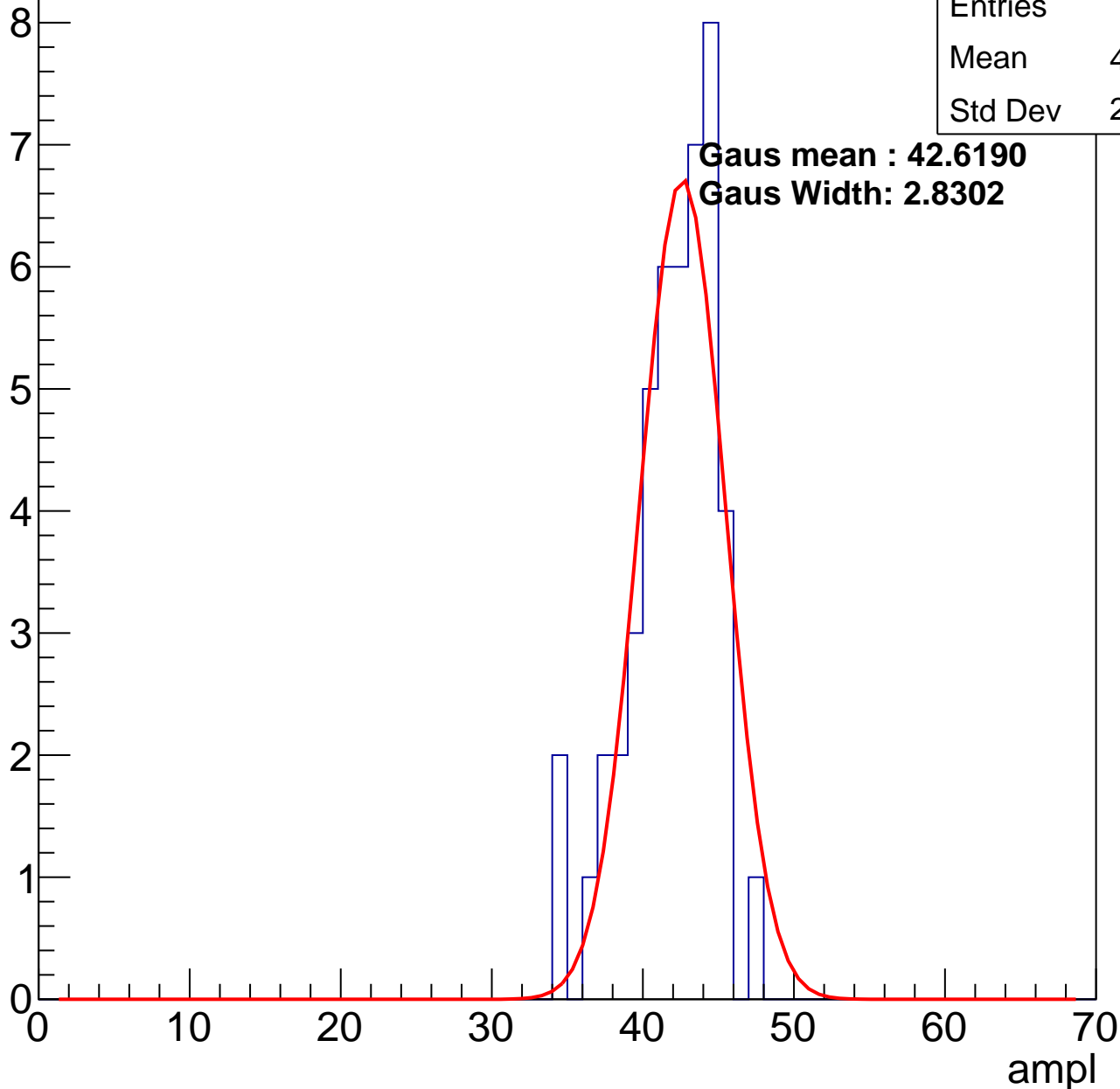
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	41.47
Std Dev	2.865

**Gaus mean : 42.6190**

**Gaus Width: 2.8302**

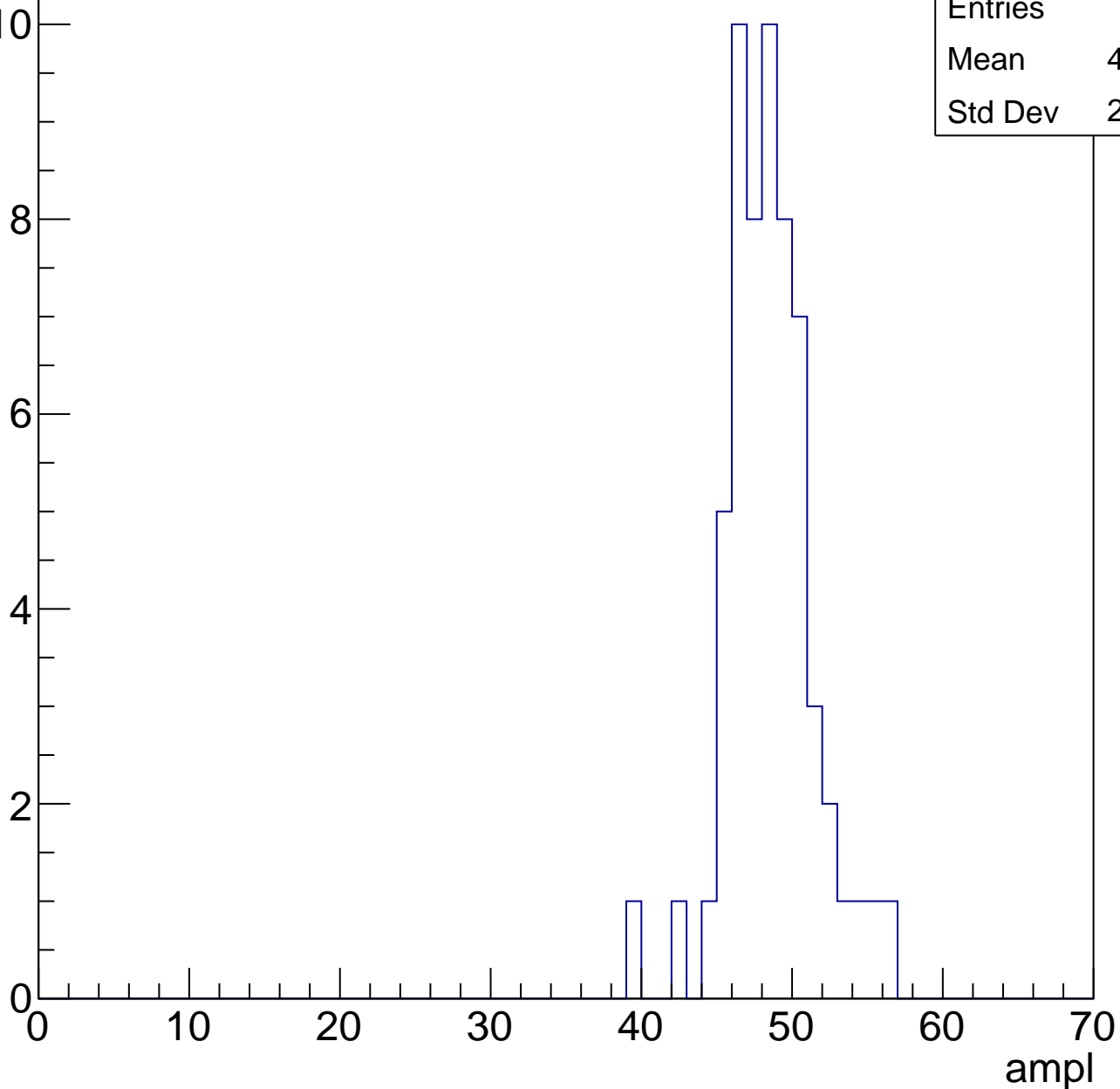


# B1L002S, U18-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.05
Std Dev	2.872

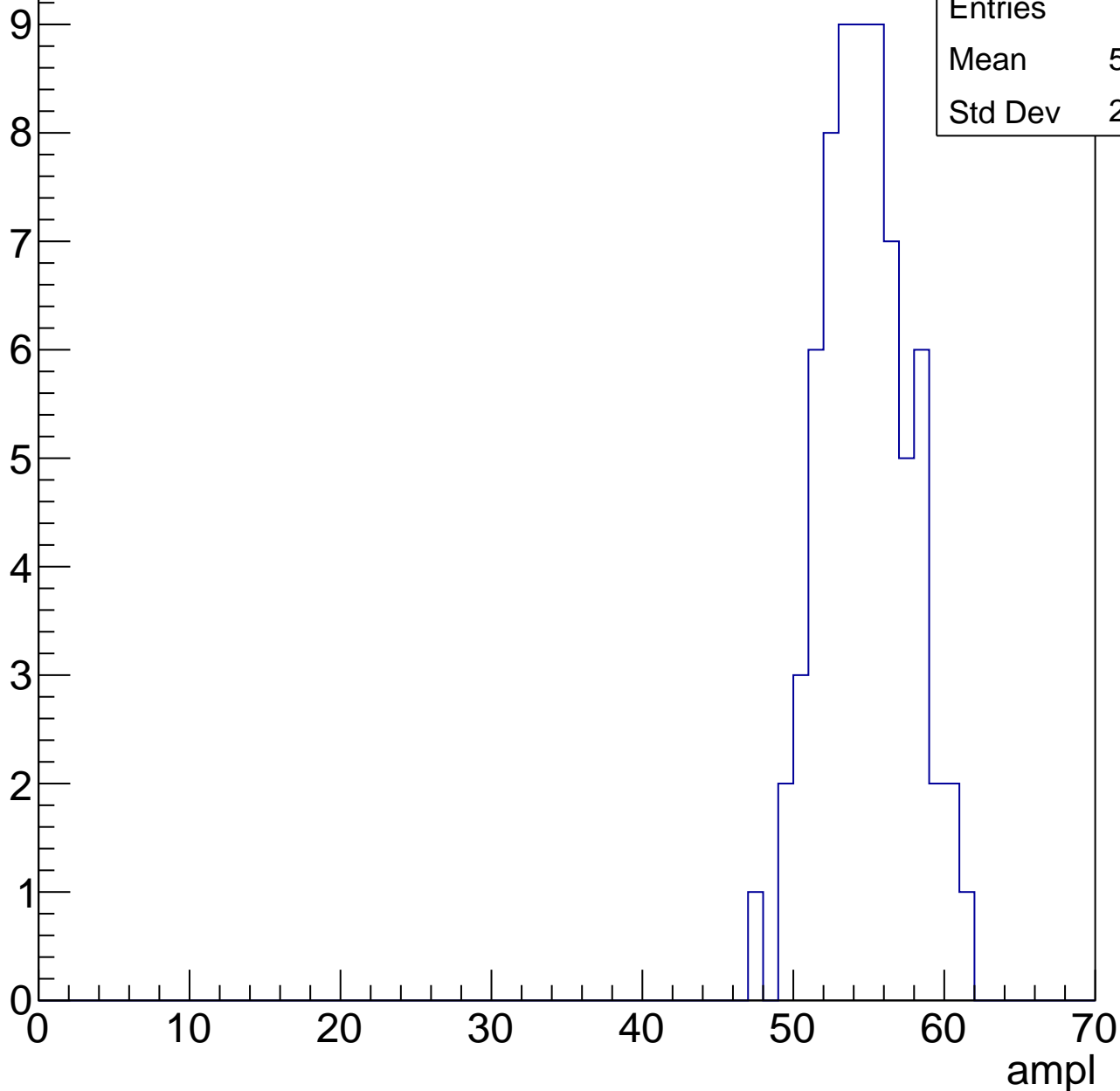


# B1L002S, U18-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	54.27
Std Dev	2.893

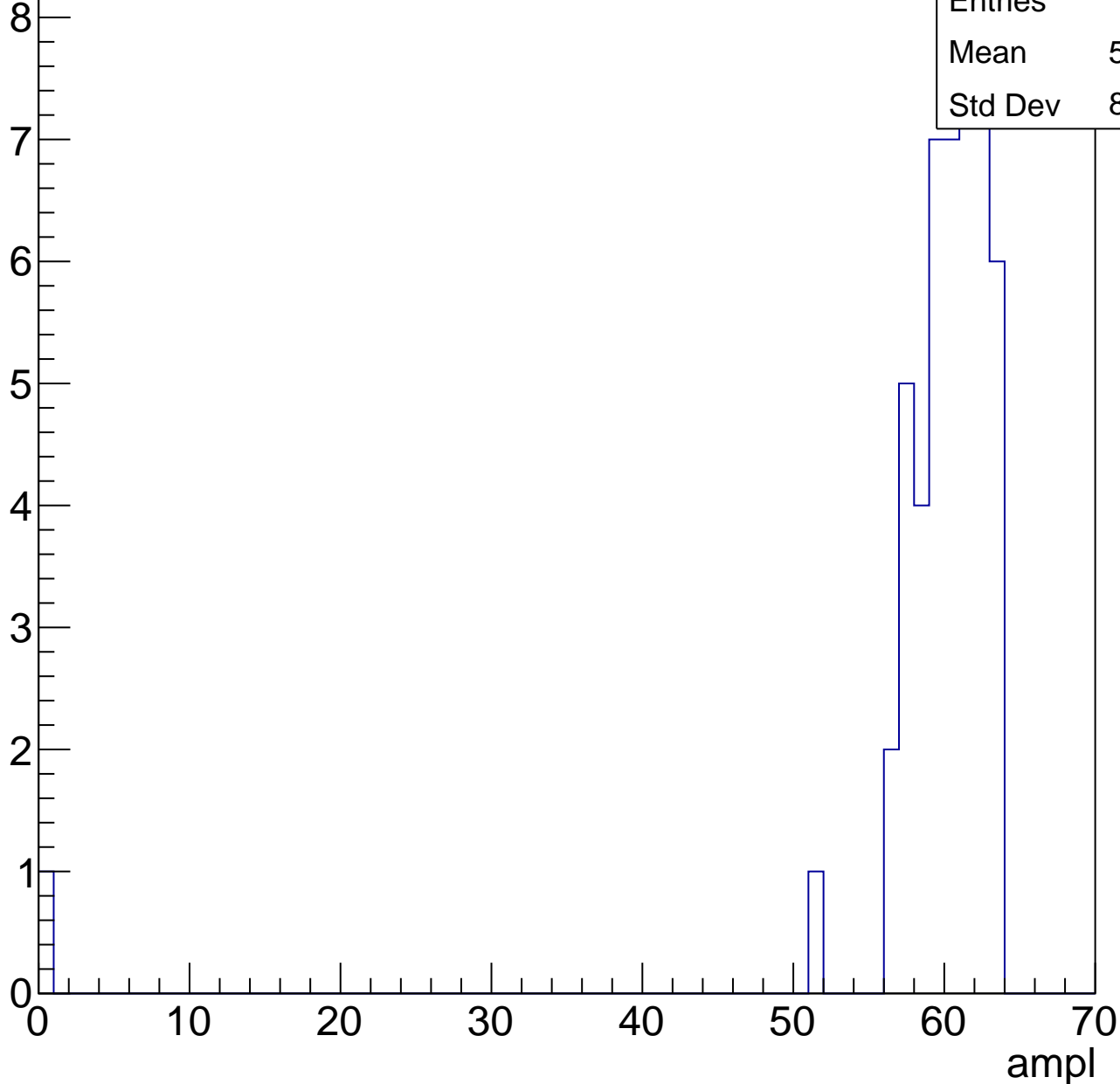


# B1L002S, U18-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

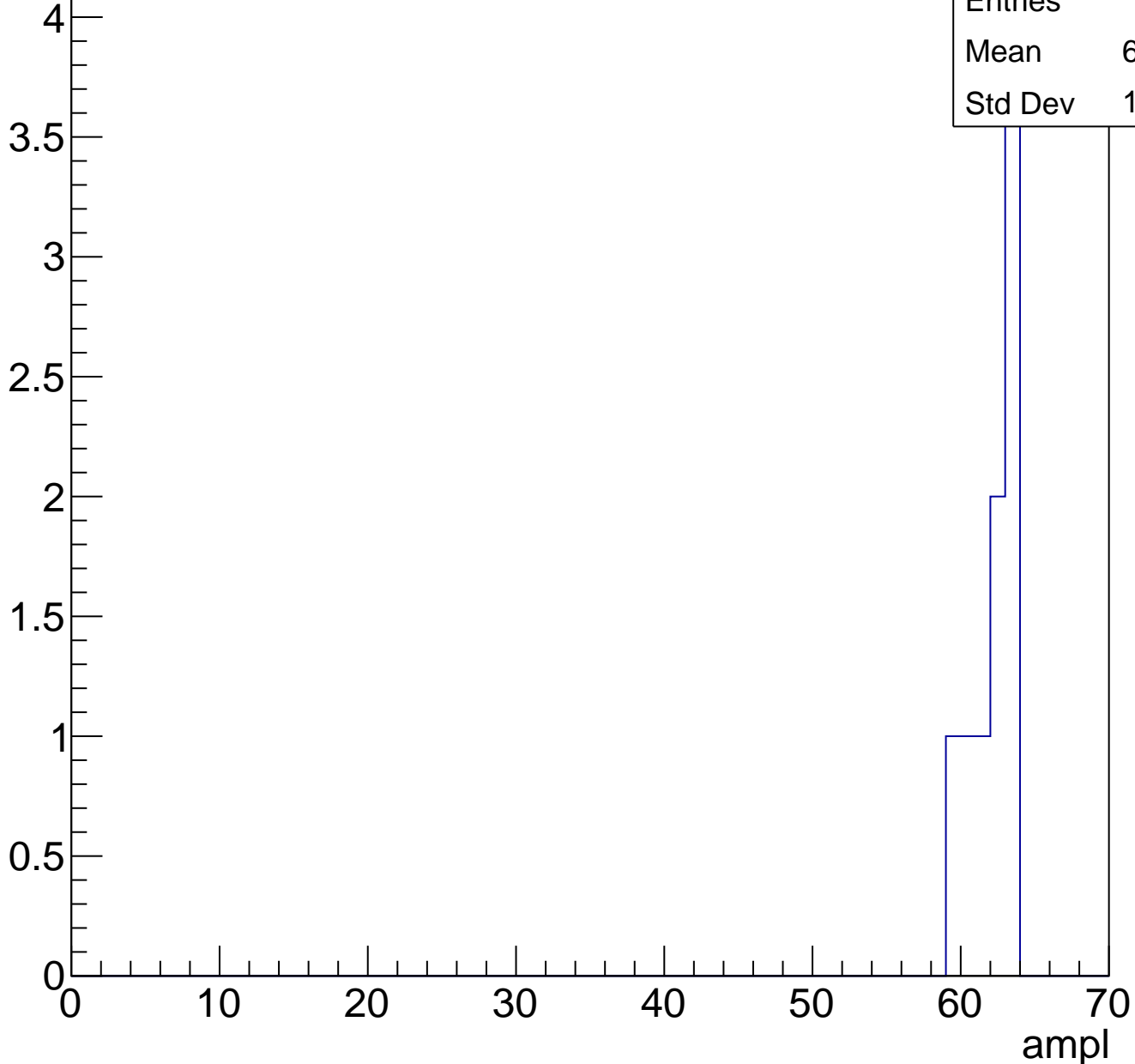
Entries	49
Mean	58.67
Std Dev	8.793



# B1L002S, U18-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	9
Mean	61.78
Std Dev	1.397



# B1L002S, U18-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch96, adc0

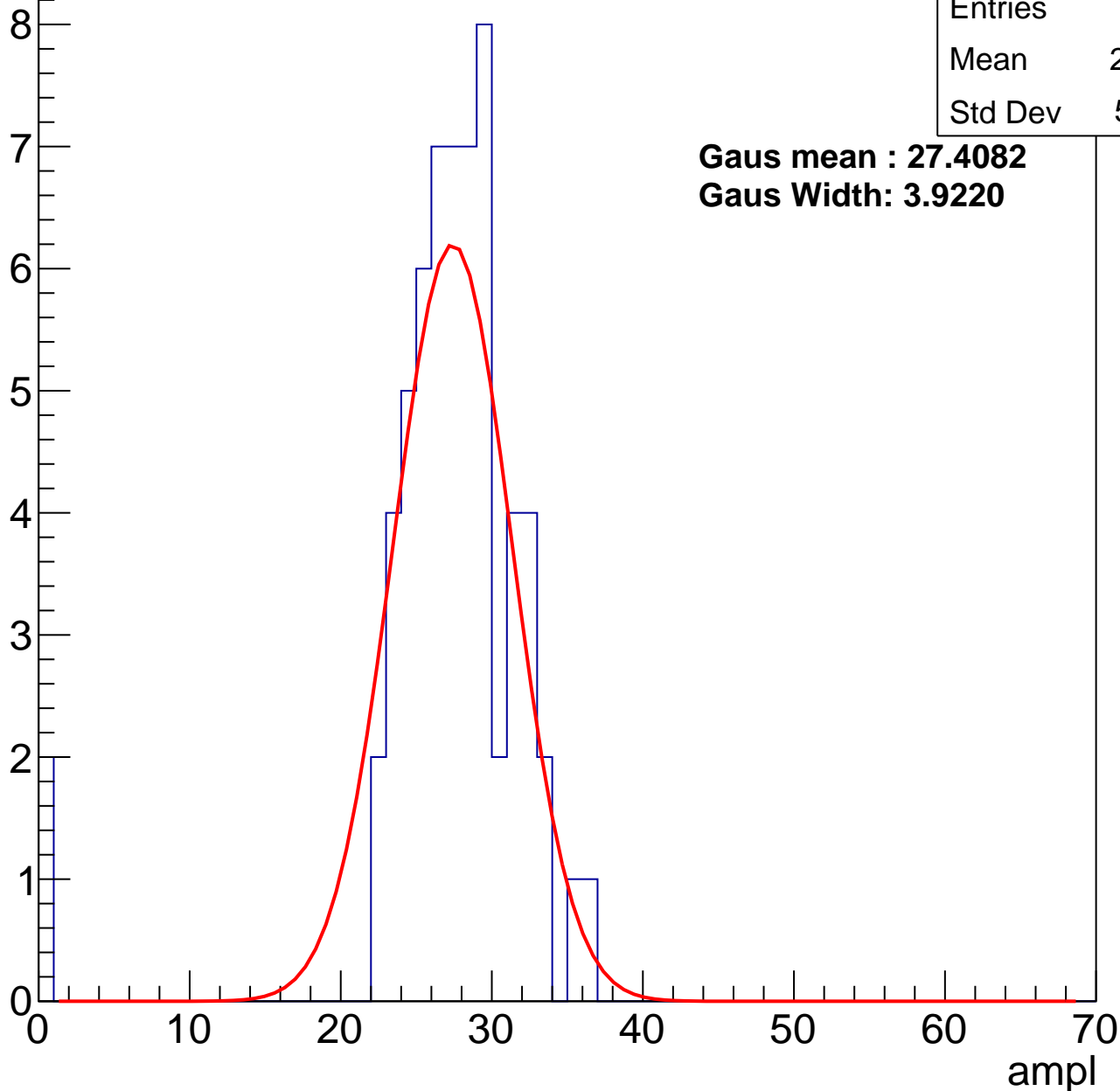
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26.68
Std Dev	5.791

**Gaus mean : 27.4082**

**Gaus Width: 3.9220**



# B1L002S, U18-ch96, adc1

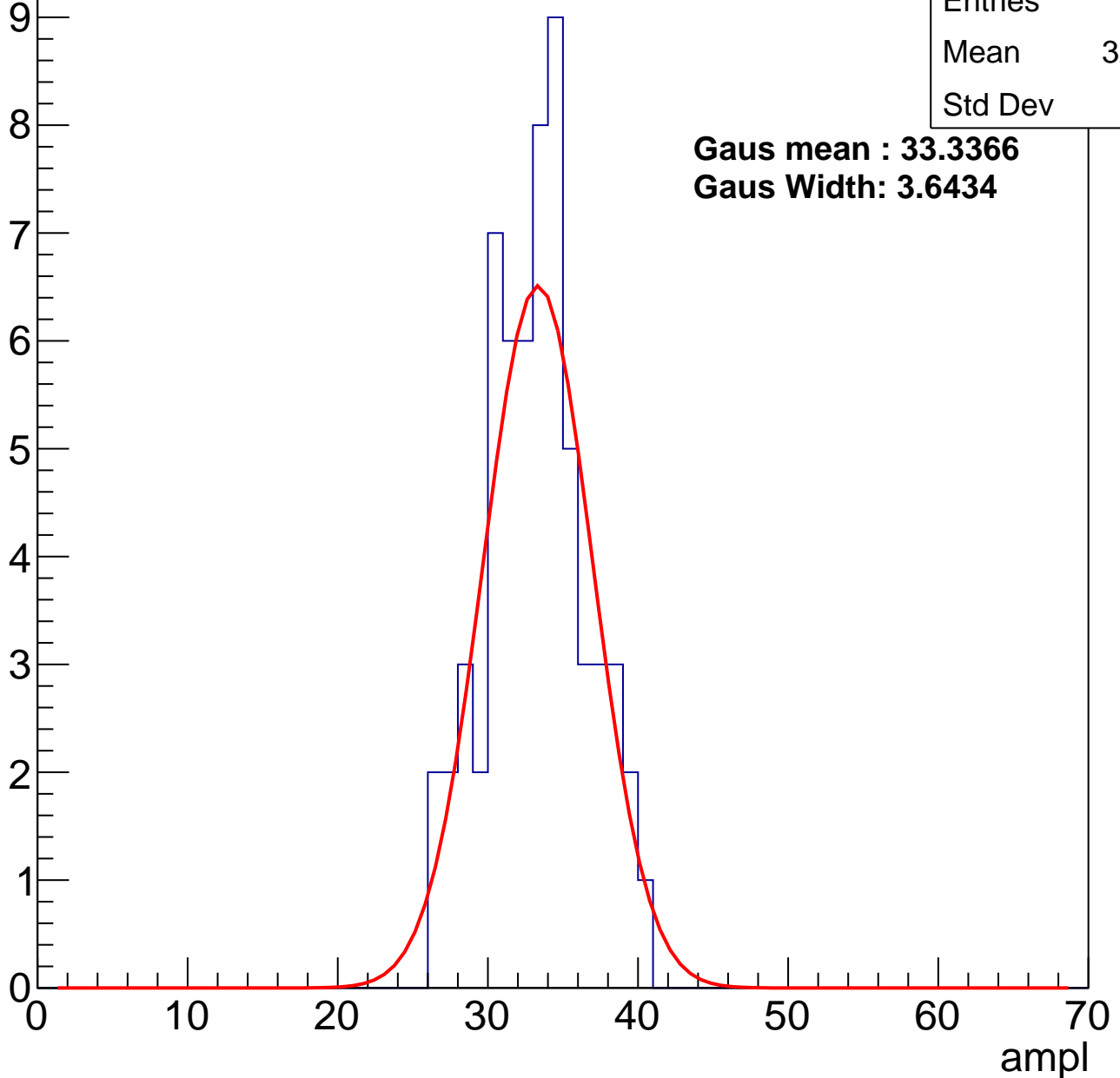
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	32.77
Std Dev	3.28

**Gaus mean : 33.3366**

**Gaus Width: 3.6434**



# B1L002S, U18-ch96, adc2

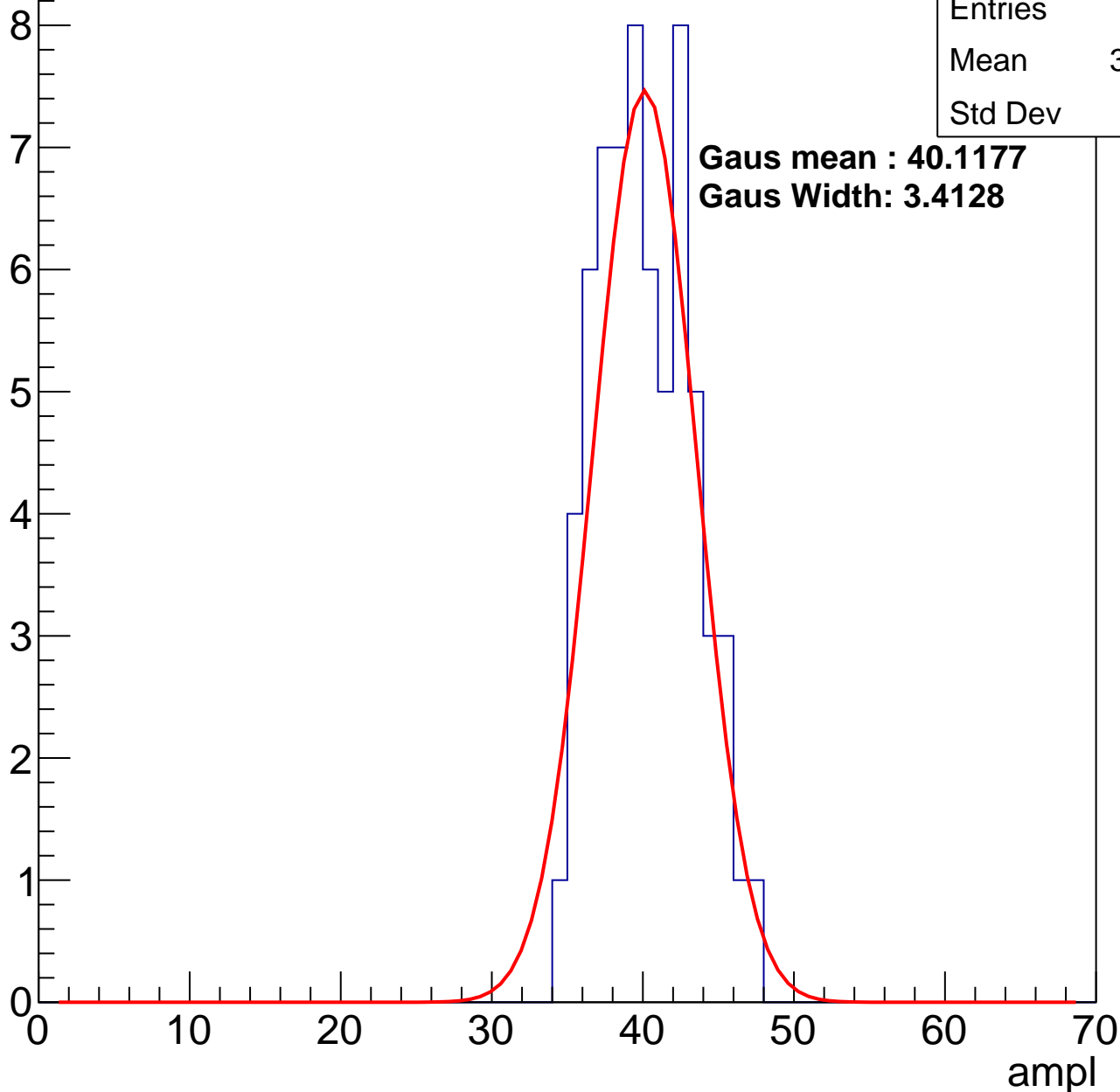
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.74
Std Dev	3.08

**Gaus mean : 40.1177**

**Gaus Width: 3.4128**

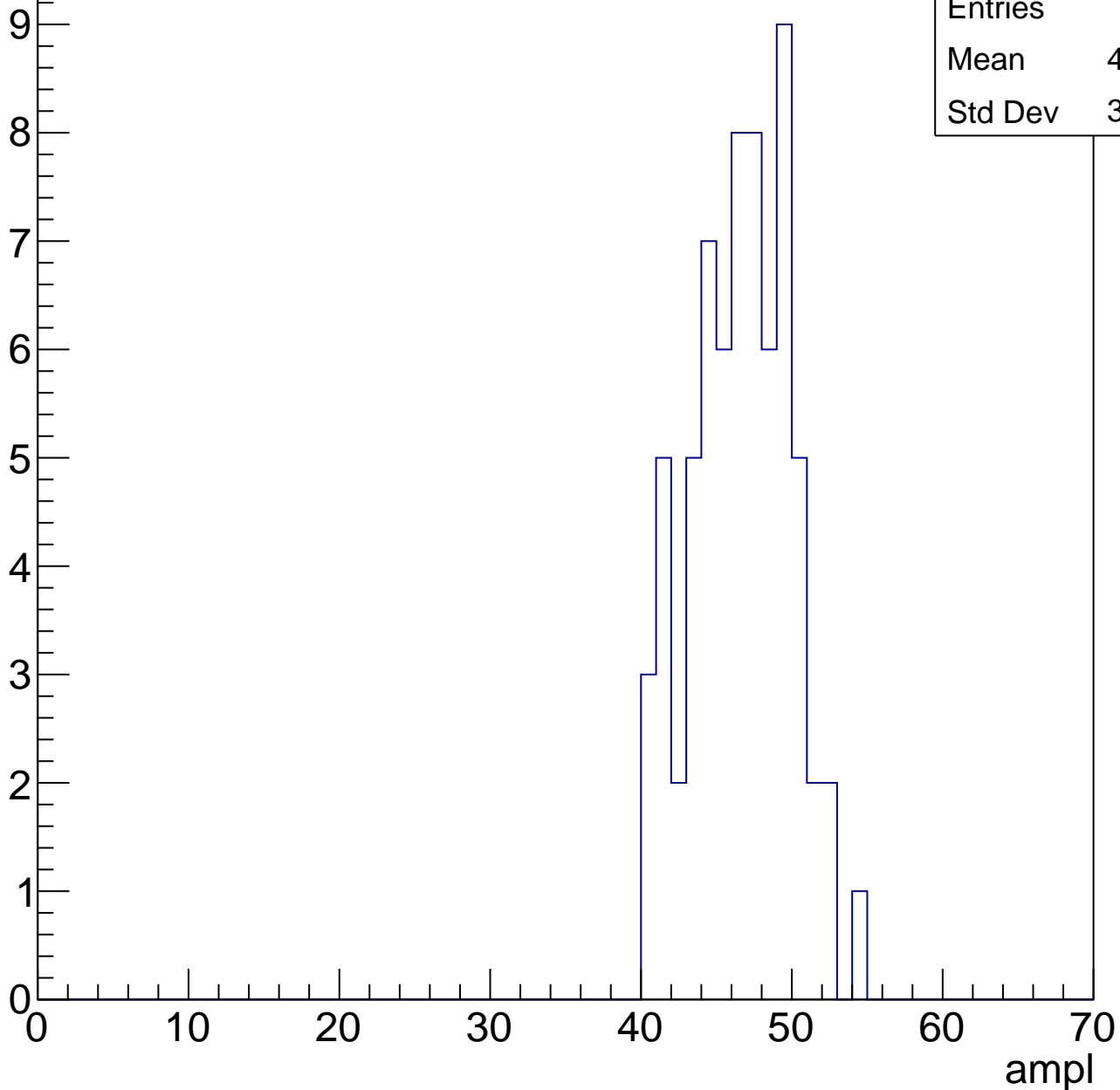


# B1L002S, U18-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.16
Std Dev	3.233

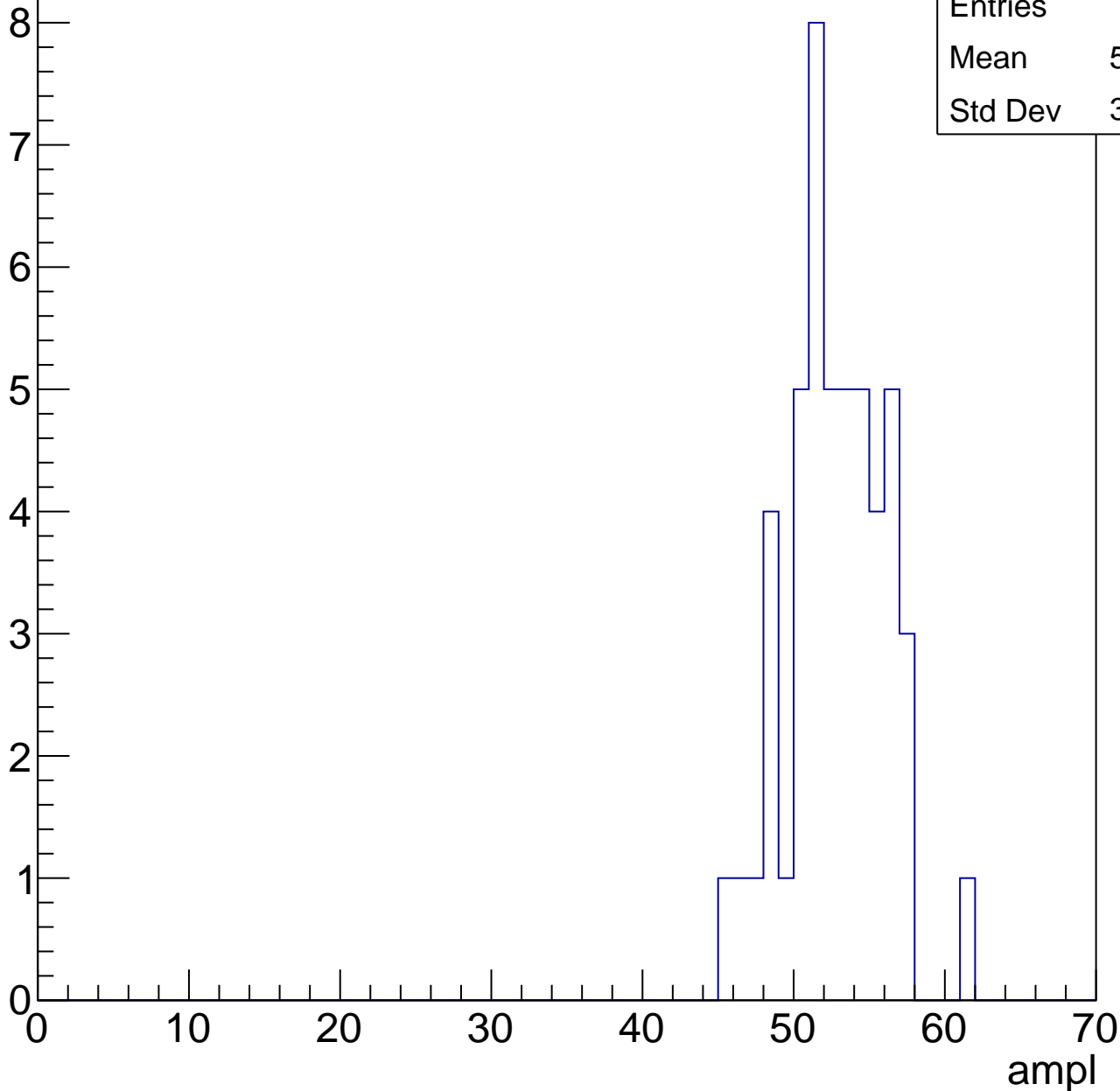


# B1L002S, U18-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.33
Std Dev	3.203



# B1L002S, U18-ch96, adc5

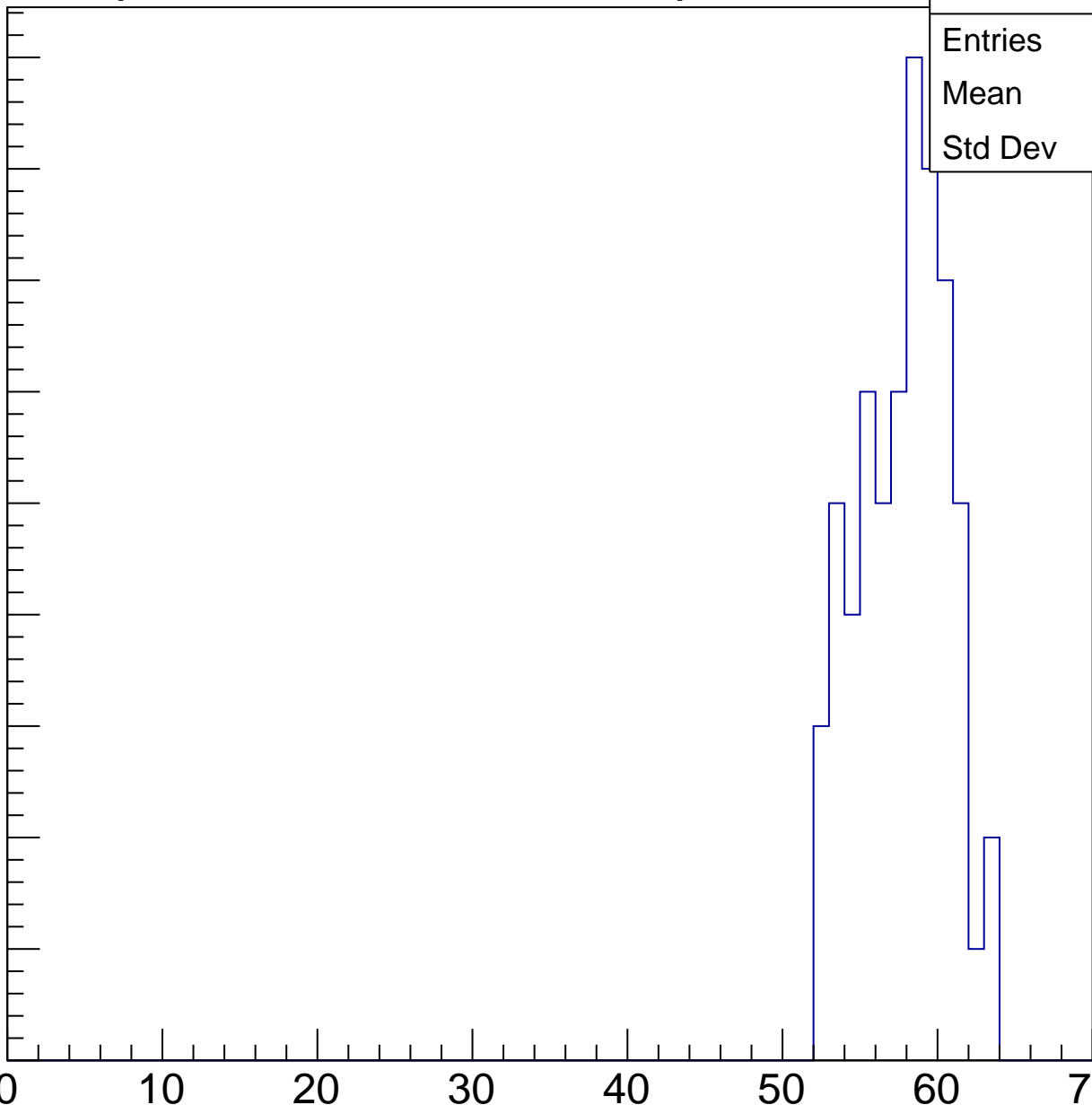
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	57.31
Std Dev	2.849

ampl

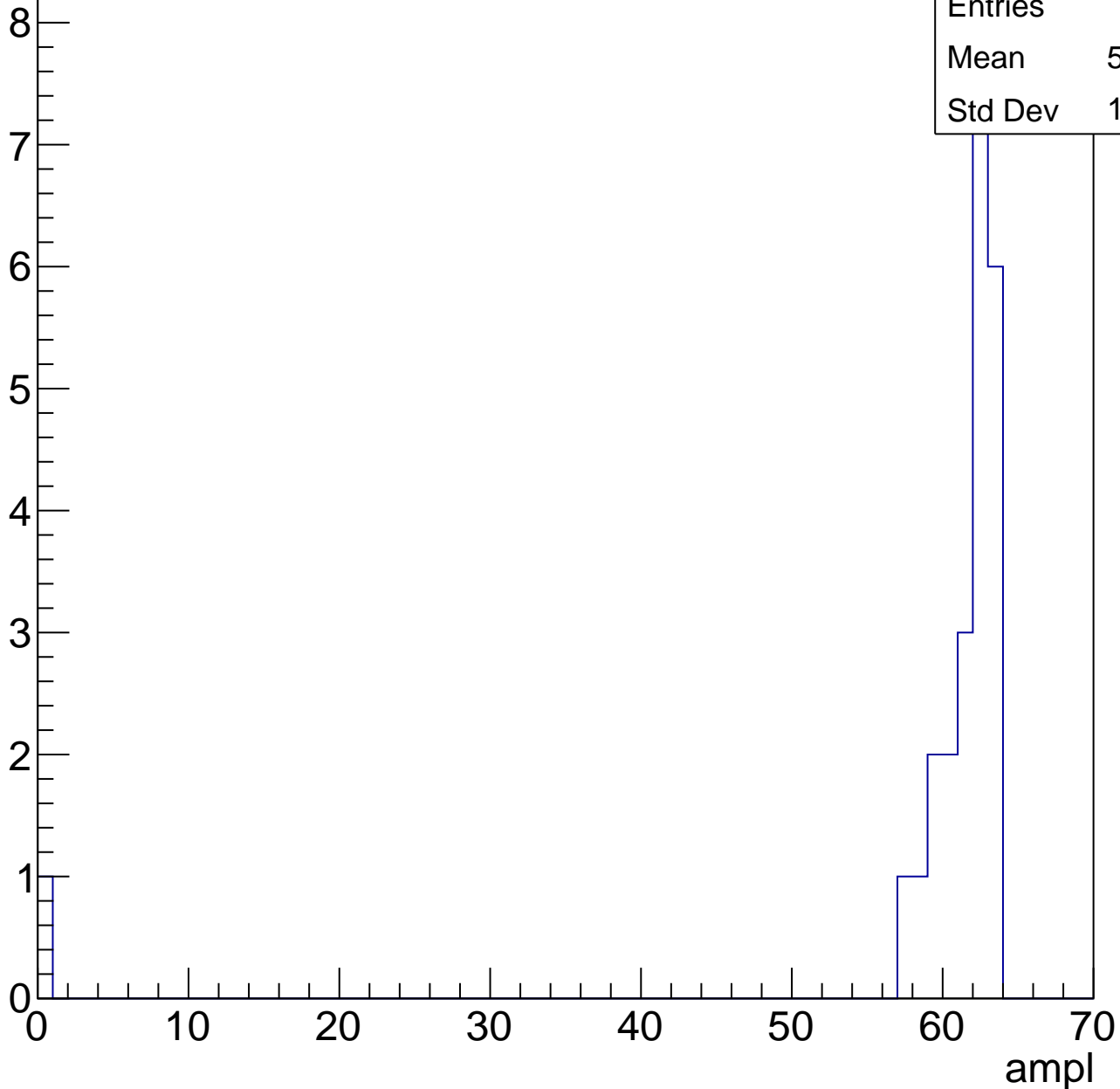


# B1L002S, U18-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.75
Std Dev	12.36

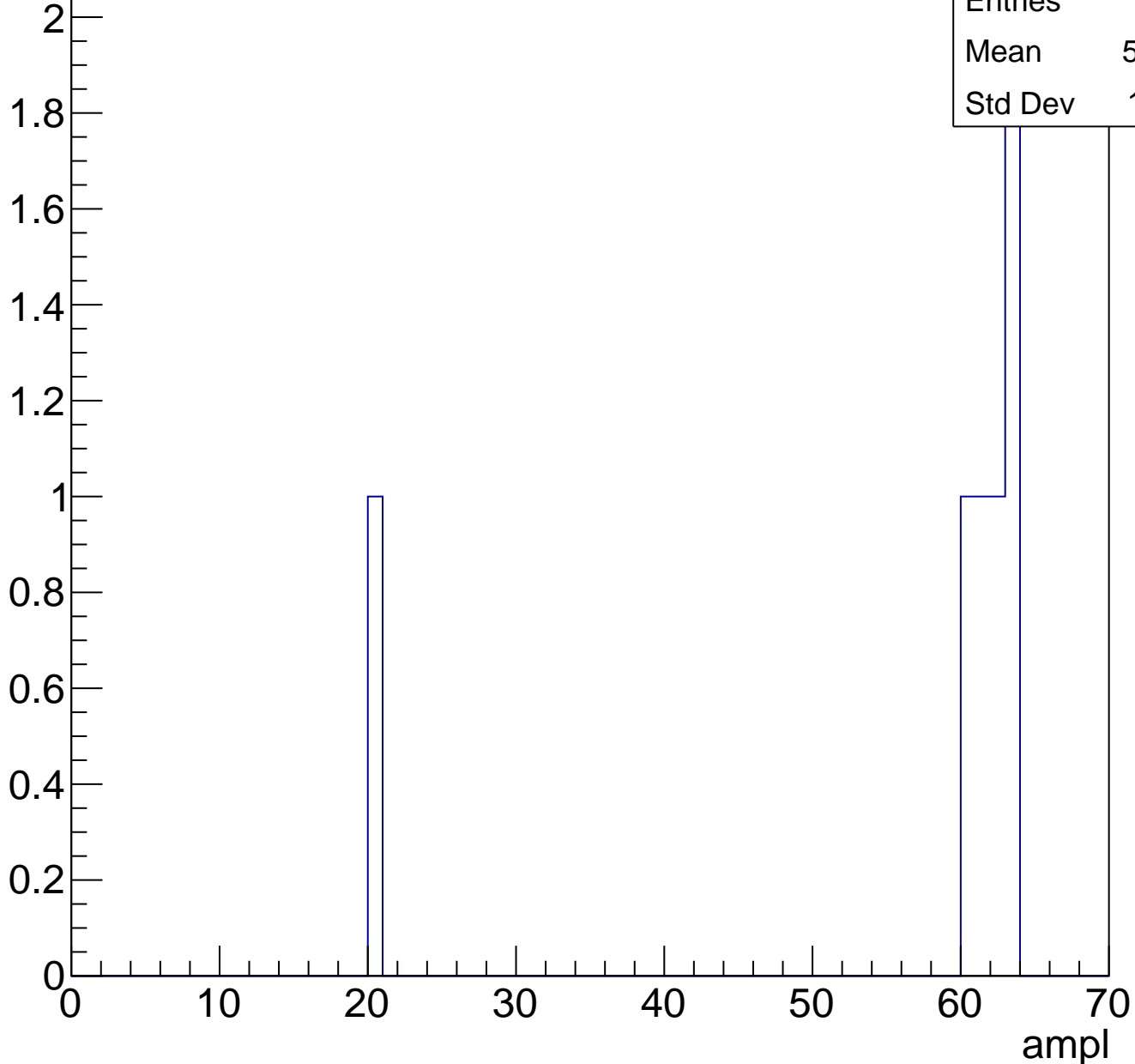




# B1L002S, U18-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	54.83
Std Dev	15.61

# B1L002S, U18-ch97, adc0

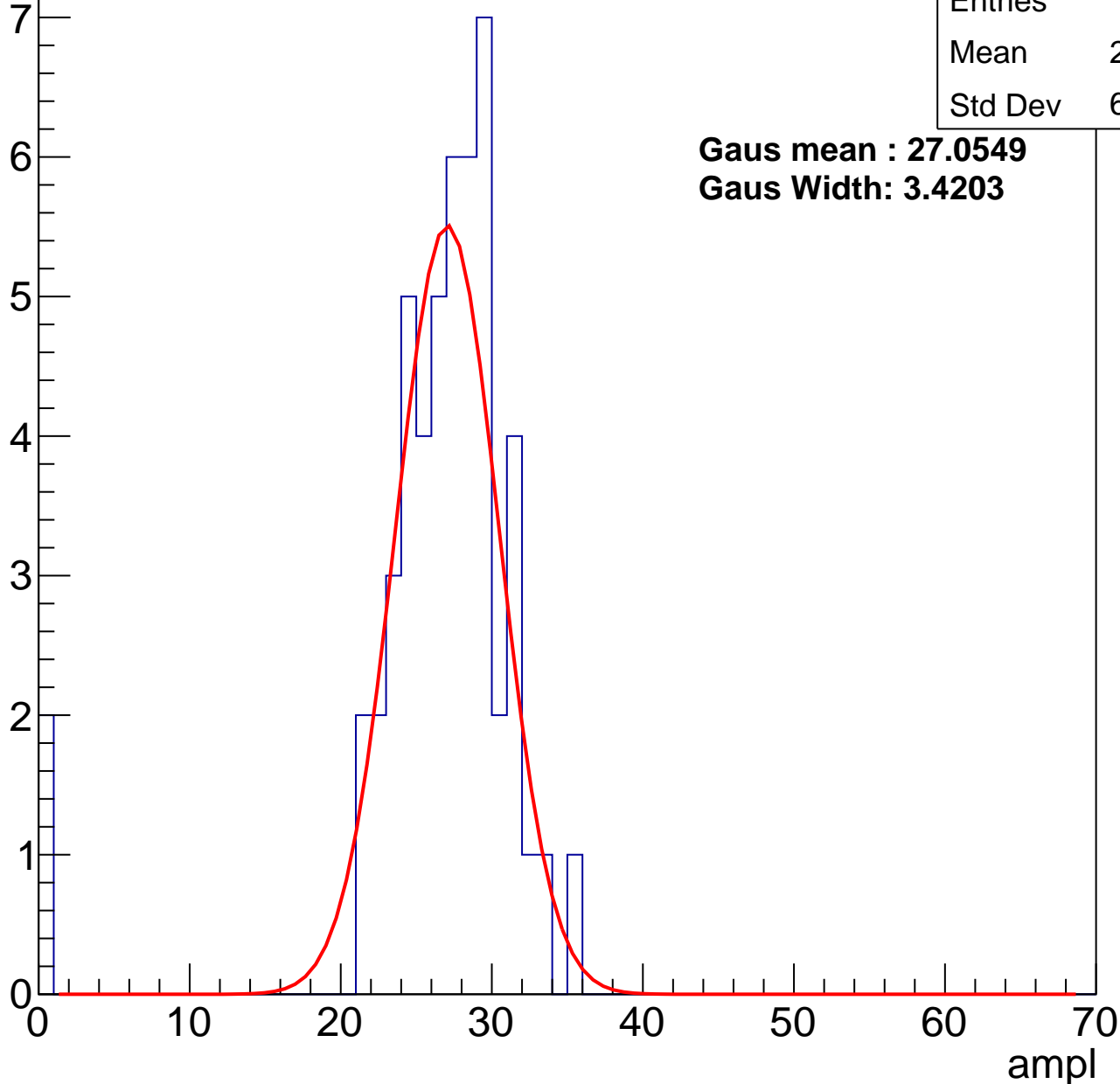
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	25.92
Std Dev	6.065

**Gaus mean : 27.0549**

**Gaus Width: 3.4203**



# B1L002S, U18-ch97, adc1

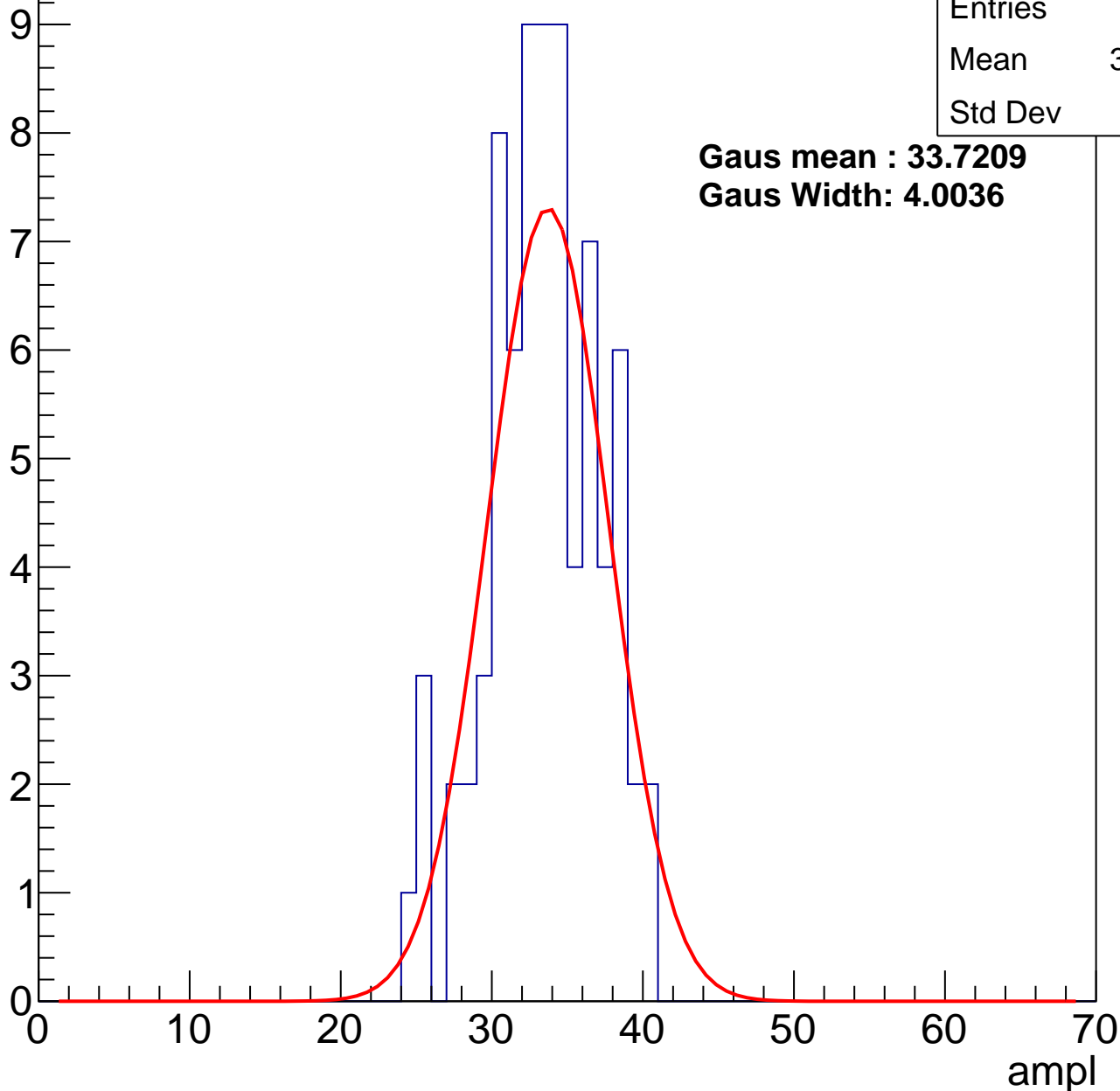
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	32.97
Std Dev	3.64

**Gaus mean : 33.7209**

**Gaus Width: 4.0036**



# B1L002S, U18-ch97, adc2

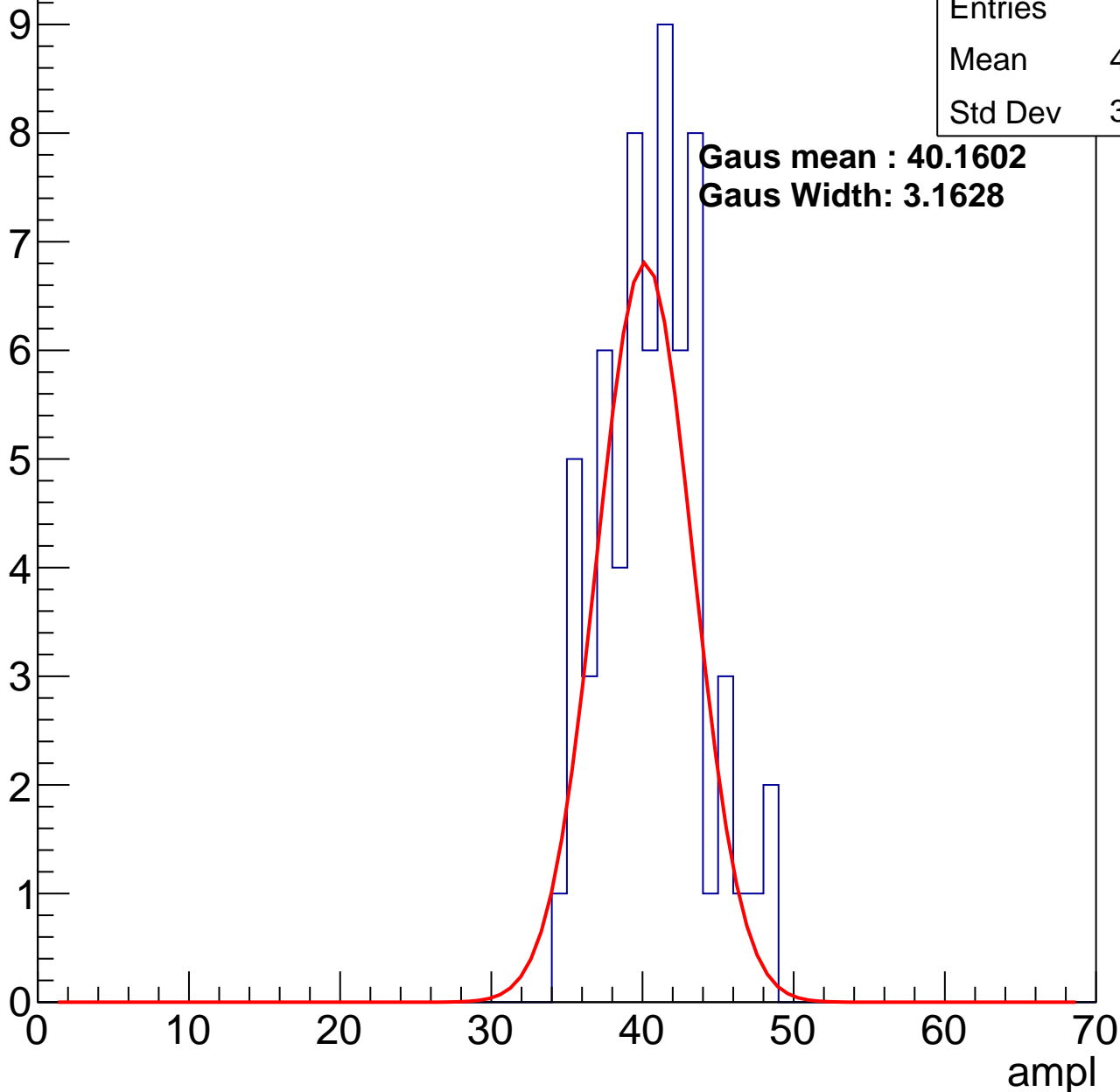
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.25
Std Dev	3.288

**Gaus mean : 40.1602**

**Gaus Width: 3.1628**

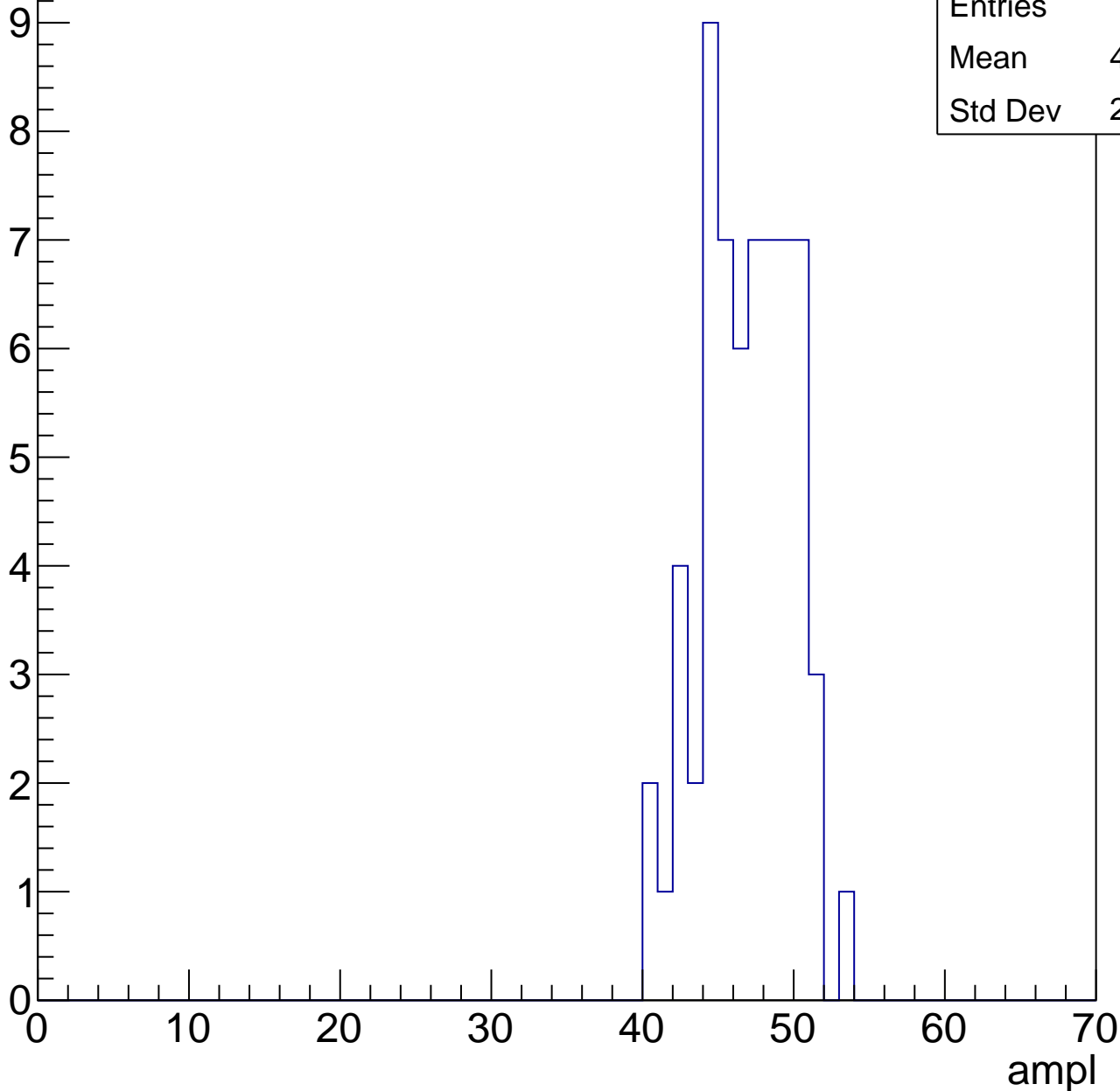


# B1L002S, U18-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.44
Std Dev	2.937

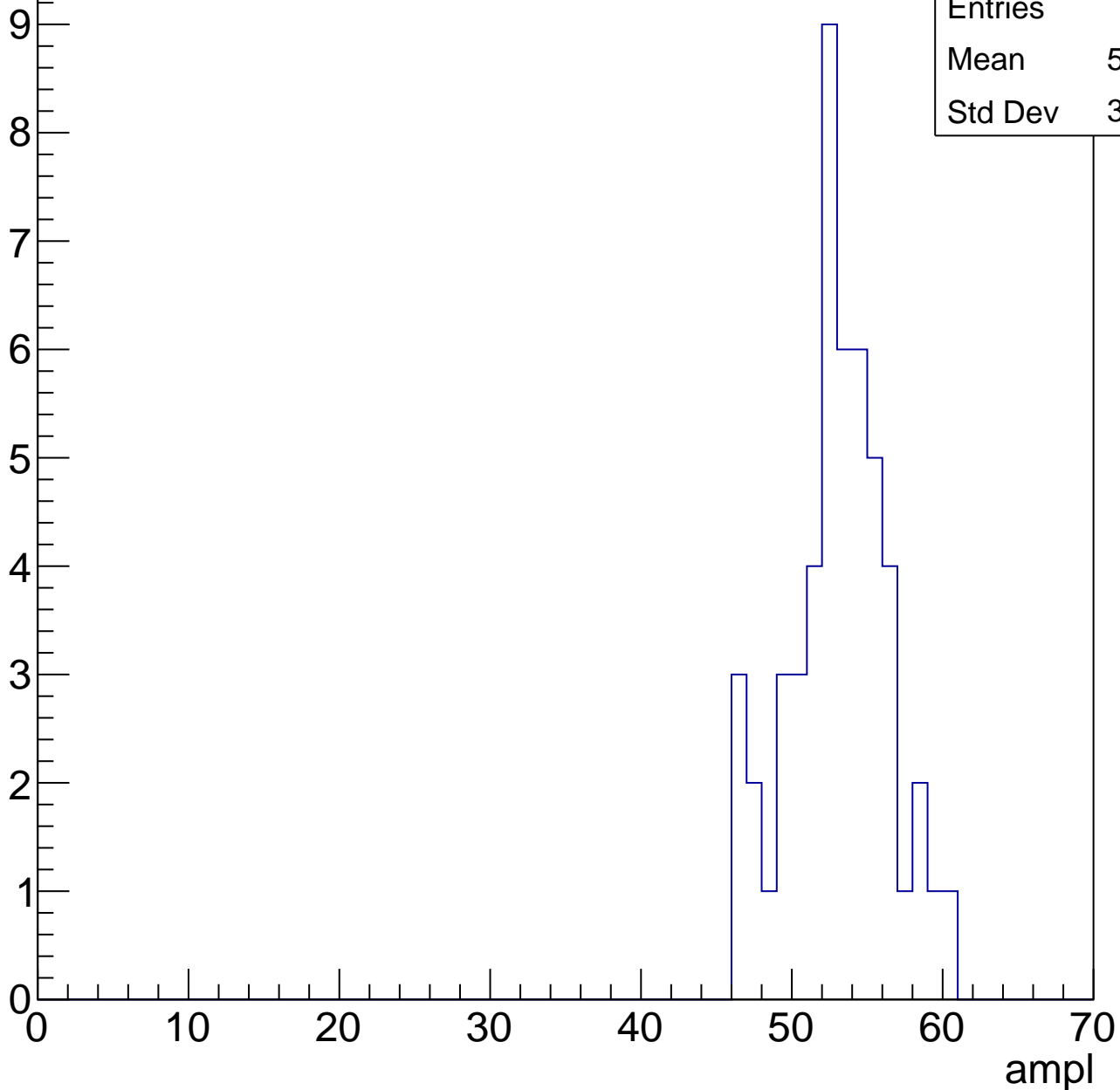


# B1L002S, U18-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	52.59
Std Dev	3.279

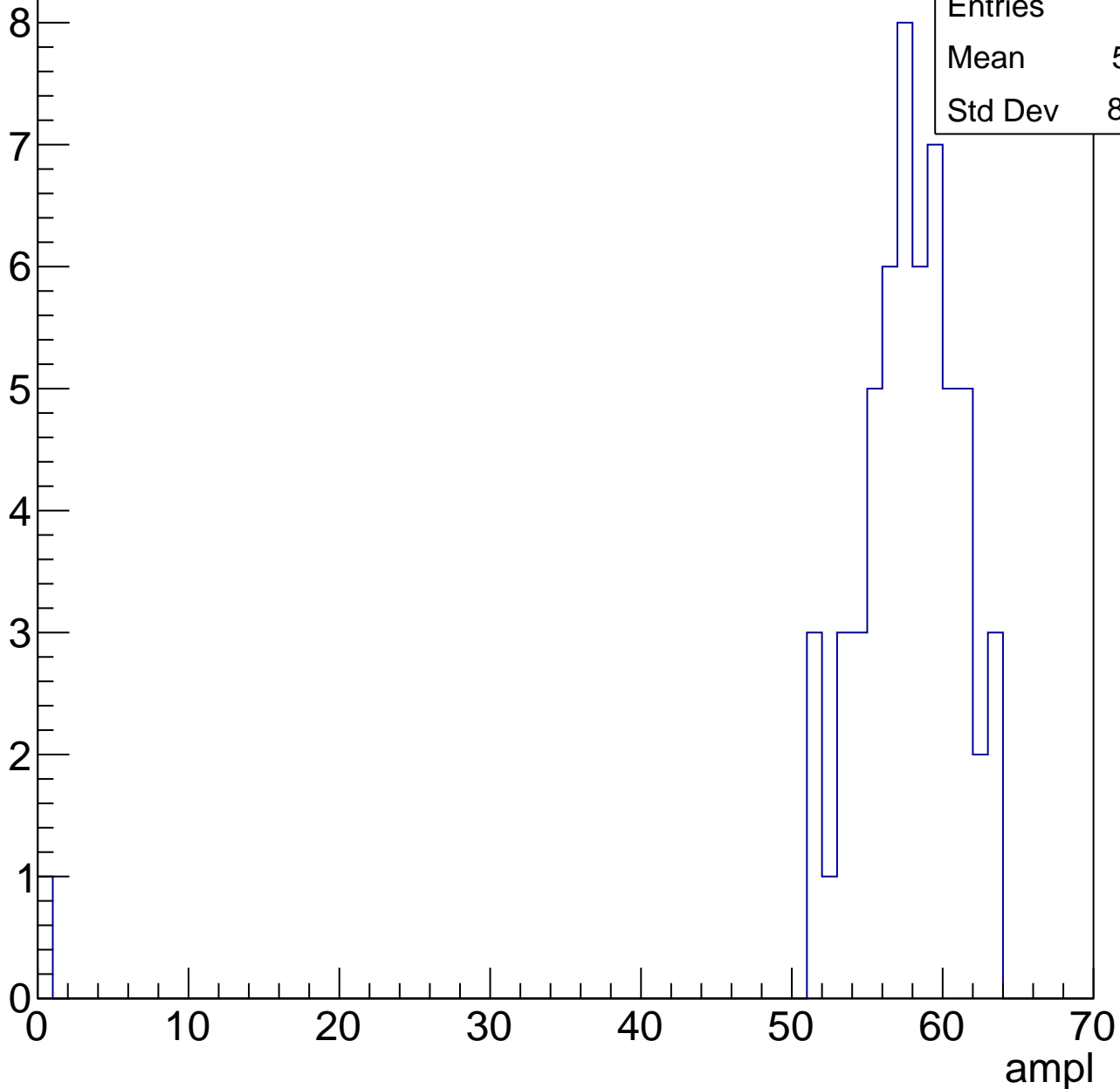


# B1L002S, U18-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.41
Std Dev	8.073

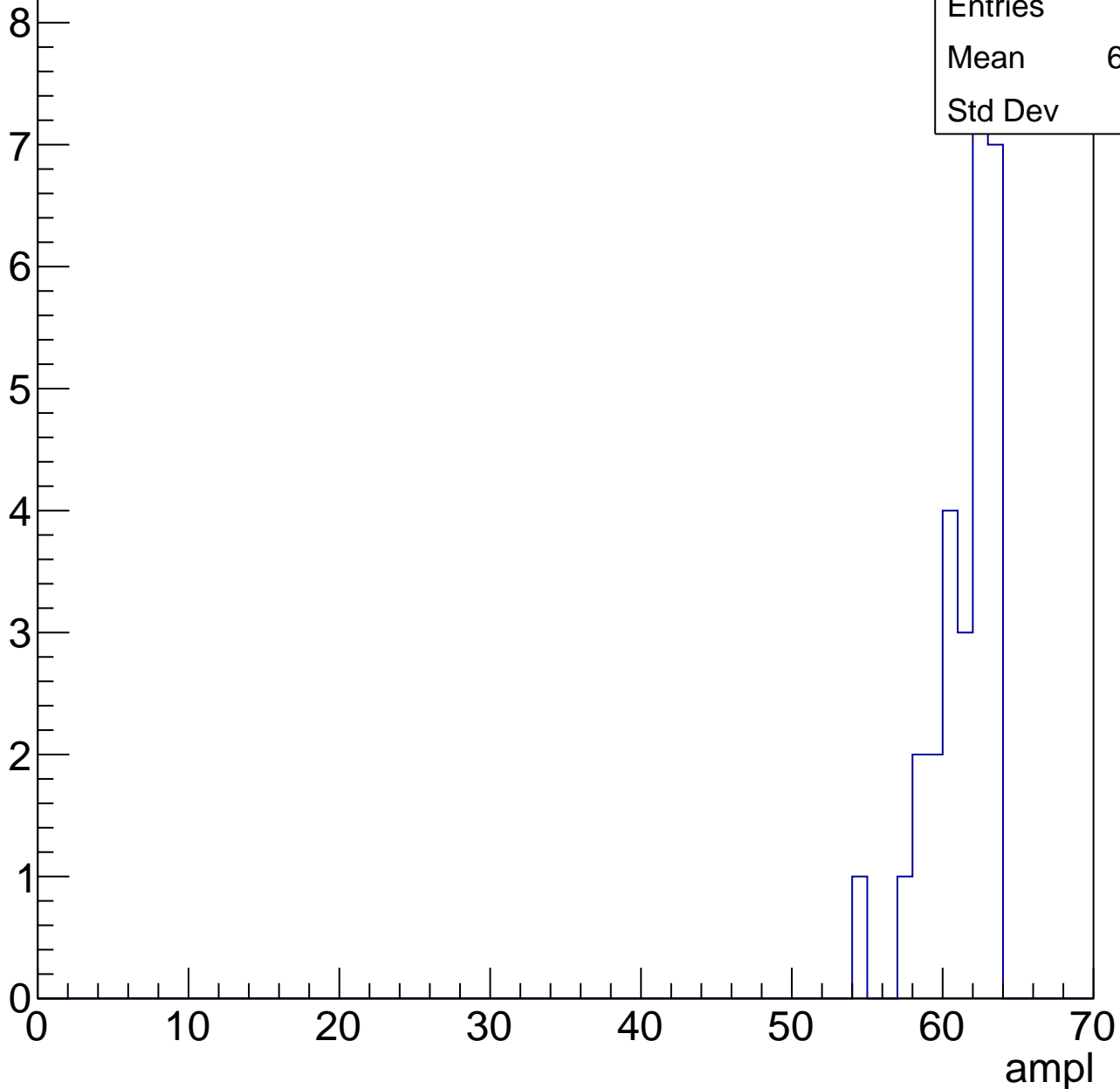


# B1L002S, U18-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	60.89
Std Dev	2.16

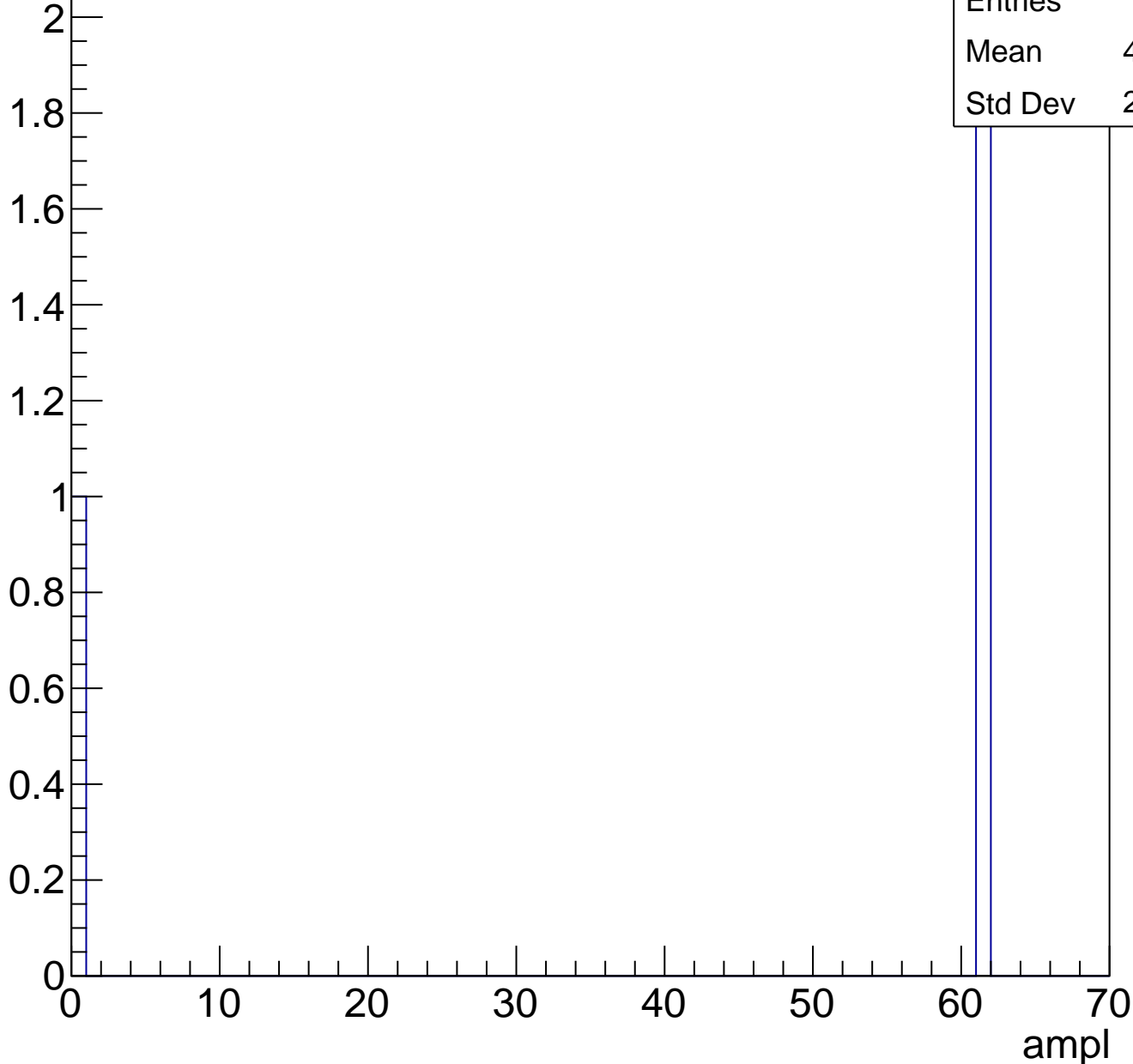




# B1L002S, U18-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch98, adc0

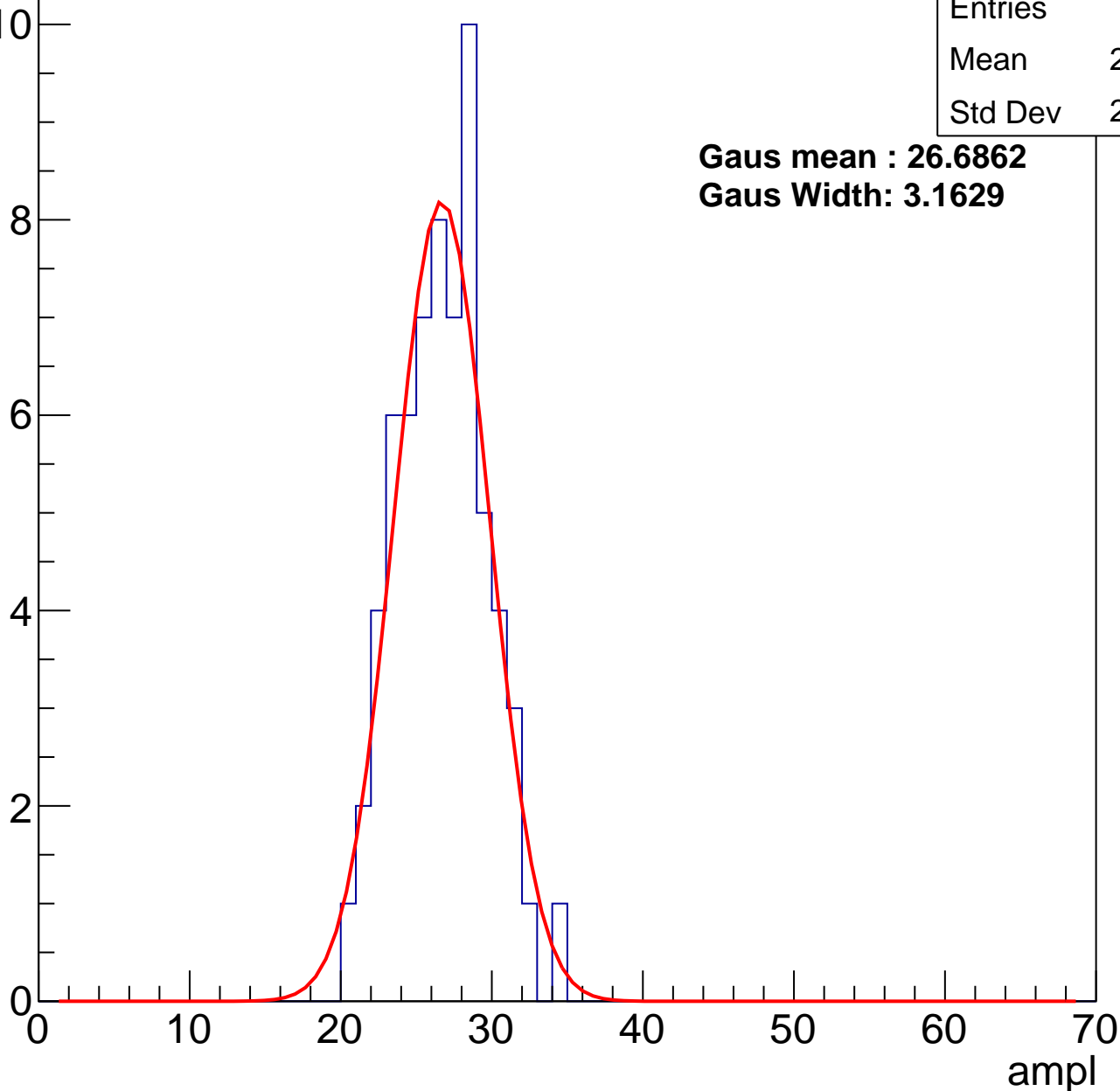
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	26.28
Std Dev	2.938

**Gaus mean : 26.6862**

**Gaus Width: 3.1629**



# B1L002S, U18-ch98, adc1

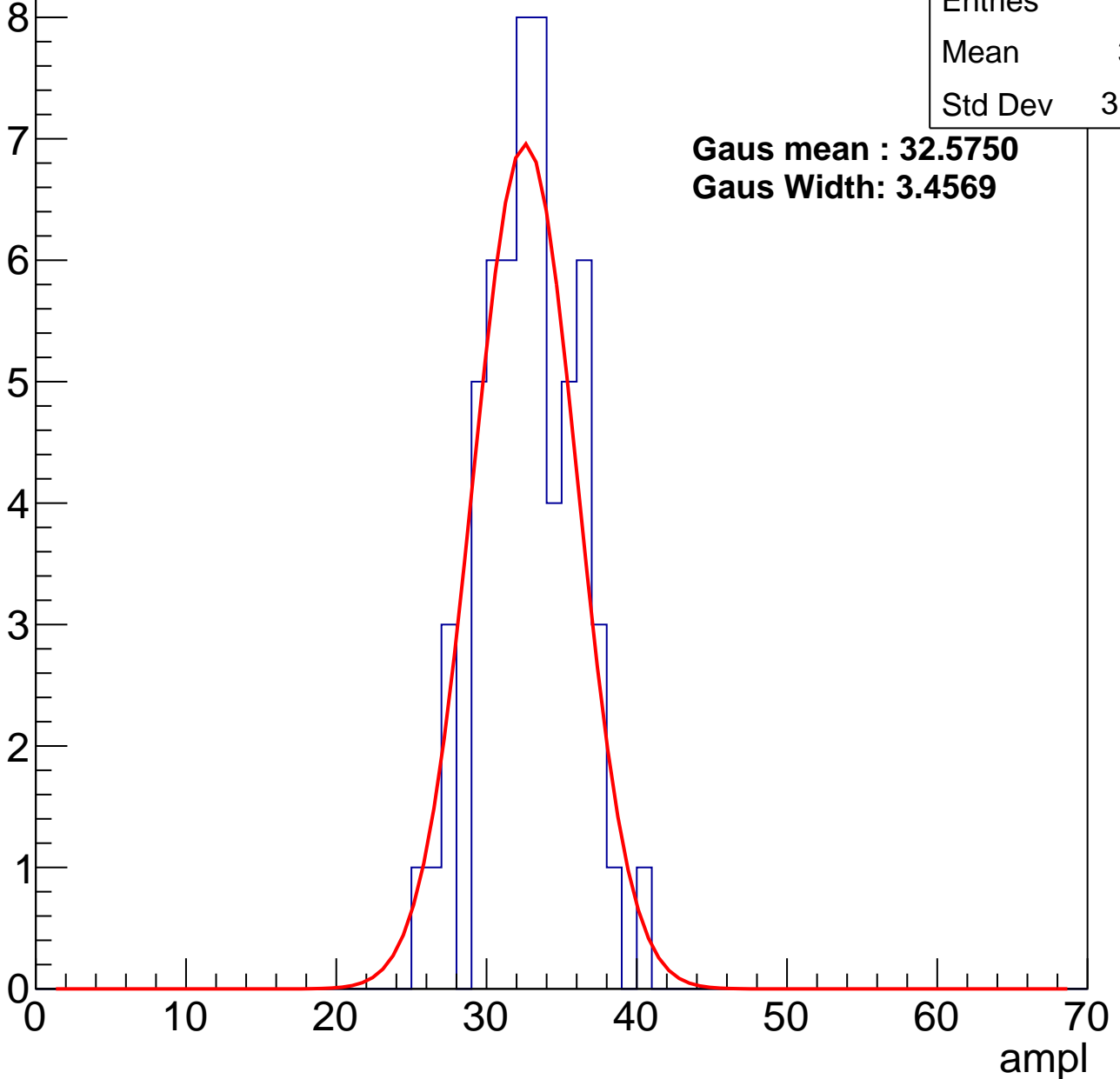
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	32.4
Std Dev	3.124

**Gaus mean : 32.5750**

**Gaus Width: 3.4569**



# B1L002S, U18-ch98, adc2

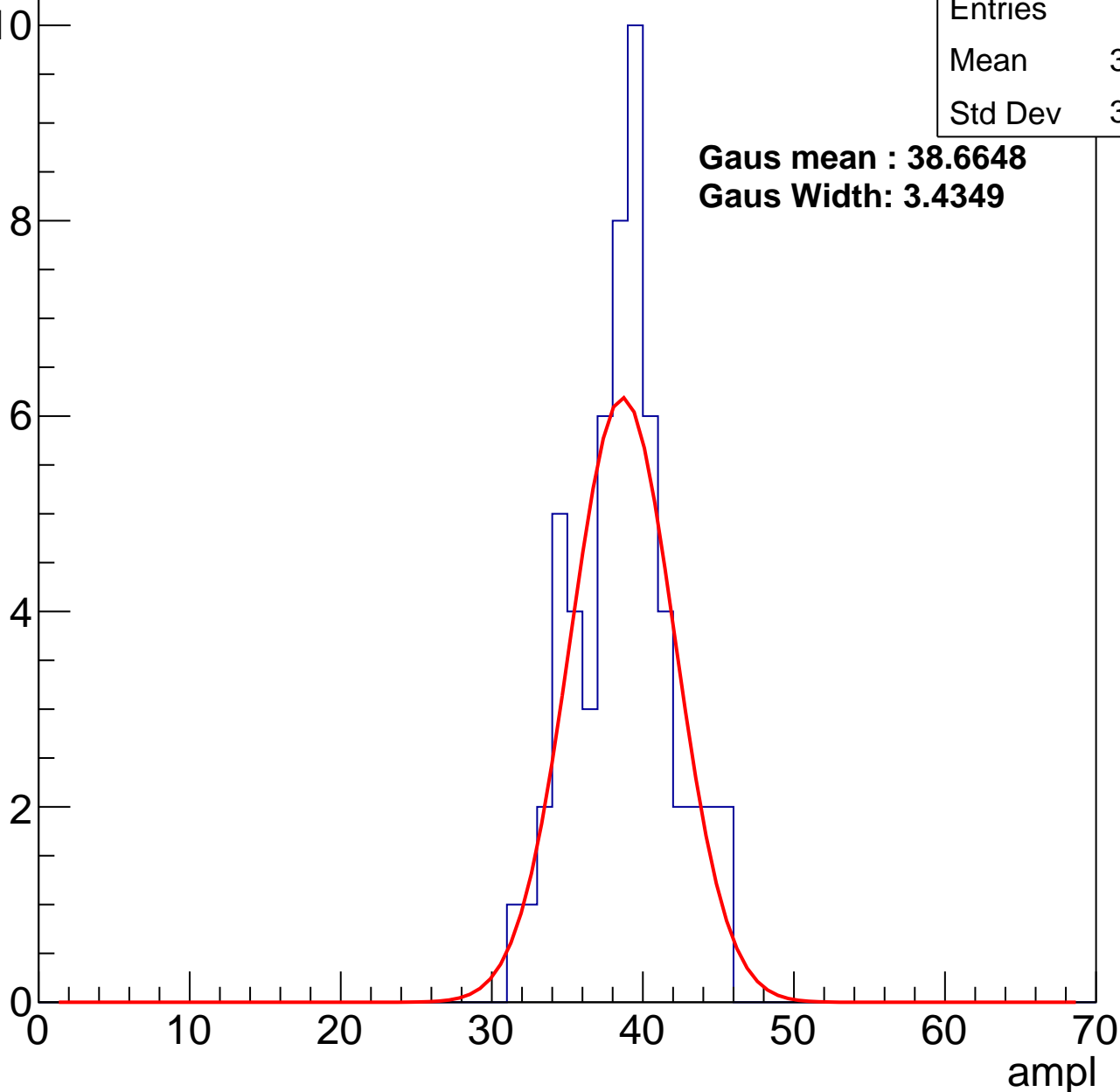
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	38.19
Std Dev	3.186

**Gaus mean : 38.6648**

**Gaus Width: 3.4349**

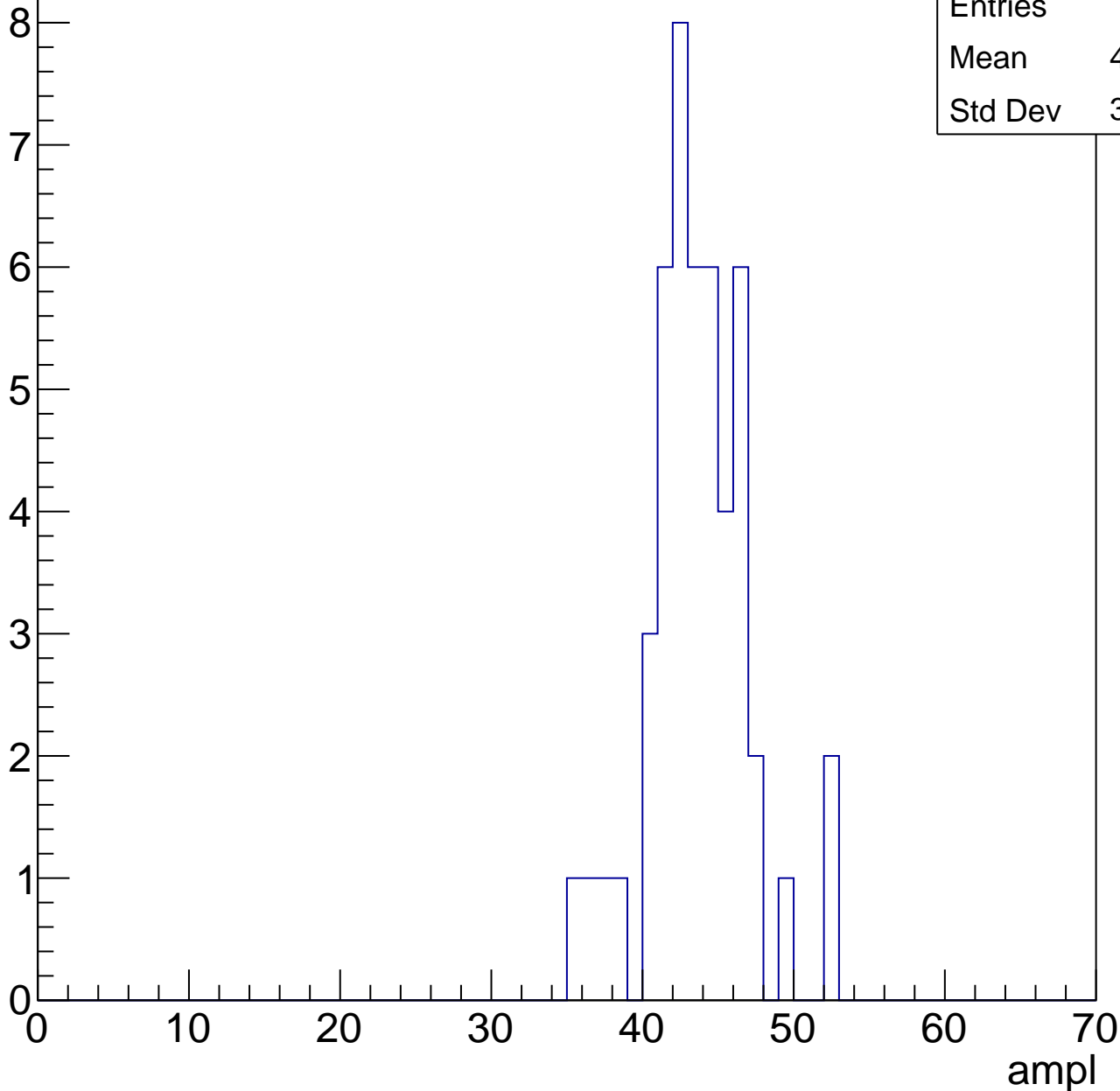


# B1L002S, U18-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	43.19
Std Dev	3.346

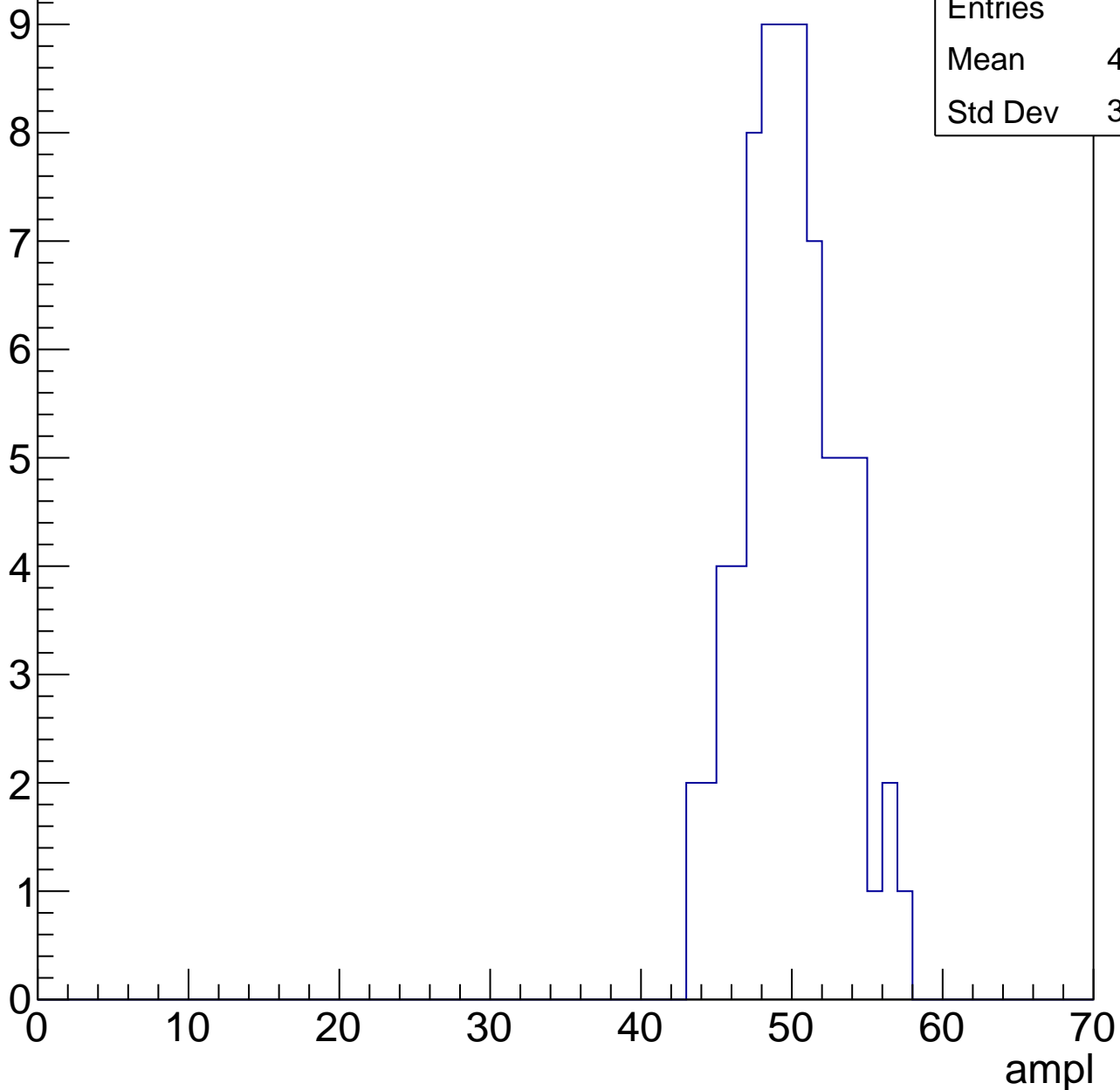


# B1L002S, U18-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

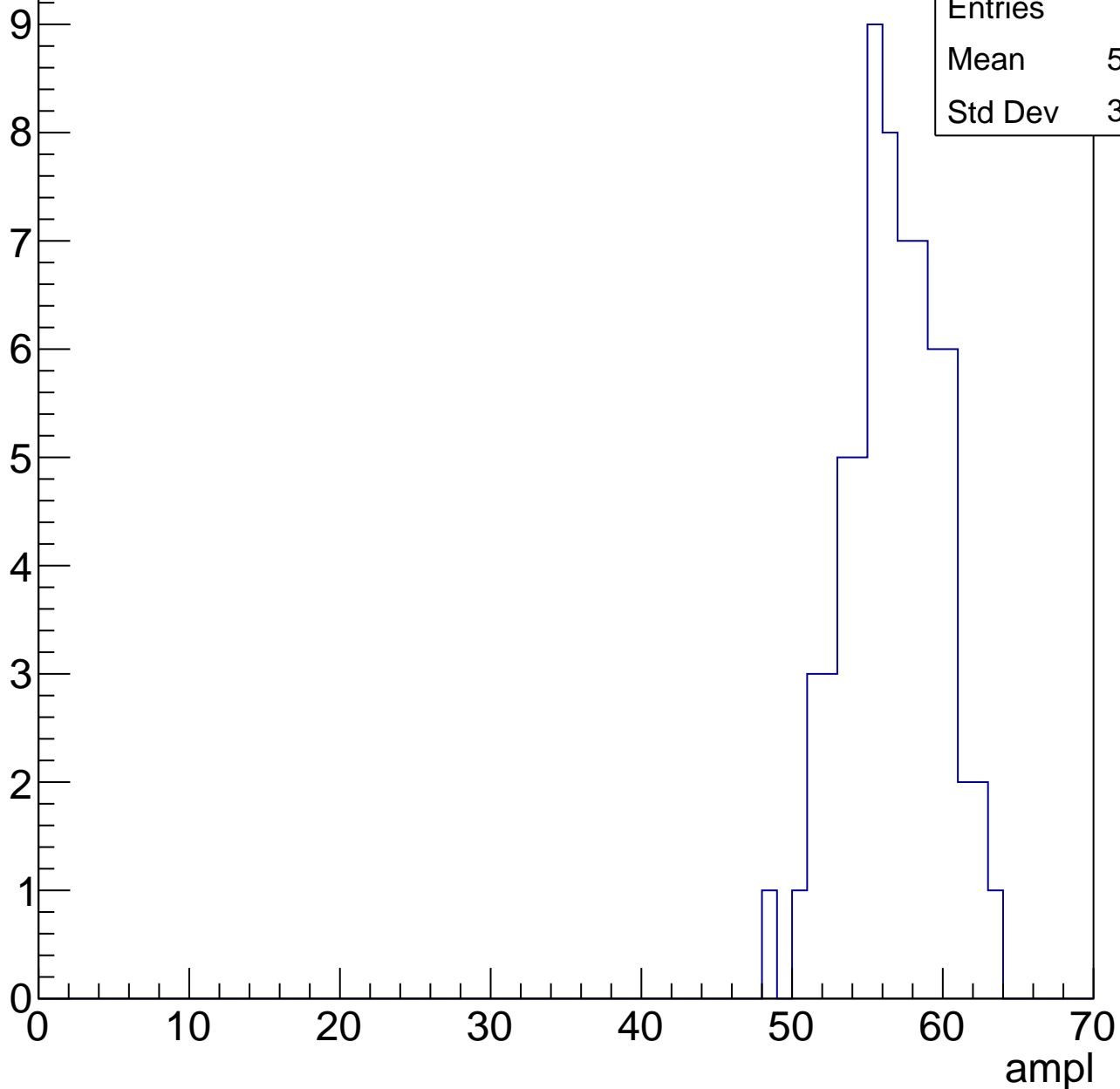
Entries	73
Mean	49.49
Std Dev	3.163



# B1L002S, U18-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

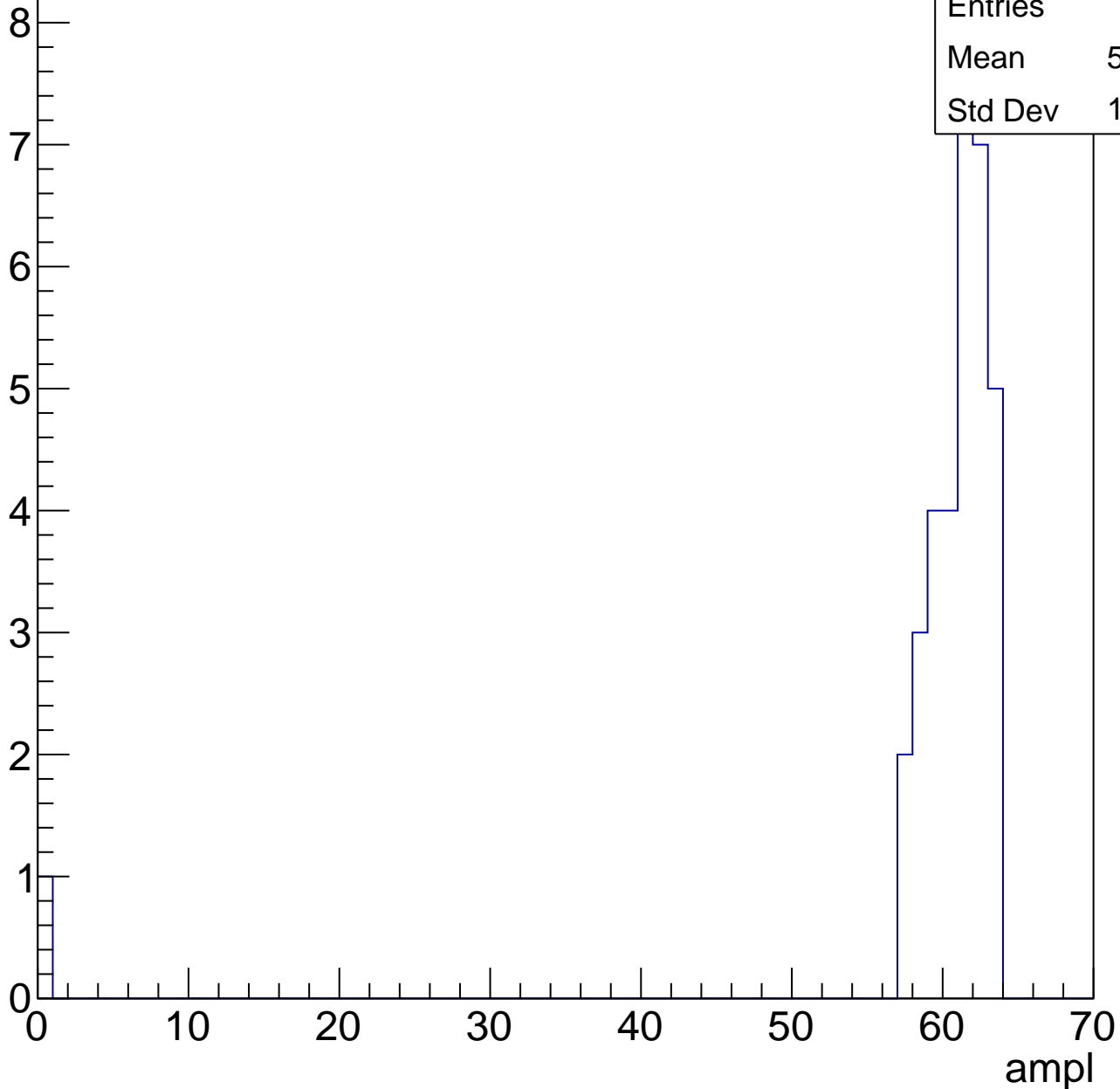


# B1L002S, U18-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	58.85
Std Dev	10.39

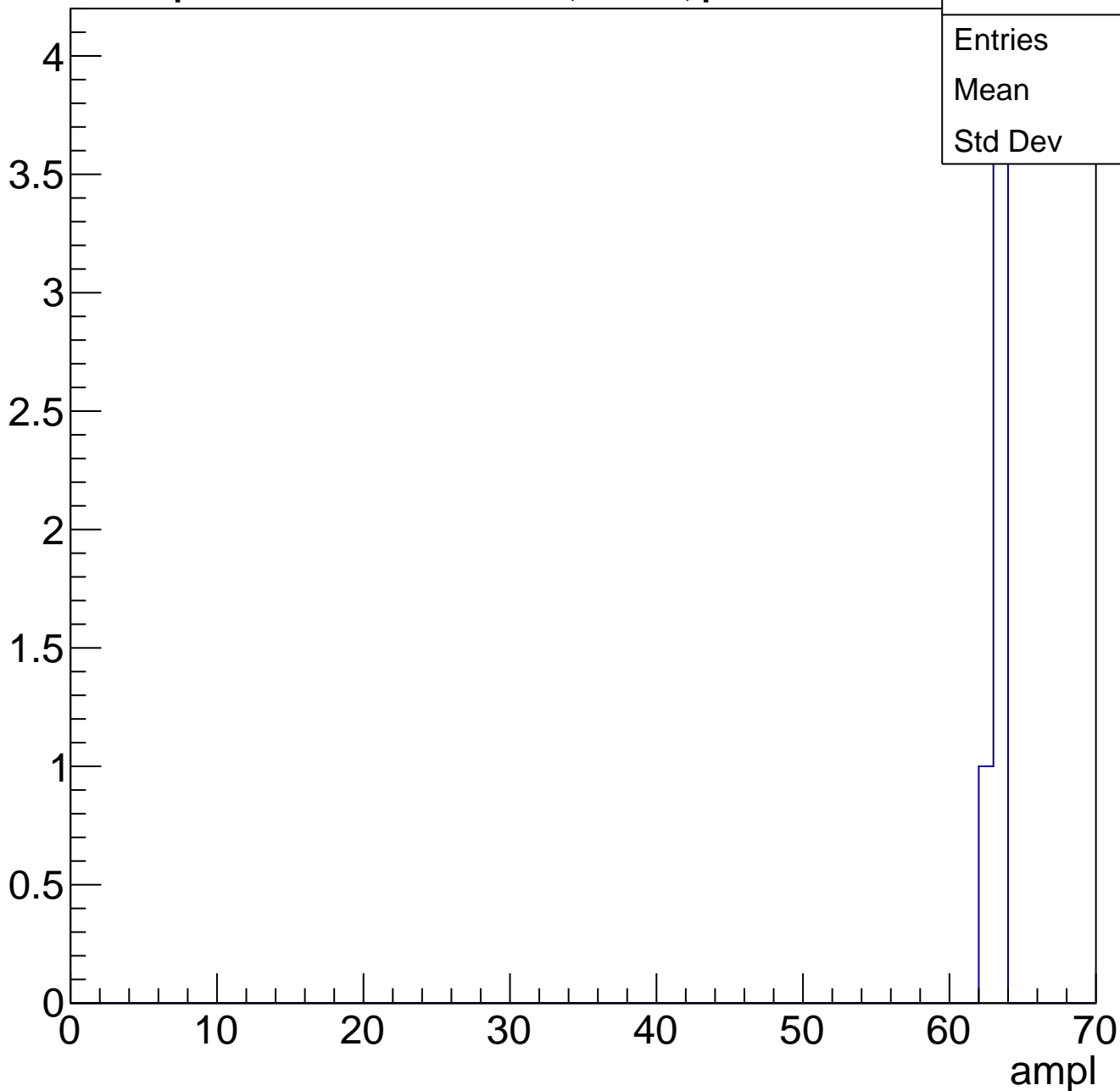




# B1L002S, U18-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch99, adc0

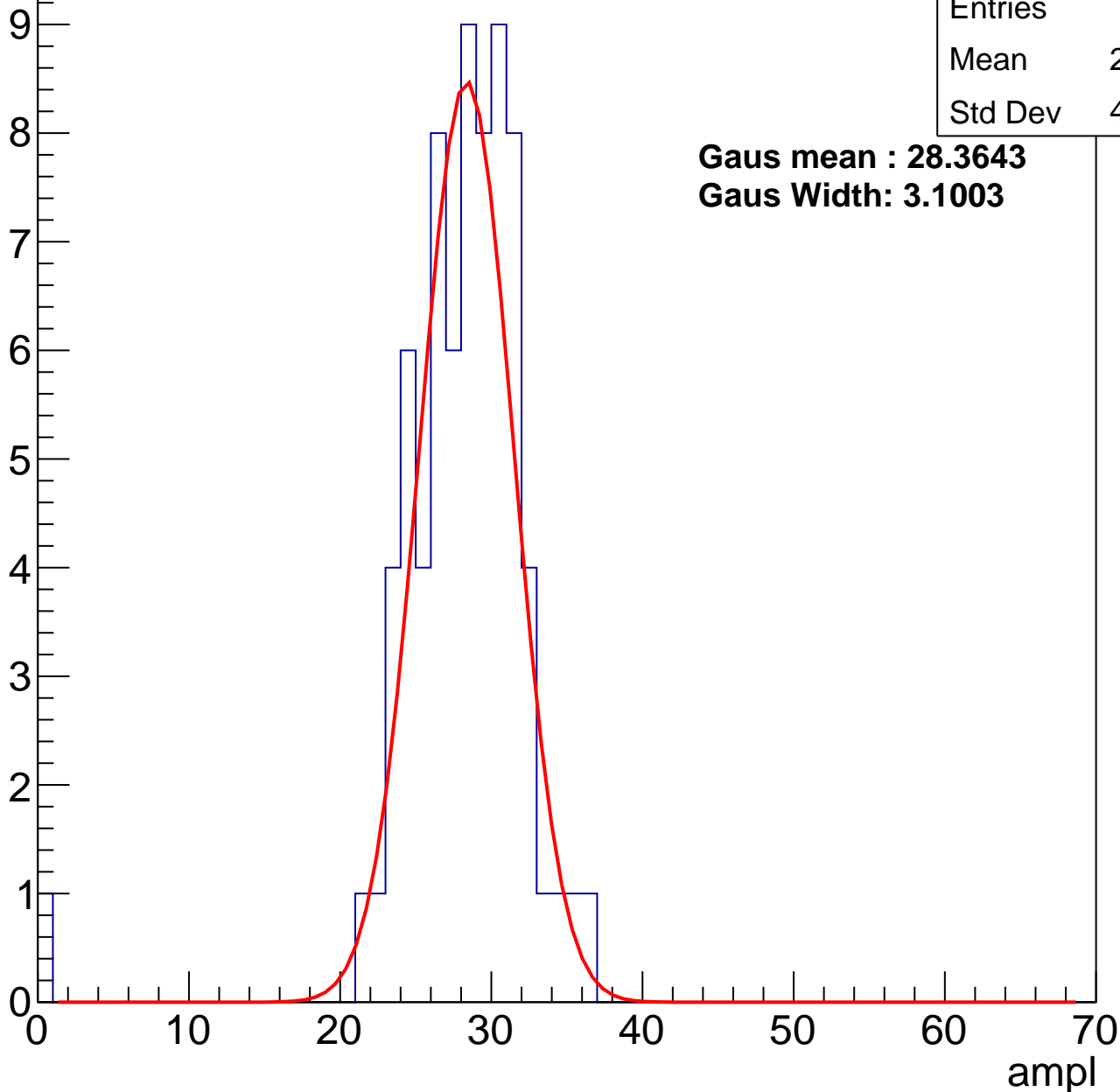
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	27.63
Std Dev	4.507

**Gaus mean : 28.3643**

**Gaus Width: 3.1003**



# B1L002S, U18-ch99, adc1

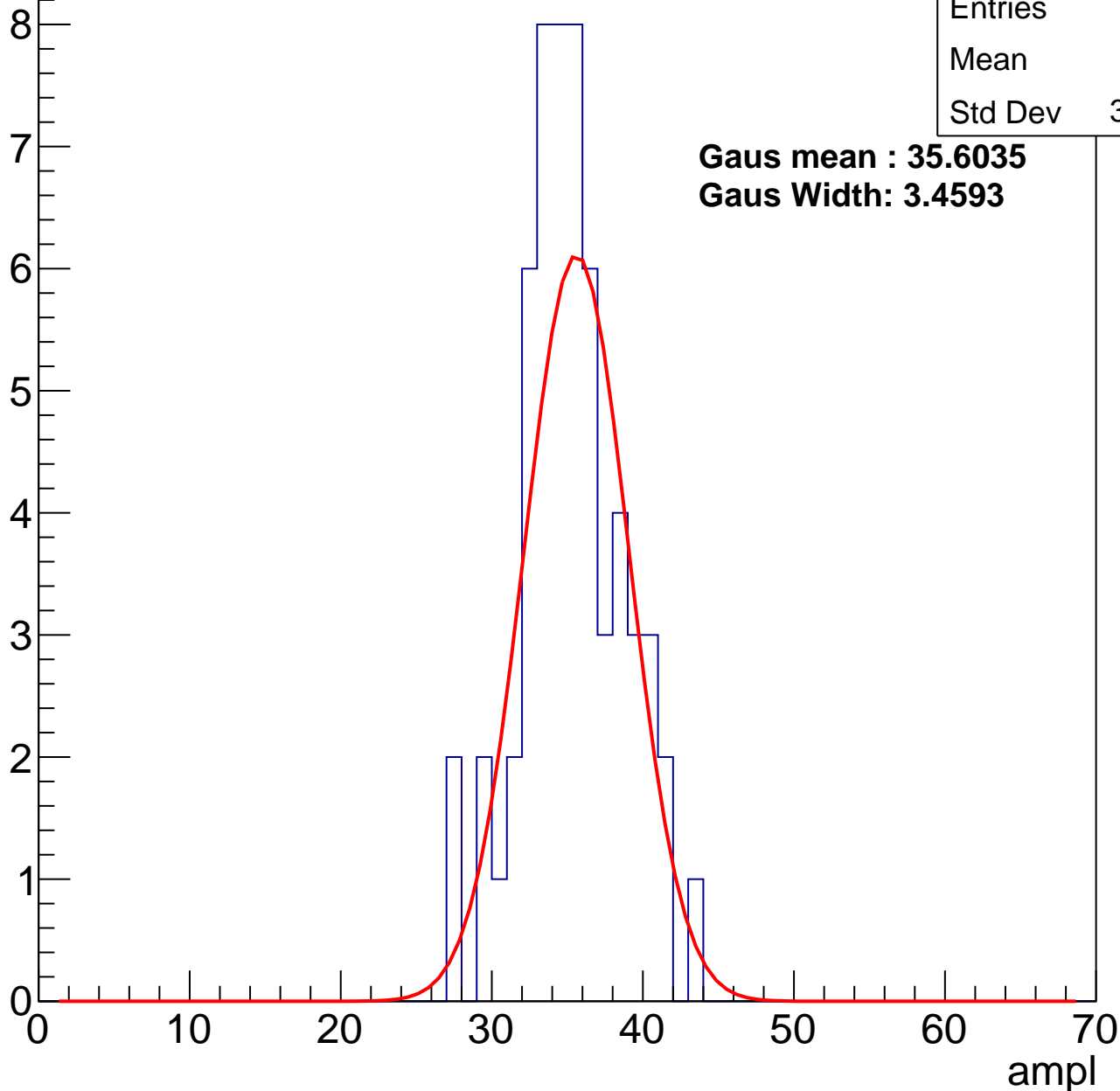
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.8
Std Dev	3.349

**Gaus mean : 35.6035**

**Gaus Width: 3.4593**



# B1L002S, U18-ch99, adc2

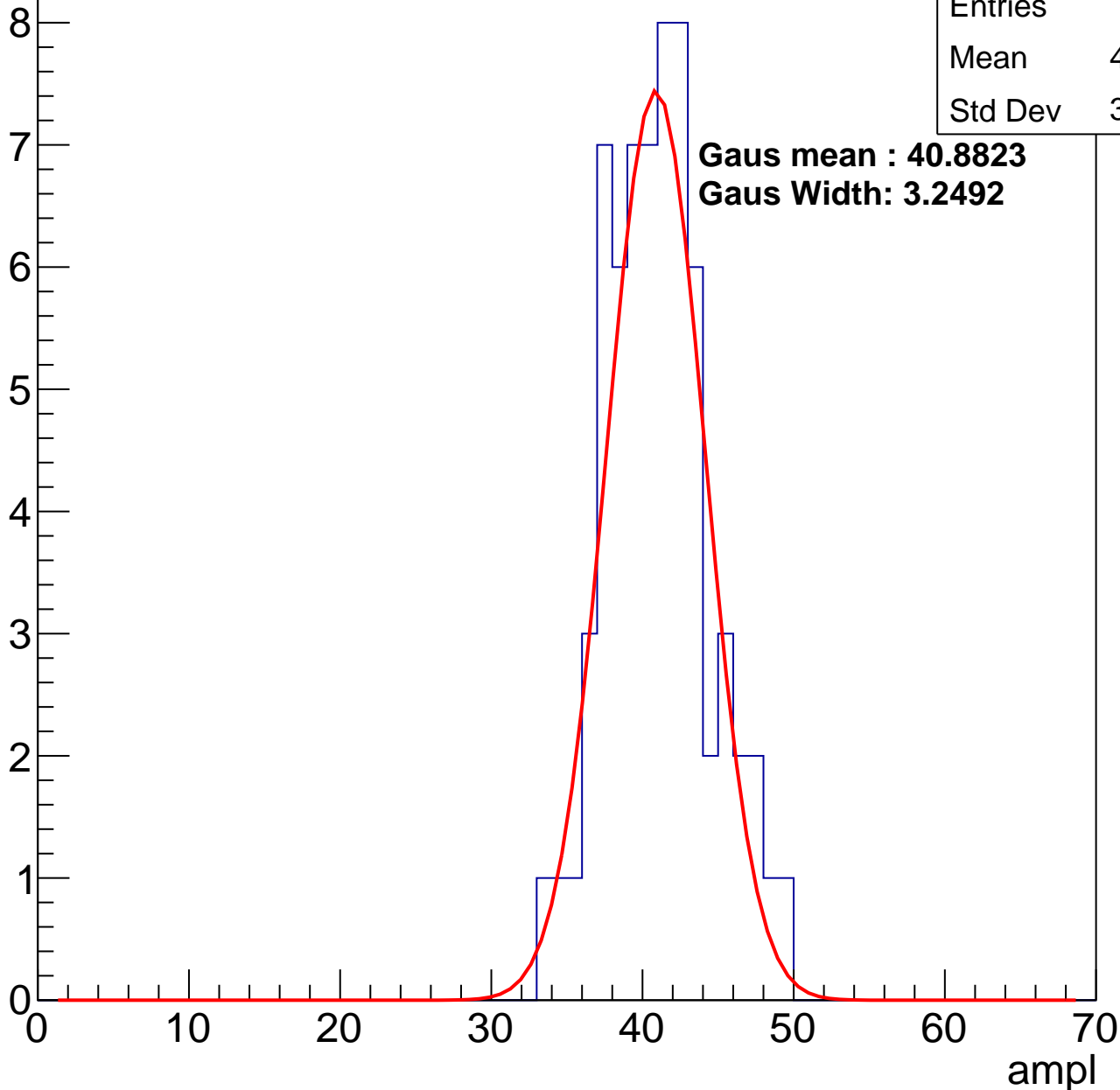
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.58
Std Dev	3.362

**Gaus mean : 40.8823**

**Gaus Width: 3.2492**

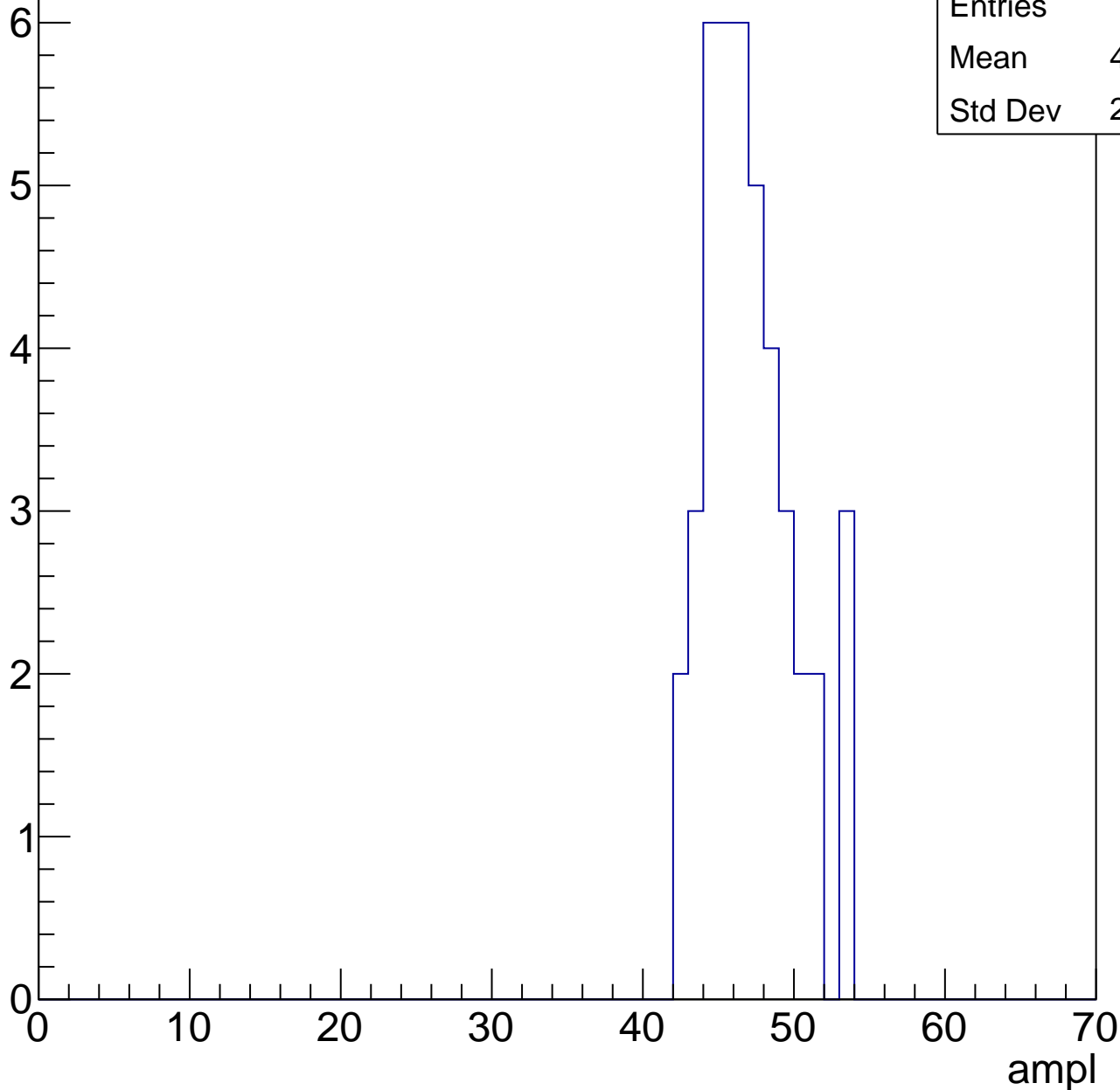


# B1L002S, U18-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

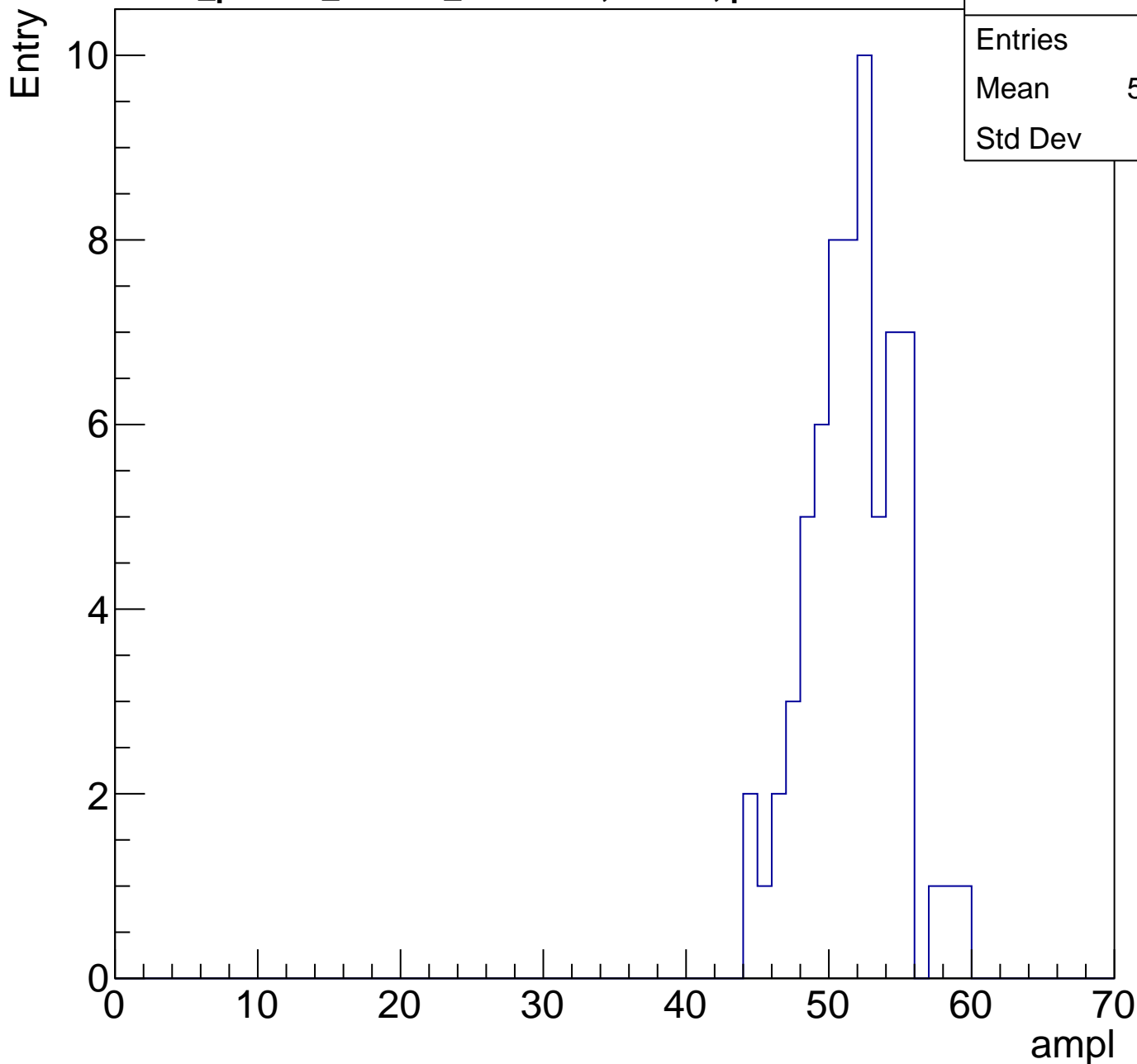
Entries	42
Mean	46.62
Std Dev	2.886



# B1L002S, U18-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	51.19
Std Dev	3.14

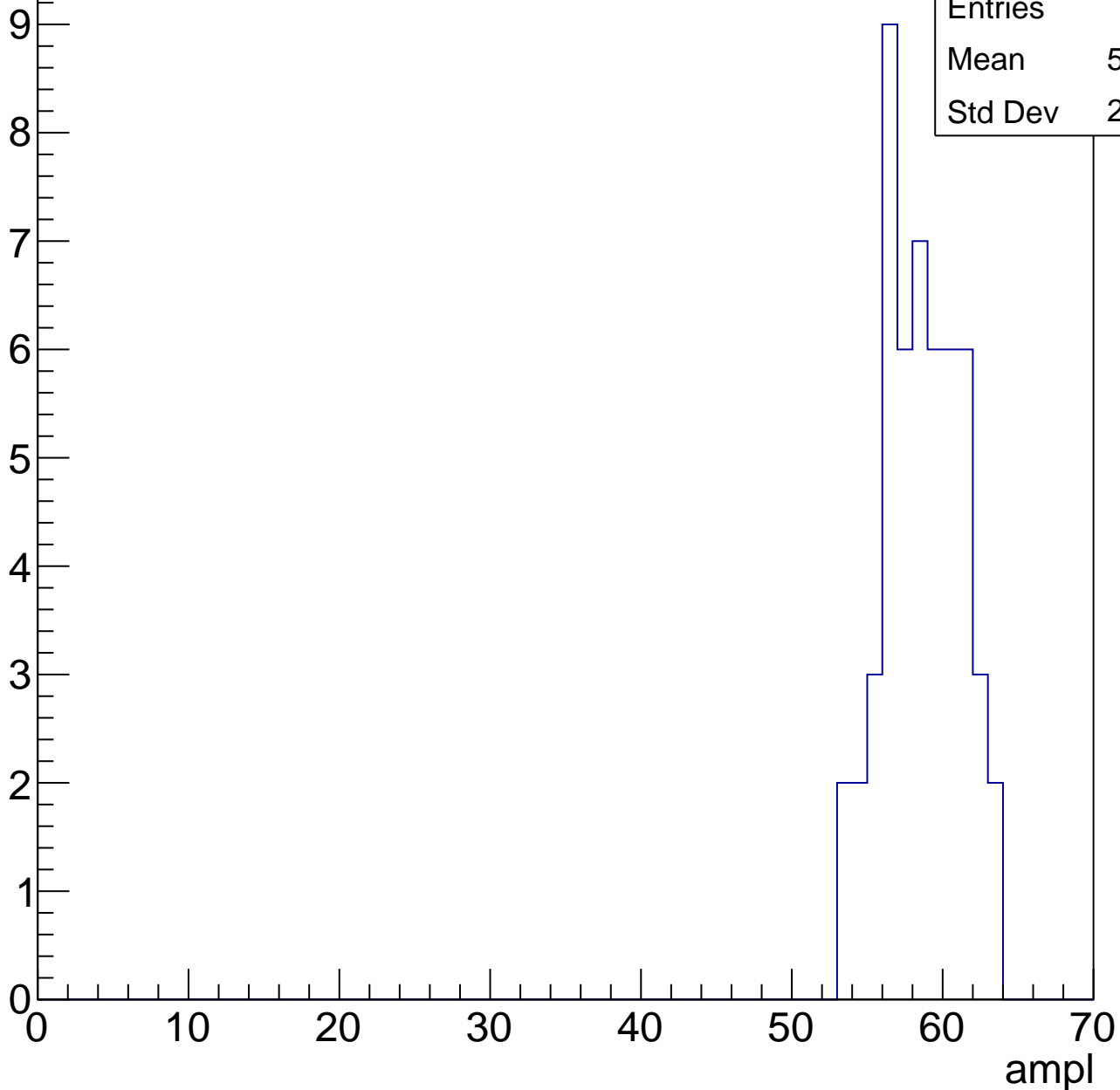


# B1L002S, U18-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.13
Std Dev	2.527

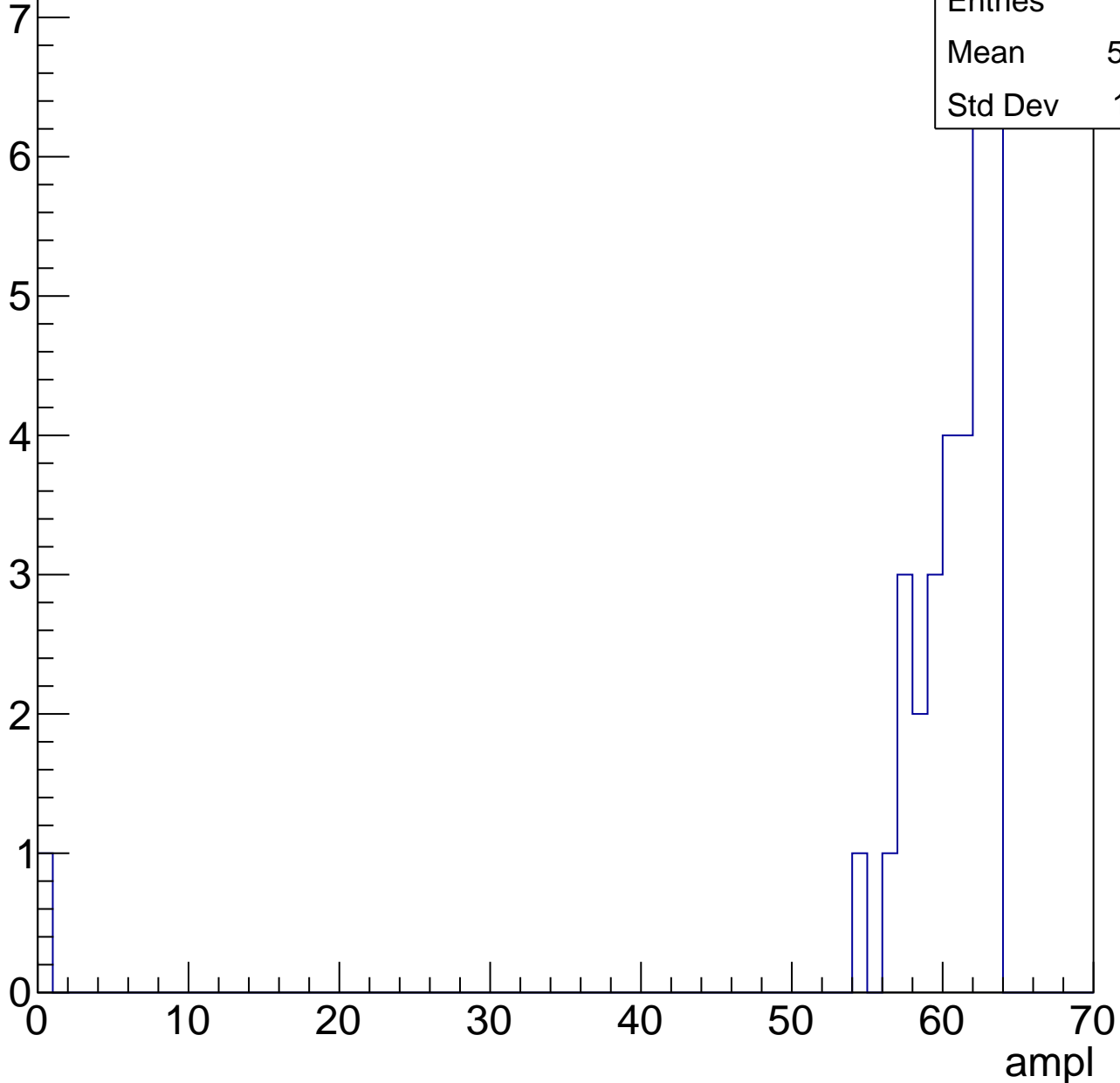


# B1L002S, U18-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	58.58
Std Dev	10.61

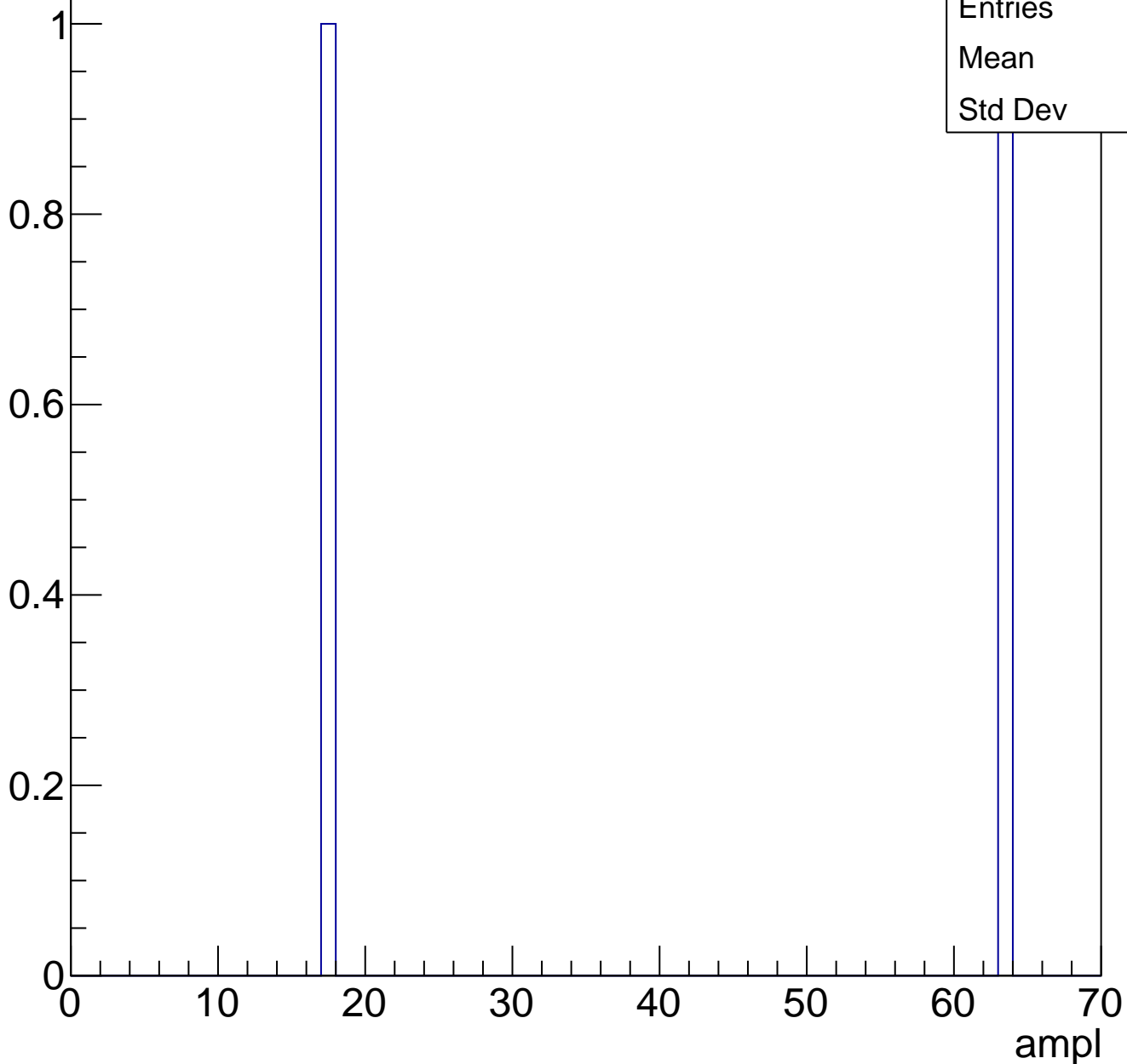




# B1L002S, U18-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	40
Std Dev	23

# B1L002S, U18-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	42
Mean	26.45
Std Dev	2.5

**Gaus mean : 27.4617**

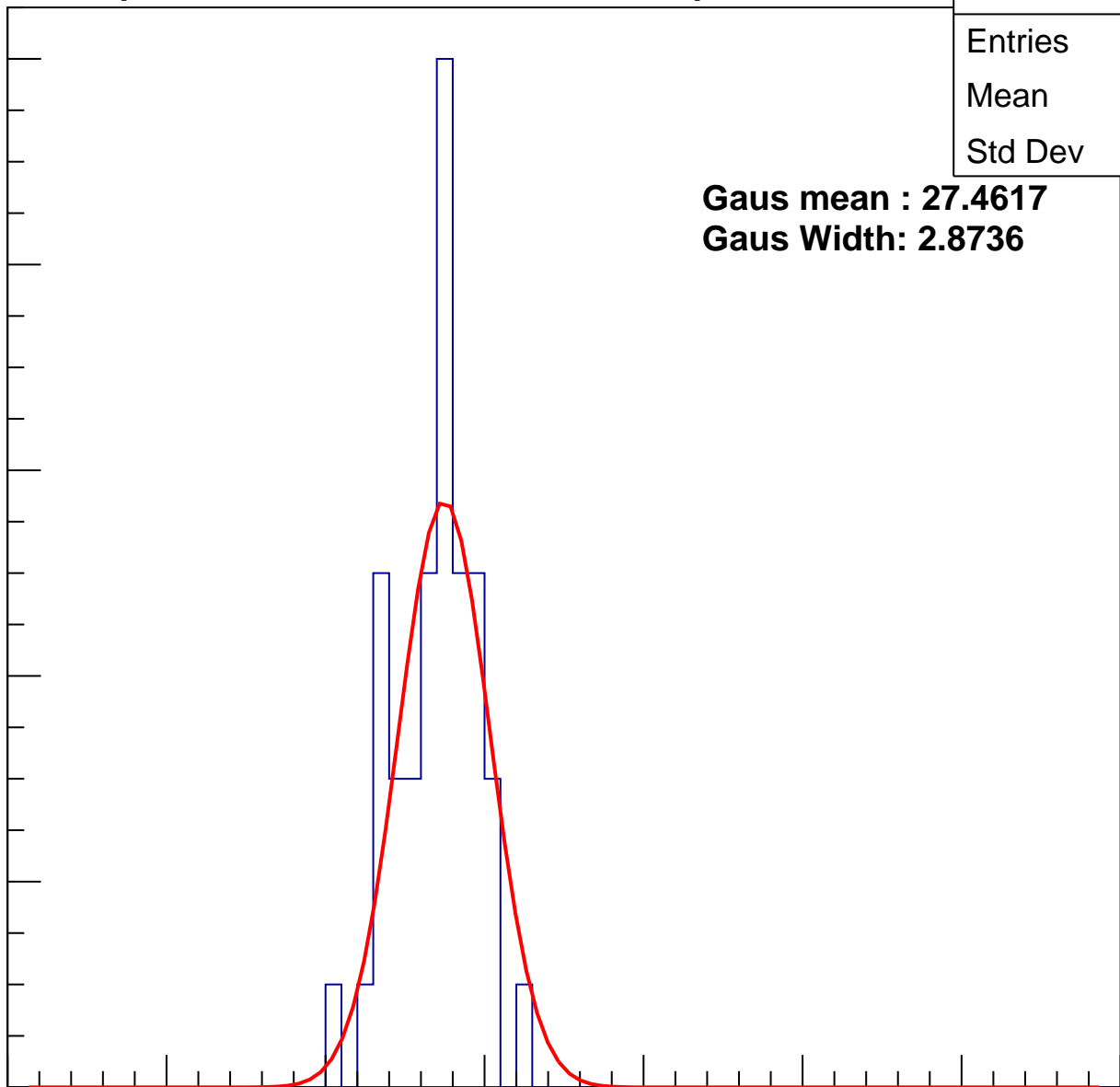
**Gaus Width: 2.8736**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch100, adc1

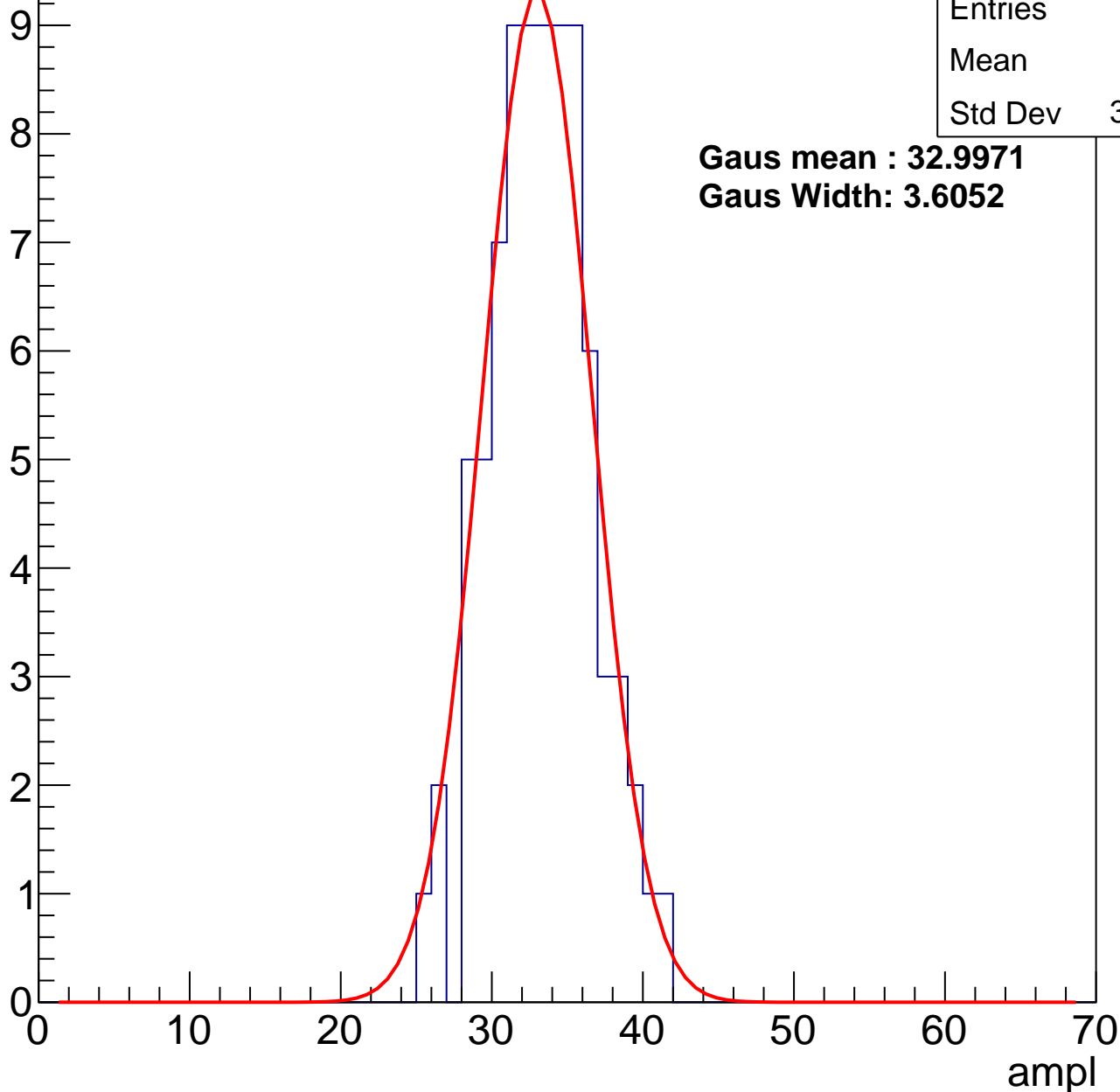
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	32.8
Std Dev	3.294

**Gaus mean : 32.9971**

**Gaus Width: 3.6052**



# B1L002S, U18-ch100, adc2

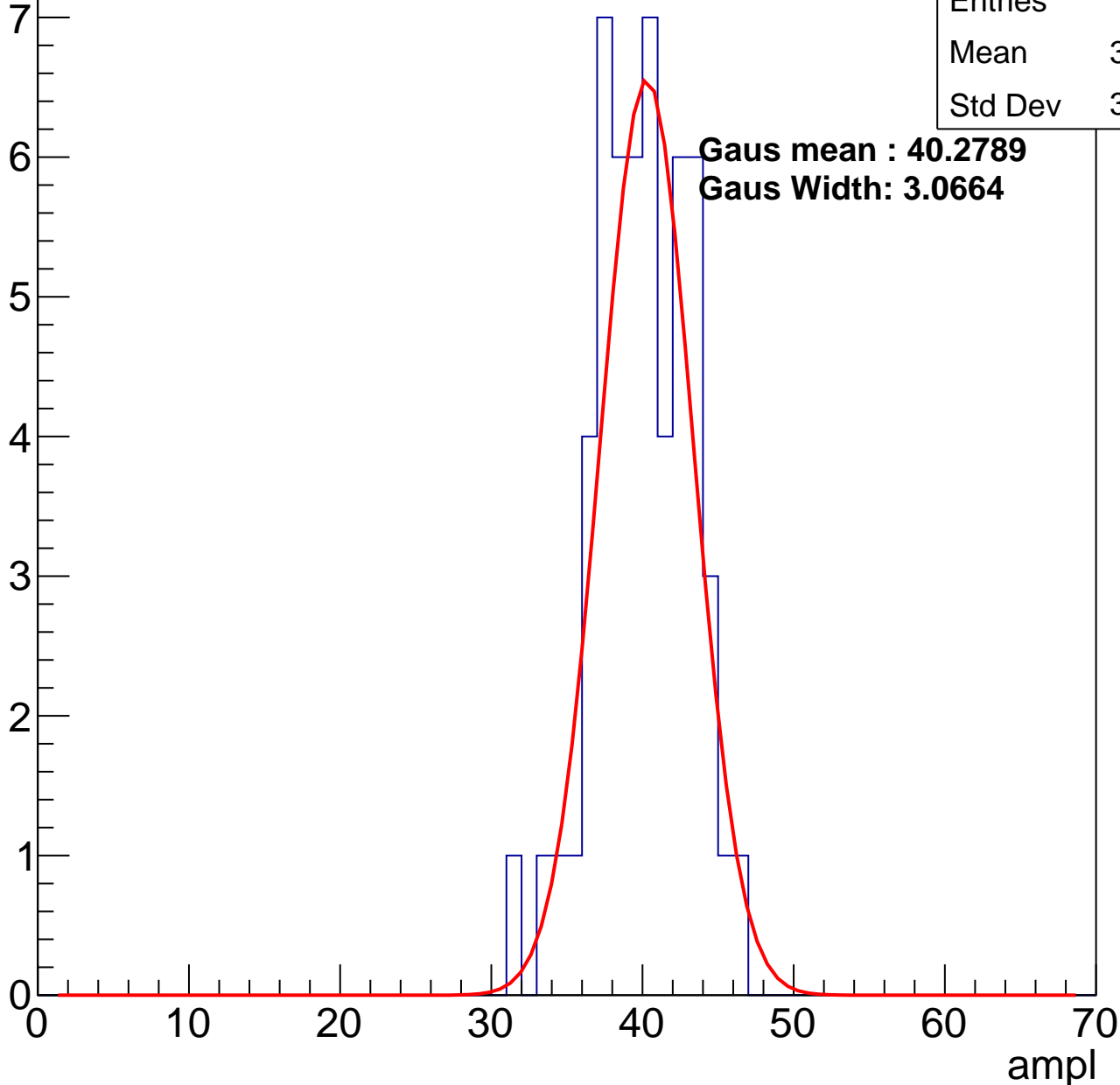
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	39.55
Std Dev	3.097

**Gaus mean : 40.2789**

**Gaus Width: 3.0664**

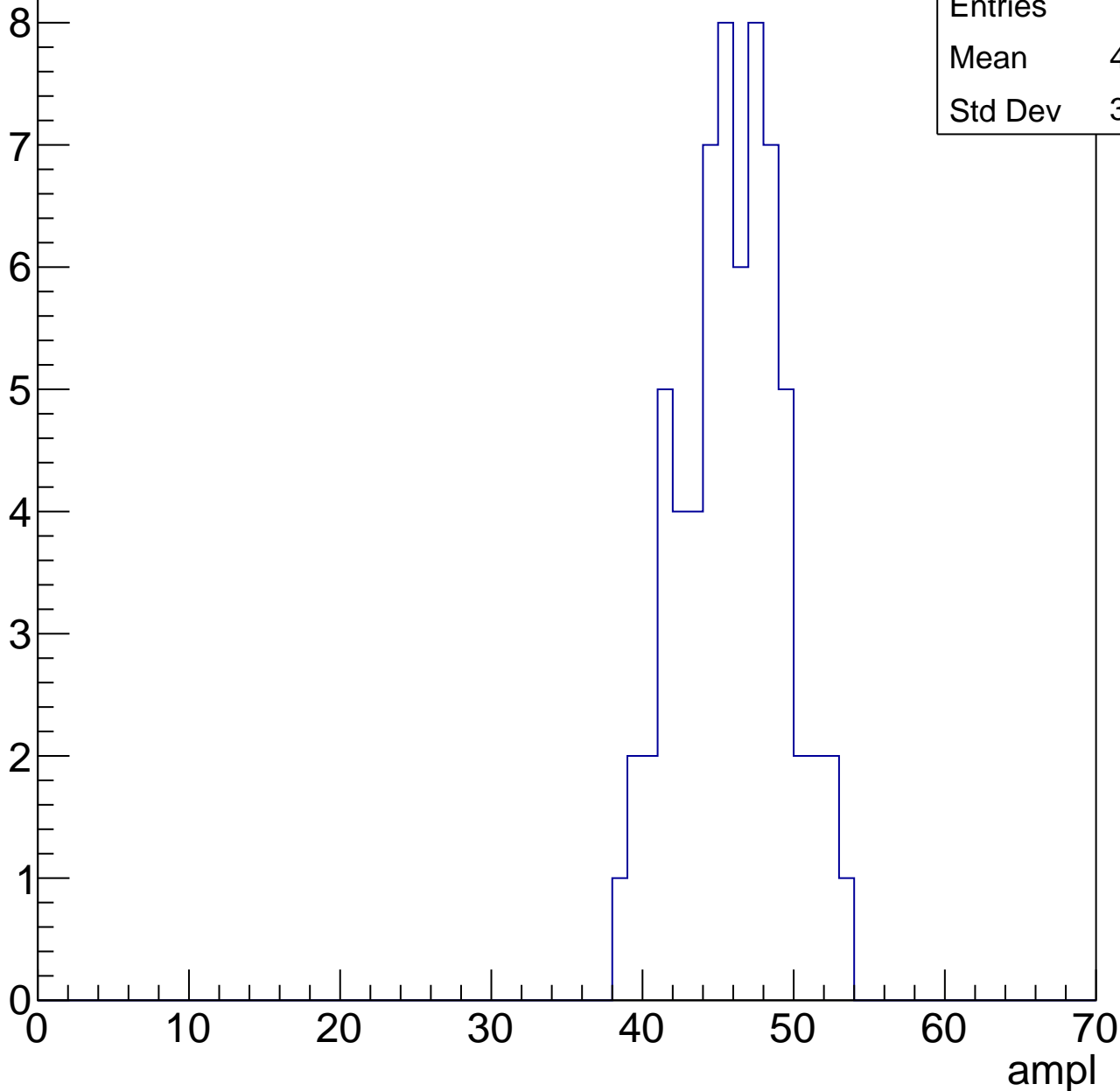


# B1L002S, U18-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	45.47
Std Dev	3.394

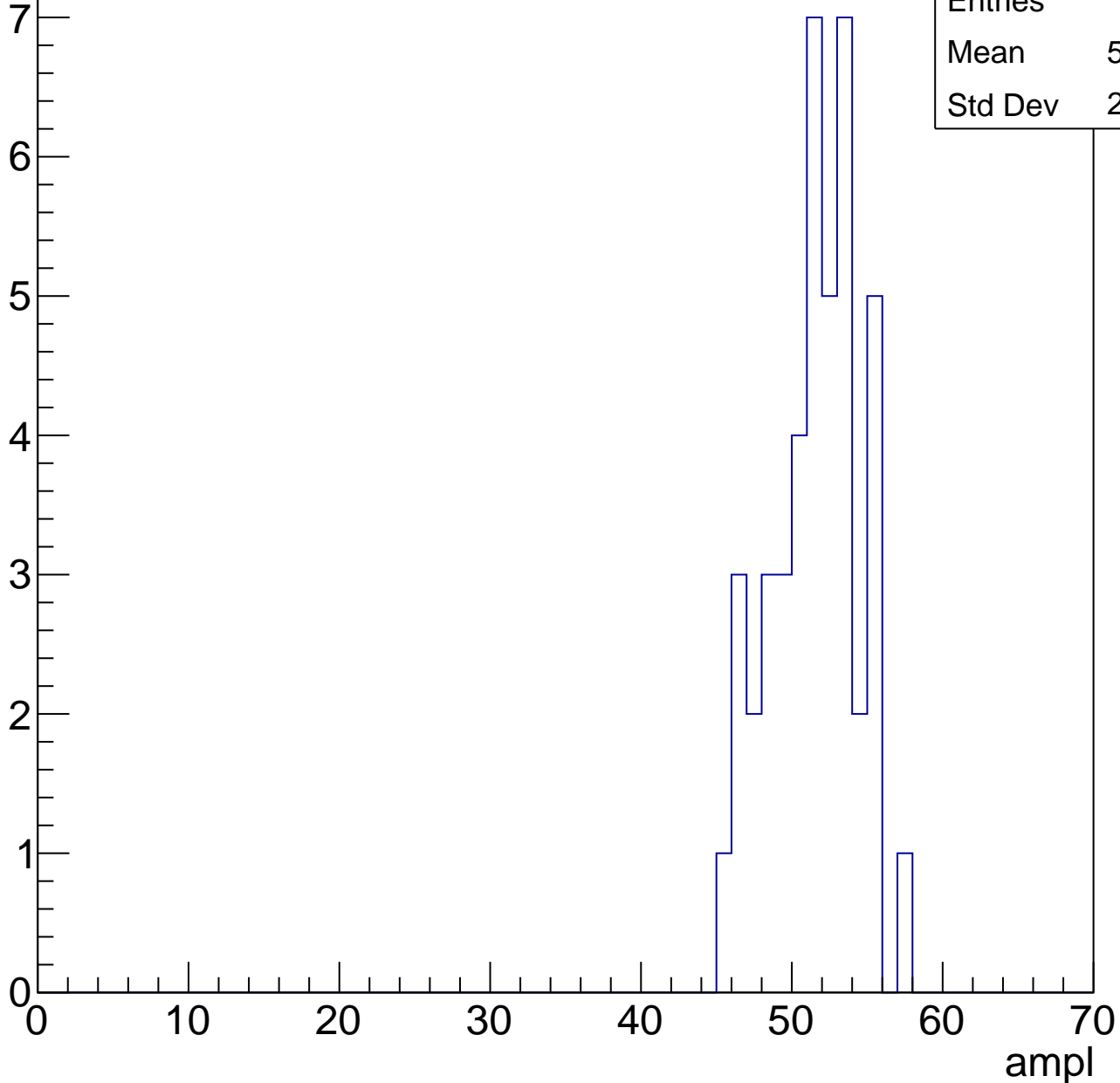


# B1L002S, U18-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

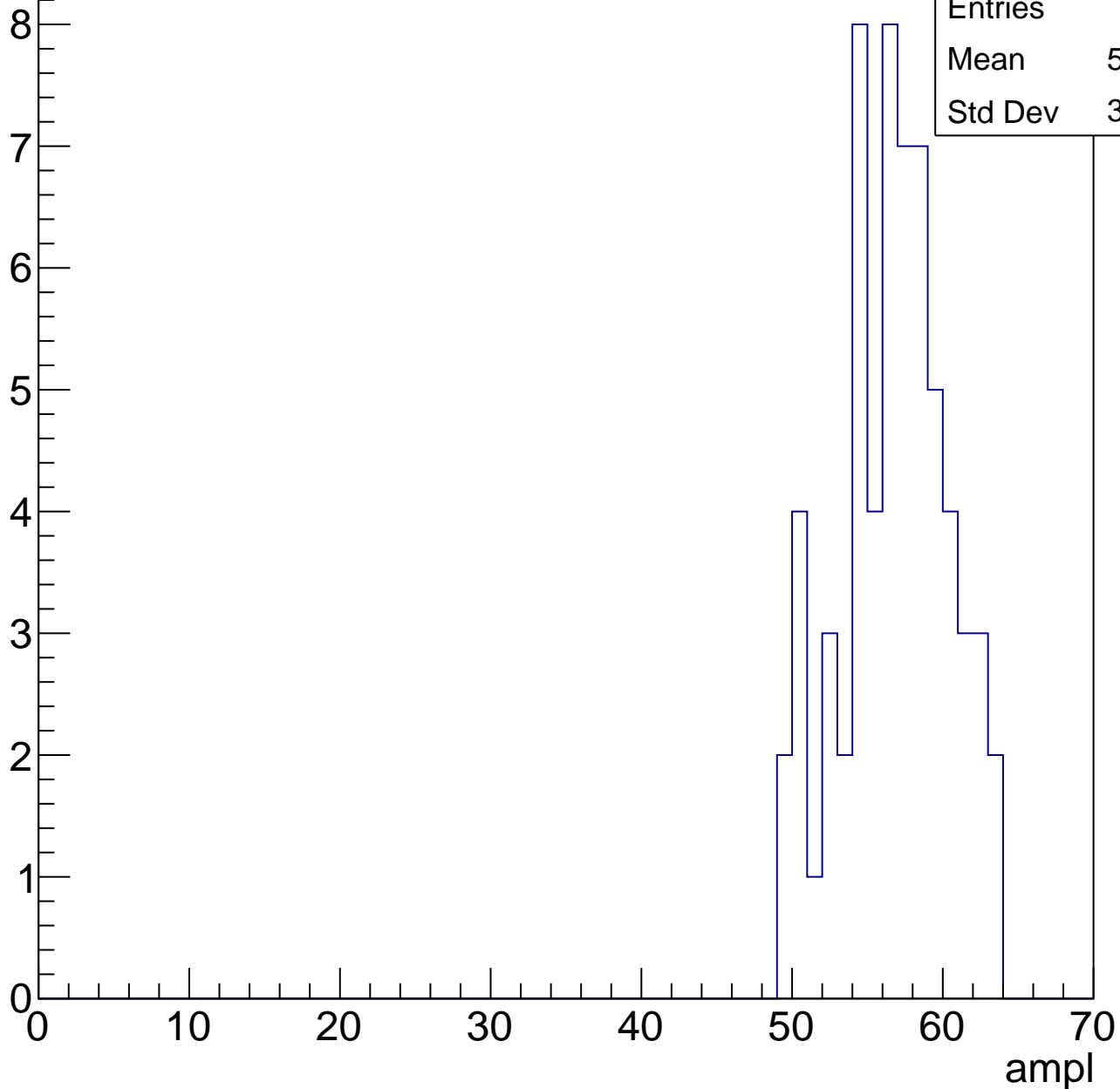
Entries	43
Mean	51.07
Std Dev	2.864



# B1L002S, U18-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

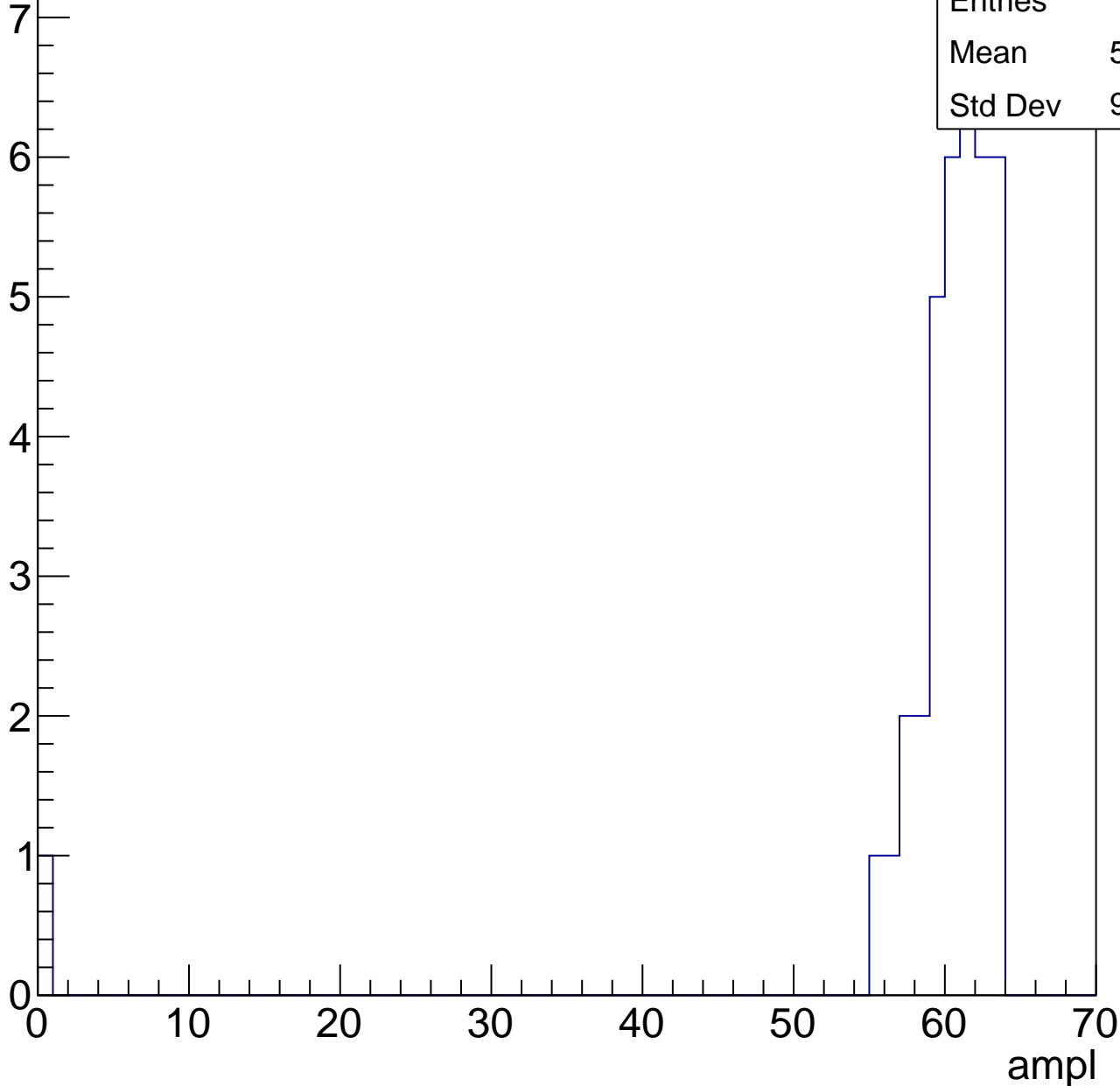


# B1L002S, U18-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	58.73
Std Dev	9.996





# B1L002S, U18-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

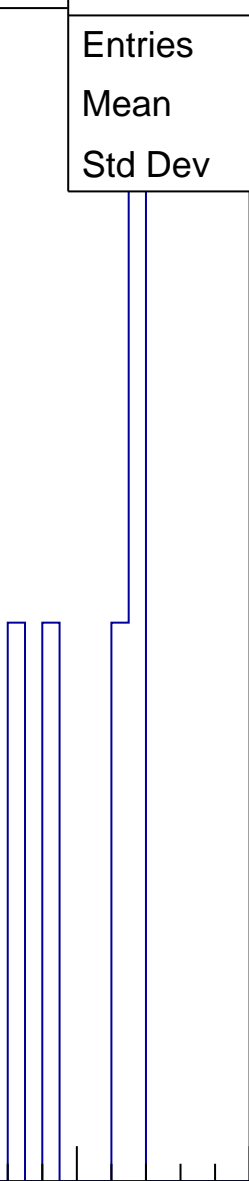
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	60.4
Std Dev	2.871

0 10 20 30 40 50 60 70

ampl



# B1L002S, U18-ch101, adc0

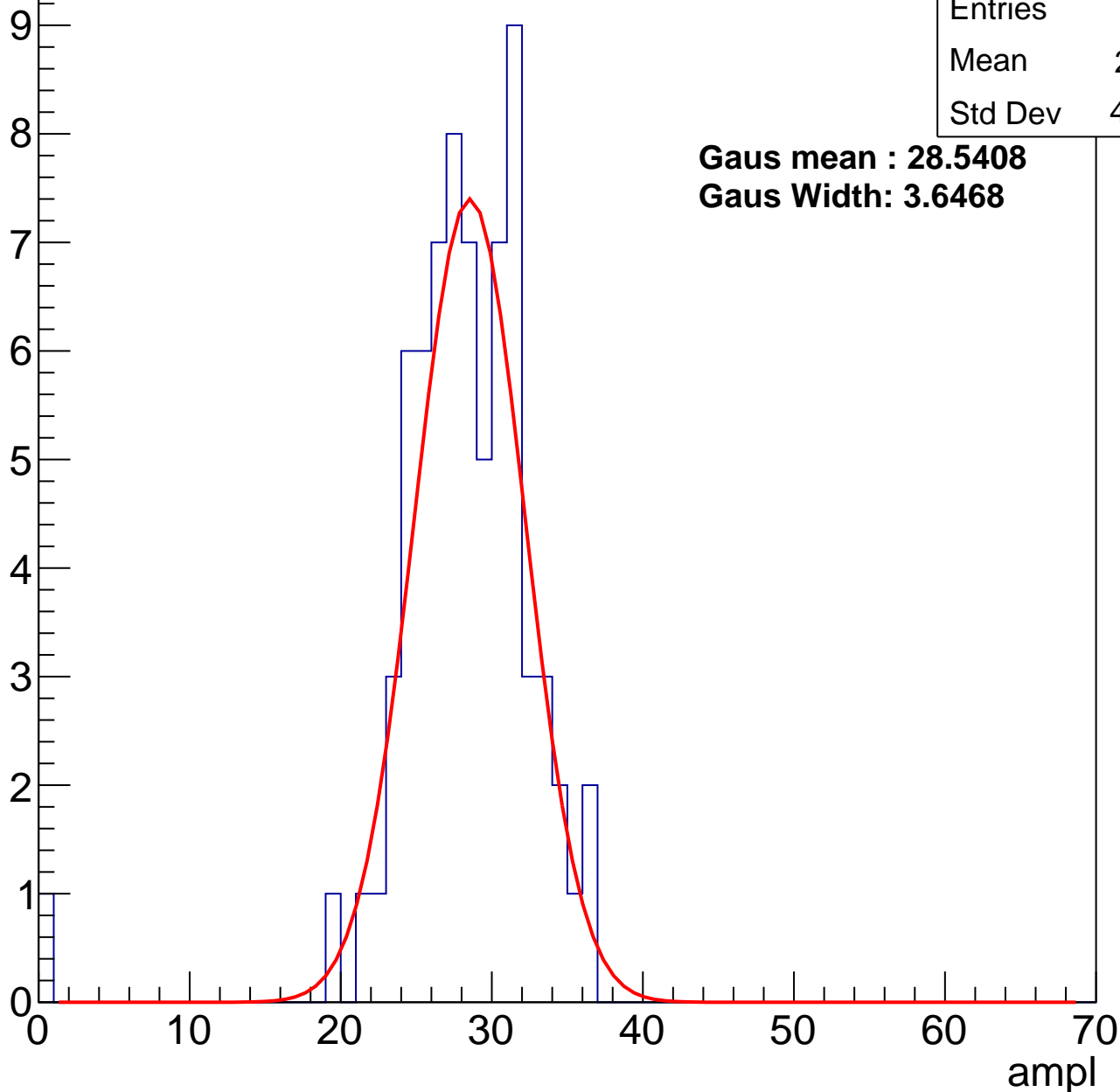
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	27.71
Std Dev	4.813

**Gaus mean : 28.5408**

**Gaus Width: 3.6468**



# B1L002S, U18-ch101, adc1

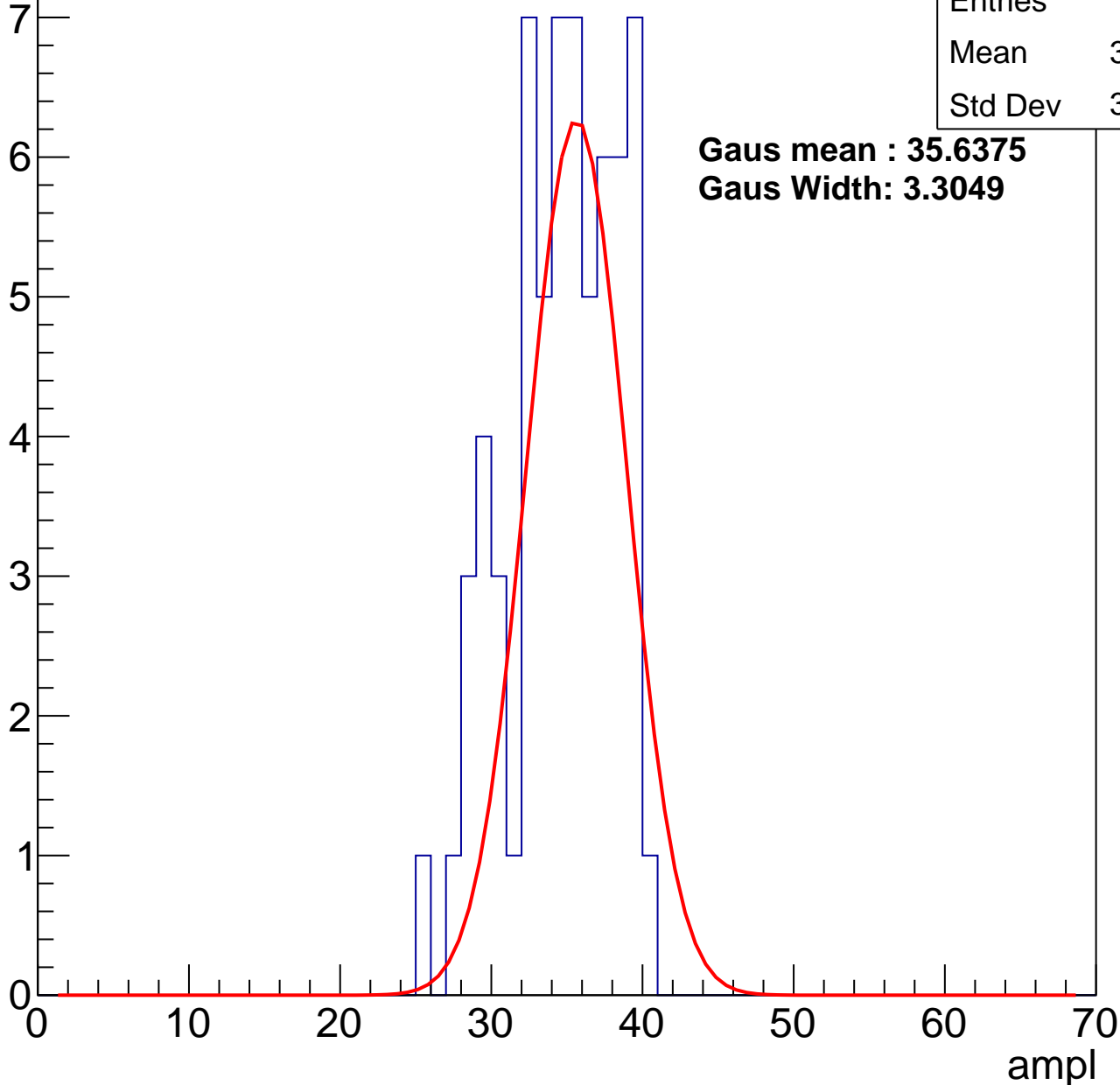
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.19
Std Dev	3.566

**Gaus mean : 35.6375**

**Gaus Width: 3.3049**



# B1L002S, U18-ch101, adc2

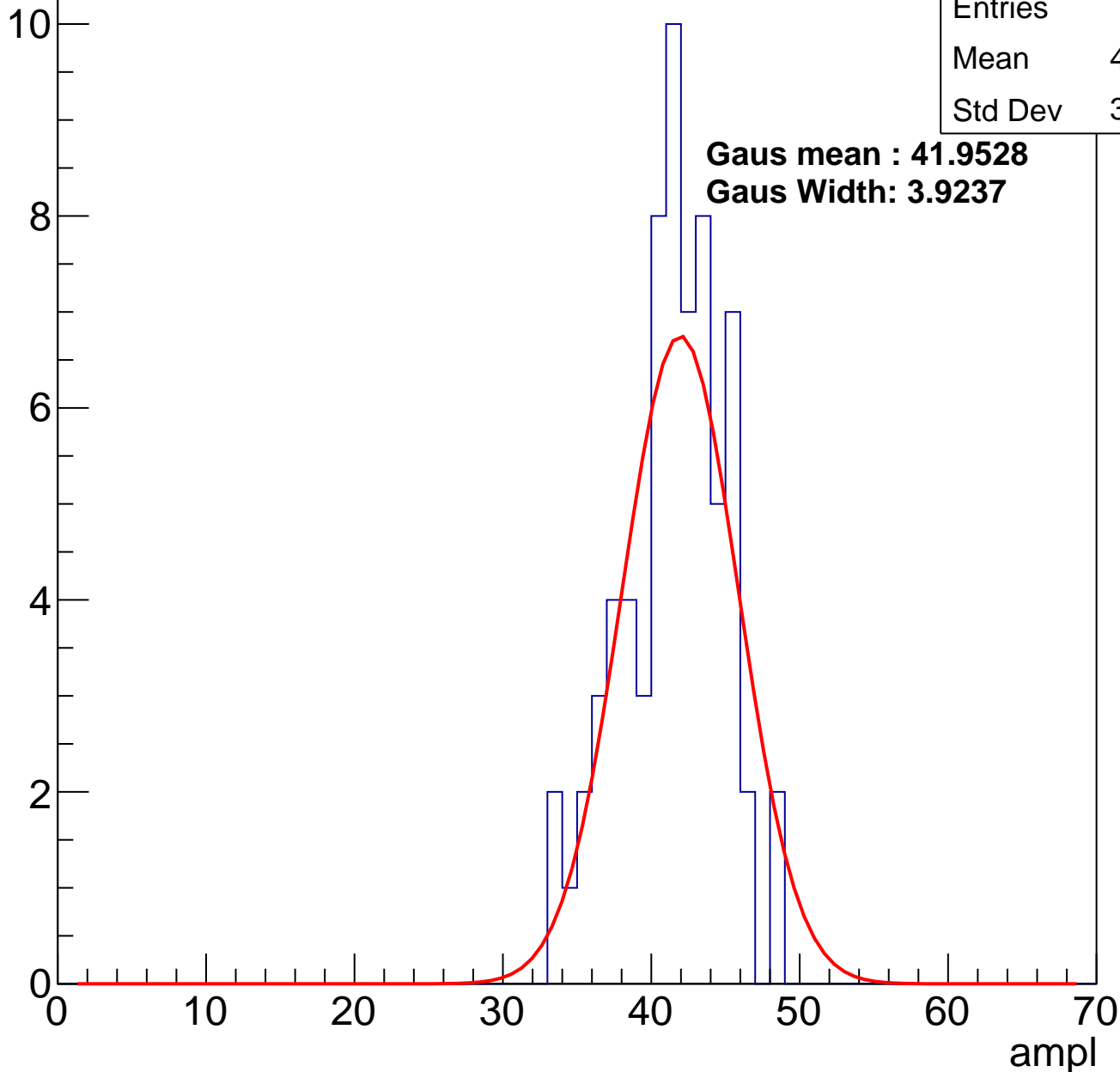
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	40.97
Std Dev	3.404

**Gaus mean : 41.9528**

**Gaus Width: 3.9237**

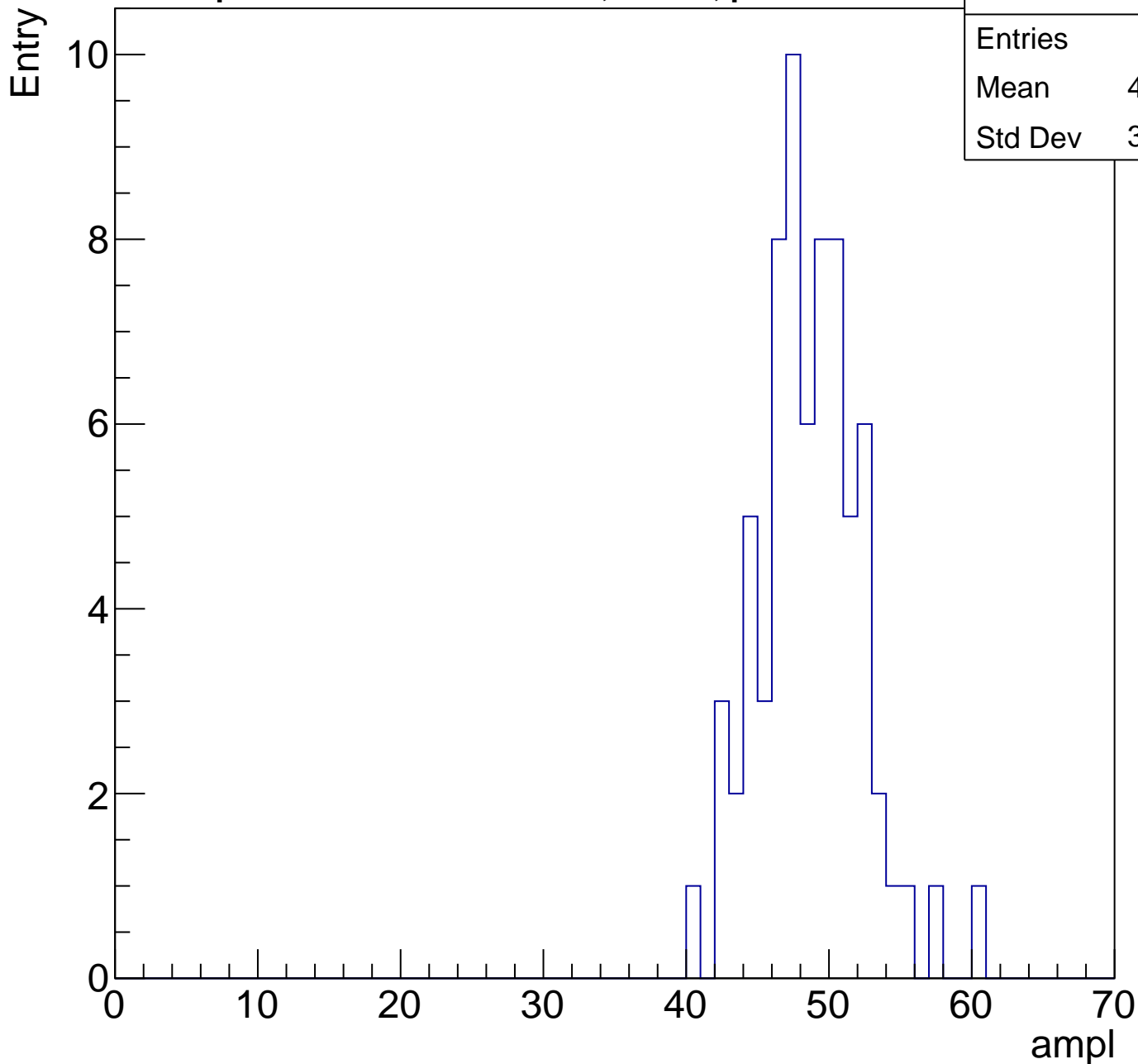
Entry



# B1L002S, U18-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	48.23
Std Dev	3.577

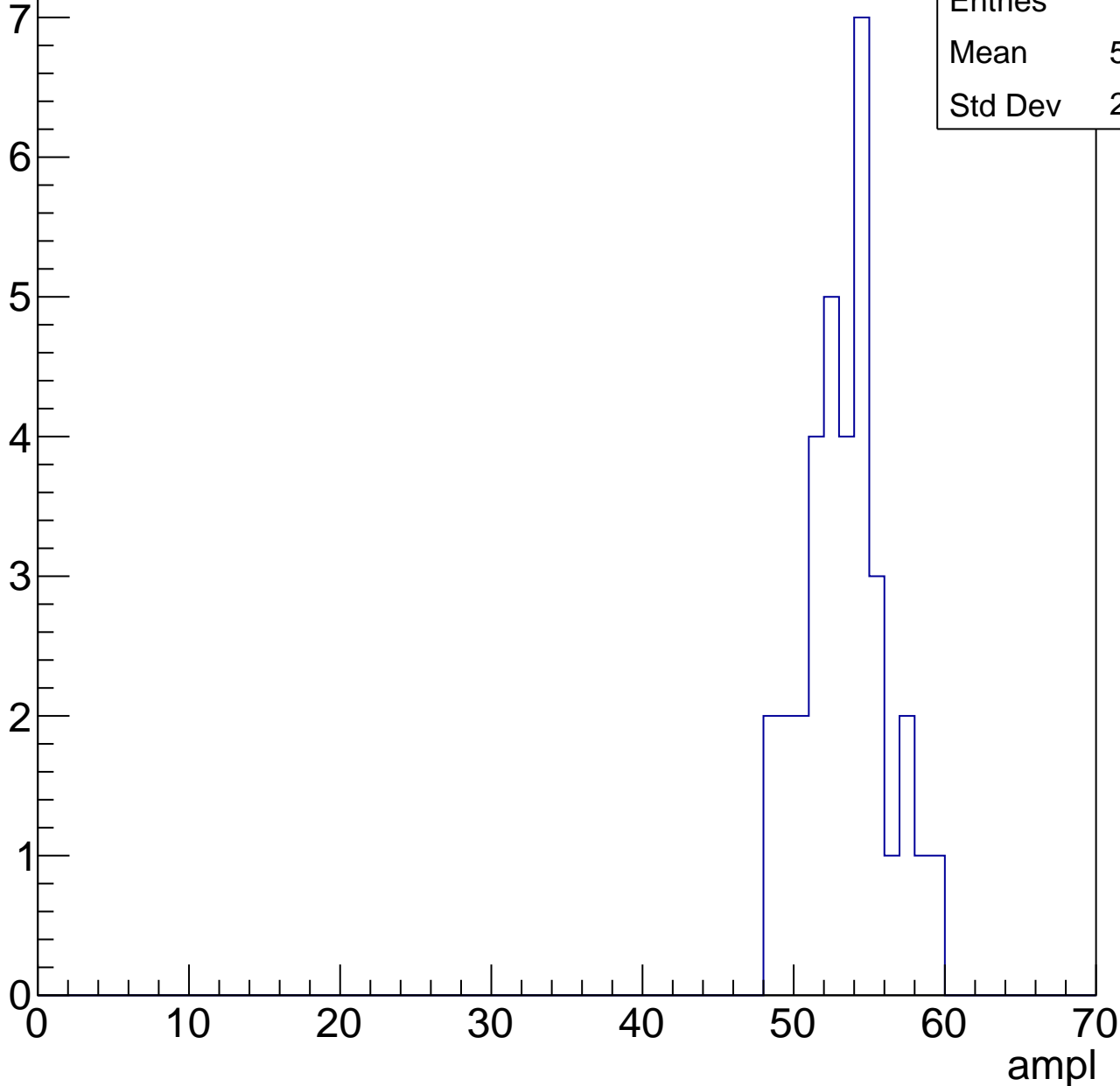


# B1L002S, U18-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	52.94
Std Dev	2.667

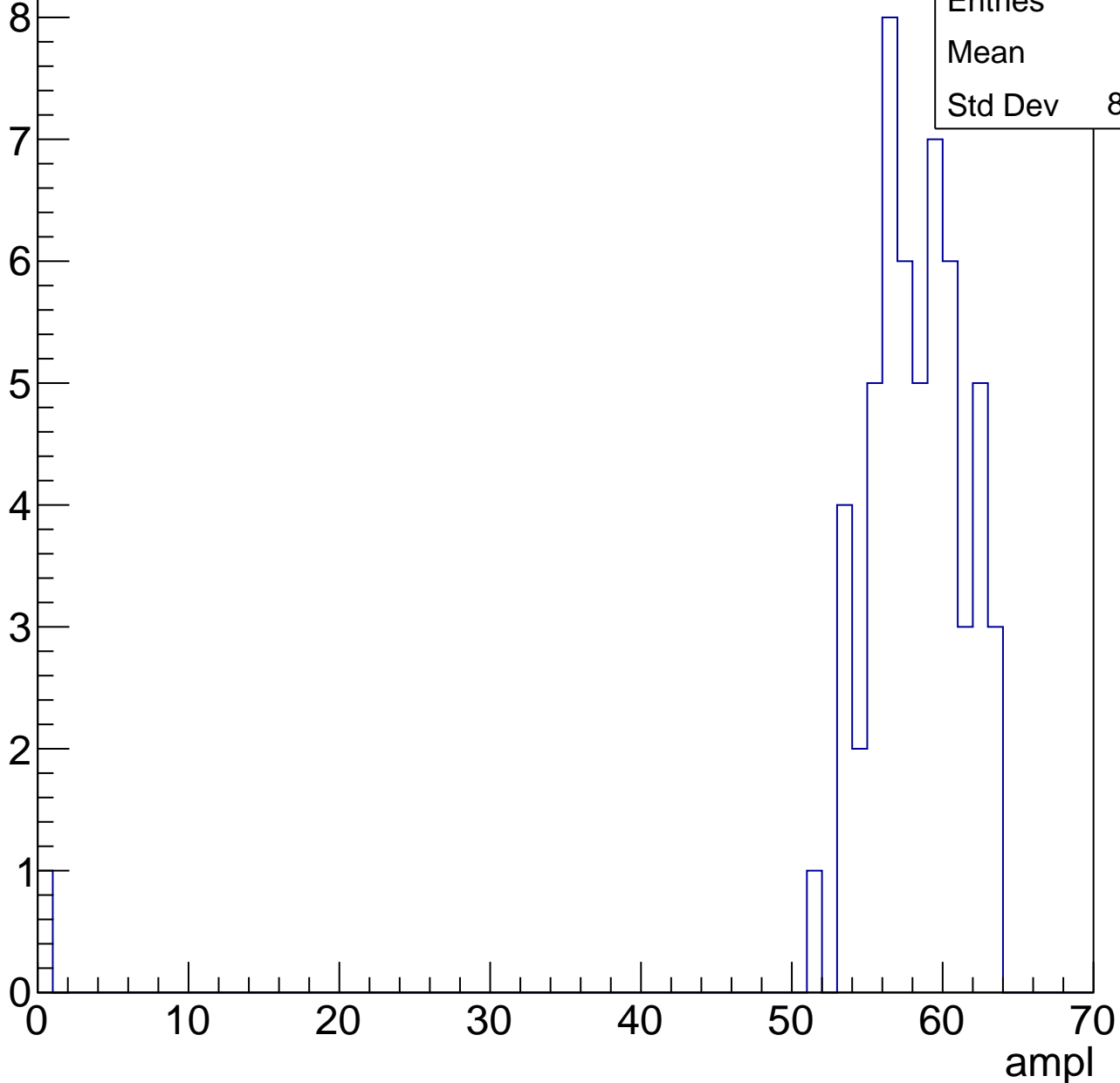


# B1L002S, U18-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.8
Std Dev	8.195

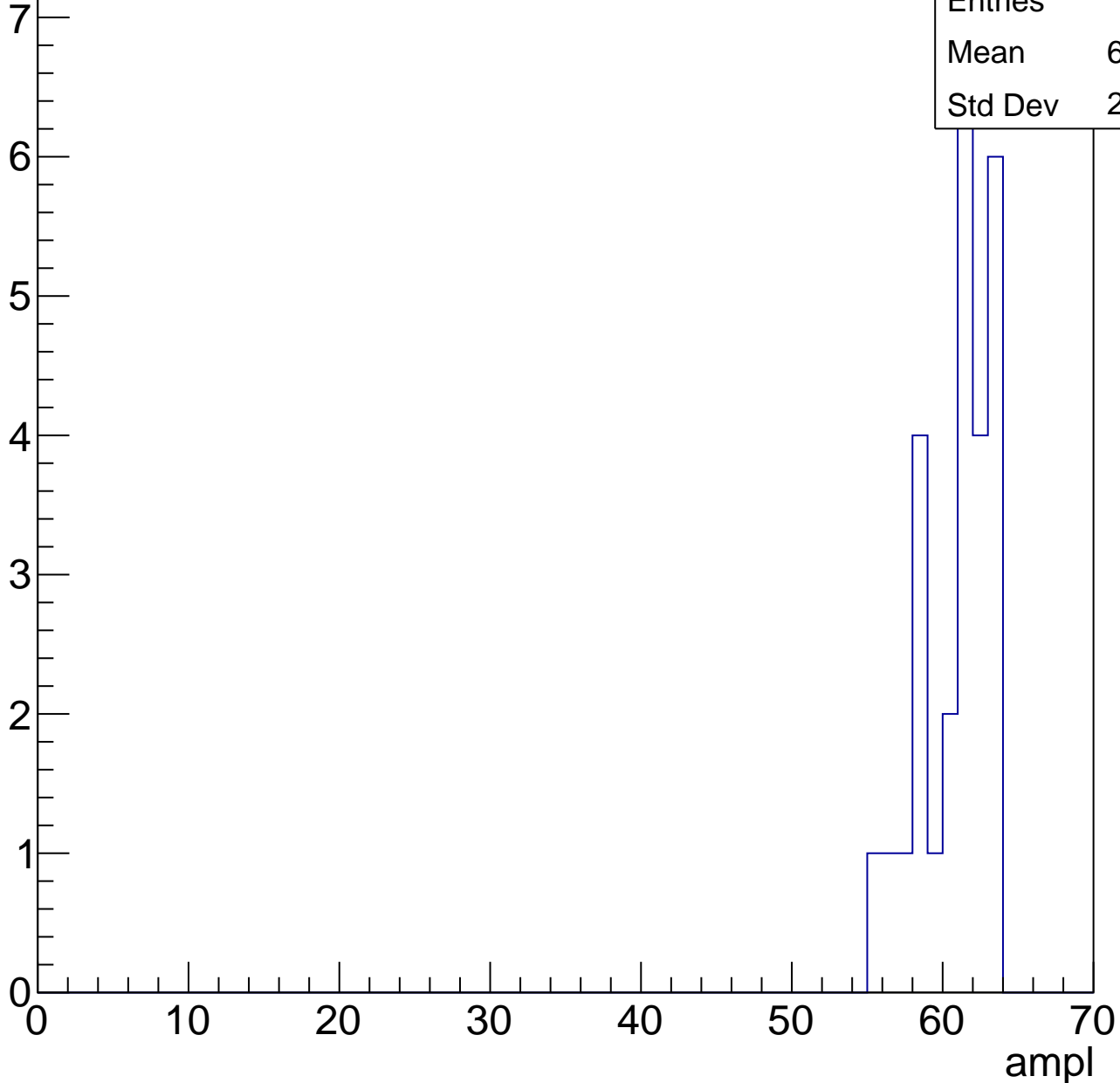


# B1L002S, U18-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.44
Std Dev	2.266

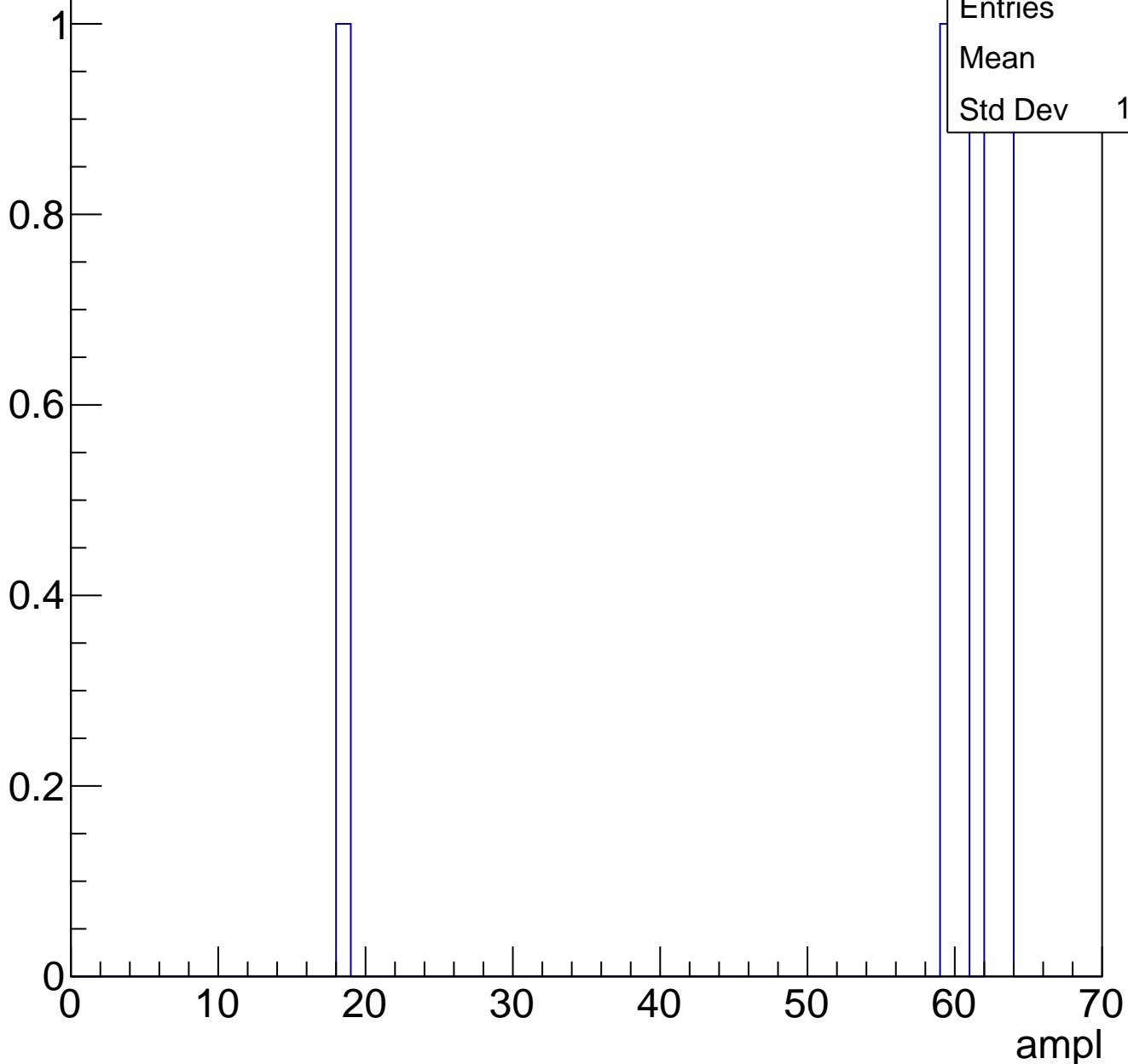




# B1L002S, U18-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	52.4
Std Dev	17.26

# B1L002S, U18-ch102, adc0

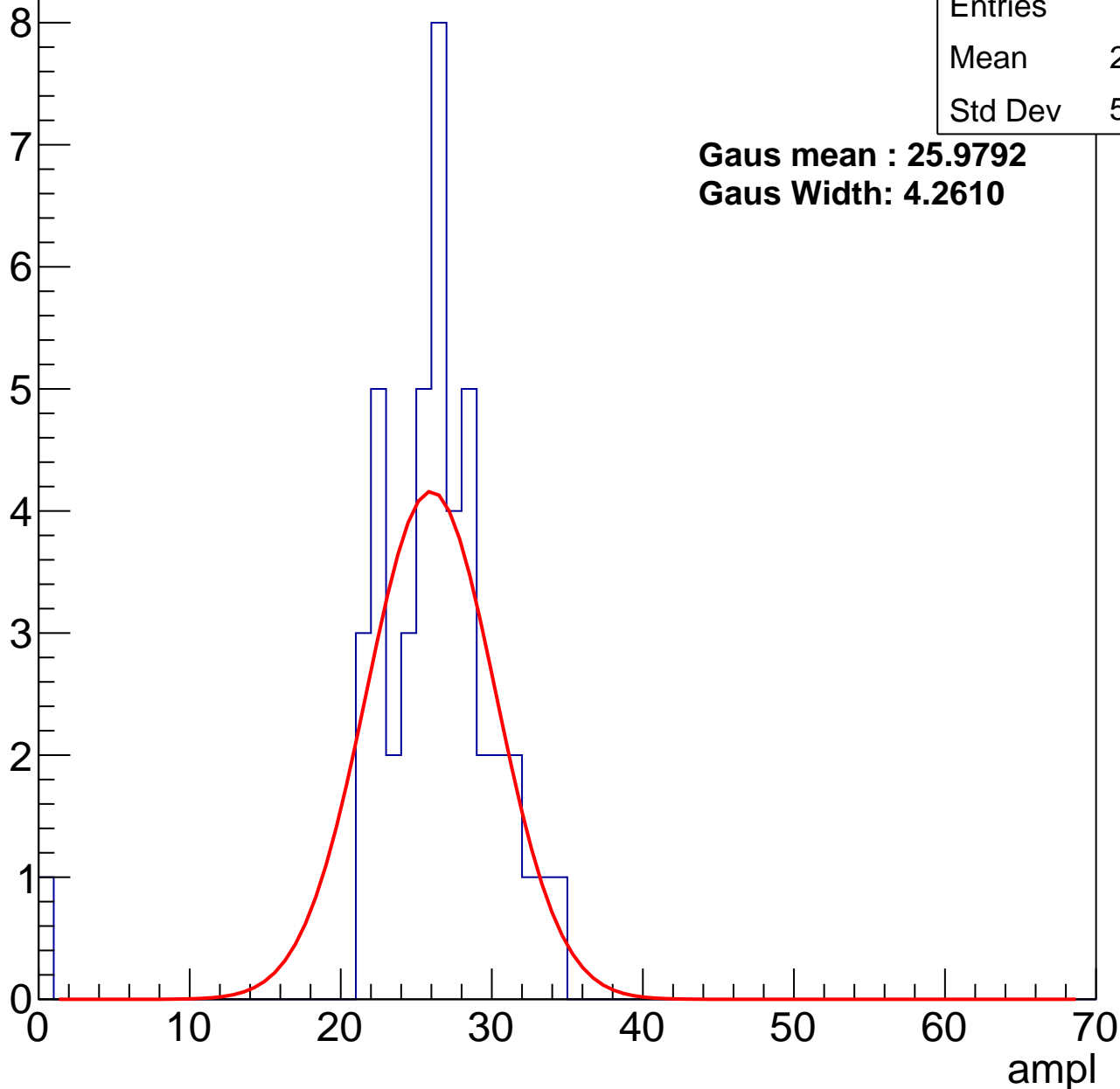
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	25.58
Std Dev	5.013

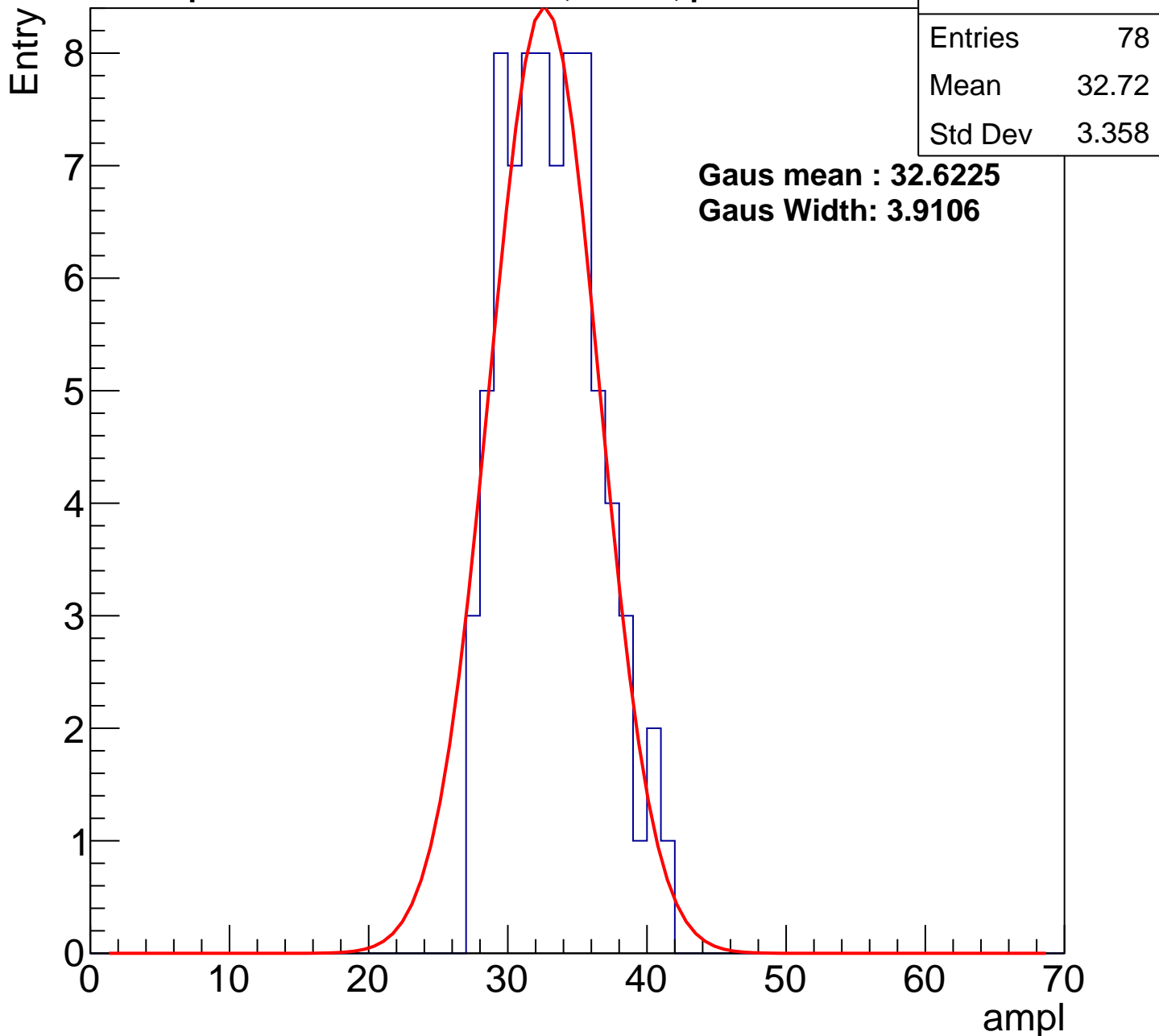
**Gaus mean : 25.9792**

**Gaus Width: 4.2610**



# B1L002S, U18-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U18-ch102, adc2

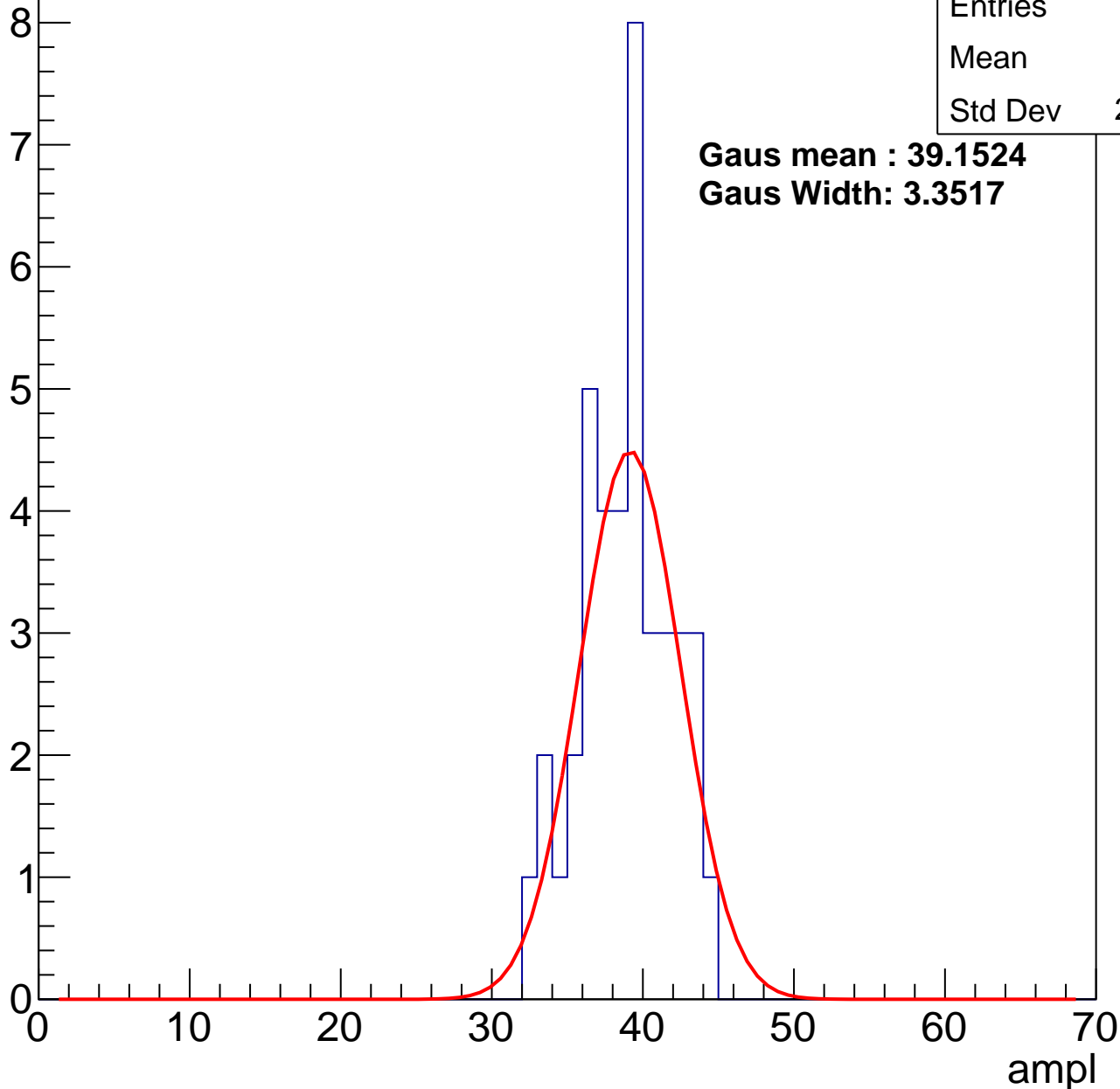
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	38.4
Std Dev	2.931

**Gaus mean : 39.1524**

**Gaus Width: 3.3517**

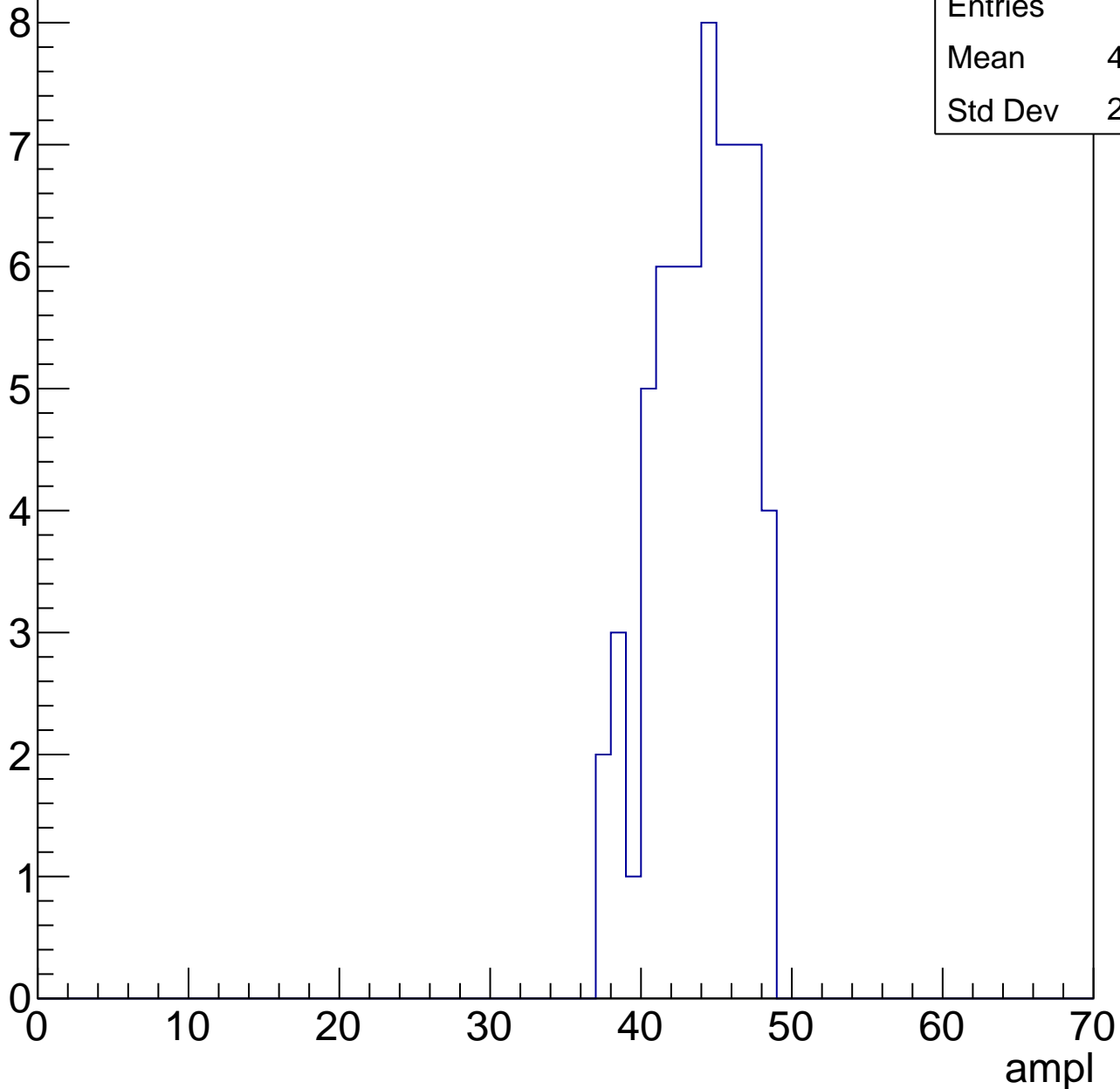


# B1L002S, U18-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	43.44
Std Dev	2.944

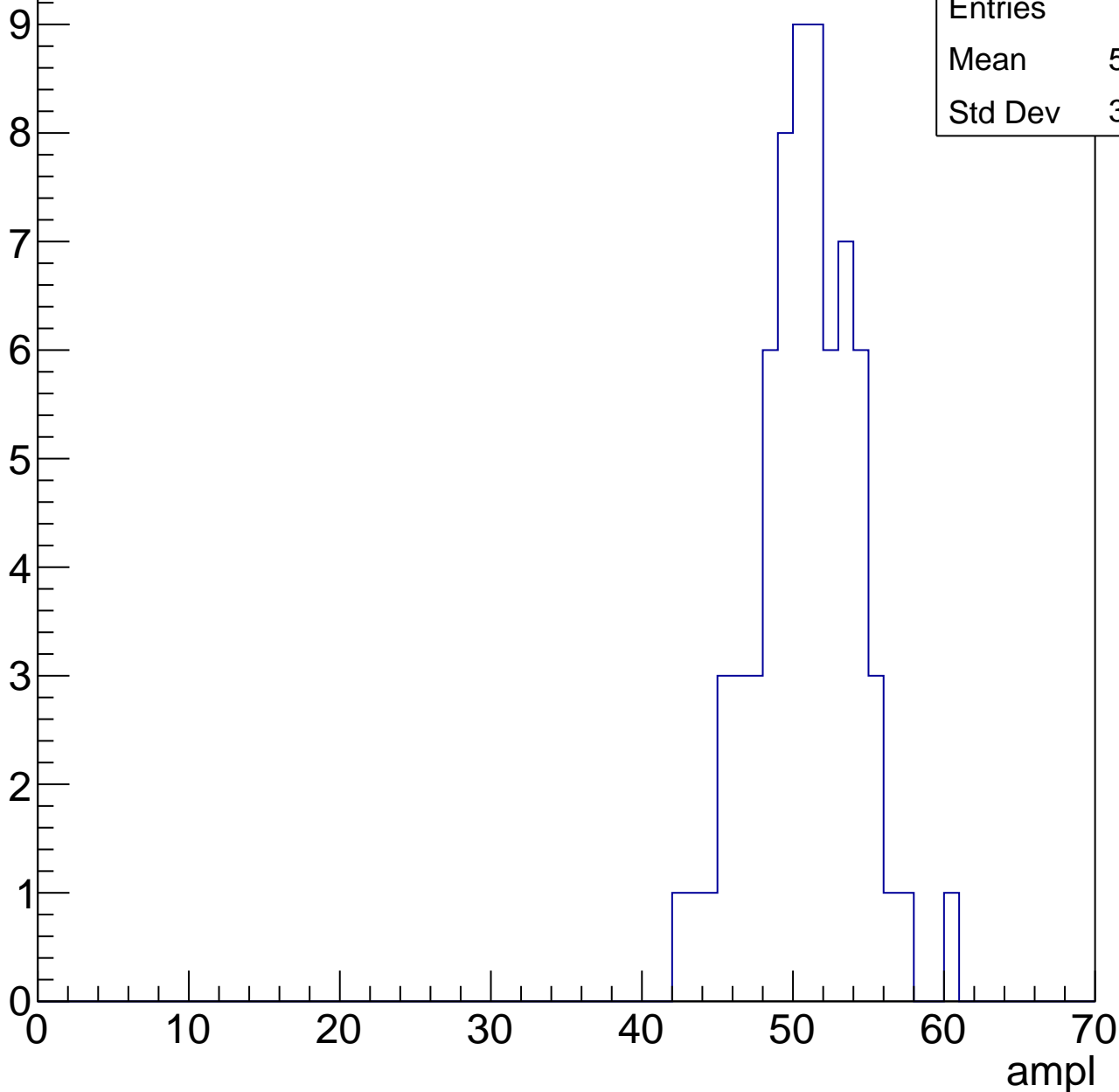


# B1L002S, U18-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	50.39
Std Dev	3.346

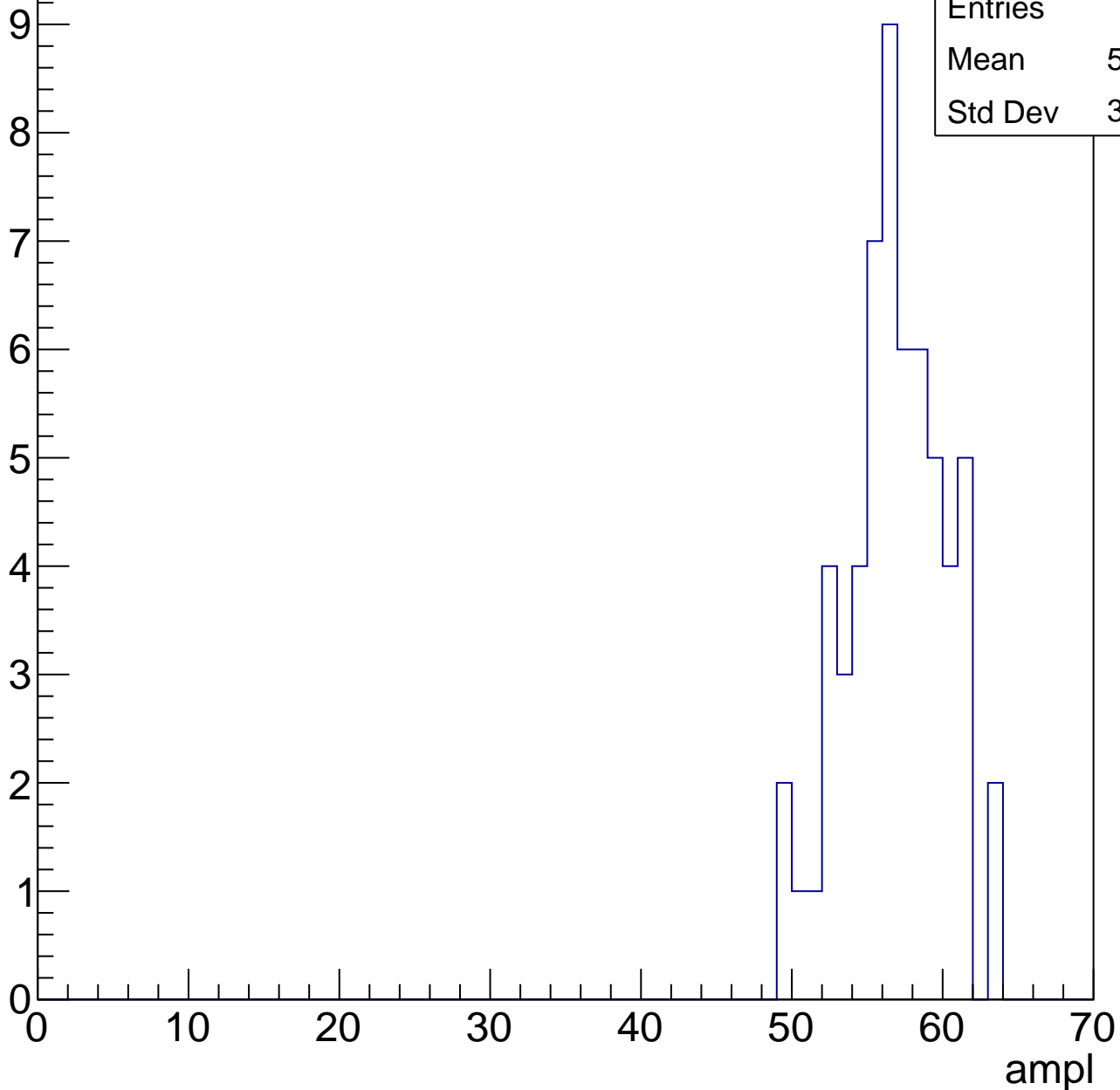


# B1L002S, U18-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	56.39
Std Dev	3.257

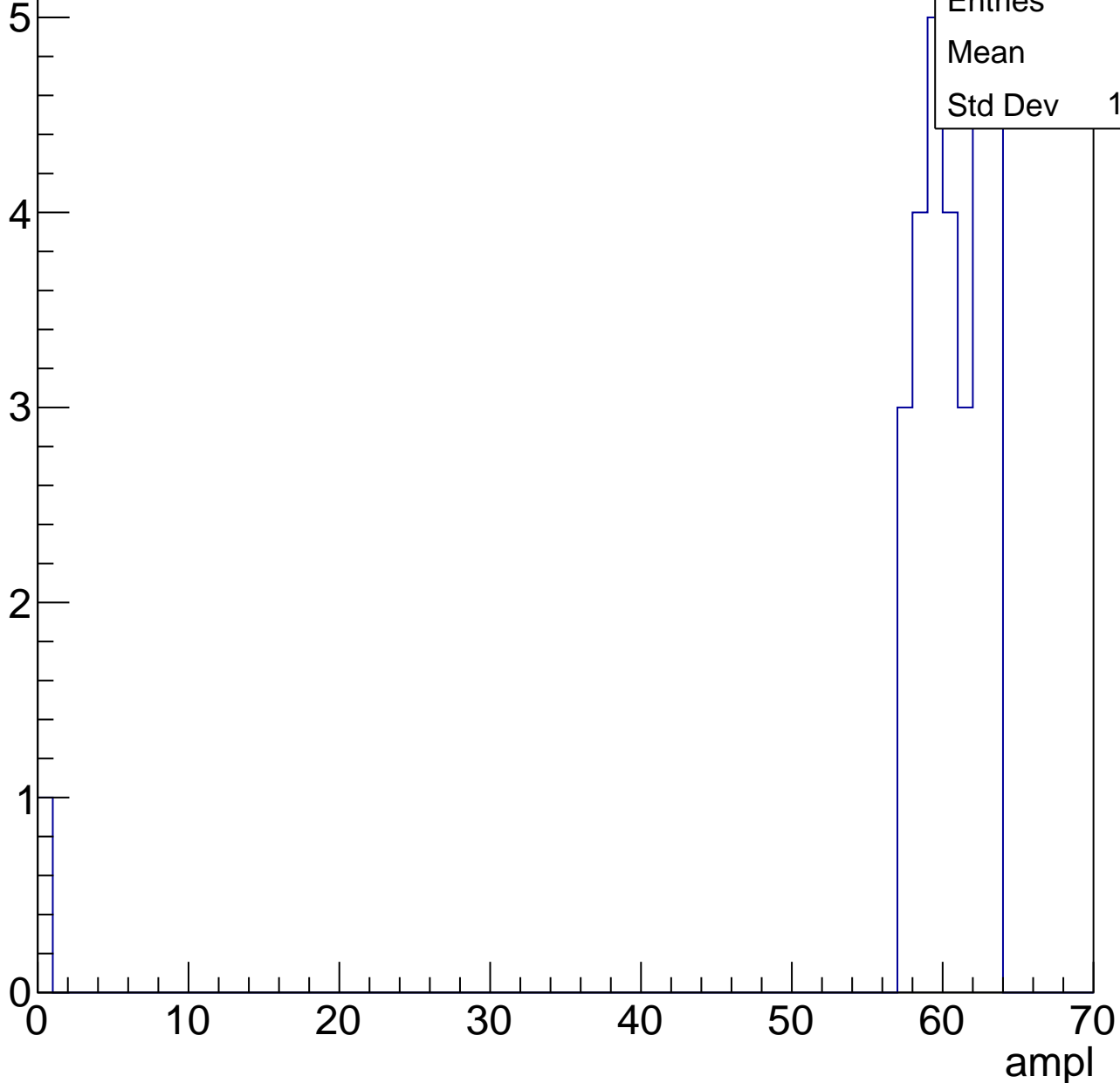


# B1L002S, U18-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.2
Std Dev	10.98



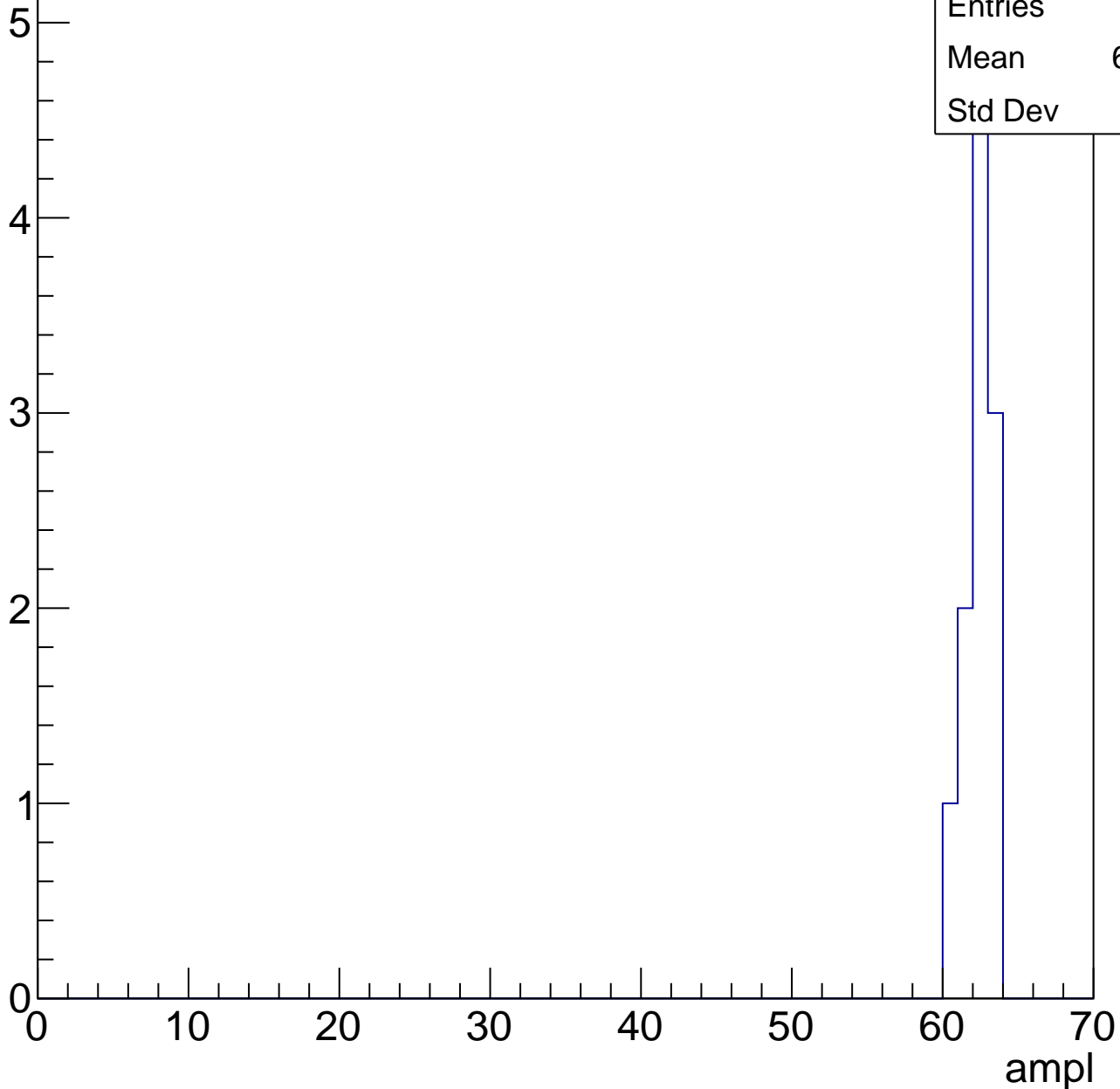


# B1L002S, U18-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	61.91
Std Dev	0.9



# B1L002S, U18-ch103, adc0

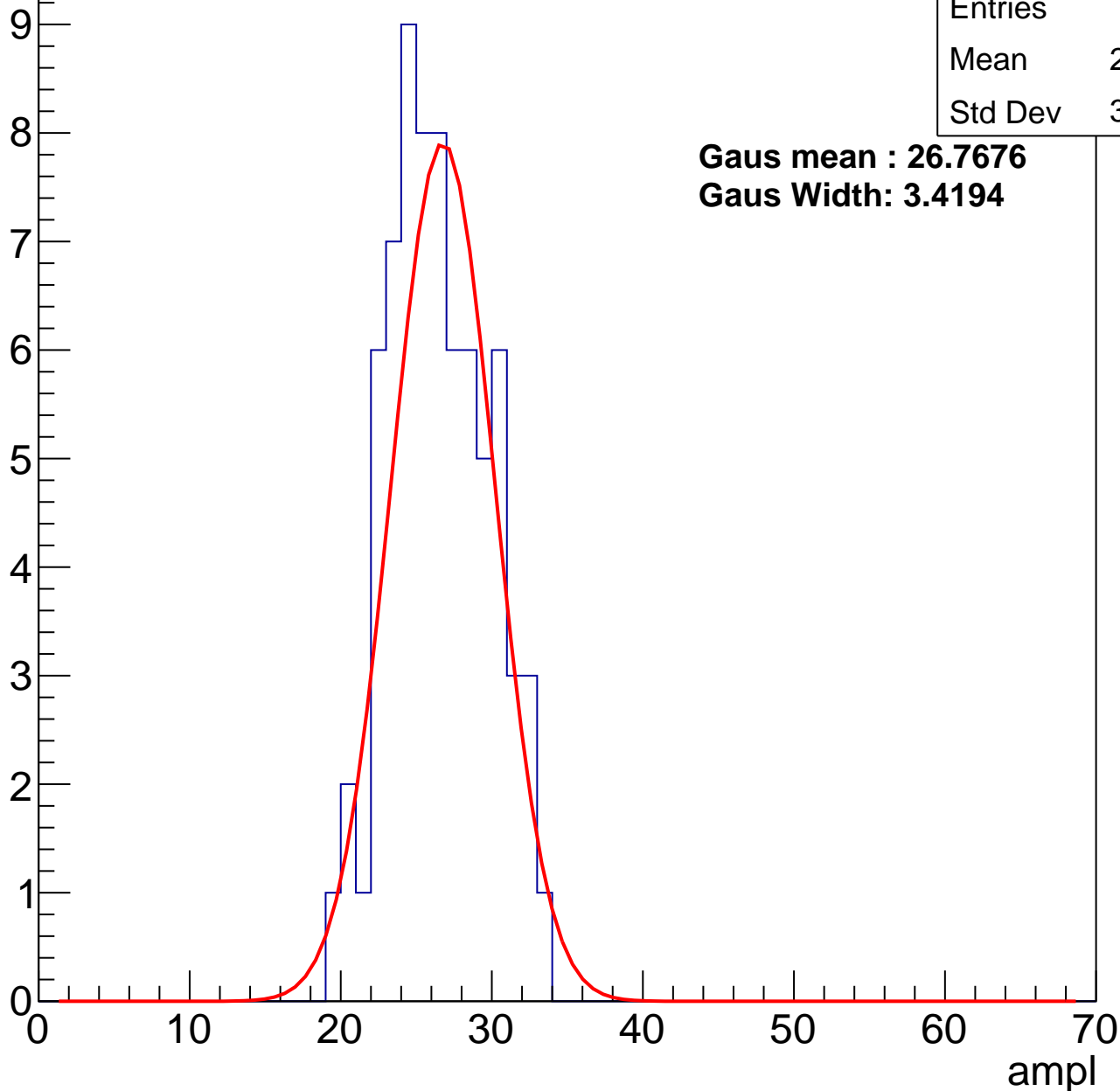
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	26.03
Std Dev	3.232

**Gaus mean : 26.7676**

**Gaus Width: 3.4194**



# B1L002S, U18-ch103, adc1

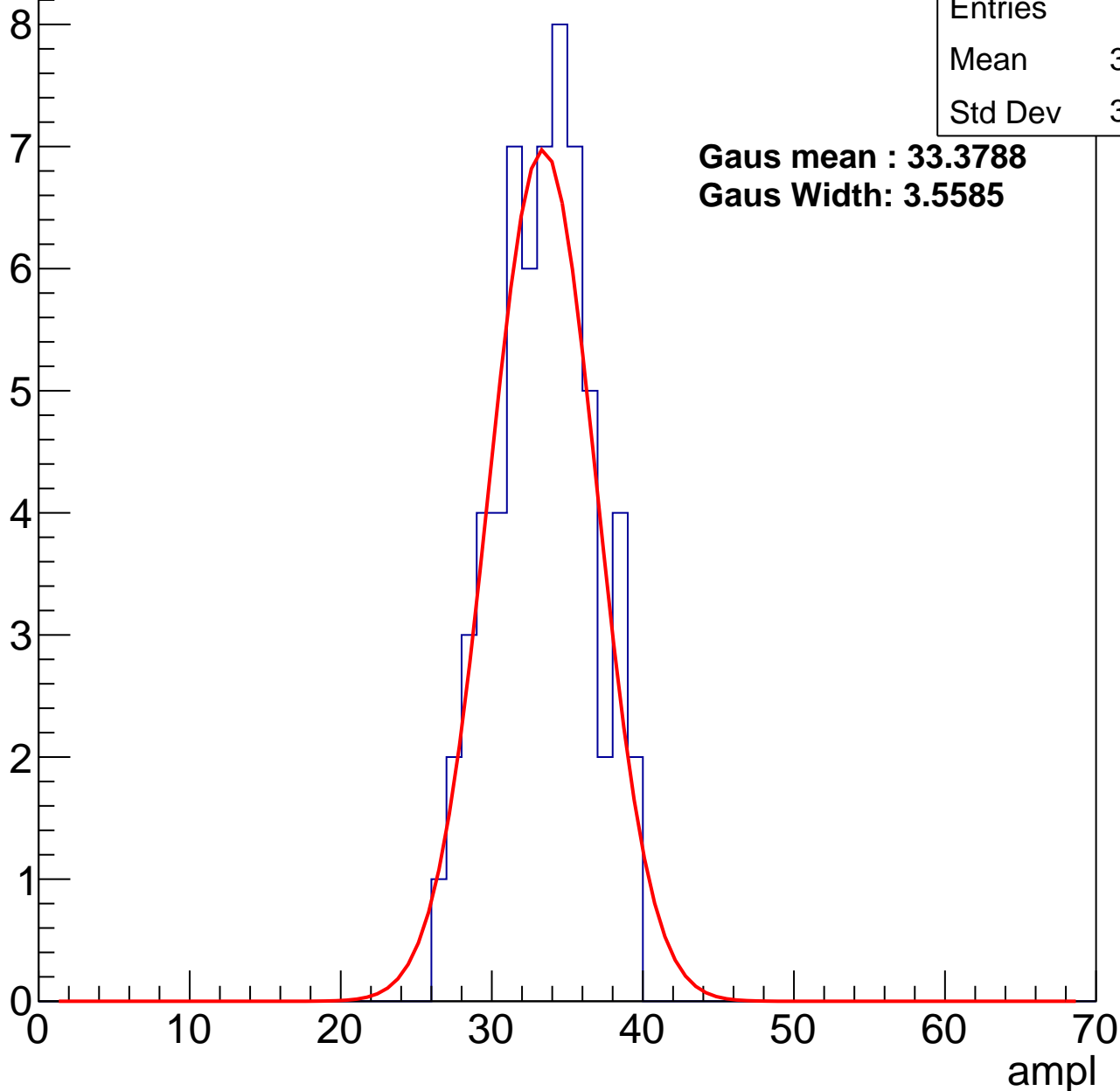
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	32.92
Std Dev	3.148

**Gaus mean : 33.3788**

**Gaus Width: 3.5585**



# B1L002S, U18-ch103, adc2

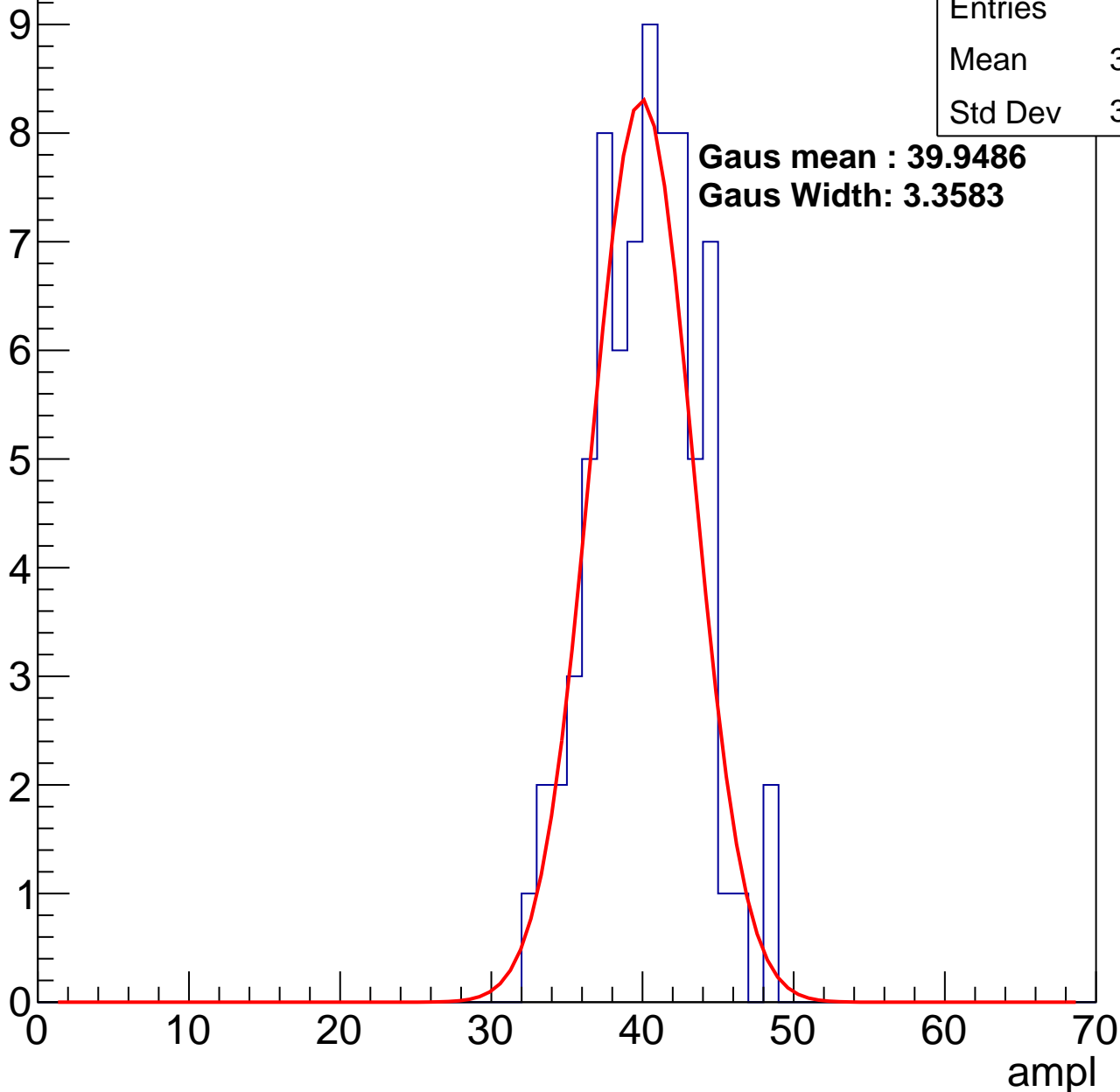
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	39.76
Std Dev	3.413

**Gaus mean : 39.9486**

**Gaus Width: 3.3583**

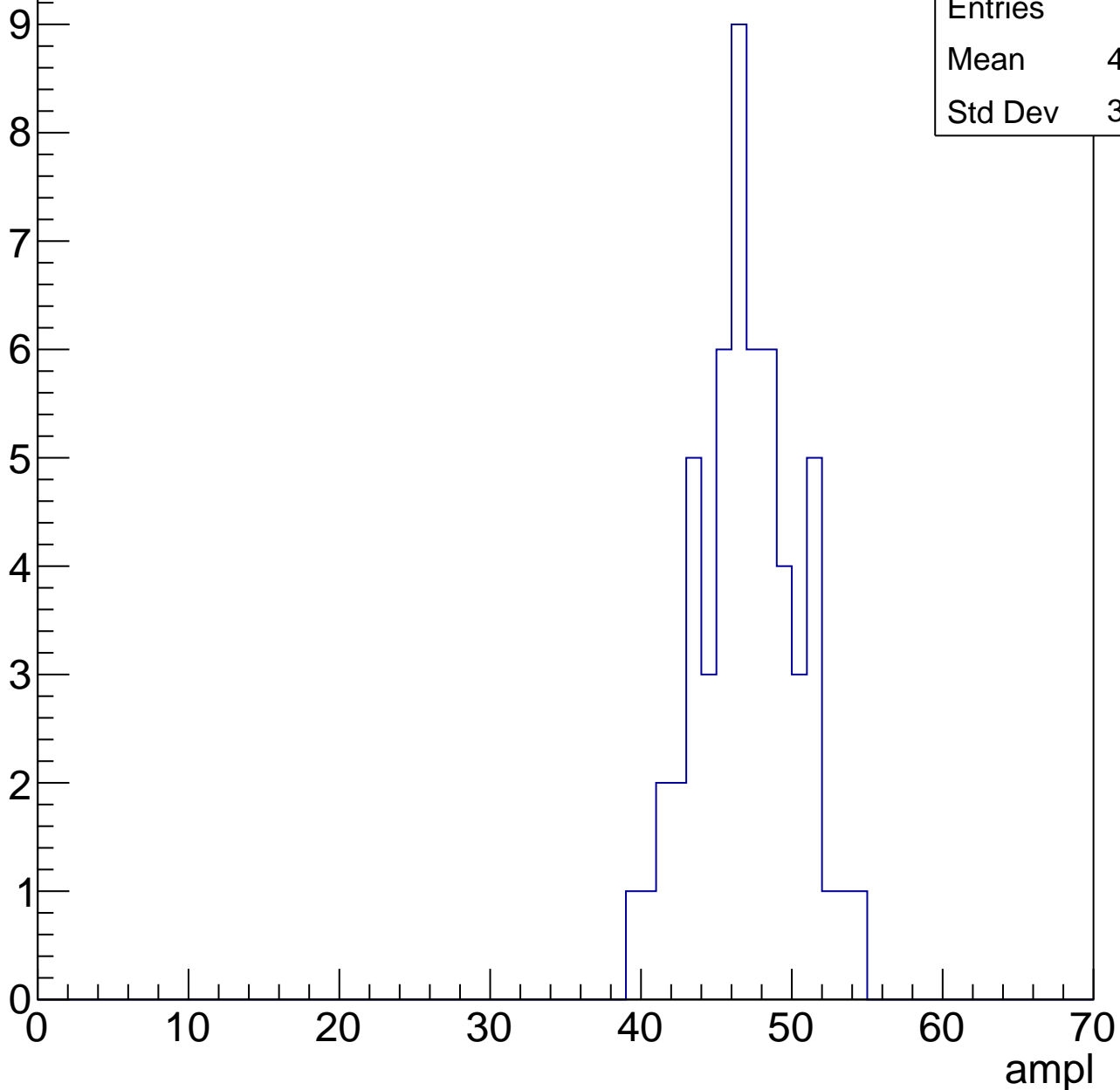


# B1L002S, U18-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	46.54
Std Dev	3.279

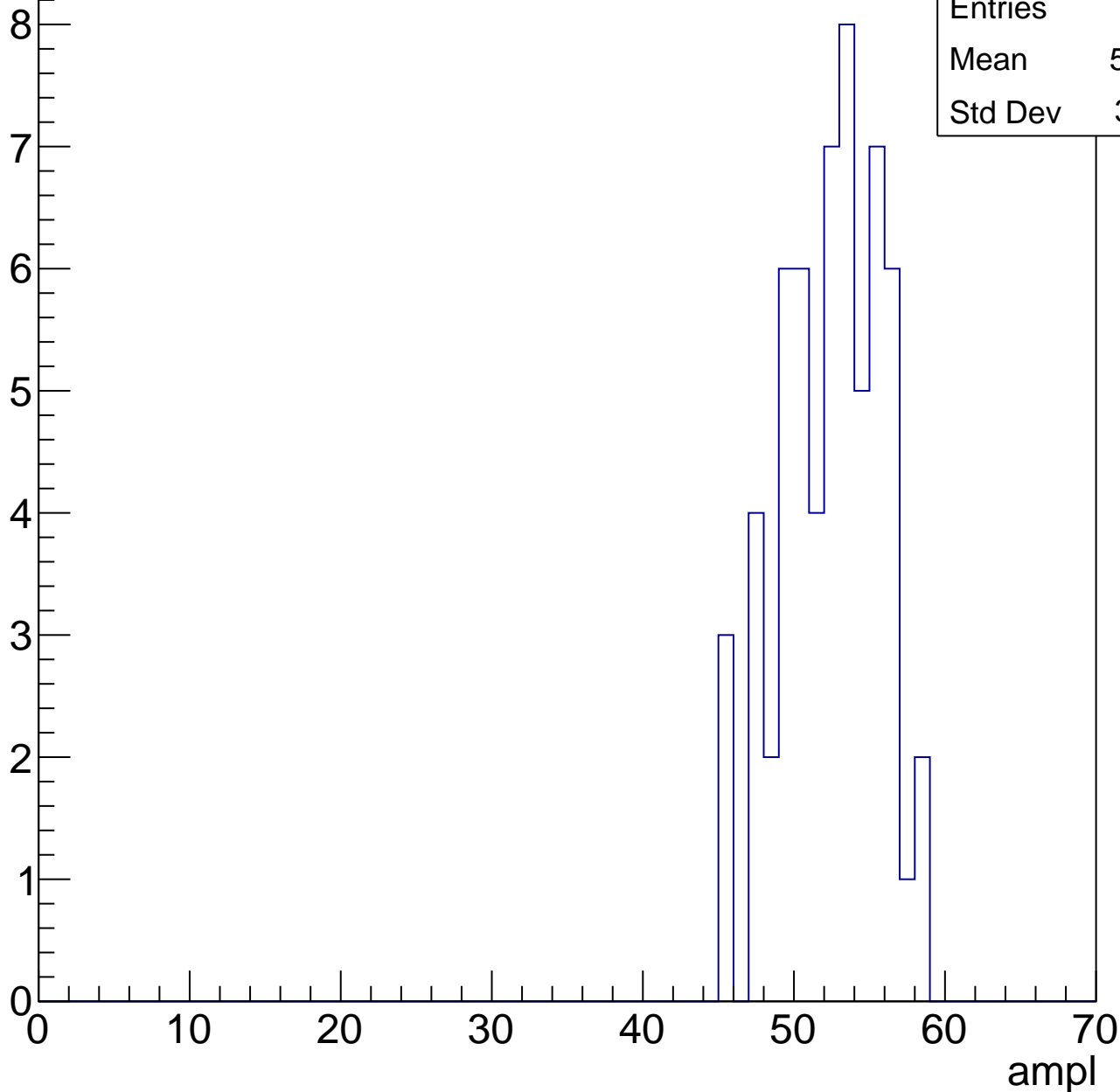


# B1L002S, U18-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	51.95
Std Dev	3.251

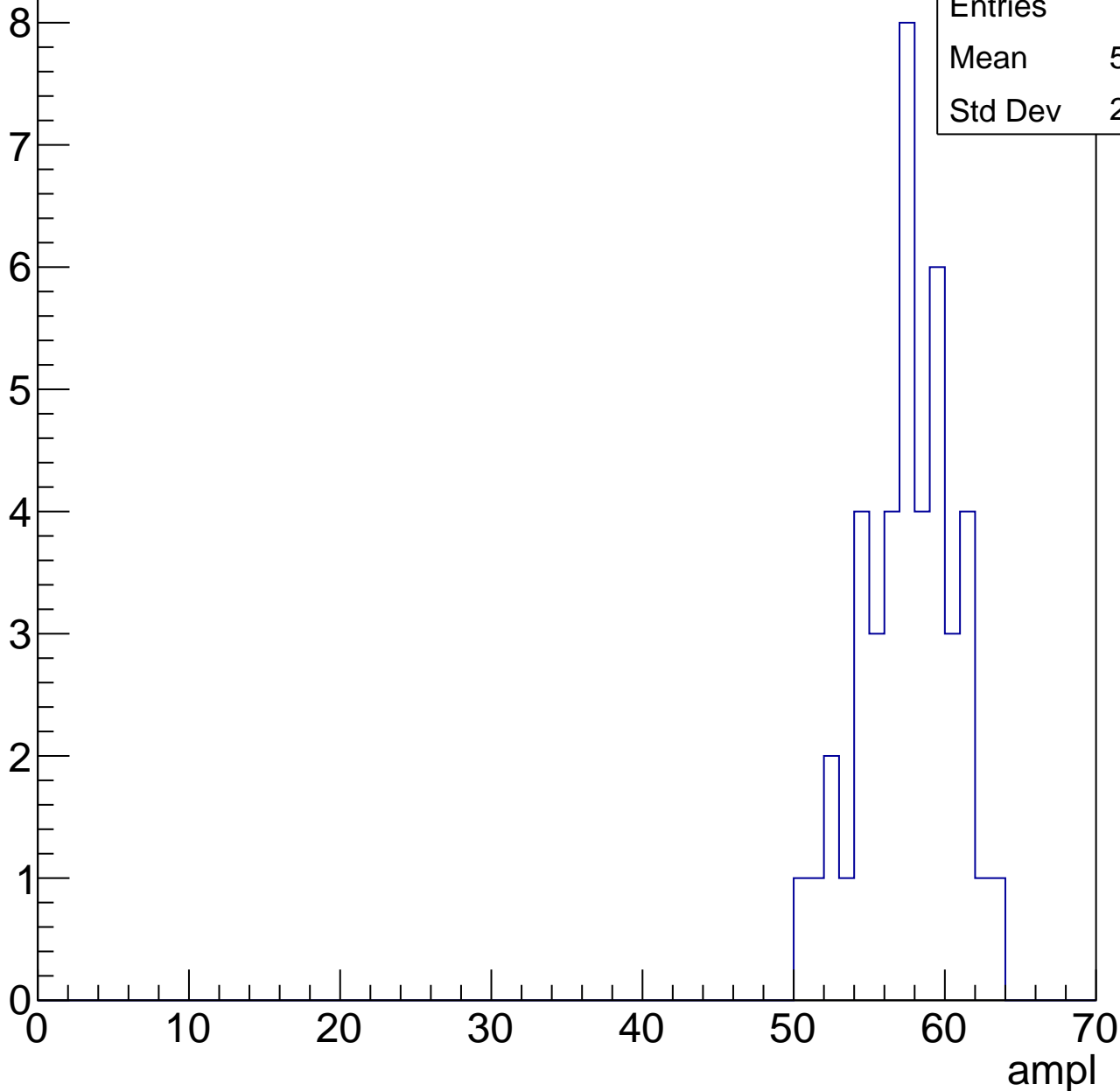


# B1L002S, U18-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	57.07
Std Dev	2.984

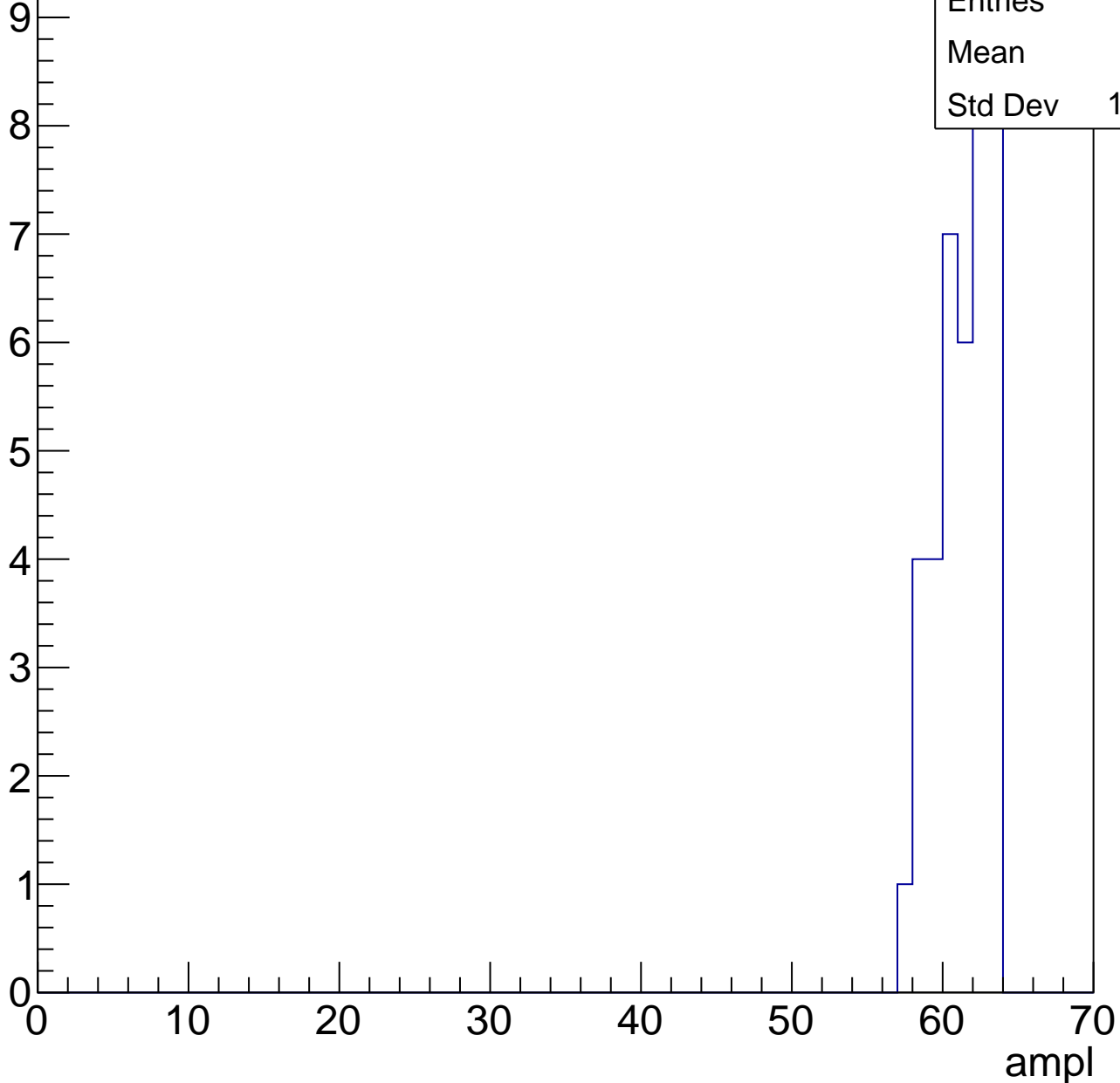


# B1L002S, U18-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	60.9
Std Dev	1.729





# B1L002S, U18-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U18-ch104, adc0

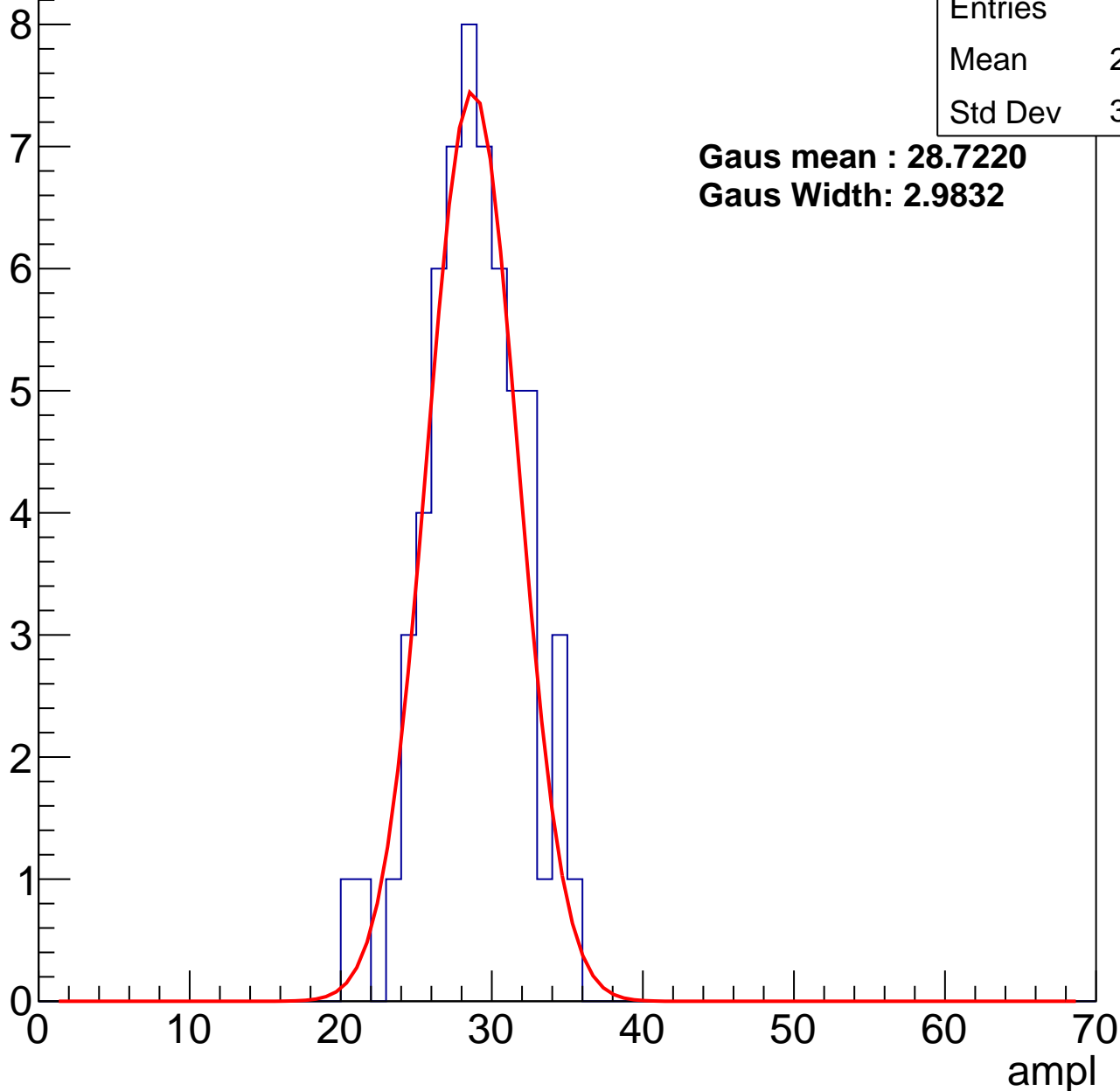
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.36
Std Dev	3.145

**Gaus mean : 28.7220**

**Gaus Width: 2.9832**



# B1L002S, U18-ch104, adc1

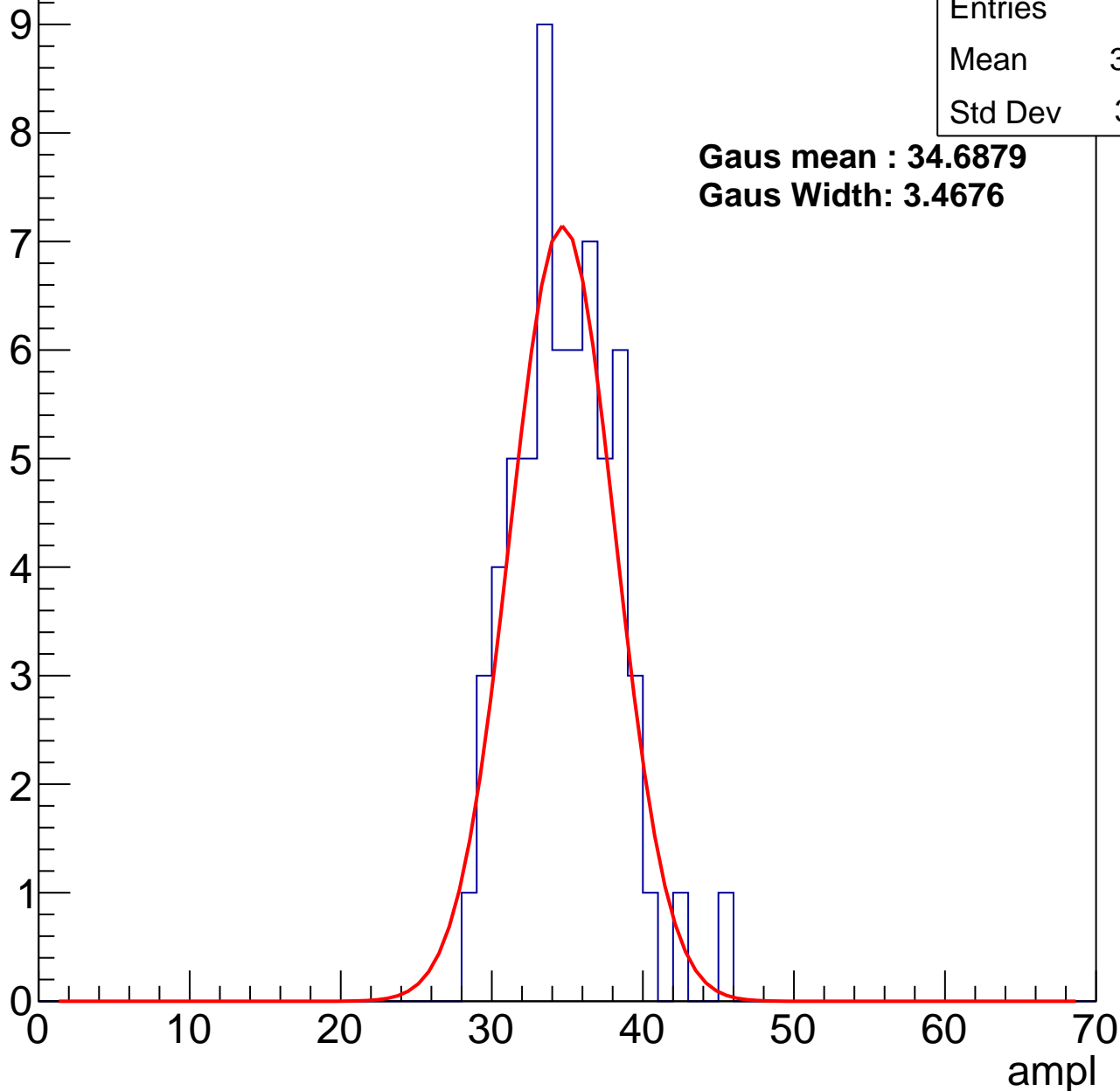
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.44
Std Dev	3.351

**Gaus mean : 34.6879**

**Gaus Width: 3.4676**



# B1L002S, U18-ch104, adc2

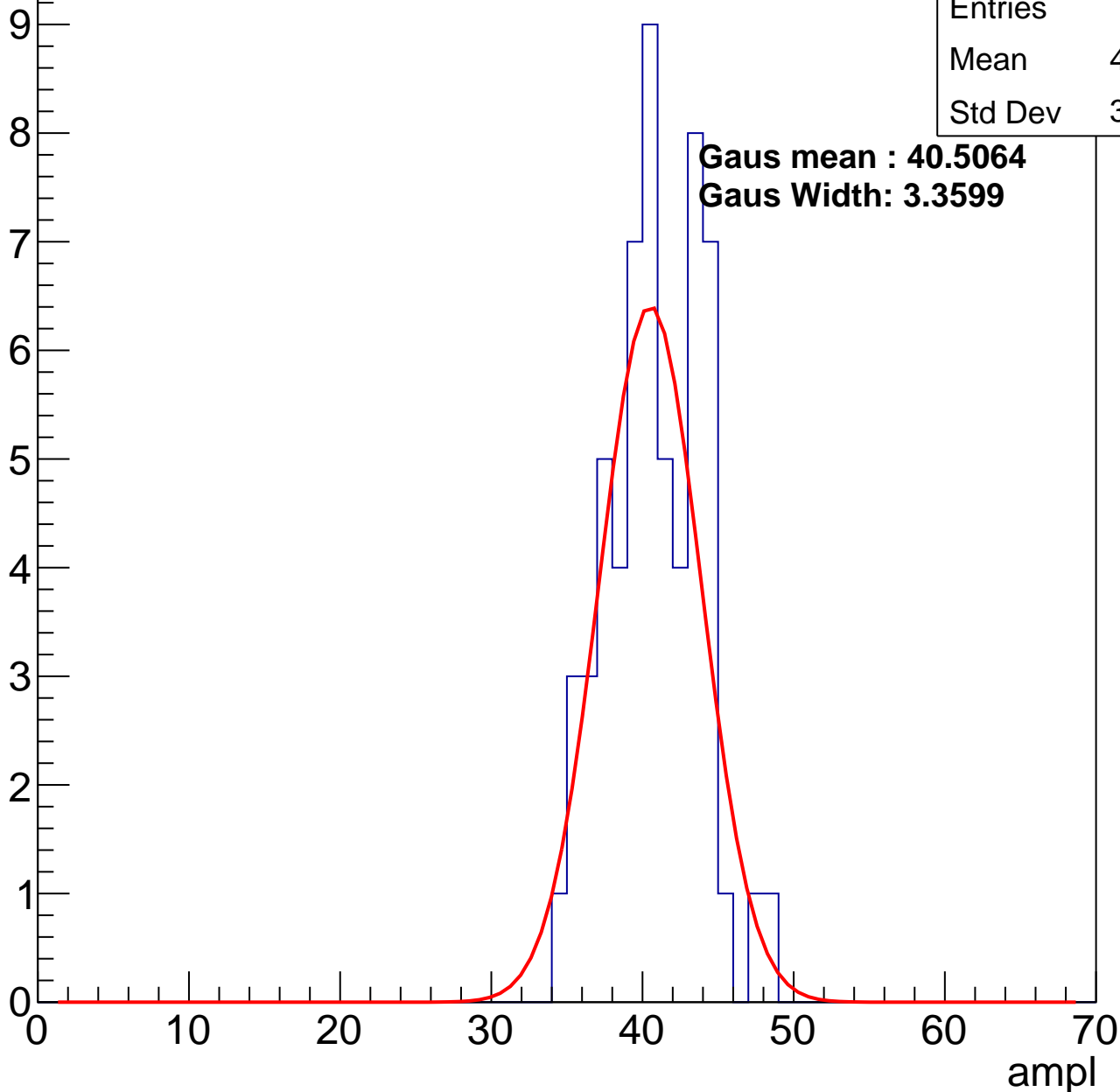
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.37
Std Dev	3.086

**Gaus mean : 40.5064**

**Gaus Width: 3.3599**

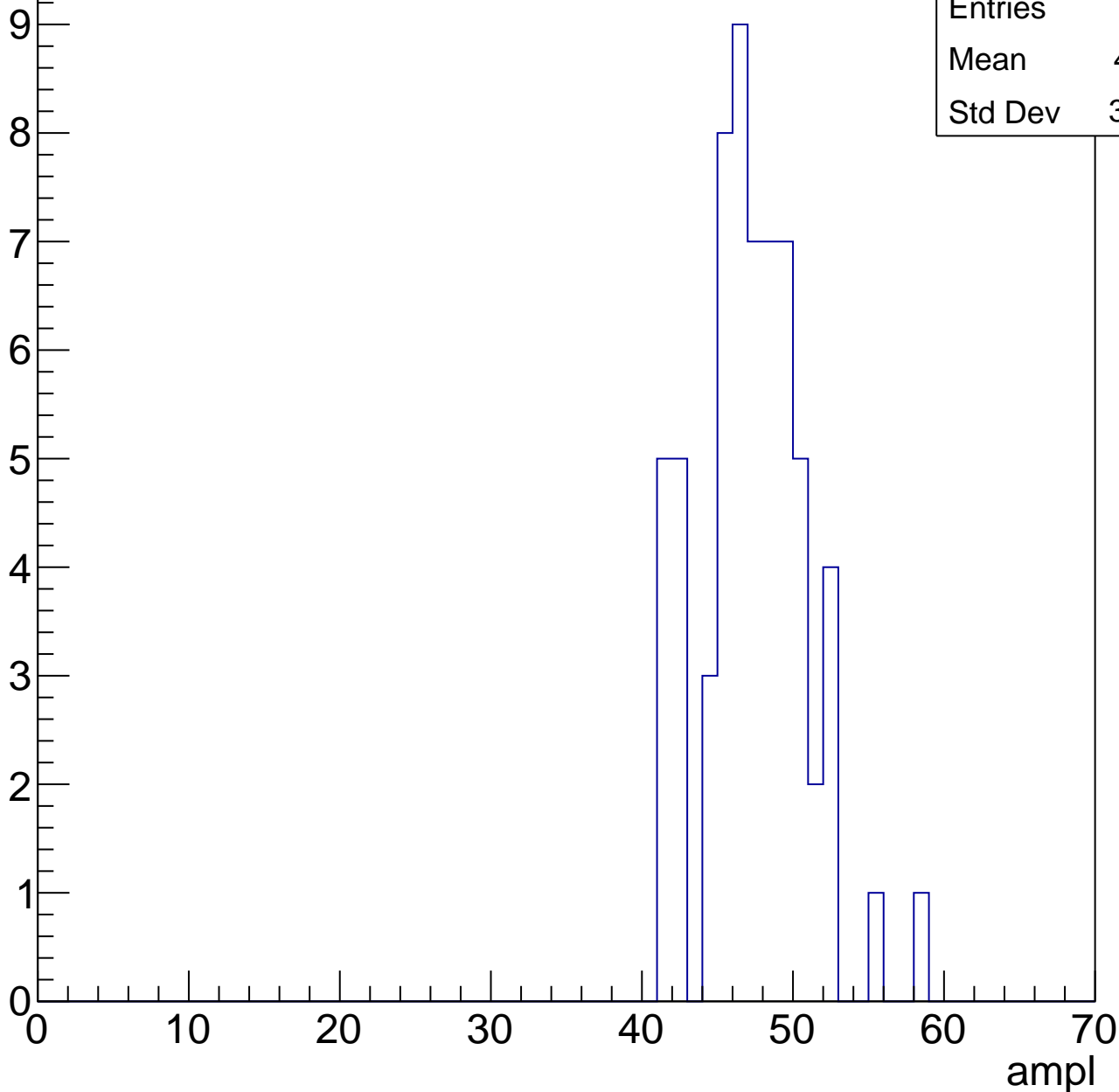


# B1L002S, U18-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.91
Std Dev	3.467

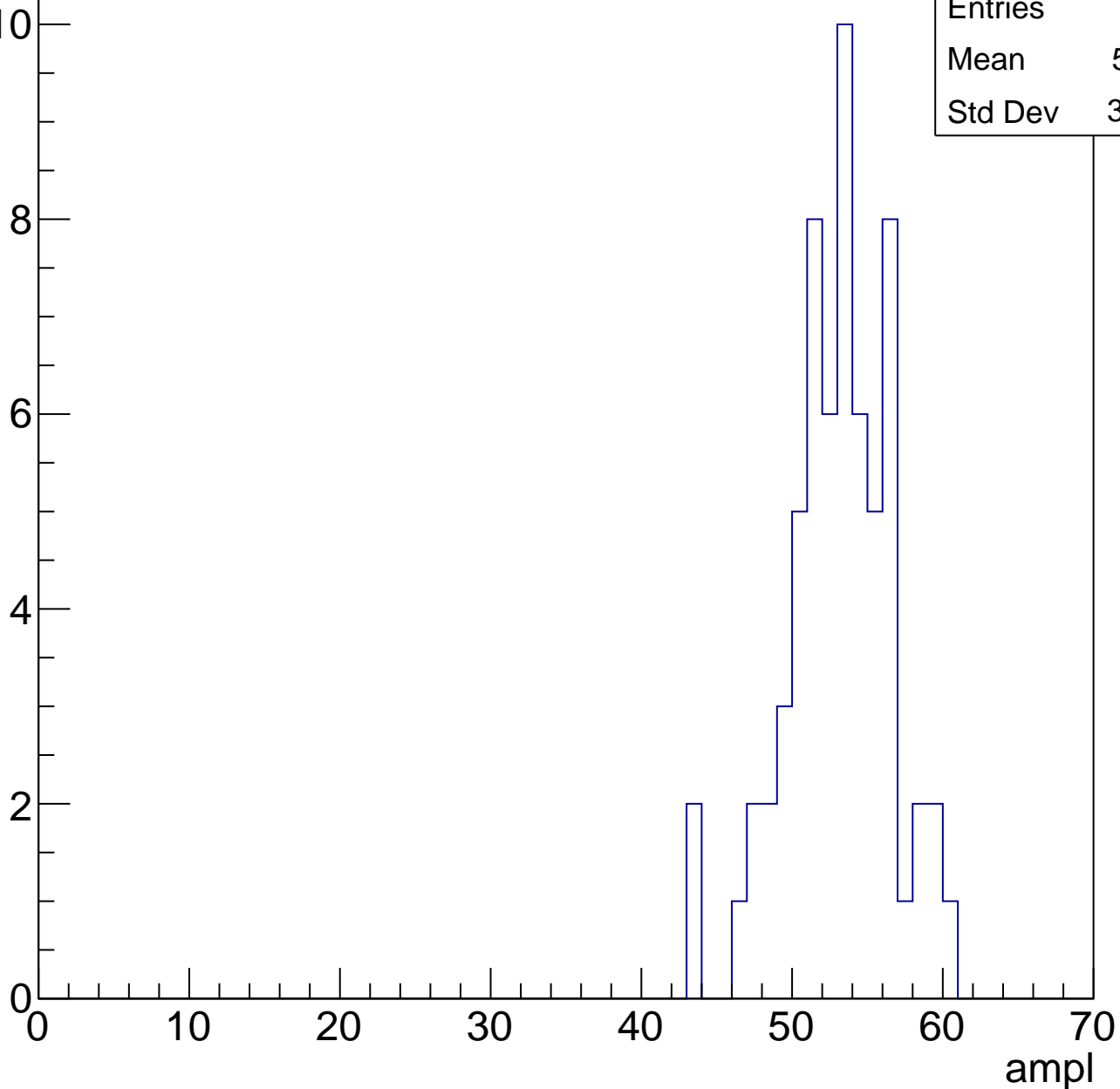


# B1L002S, U18-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	52.61
Std Dev	3.485

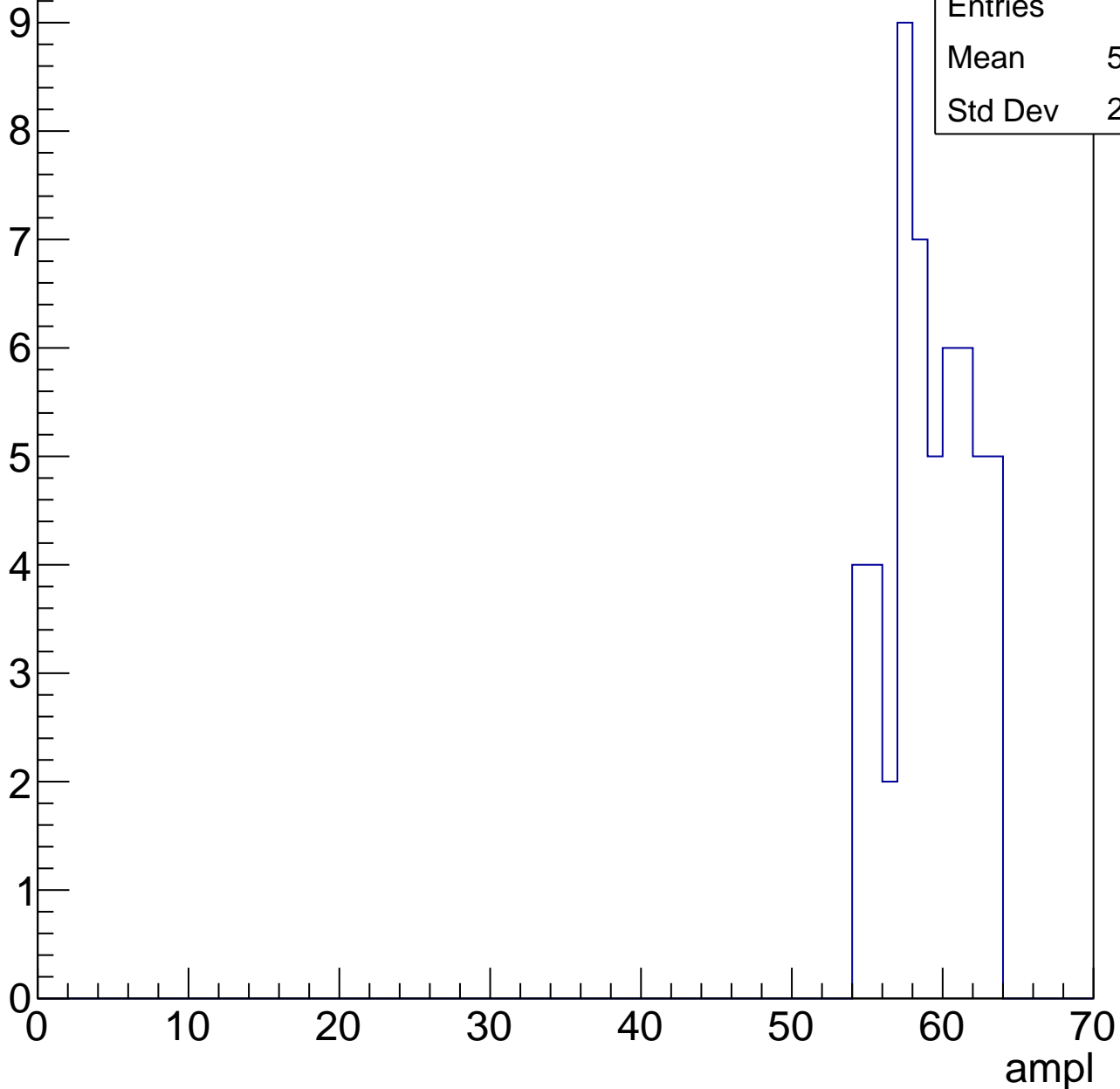


# B1L002S, U18-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

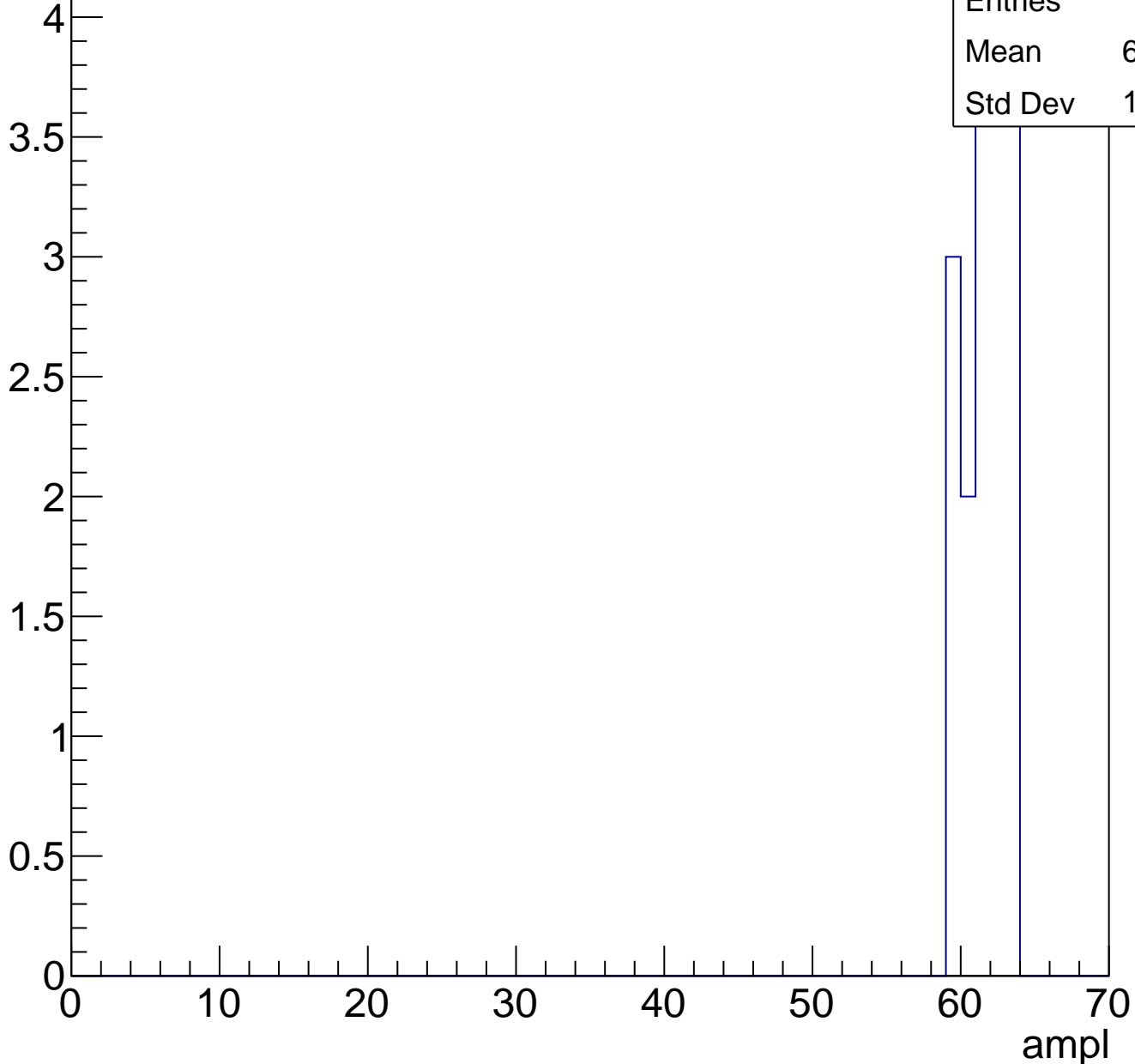
Entries	53
Mean	58.74
Std Dev	2.665



# B1L002S, U18-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



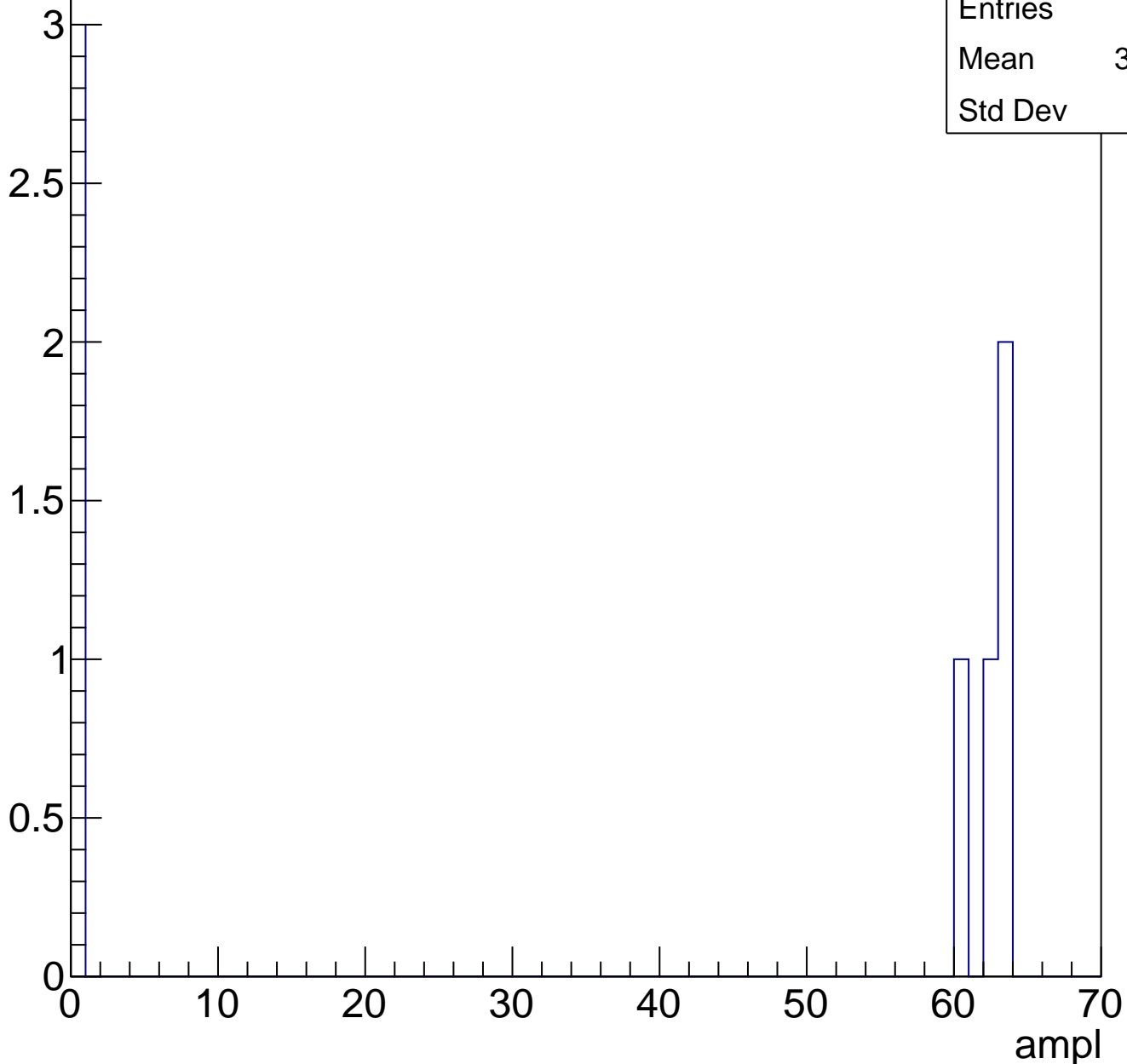
Entries	17
Mean	61.24
Std Dev	1.395



# B1L002S, U18-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch105, adc0

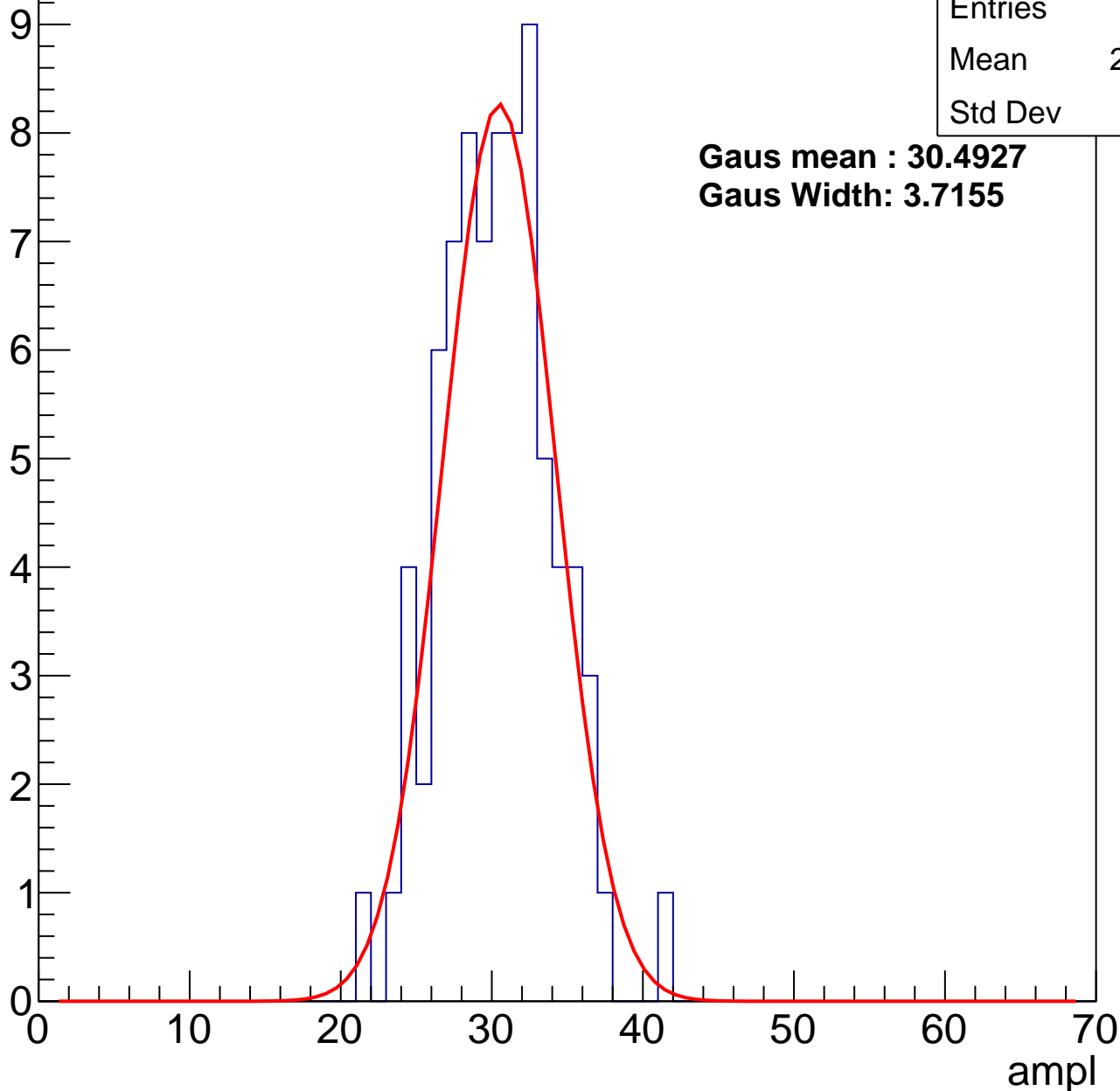
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	29.94
Std Dev	3.65

**Gaus mean : 30.4927**

**Gaus Width: 3.7155**



# B1L002S, U18-ch105, adc1

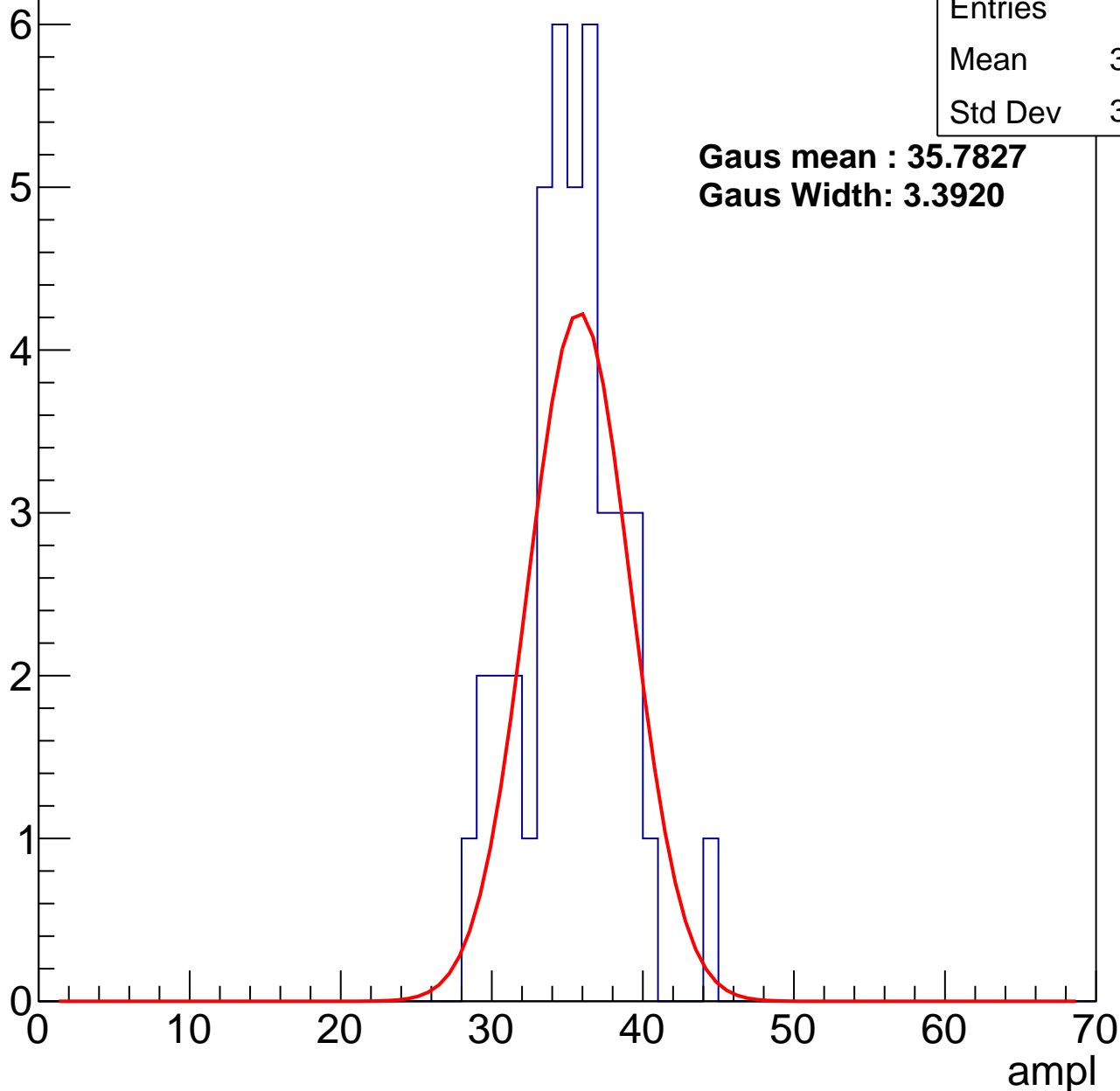
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	34.78
Std Dev	3.257

**Gaus mean : 35.7827**

**Gaus Width: 3.3920**



# B1L002S, U18-ch105, adc2

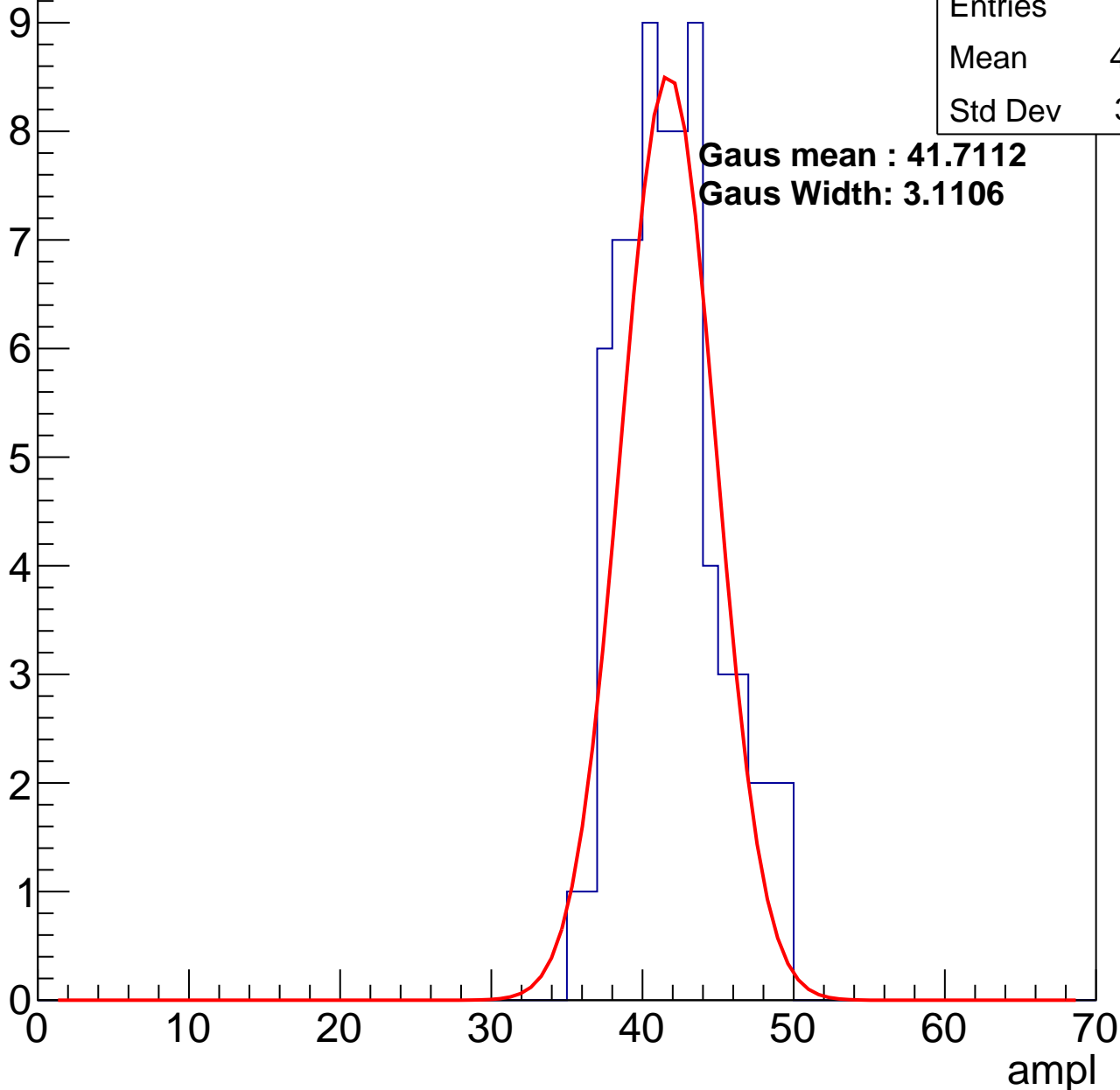
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	41.39
Std Dev	3.221

**Gaus mean : 41.7112**

**Gaus Width: 3.1106**

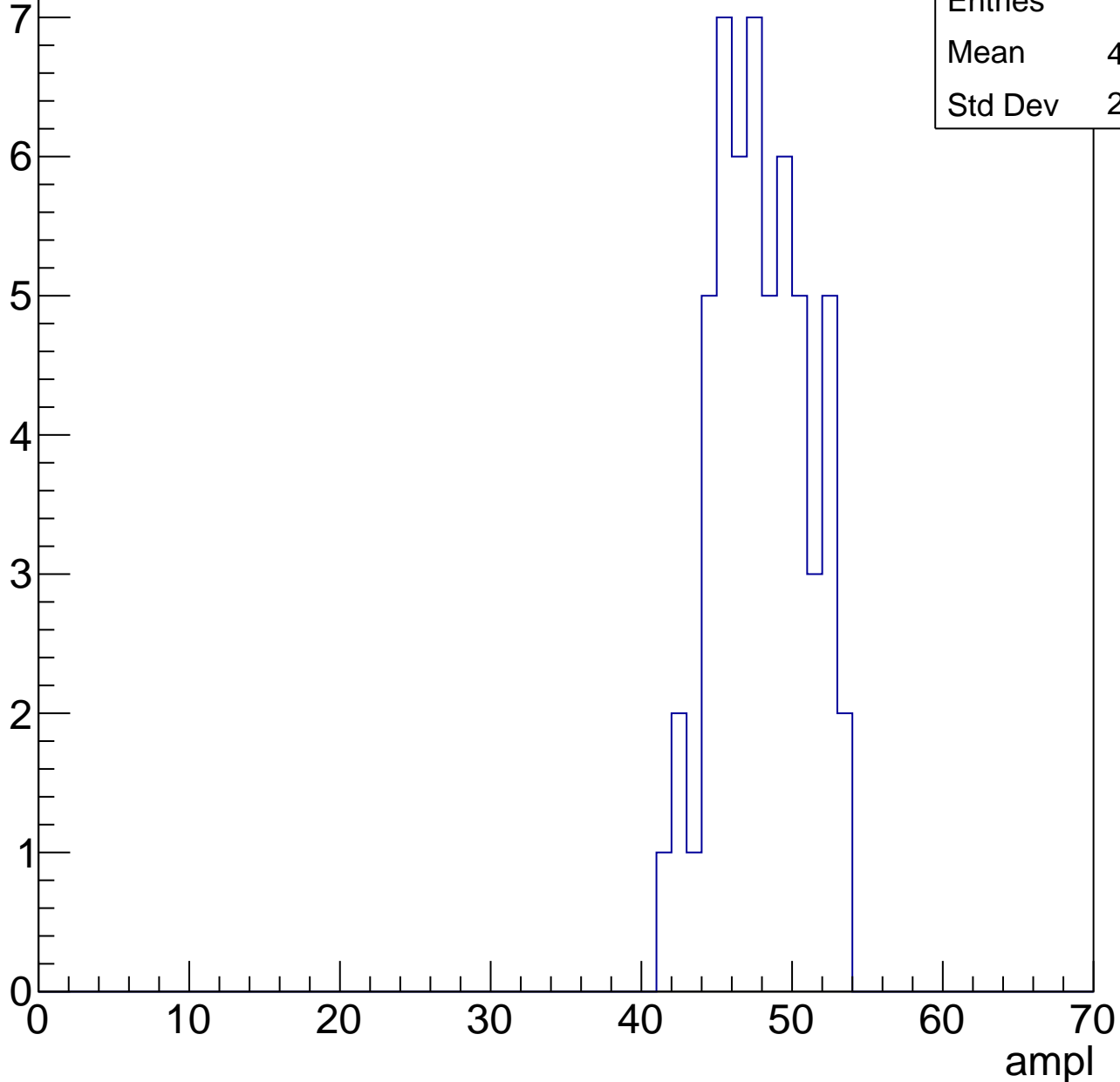


# B1L002S, U18-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	47.47
Std Dev	2.978

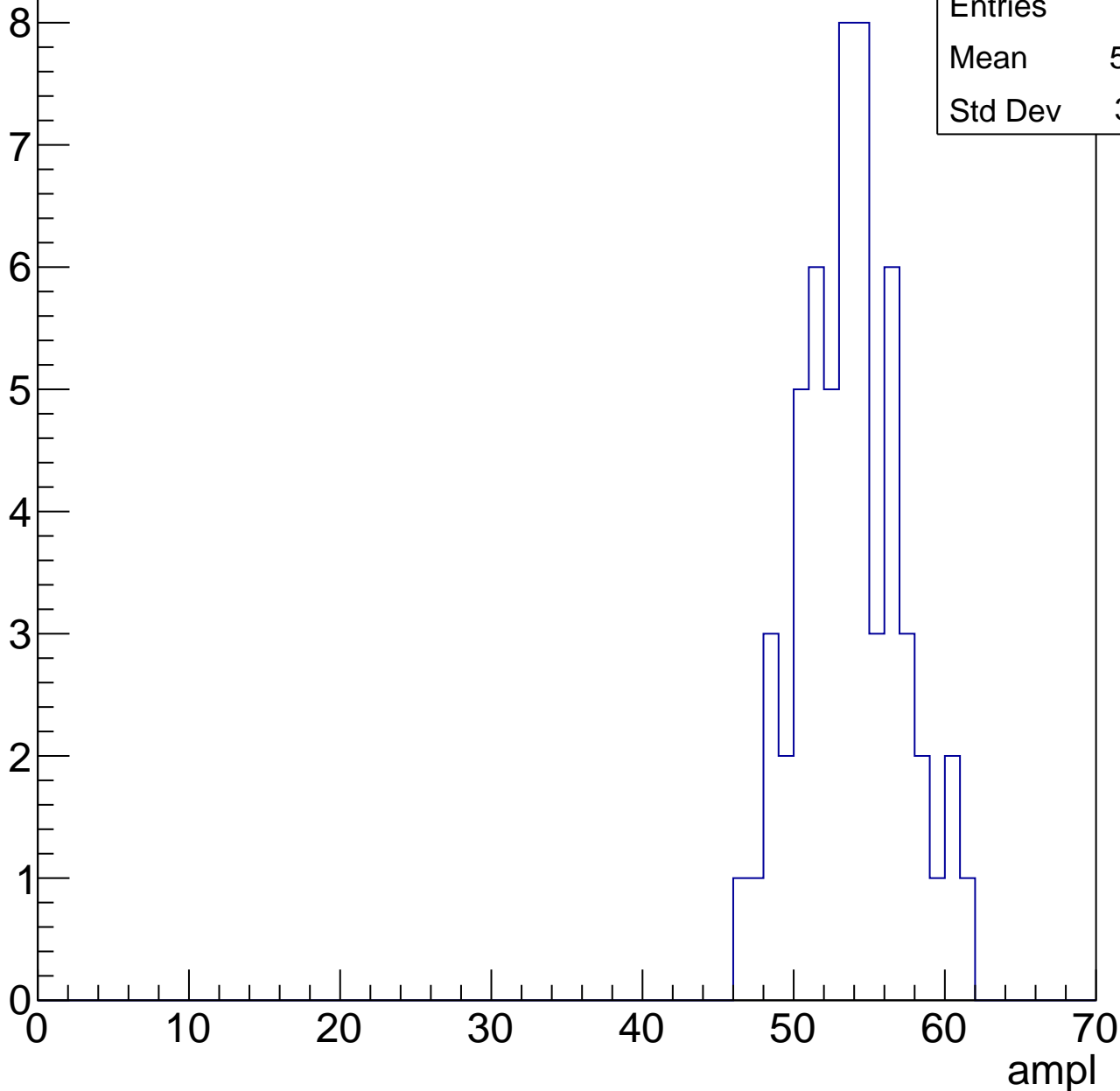


# B1L002S, U18-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.25
Std Dev	3.331

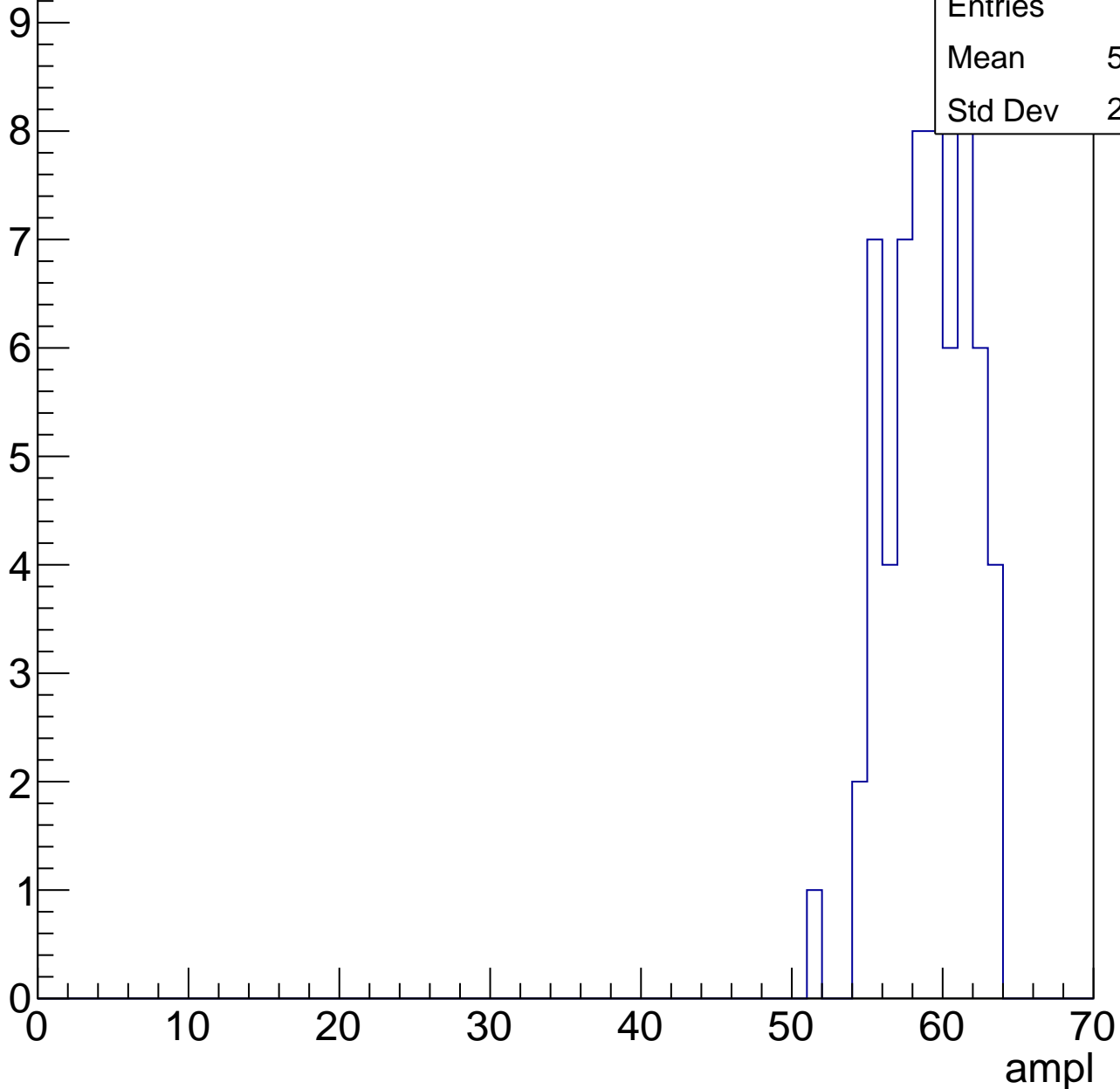


# B1L002S, U18-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.65
Std Dev	2.695

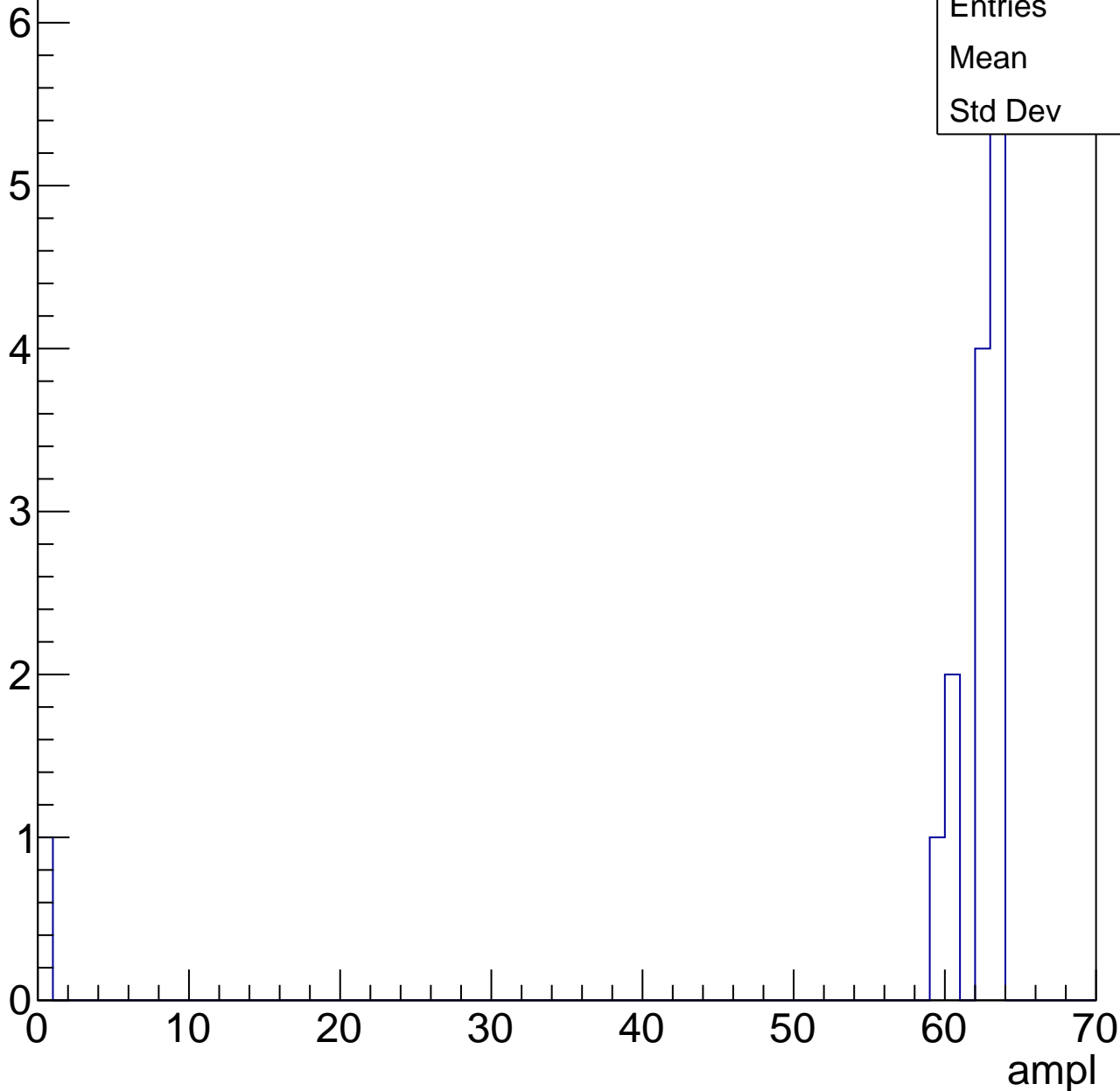


# B1L002S, U18-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.5
Std Dev	16





# B1L002S, U18-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch106, adc0

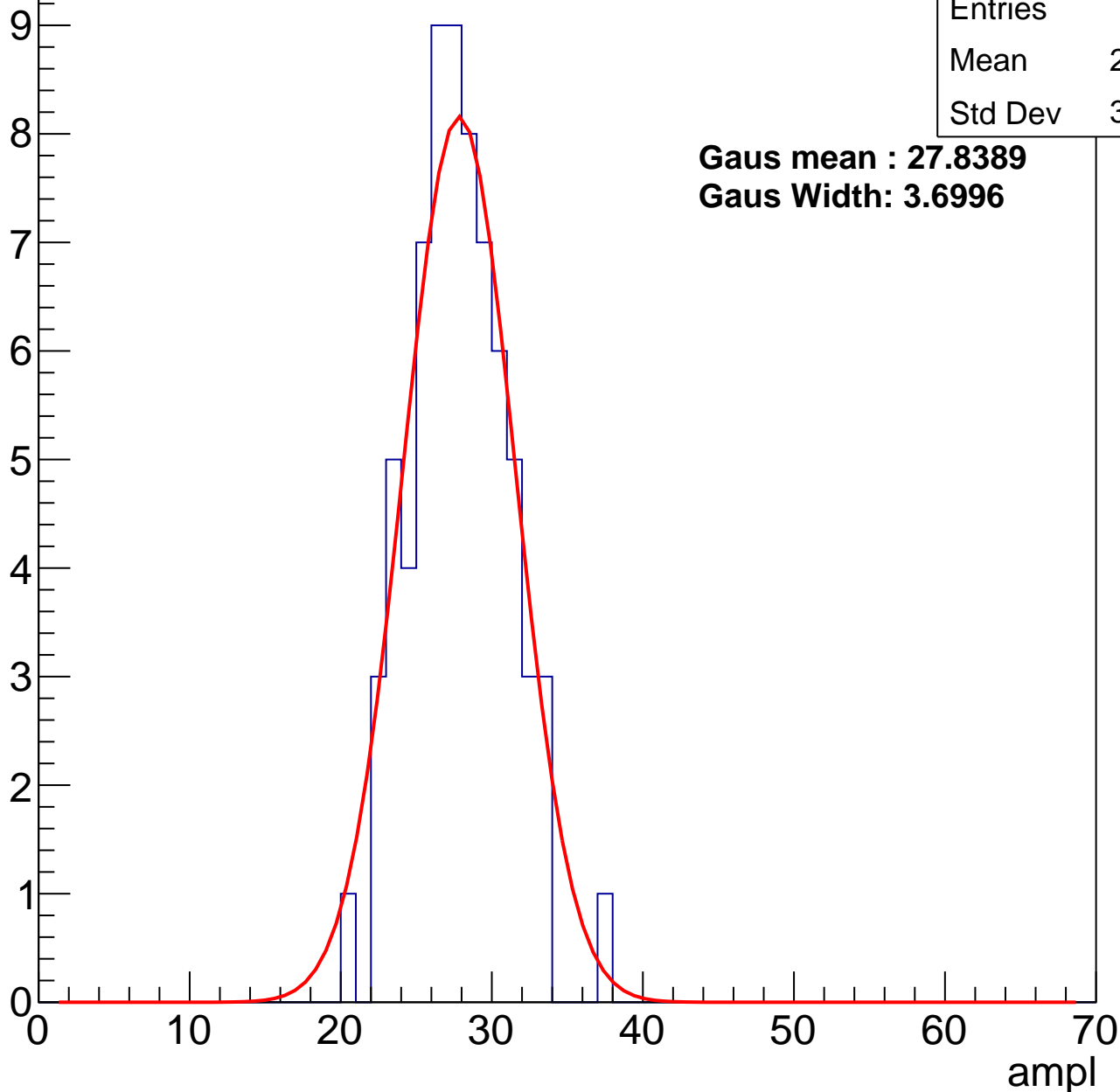
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	27.37
Std Dev	3.186

**Gaus mean : 27.8389**

**Gaus Width: 3.6996**



# B1L002S, U18-ch106, adc1

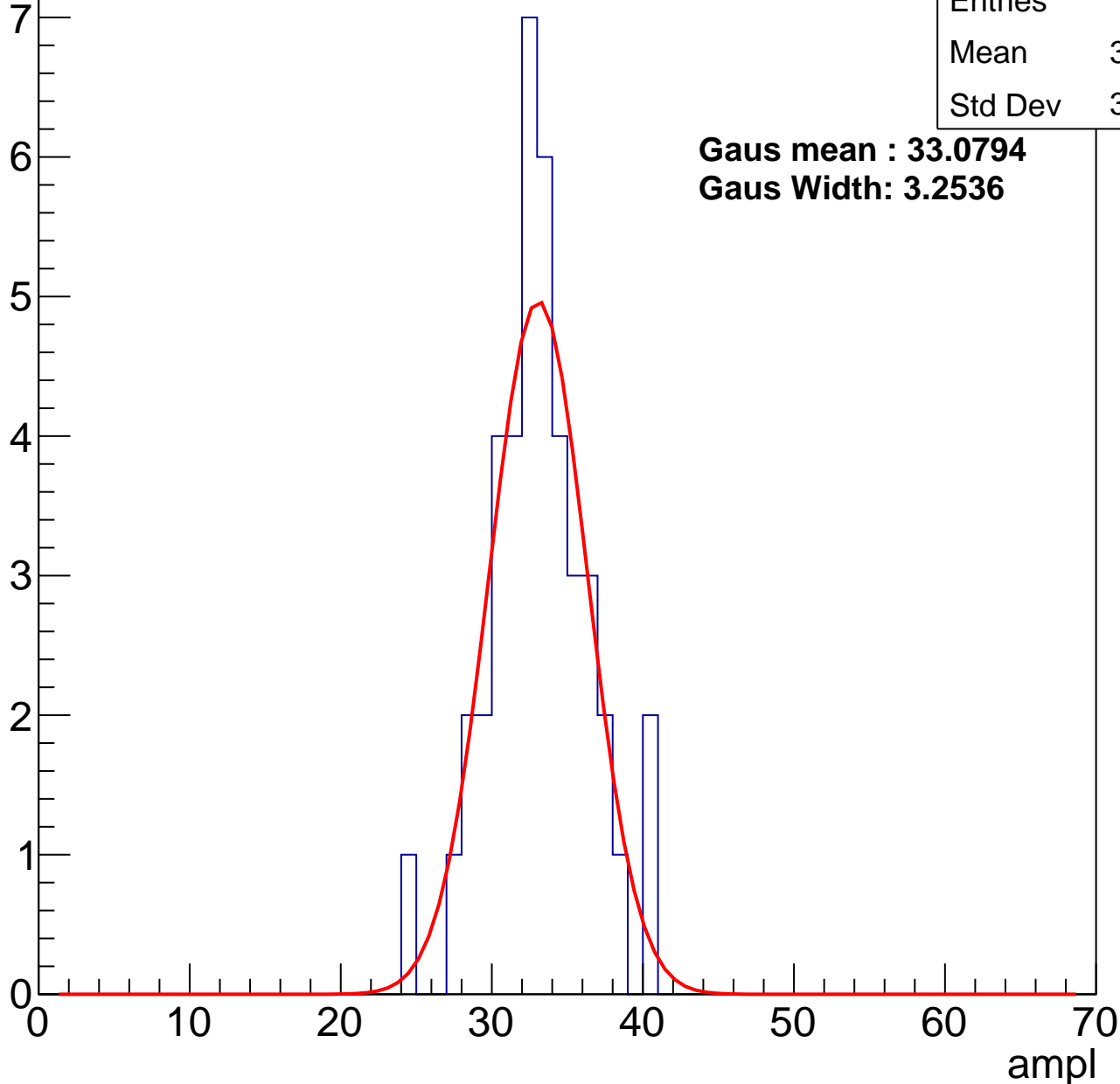
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	32.67
Std Dev	3.278

**Gaus mean : 33.0794**

**Gaus Width: 3.2536**



# B1L002S, U18-ch106, adc2

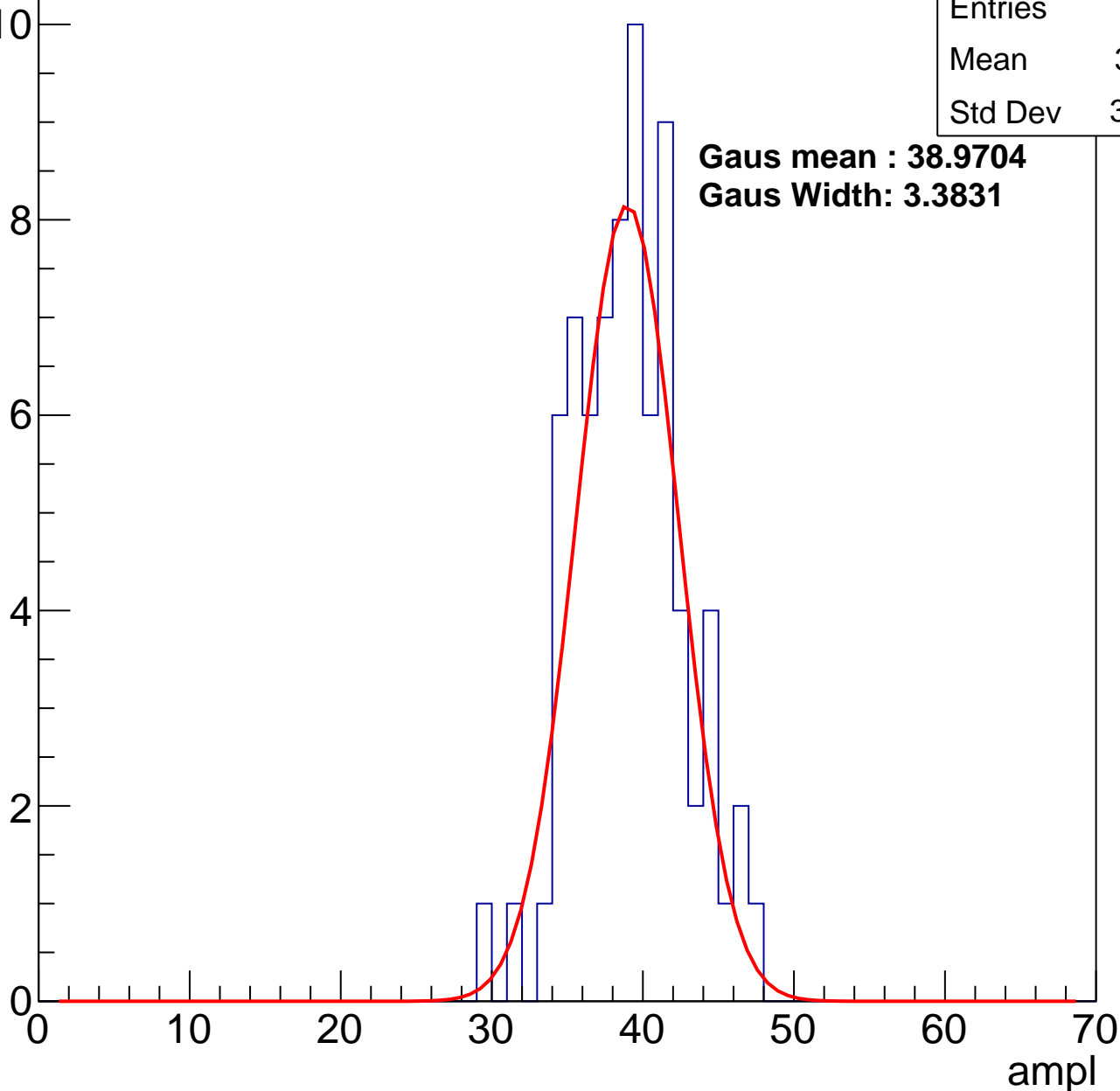
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	38.61
Std Dev	3.532

**Gaus mean : 38.9704**

**Gaus Width: 3.3831**

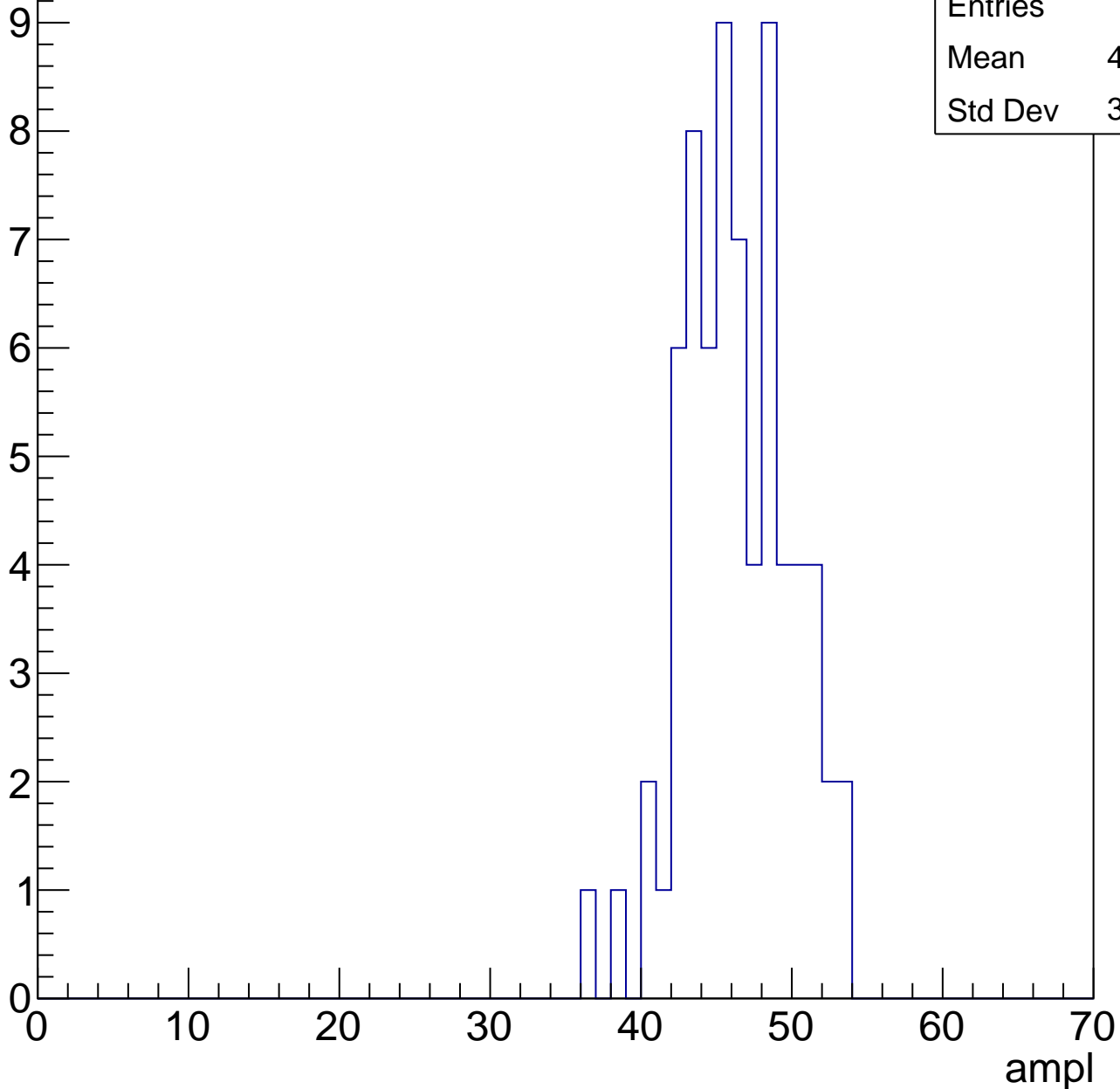


# B1L002S, U18-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	45.89
Std Dev	3.528

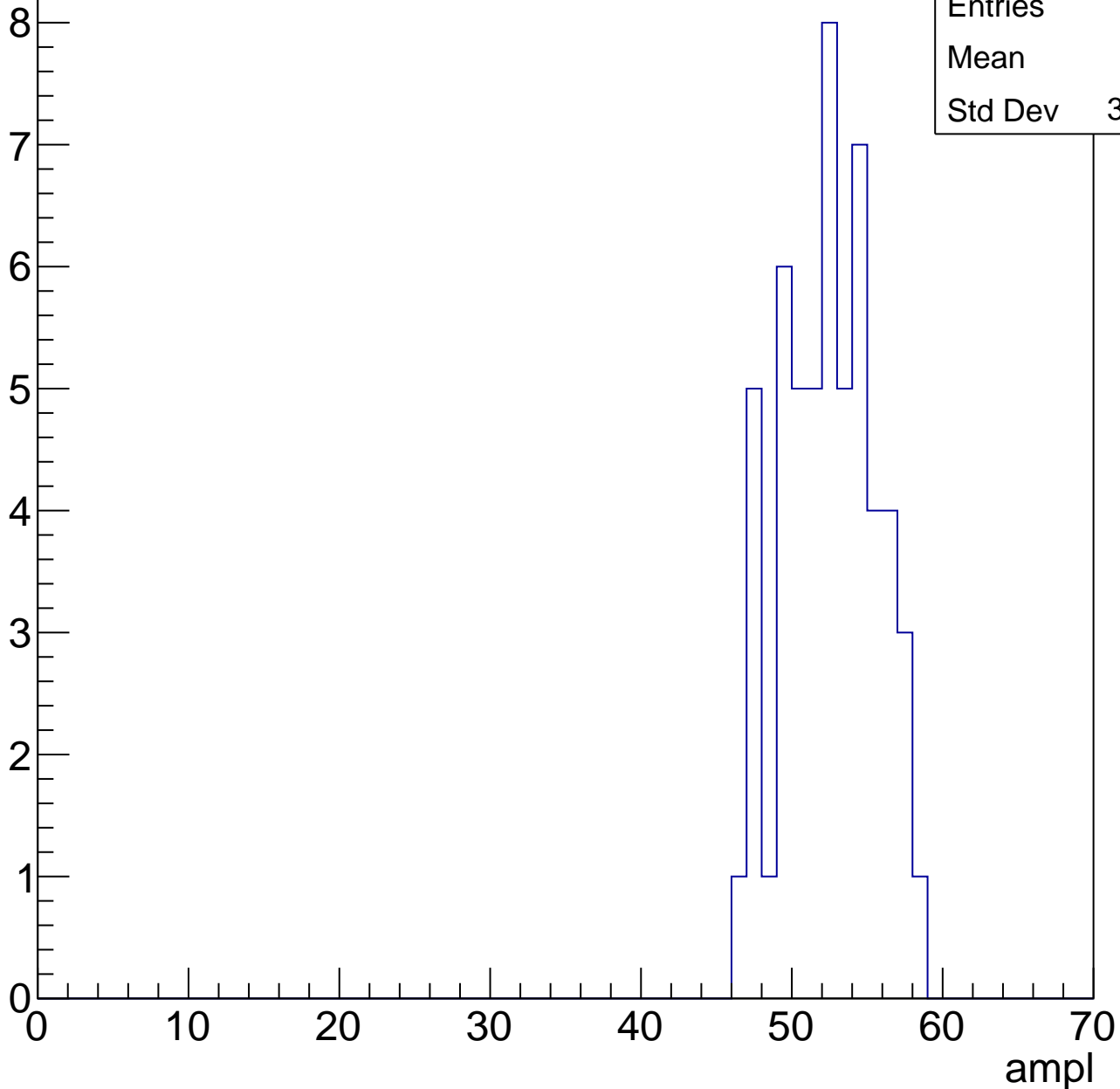


# B1L002S, U18-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52
Std Dev	3.015

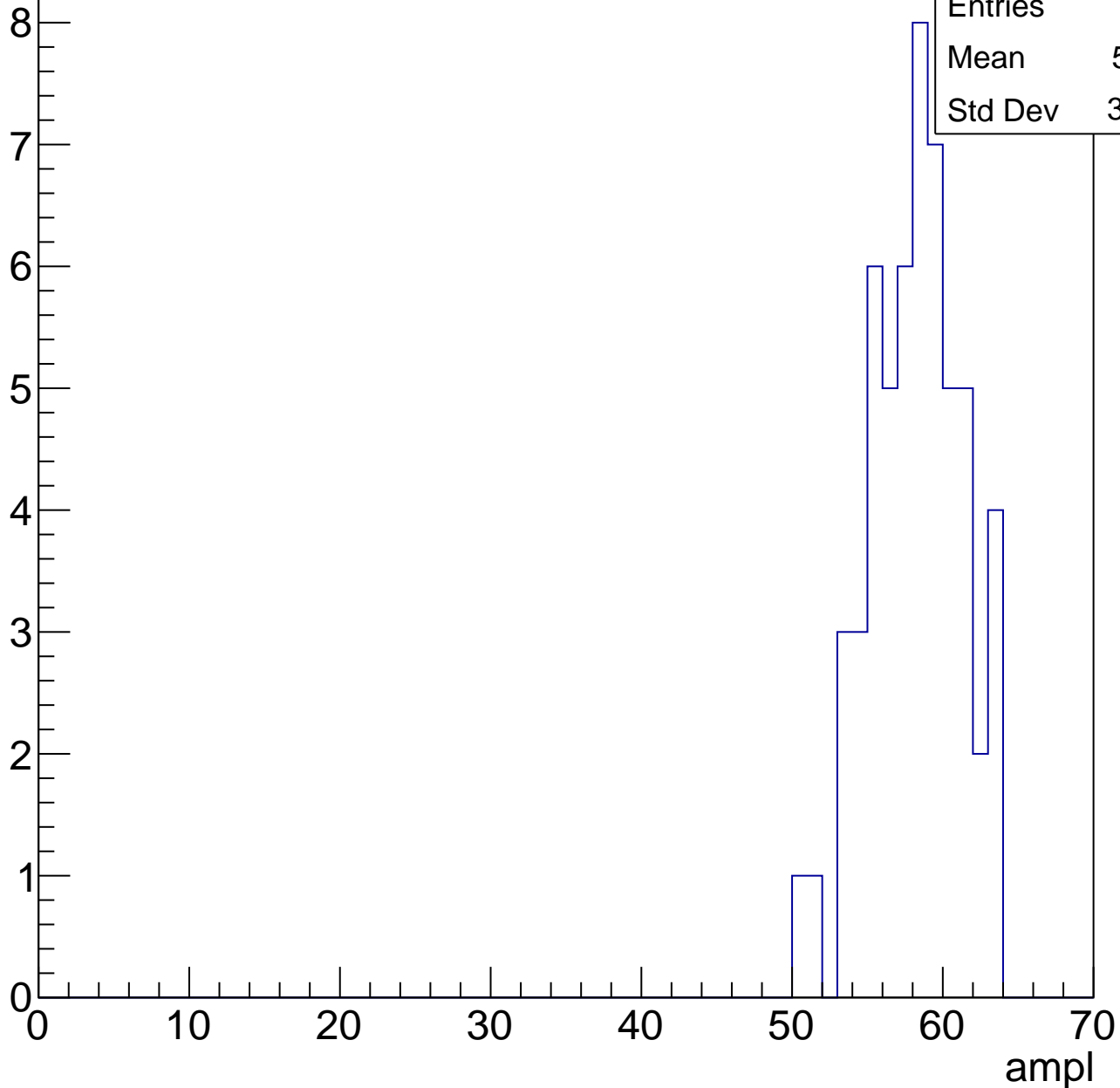


# B1L002S, U18-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.71
Std Dev	3.034

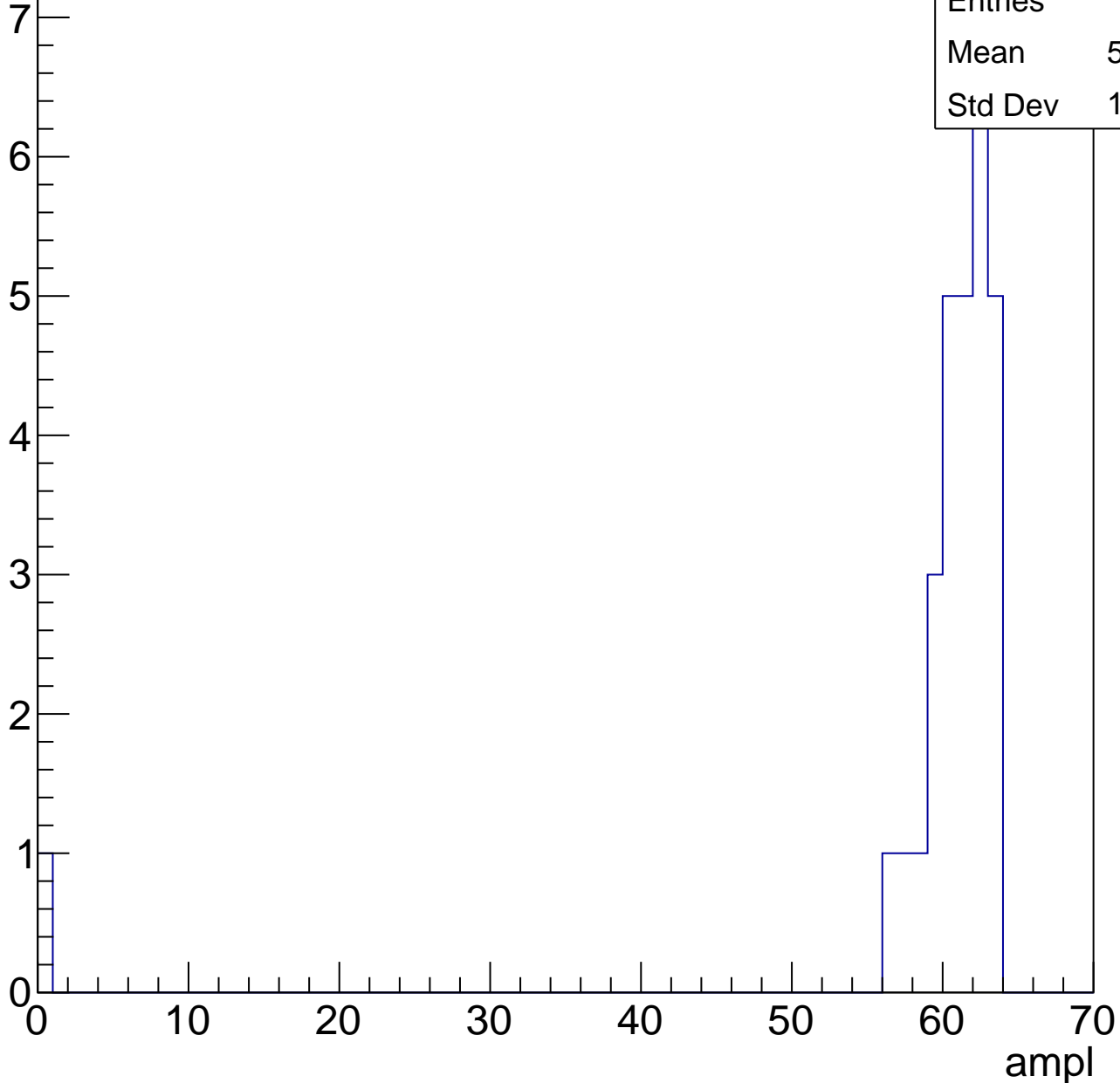


# B1L002S, U18-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.69
Std Dev	11.23





# B1L002S, U18-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	62.5
Std Dev	0.5

# B1L002S, U18-ch107, adc0

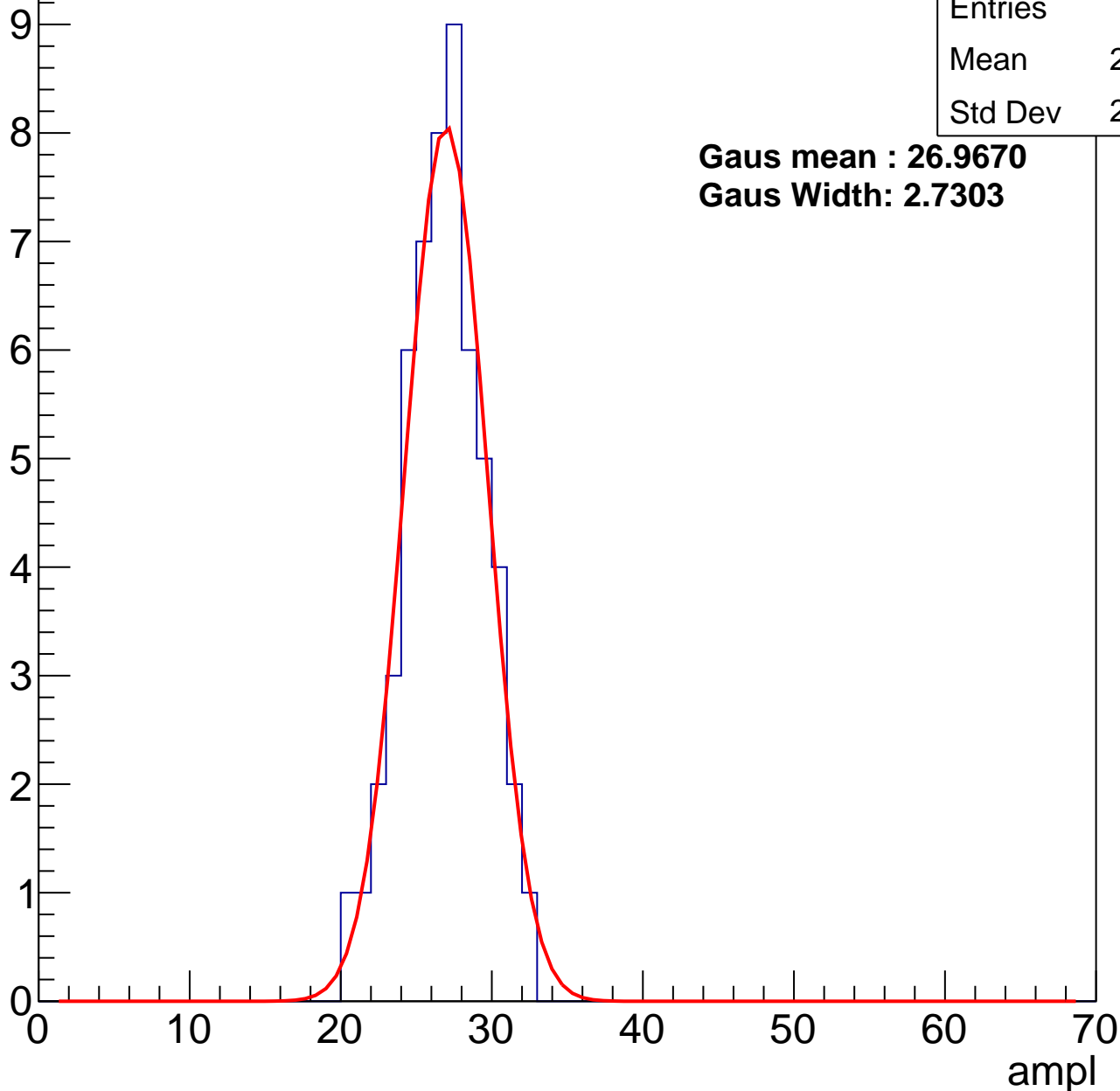
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	26.38
Std Dev	2.597

**Gaus mean : 26.9670**

**Gaus Width: 2.7303**



# B1L002S, U18-ch107, adc1

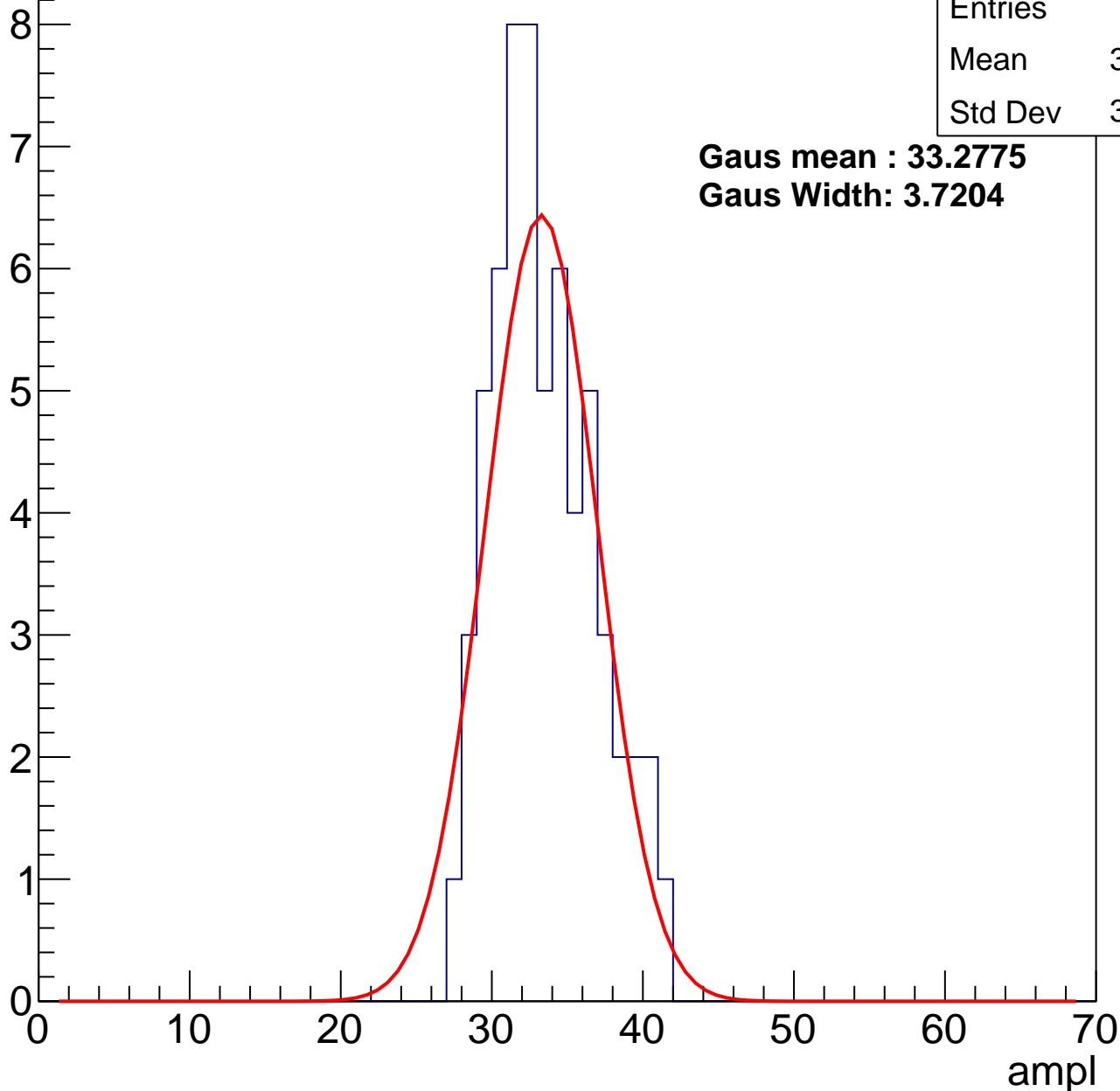
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	33.03
Std Dev	3.348

**Gaus mean : 33.2775**

**Gaus Width: 3.7204**



# B1L002S, U18-ch107, adc2

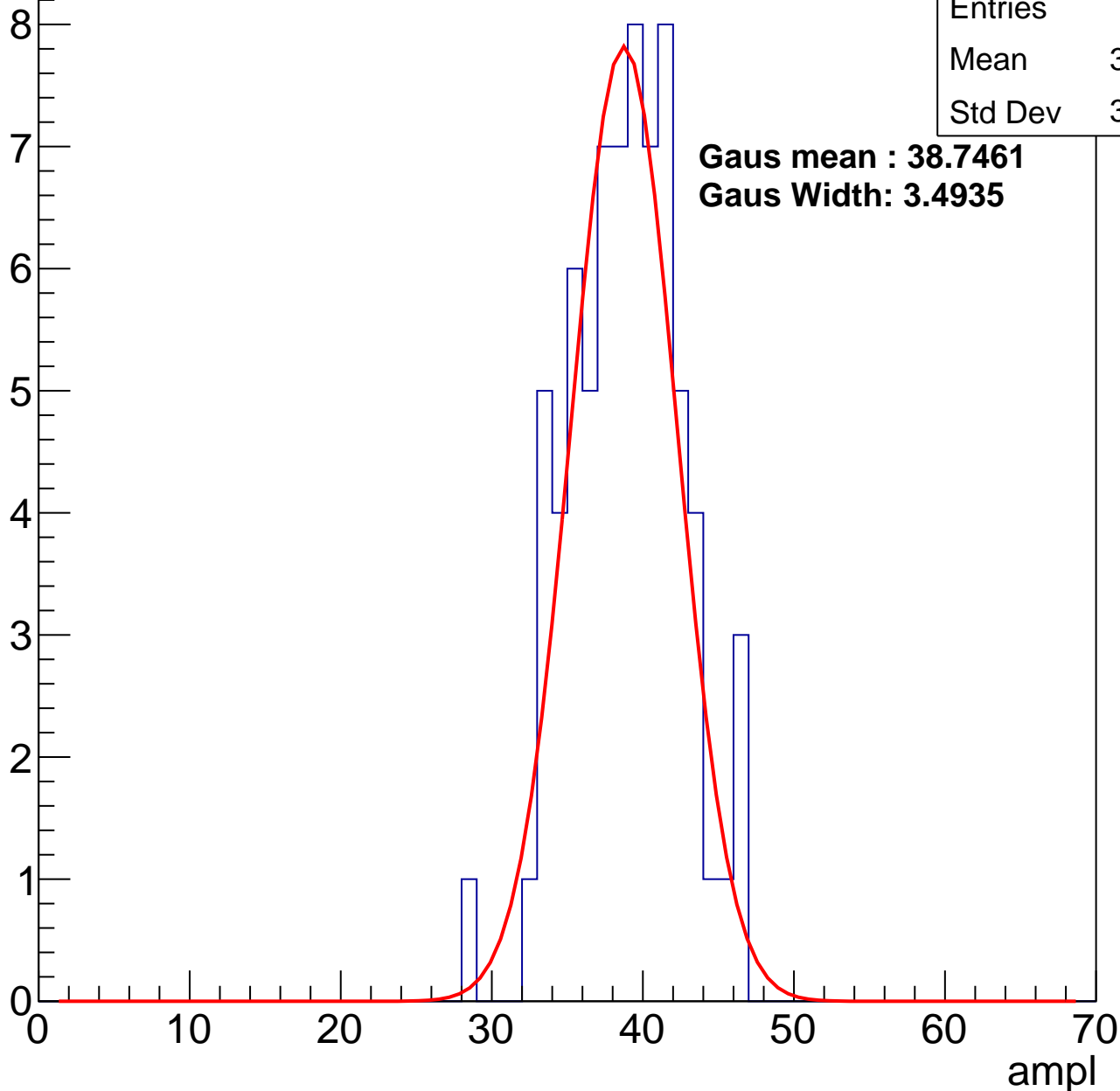
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	38.42
Std Dev	3.622

**Gaus mean : 38.7461**

**Gaus Width: 3.4935**

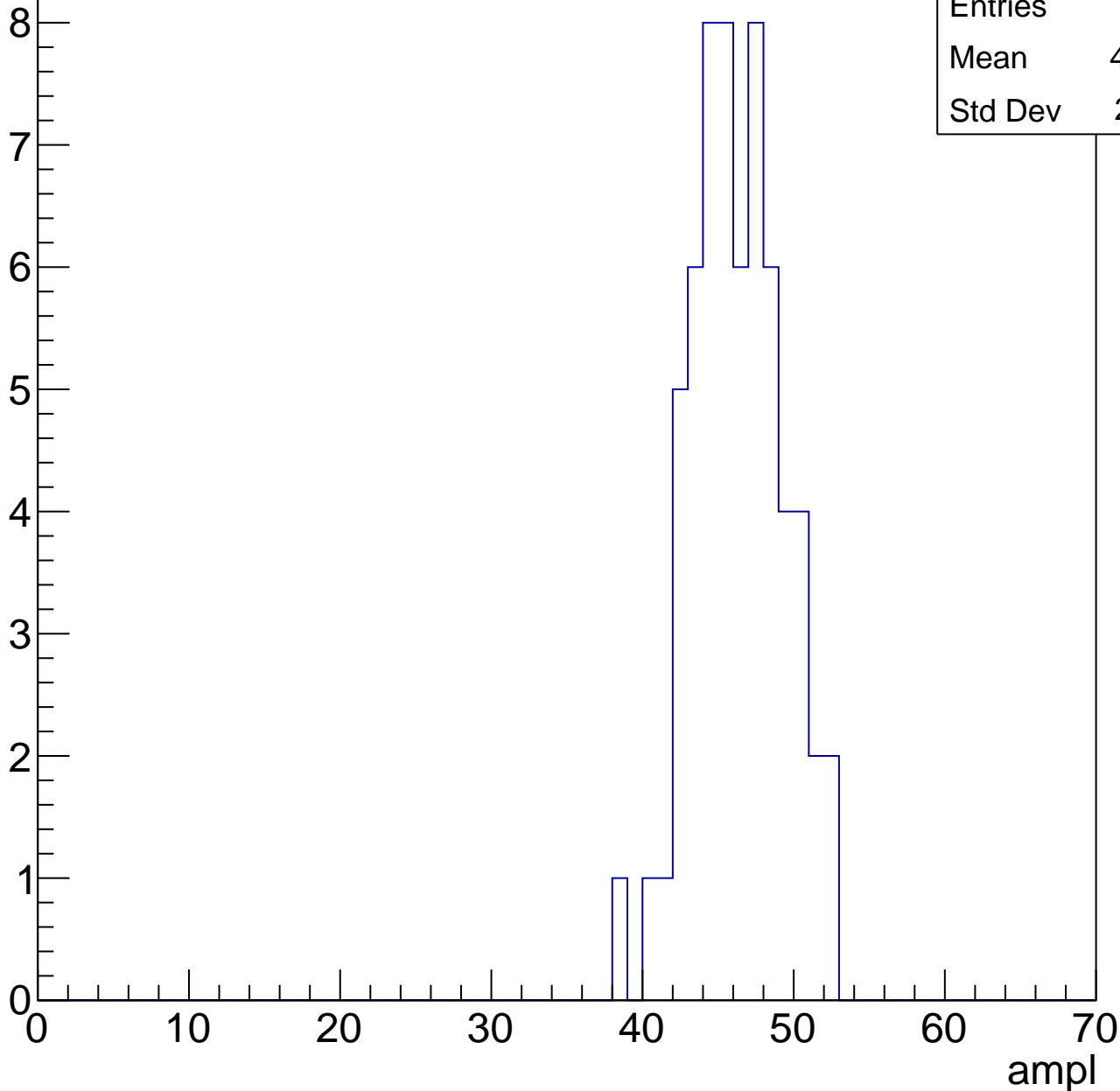


# B1L002S, U18-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	45.82
Std Dev	2.981

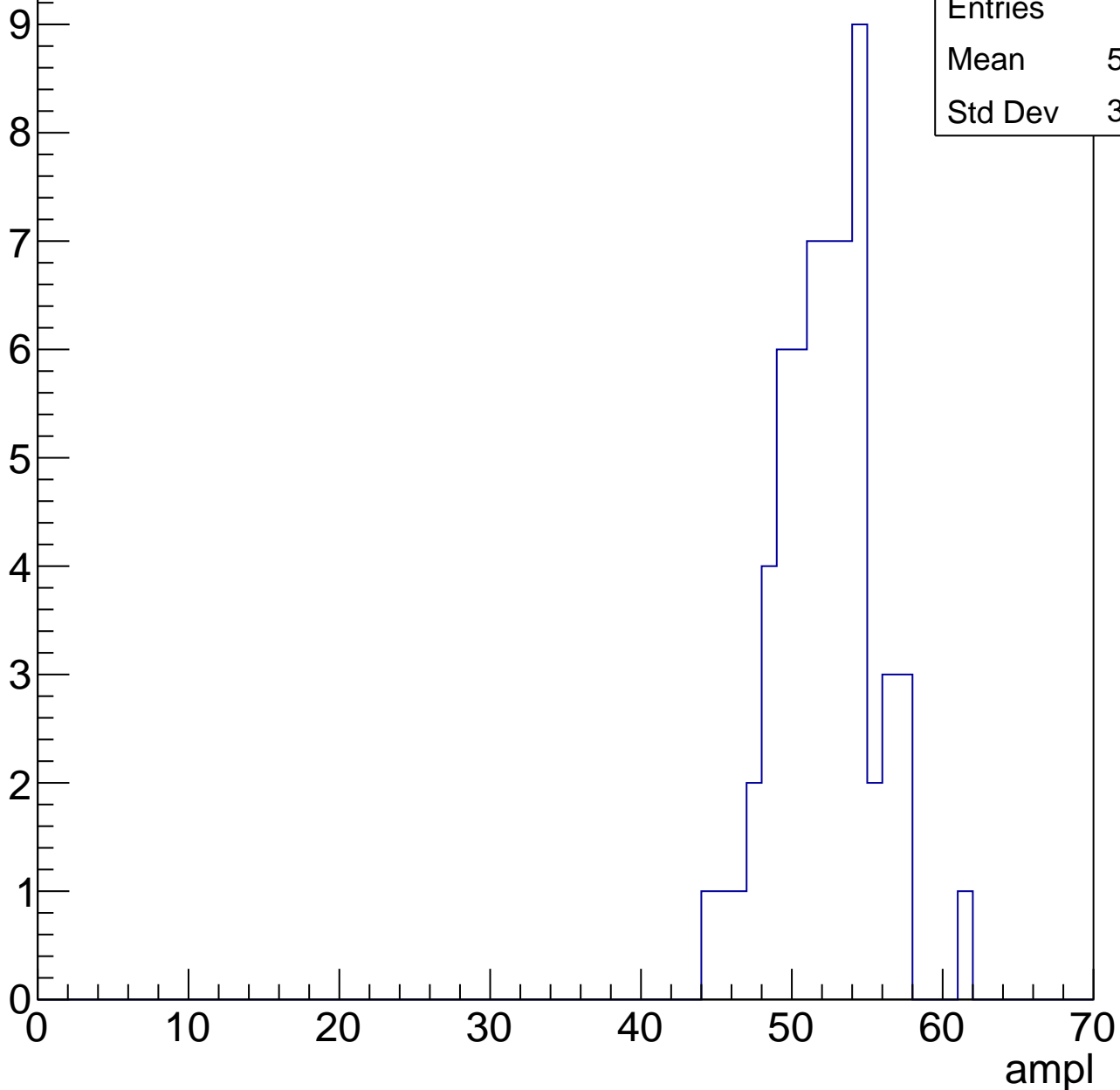


# B1L002S, U18-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	51.72
Std Dev	3.184

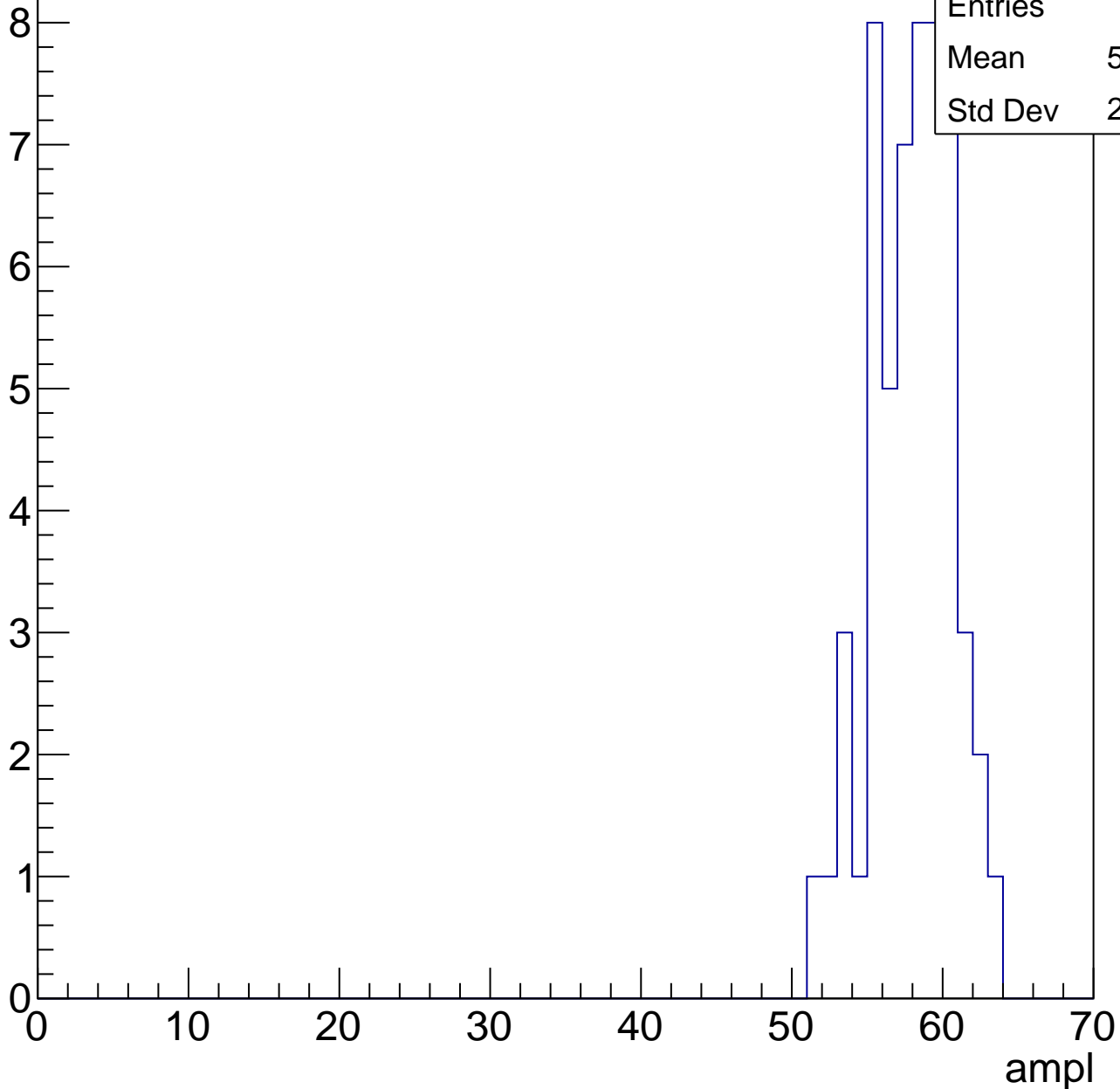


# B1L002S, U18-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.52
Std Dev	2.625

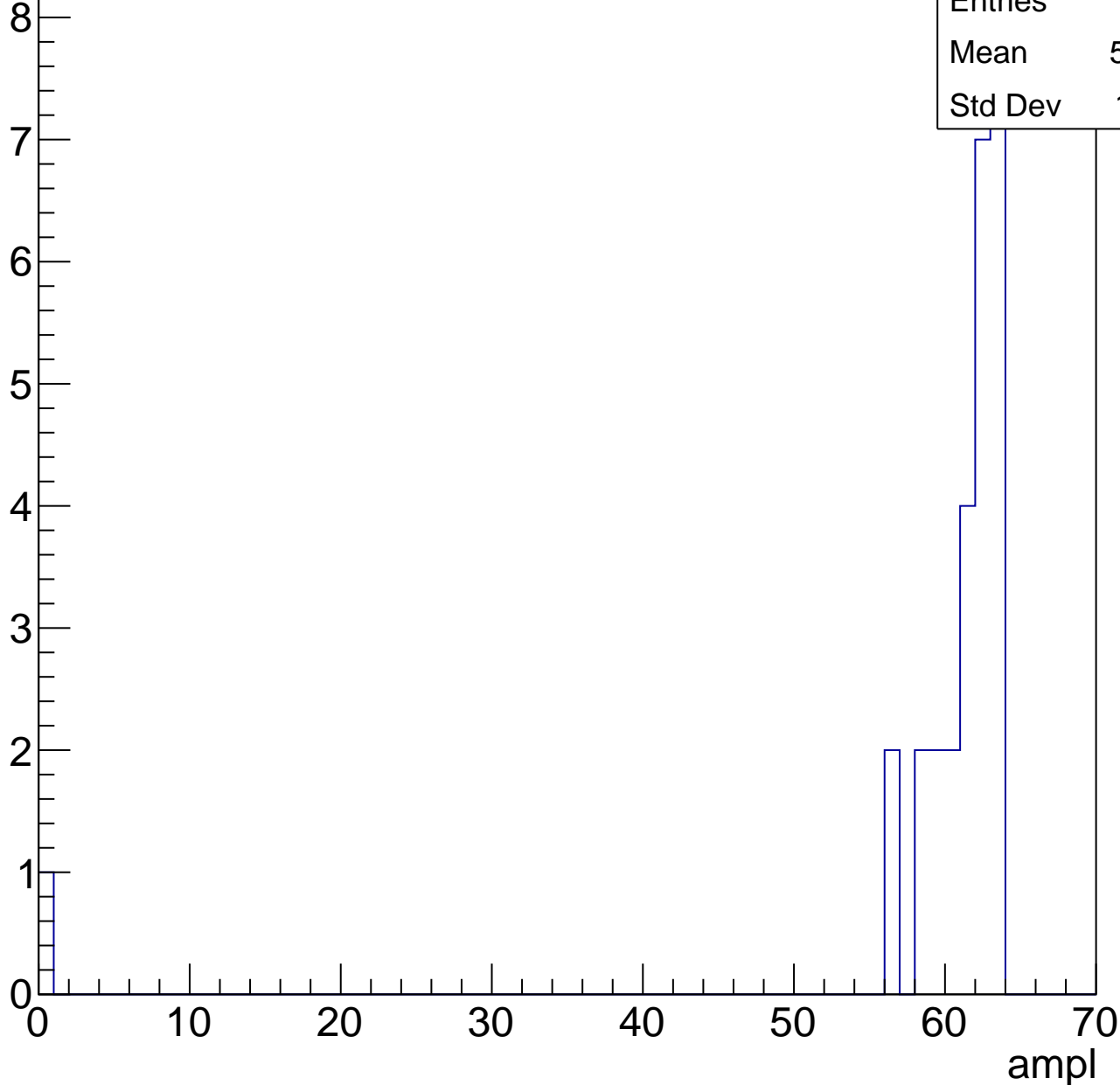


# B1L002S, U18-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.86
Std Dev	11.51

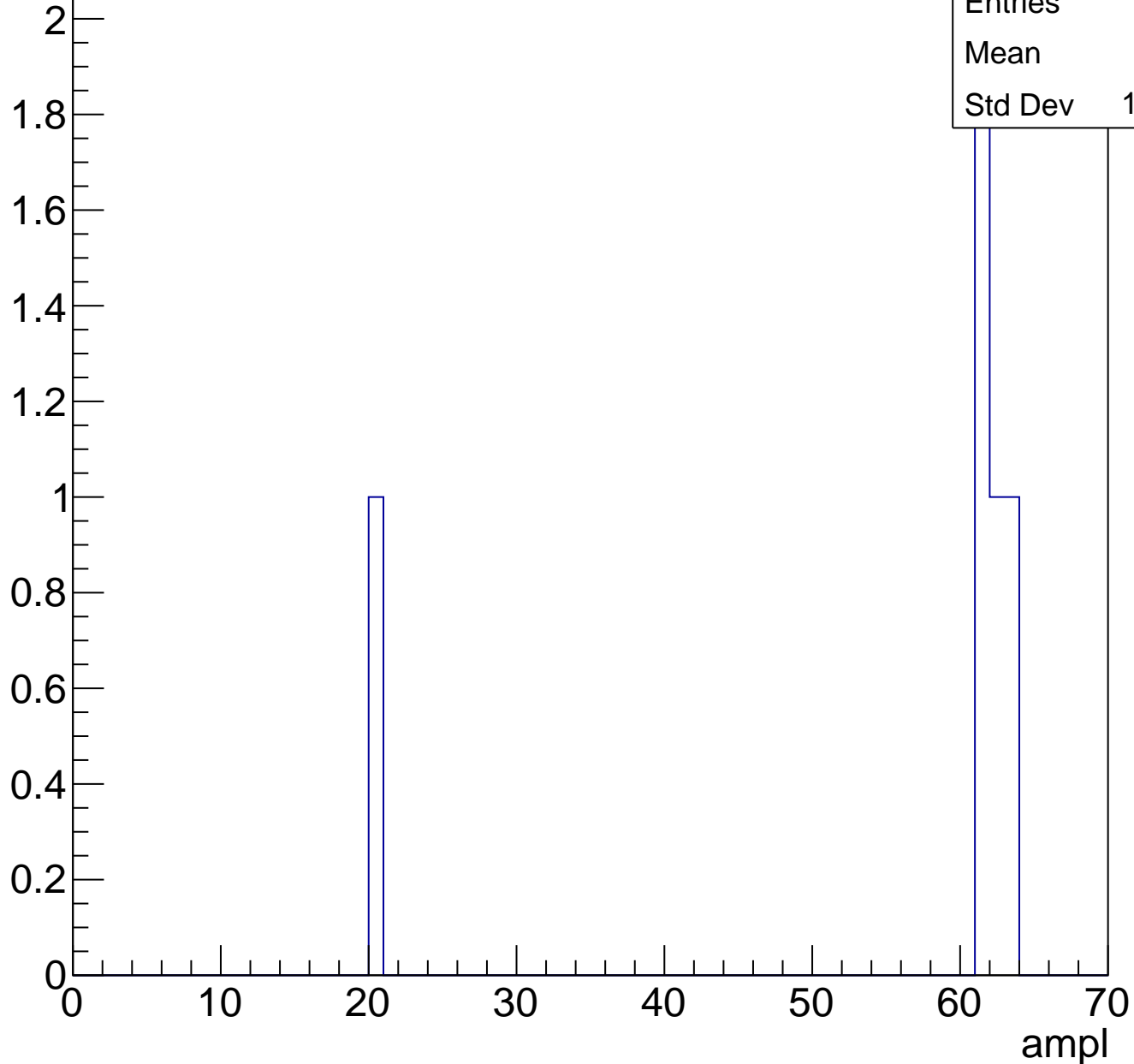




# B1L002S, U18-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	53.4
Std Dev	16.72

# B1L002S, U18-ch108, adc0

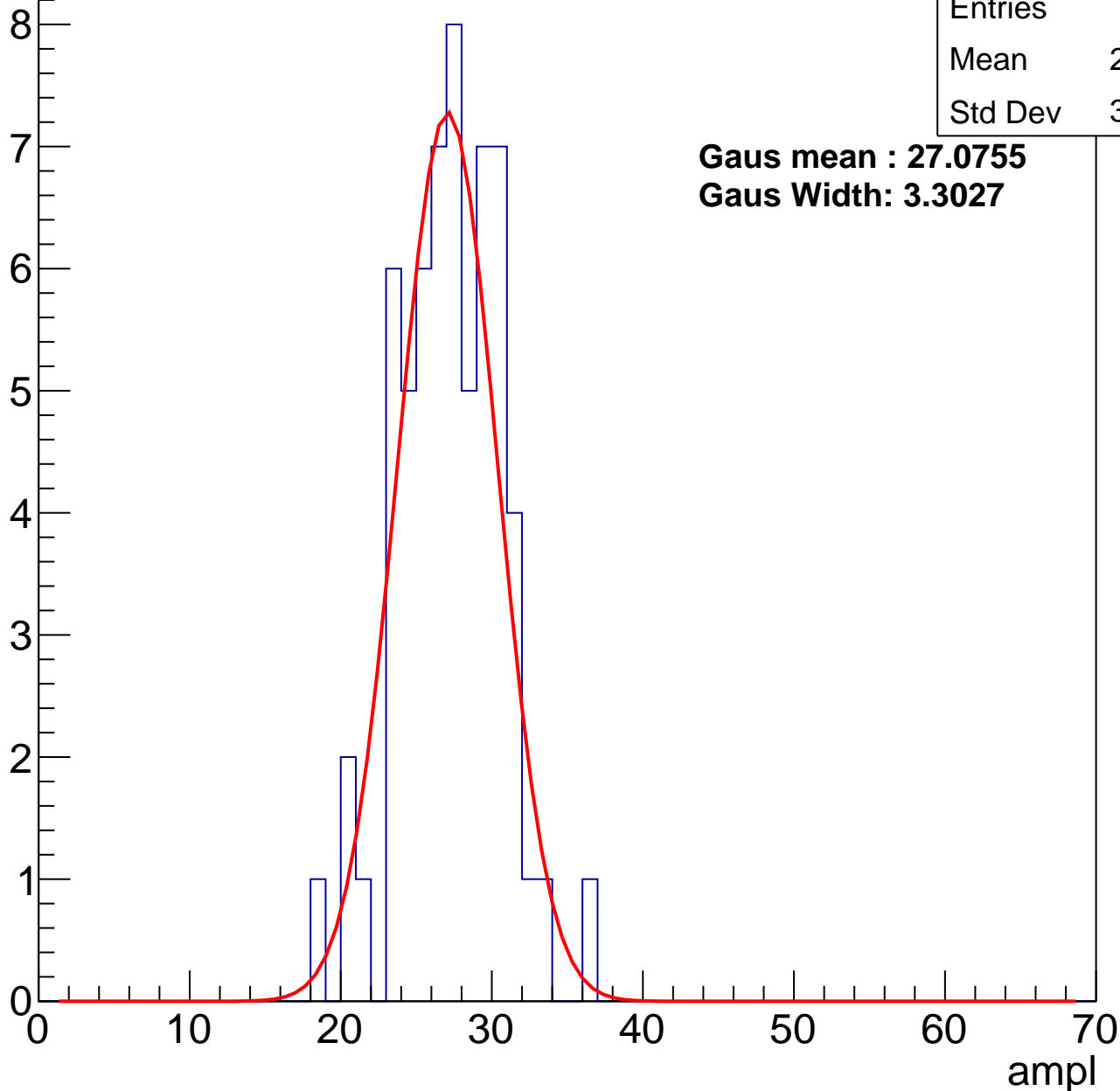
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26.82
Std Dev	3.329

**Gaus mean : 27.0755**

**Gaus Width: 3.3027**



# B1L002S, U18-ch108, adc1

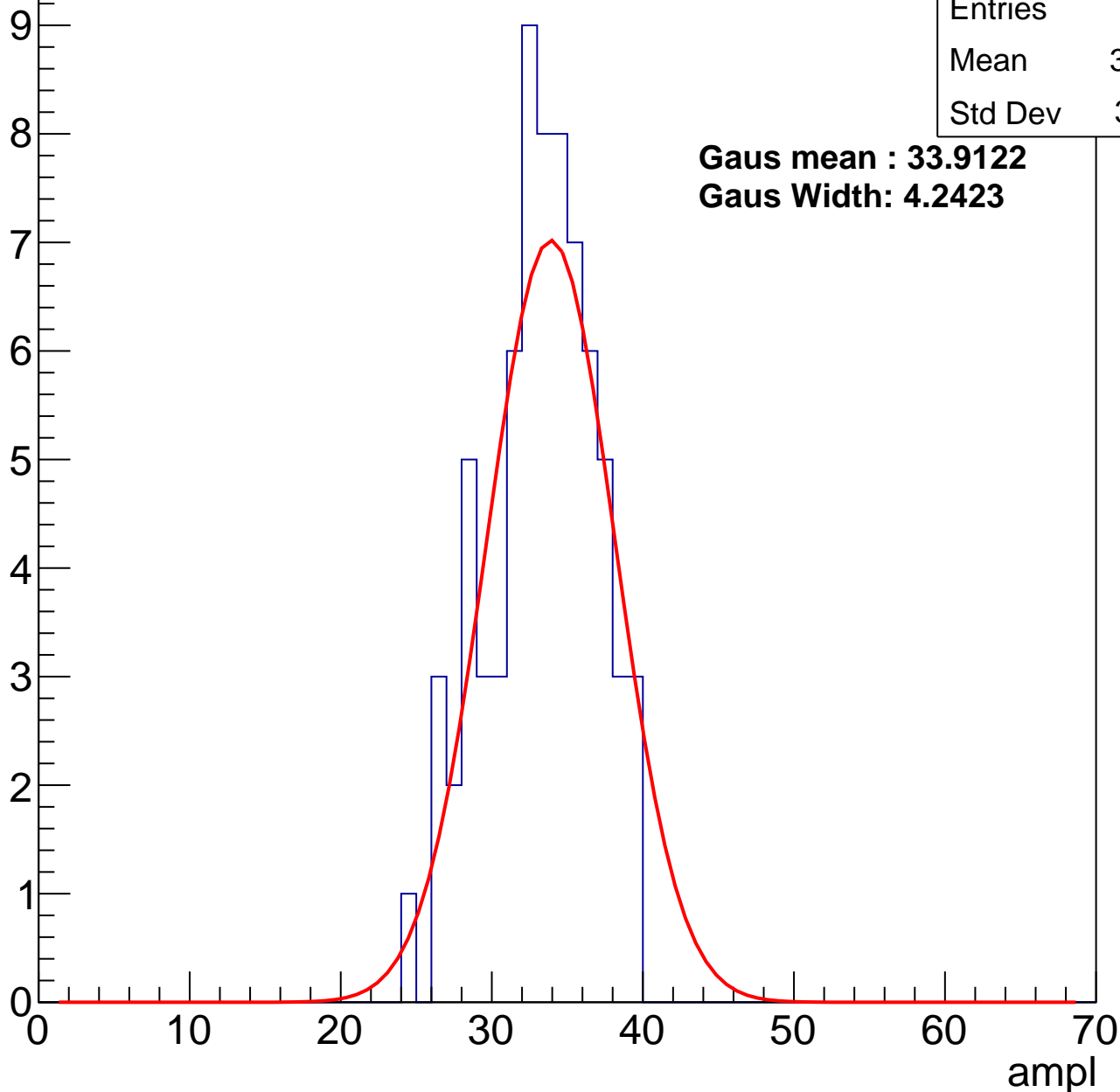
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	32.78
Std Dev	3.501

**Gaus mean : 33.9122**

**Gaus Width: 4.2423**



# B1L002S, U18-ch108, adc2

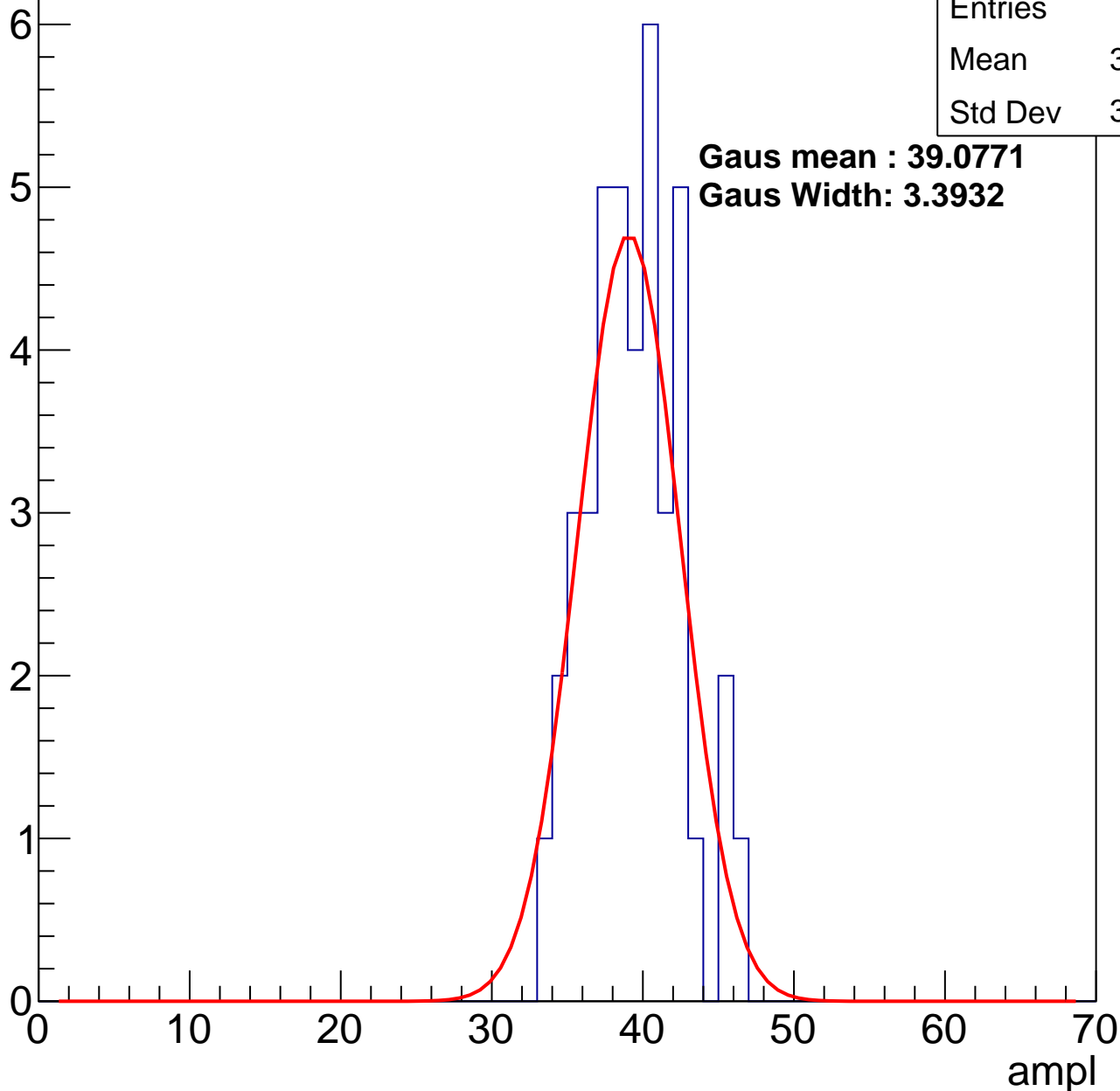
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	38.95
Std Dev	3.068

**Gaus mean : 39.0771**

**Gaus Width: 3.3932**

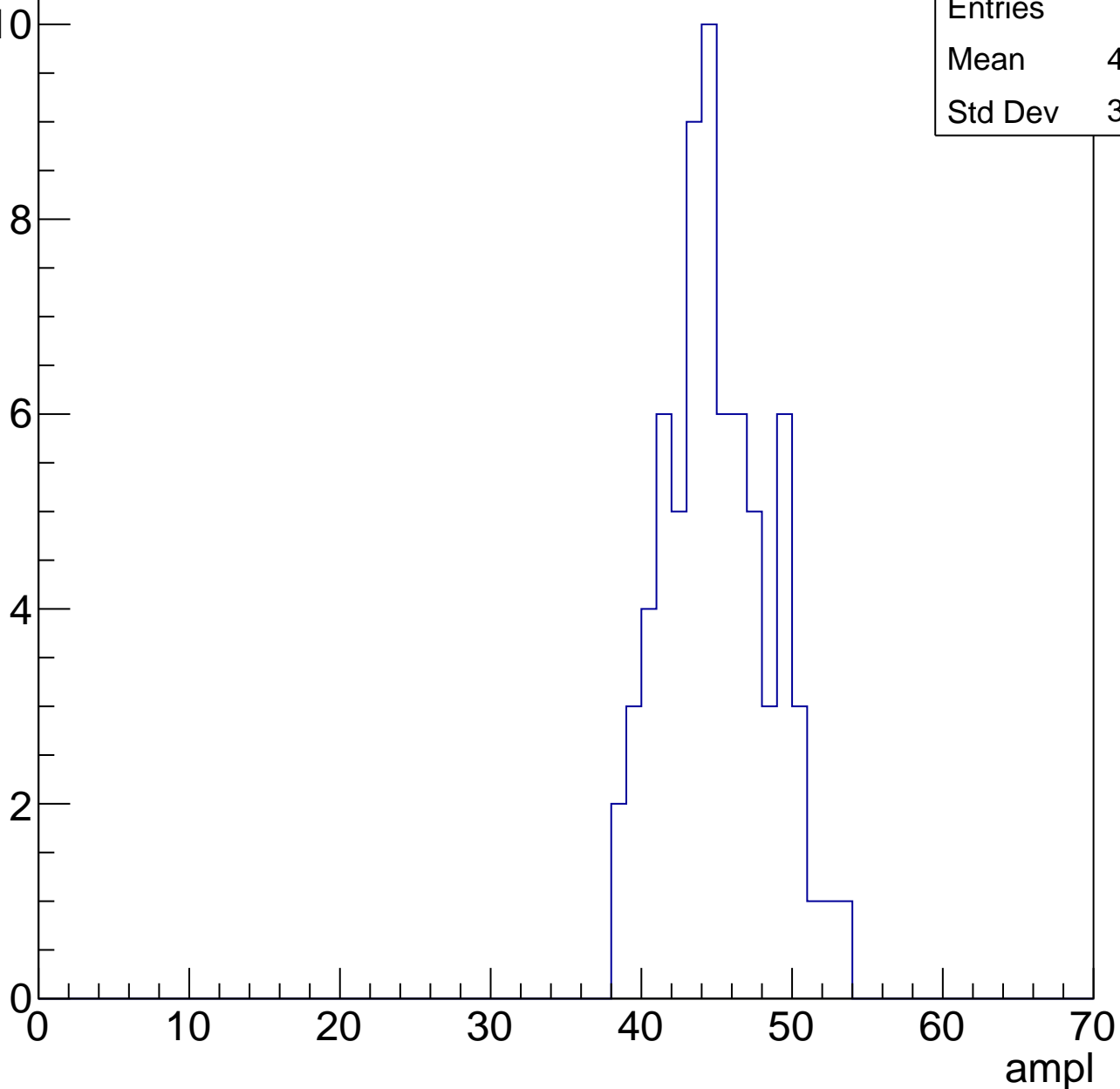


# B1L002S, U18-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	44.52
Std Dev	3.459

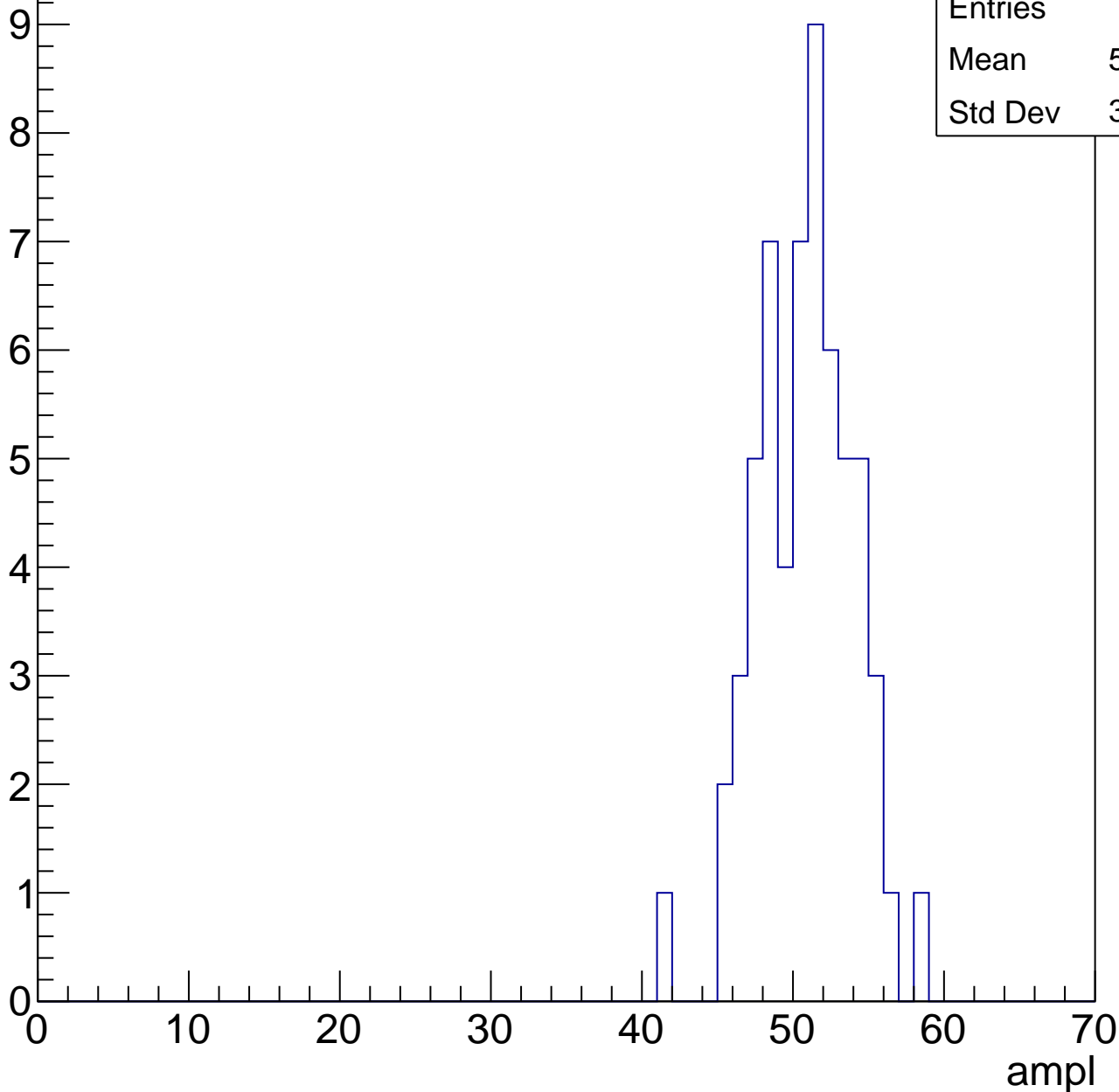


# B1L002S, U18-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	50.36
Std Dev	3.139

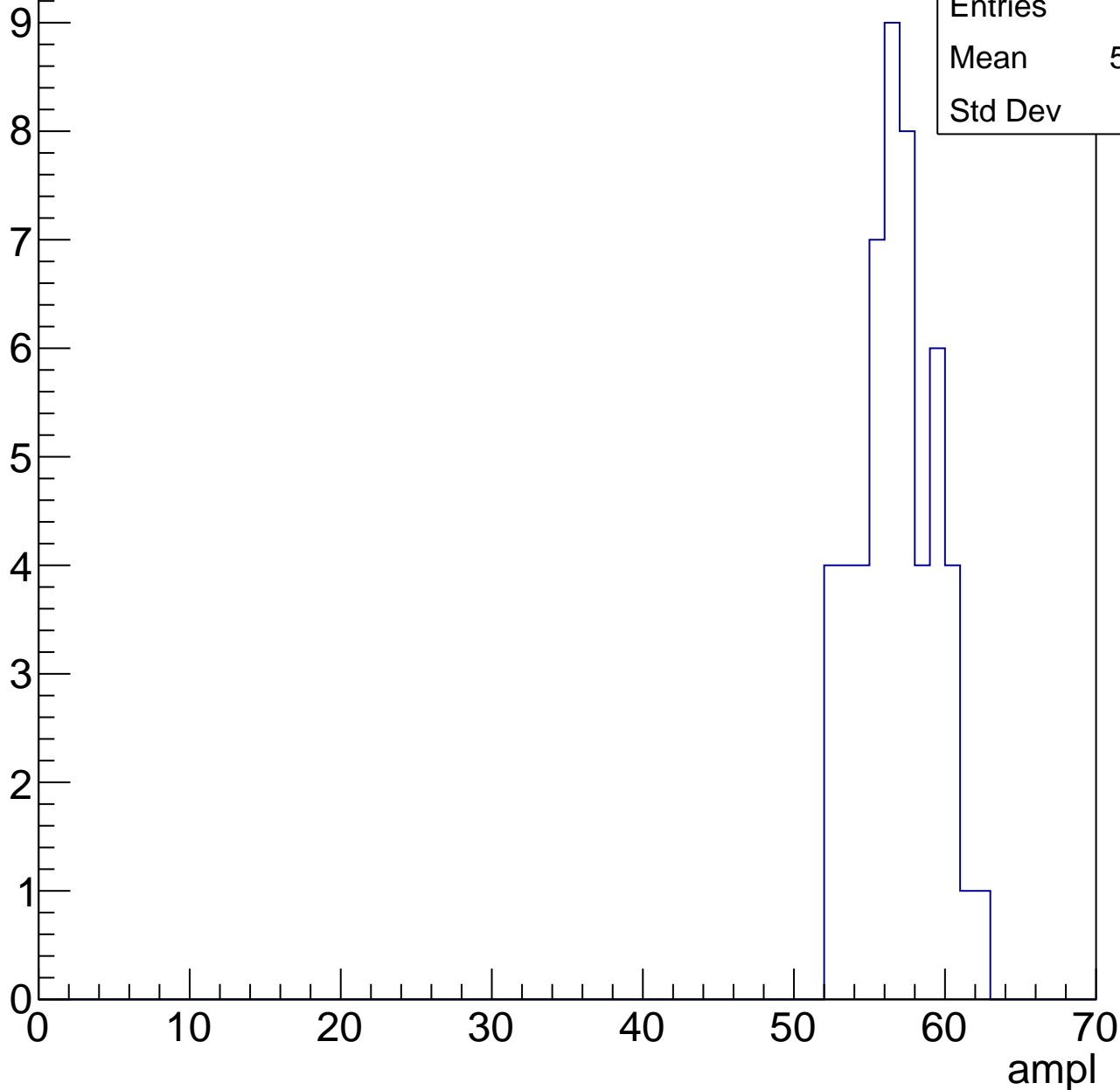


# B1L002S, U18-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	56.35
Std Dev	2.48

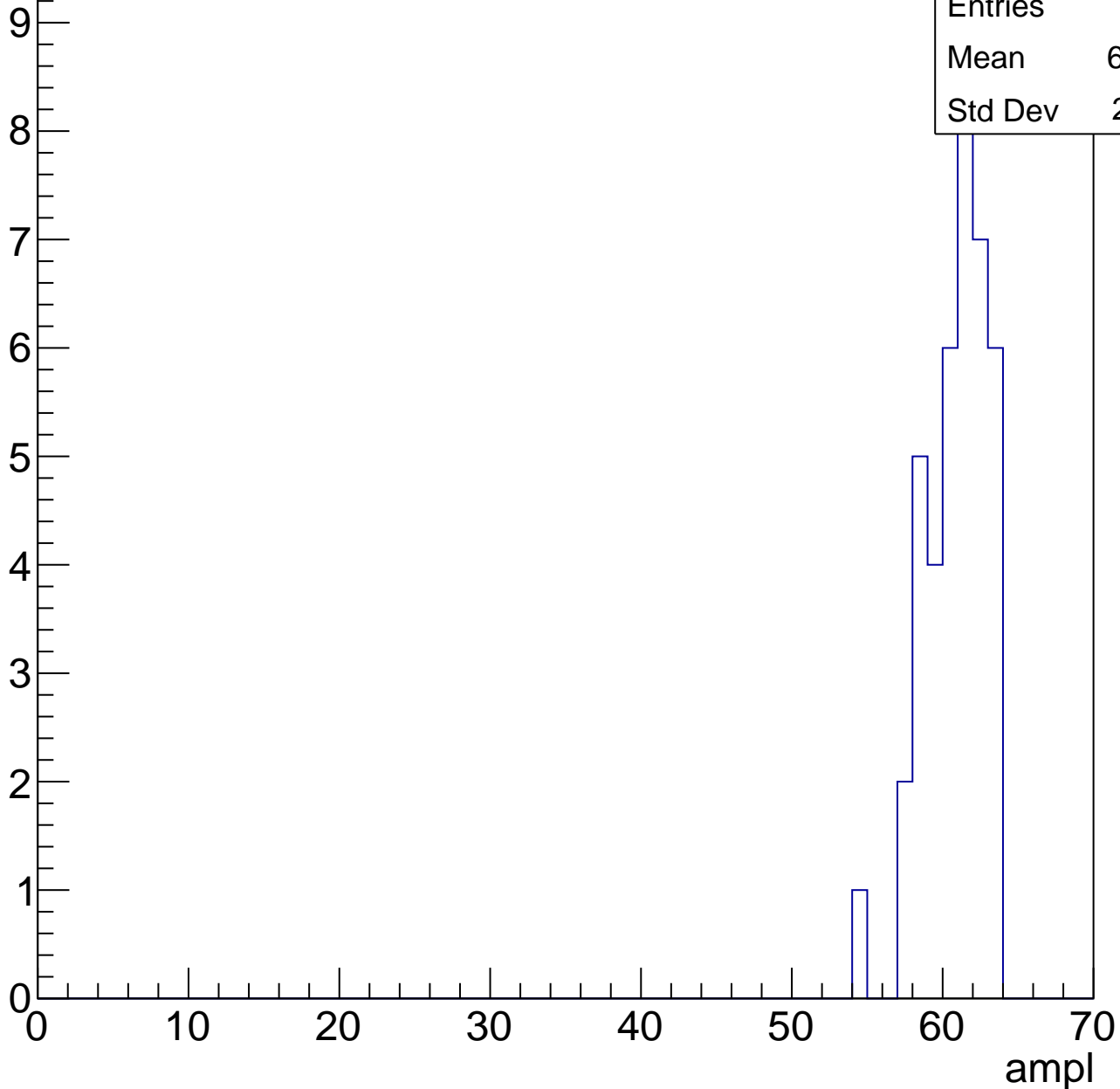


# B1L002S, U18-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	60.38
Std Dev	2.021

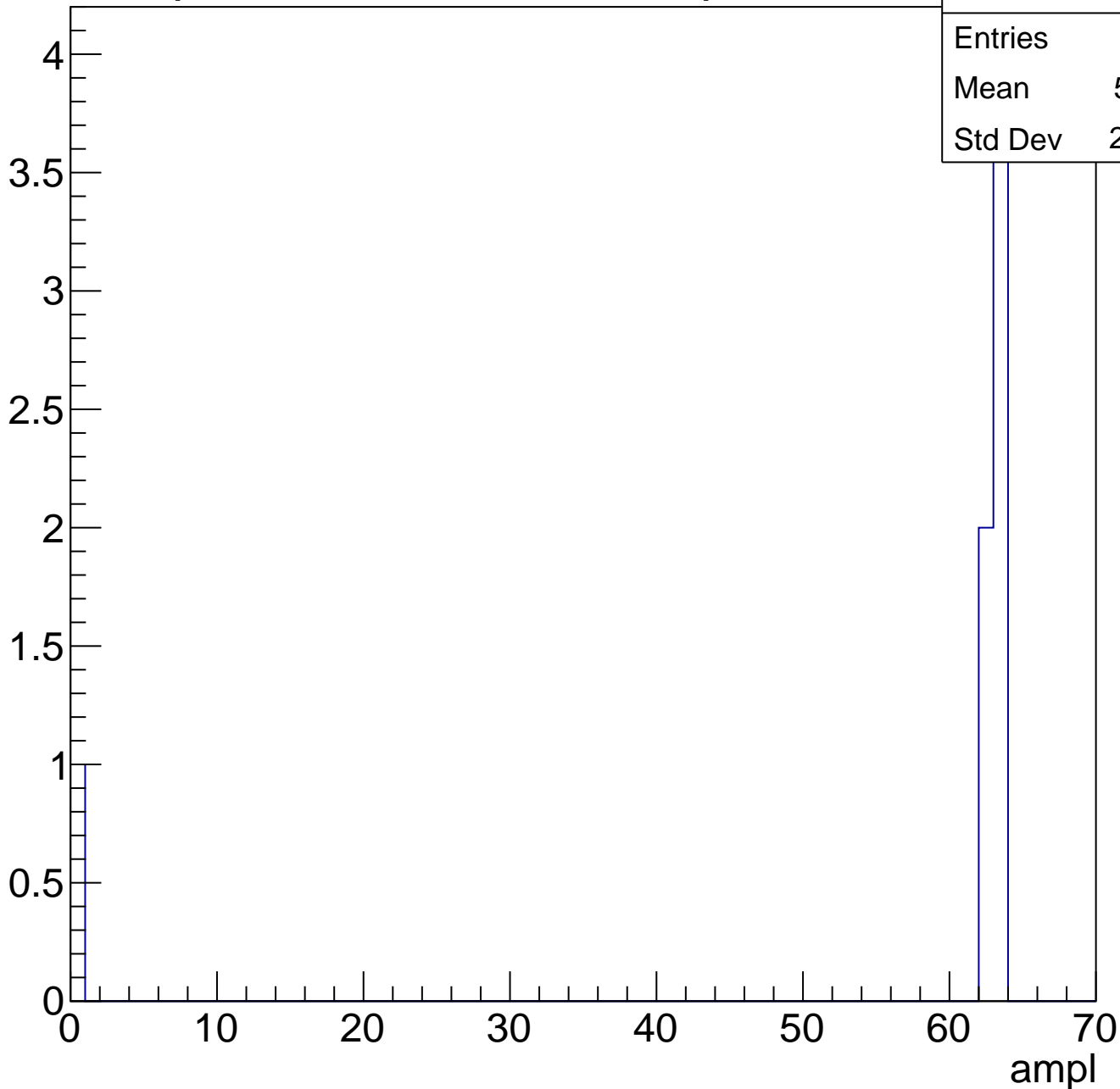




# B1L002S, U18-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch109, adc0

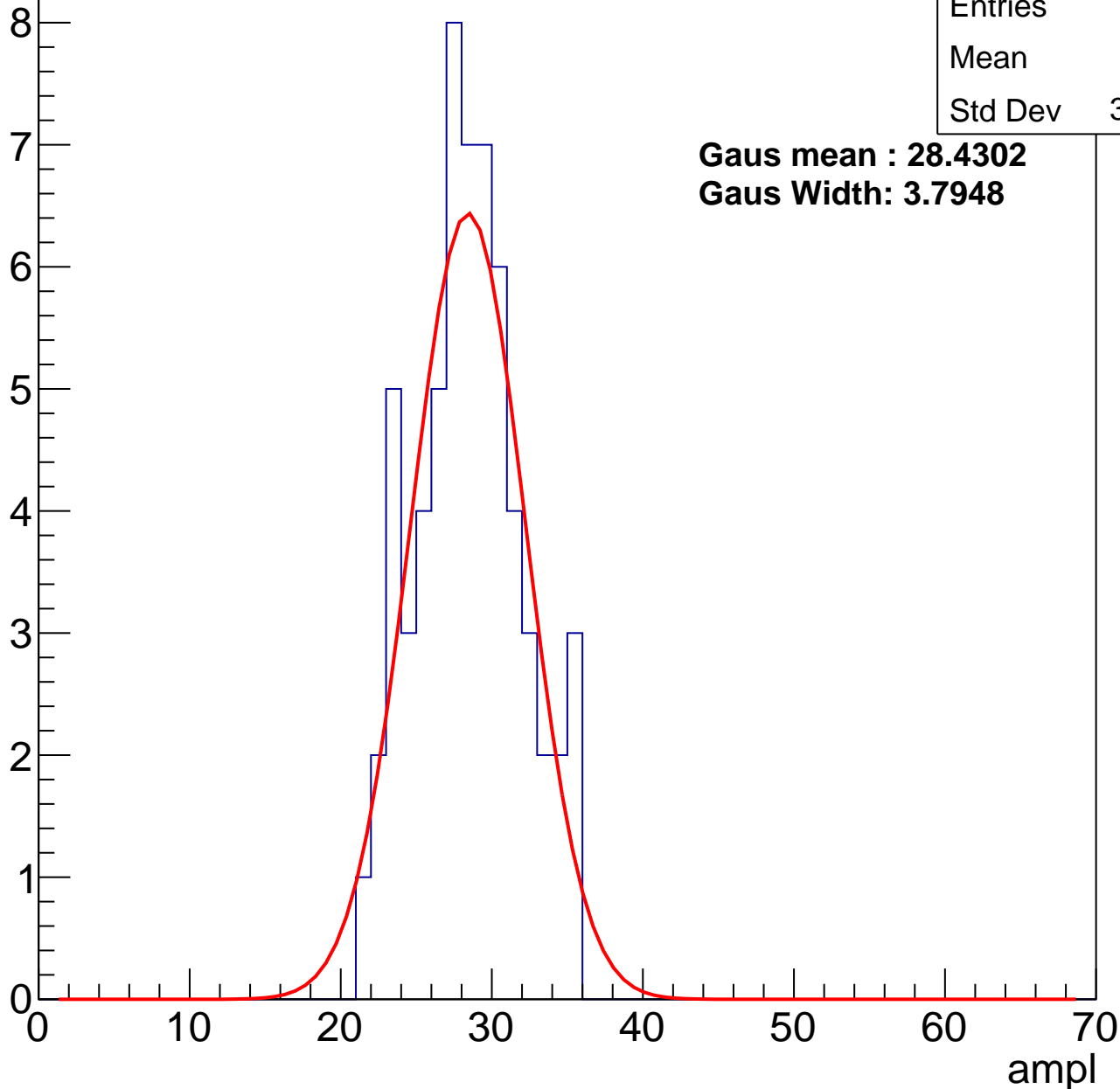
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28
Std Dev	3.459

**Gaus mean : 28.4302**

**Gaus Width: 3.7948**



# B1L002S, U18-ch109, adc1

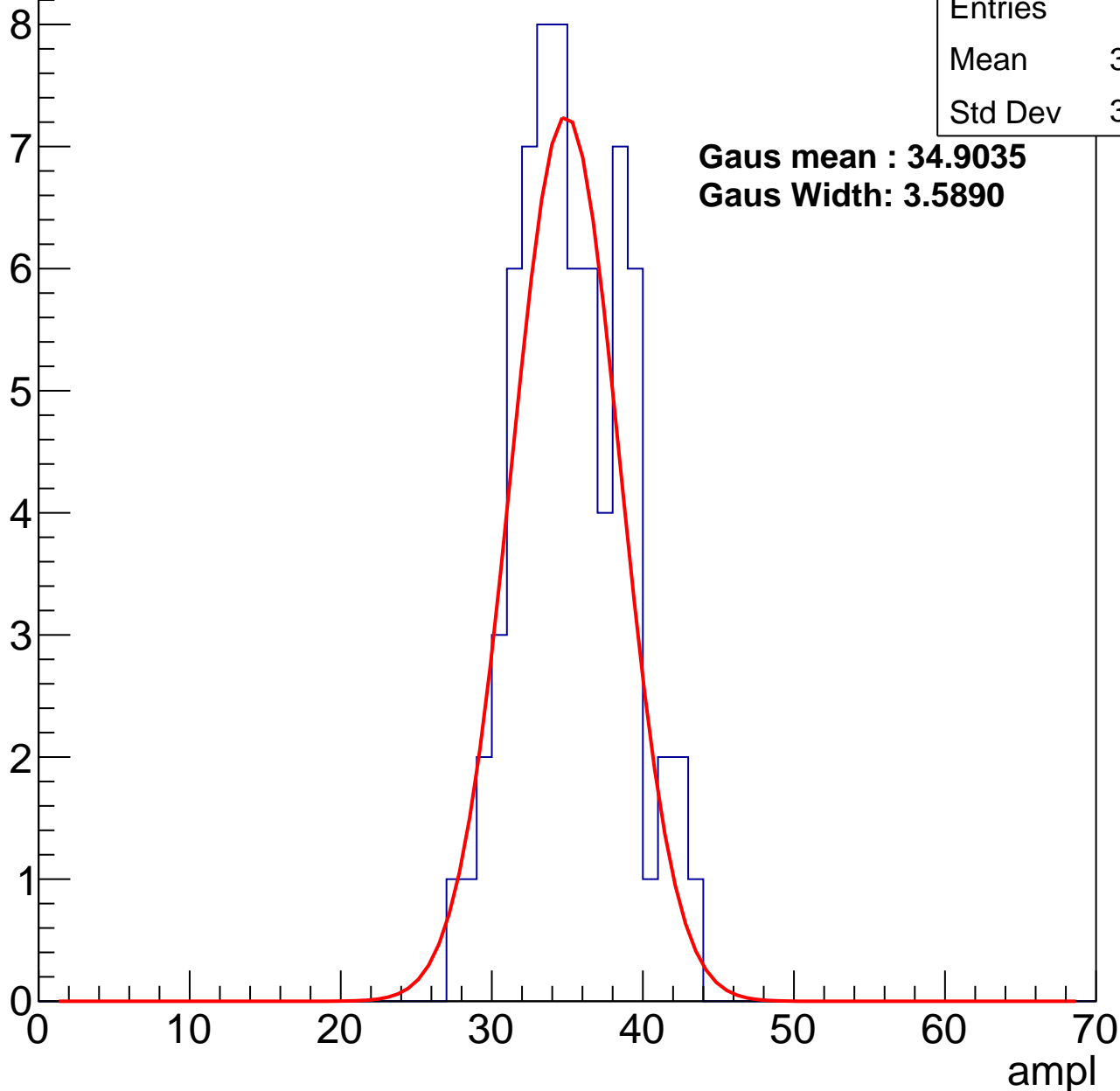
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.82
Std Dev	3.554

**Gaus mean : 34.9035**

**Gaus Width: 3.5890**



# B1L002S, U18-ch109, adc2

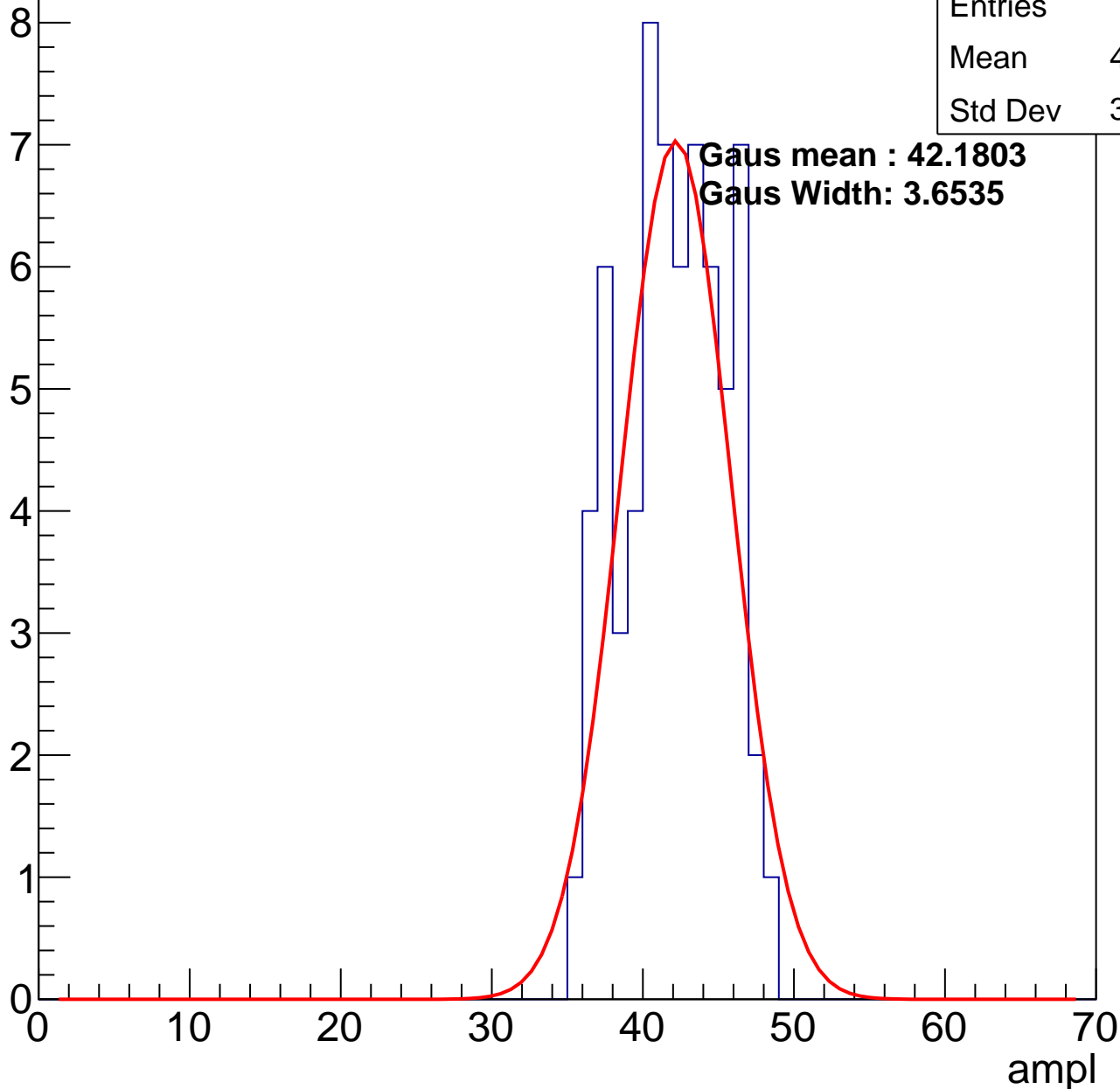
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	41.55
Std Dev	3.293

**Gaus mean : 42.1803**

**Gaus Width: 3.6535**

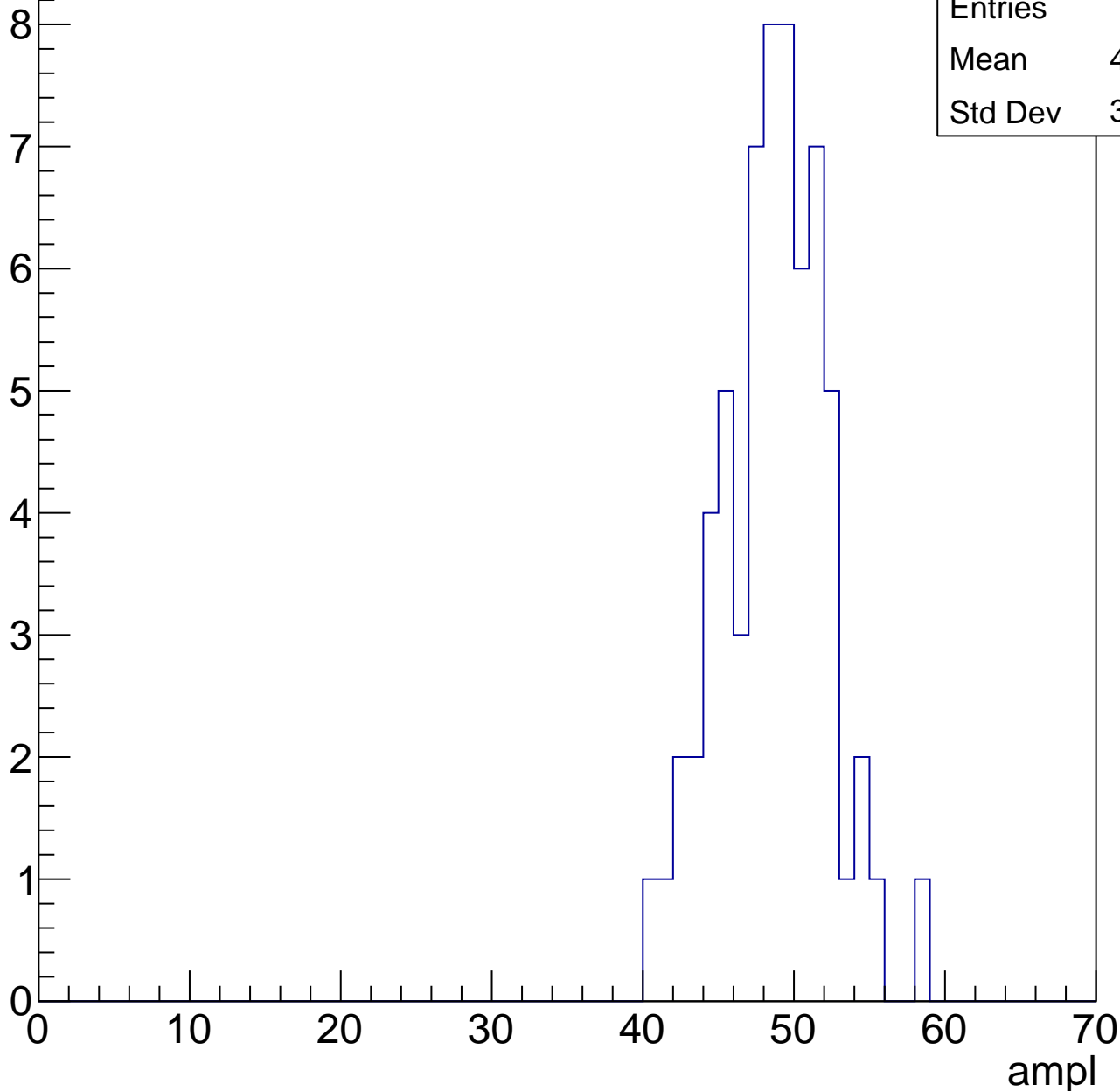


# B1L002S, U18-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	48.22
Std Dev	3.493

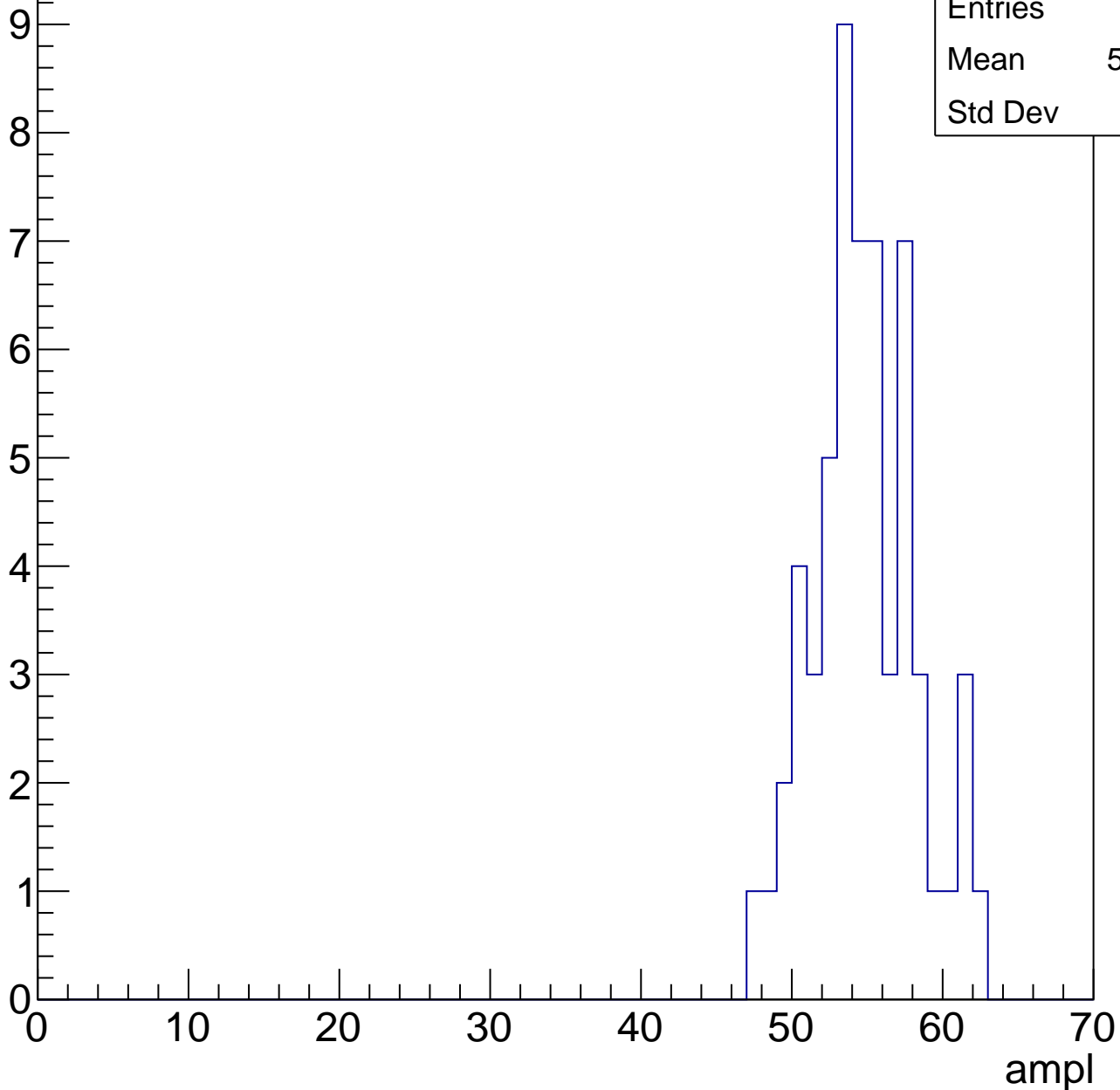


# B1L002S, U18-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

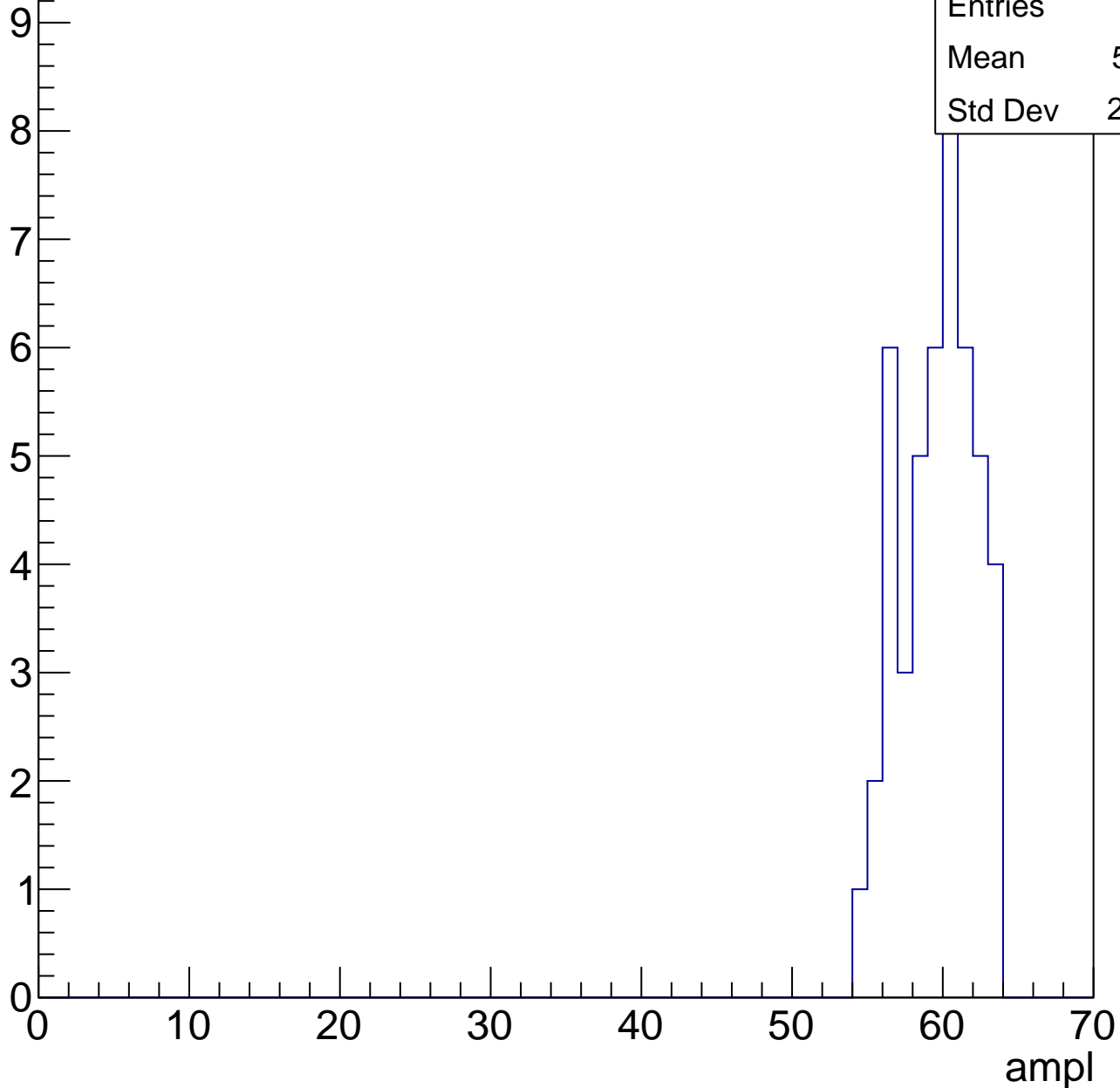
Entries	58
Mean	54.33
Std Dev	3.35



# B1L002S, U18-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

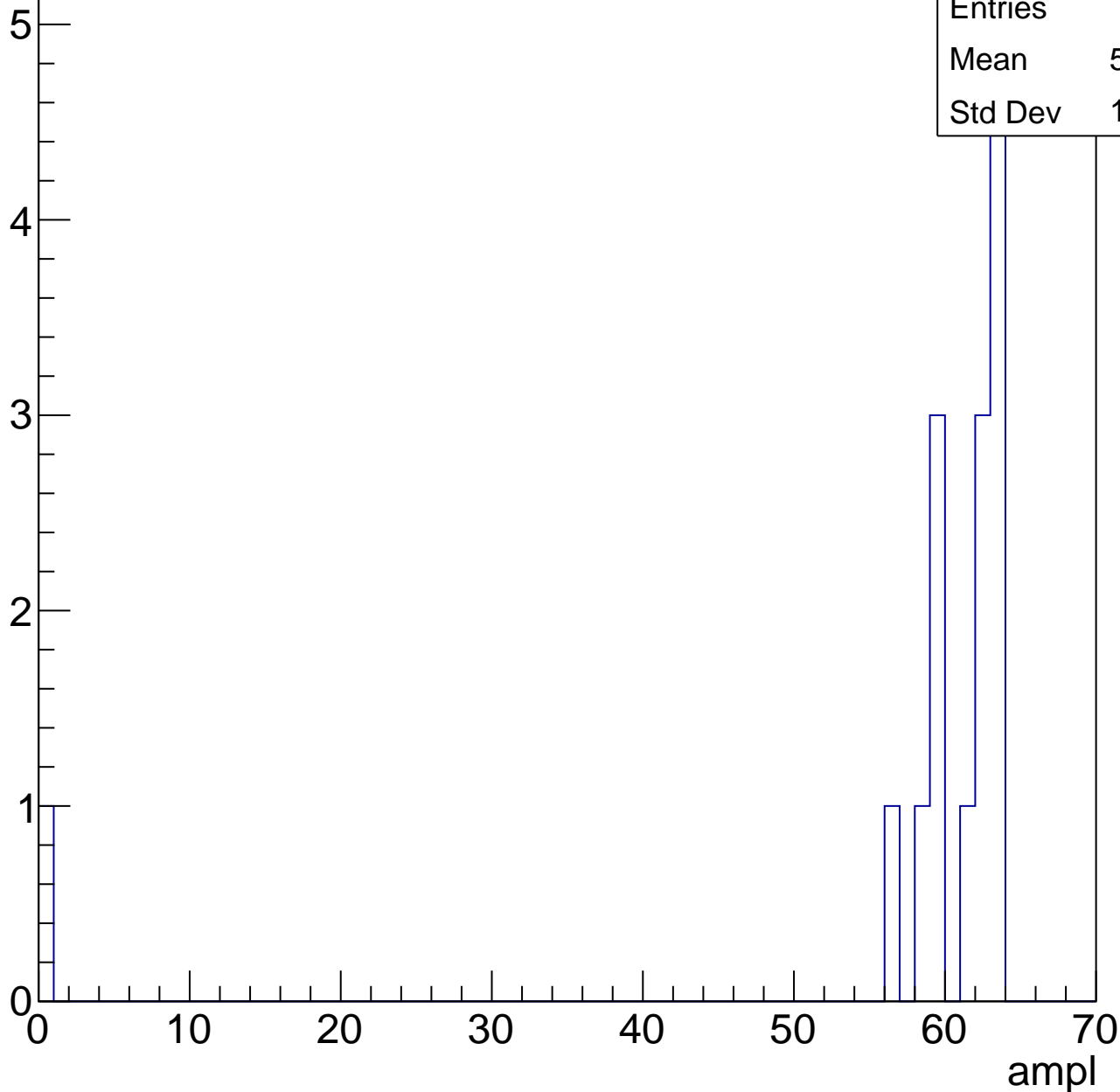


# B1L002S, U18-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	56.87
Std Dev	15.35





# B1L002S, U18-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch110, adc0

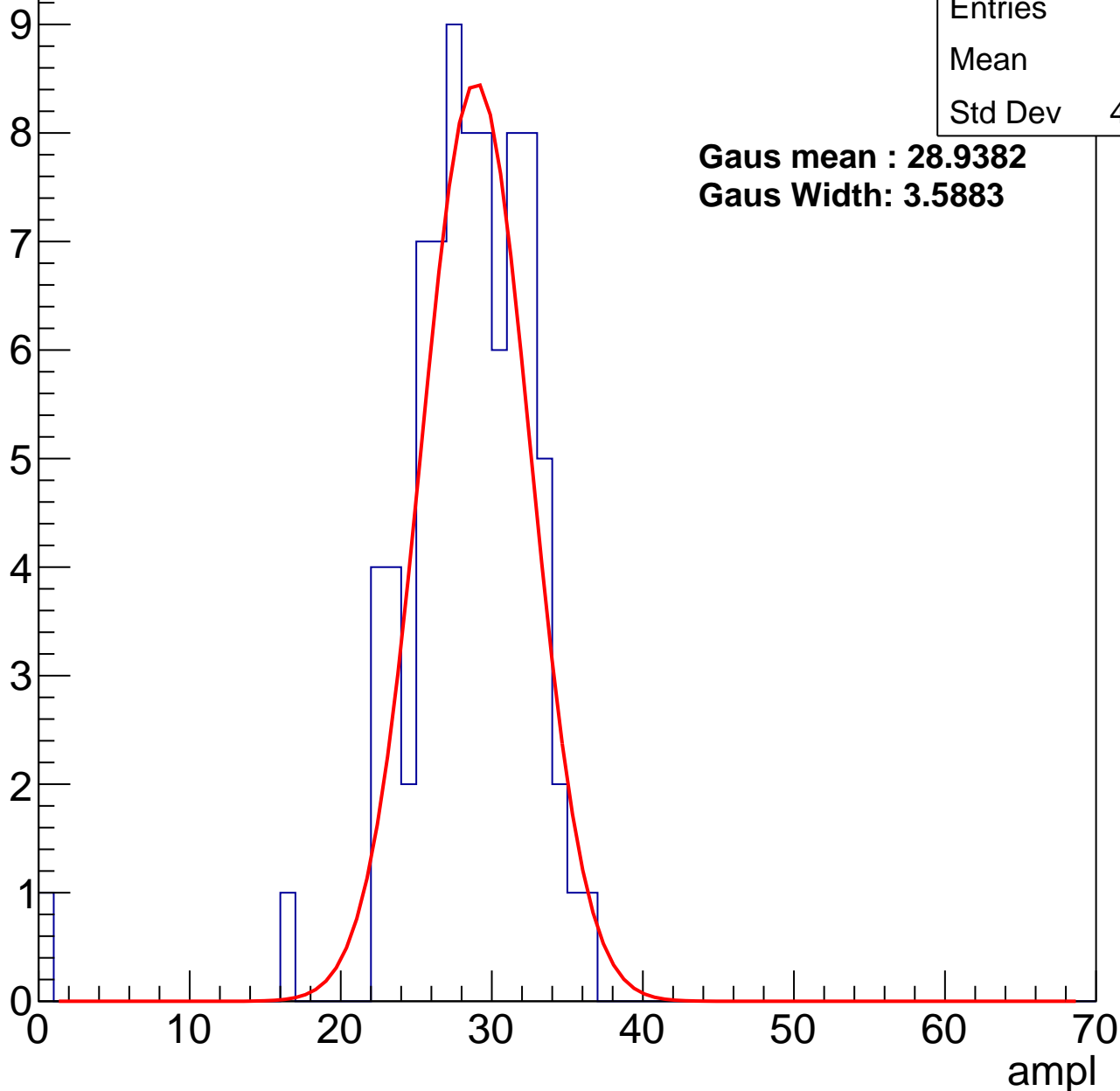
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	27.9
Std Dev	4.744

**Gaus mean : 28.9382**

**Gaus Width: 3.5883**



# B1L002S, U18-ch110, adc1

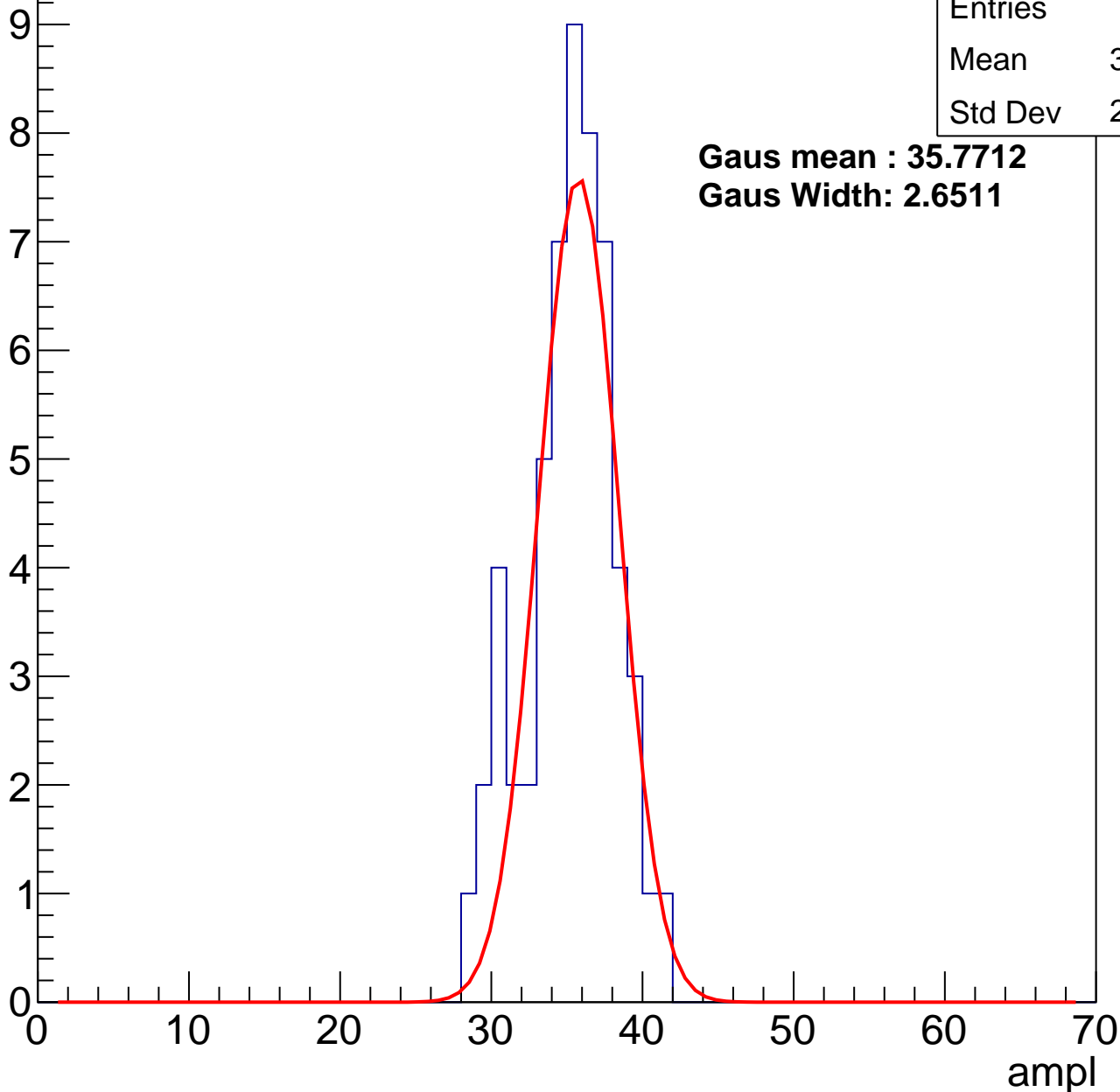
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.77
Std Dev	2.915

**Gaus mean : 35.7712**

**Gaus Width: 2.6511**



# B1L002S, U18-ch110, adc2

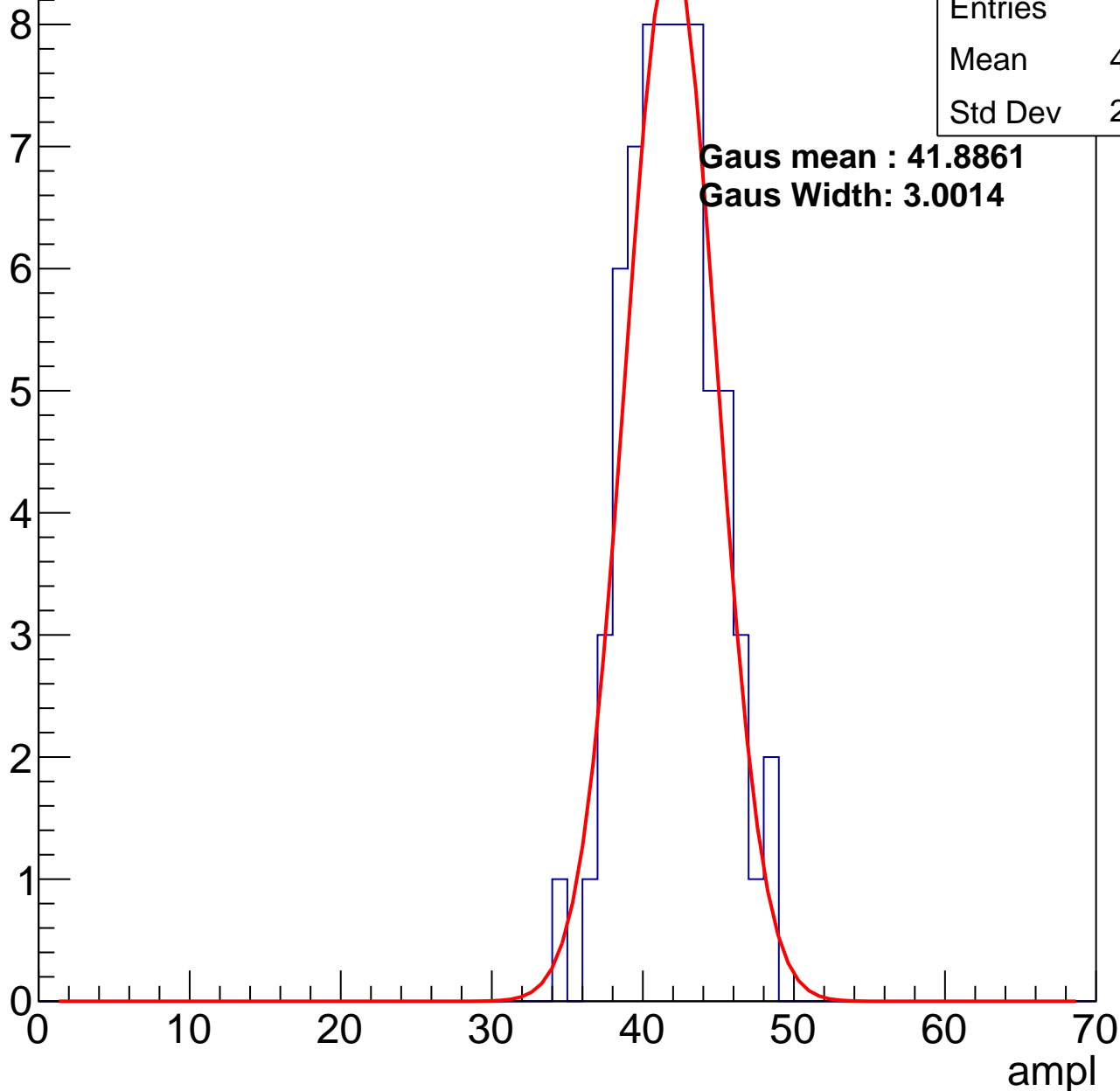
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.45
Std Dev	2.945

**Gaus mean : 41.8861**

**Gaus Width: 3.0014**

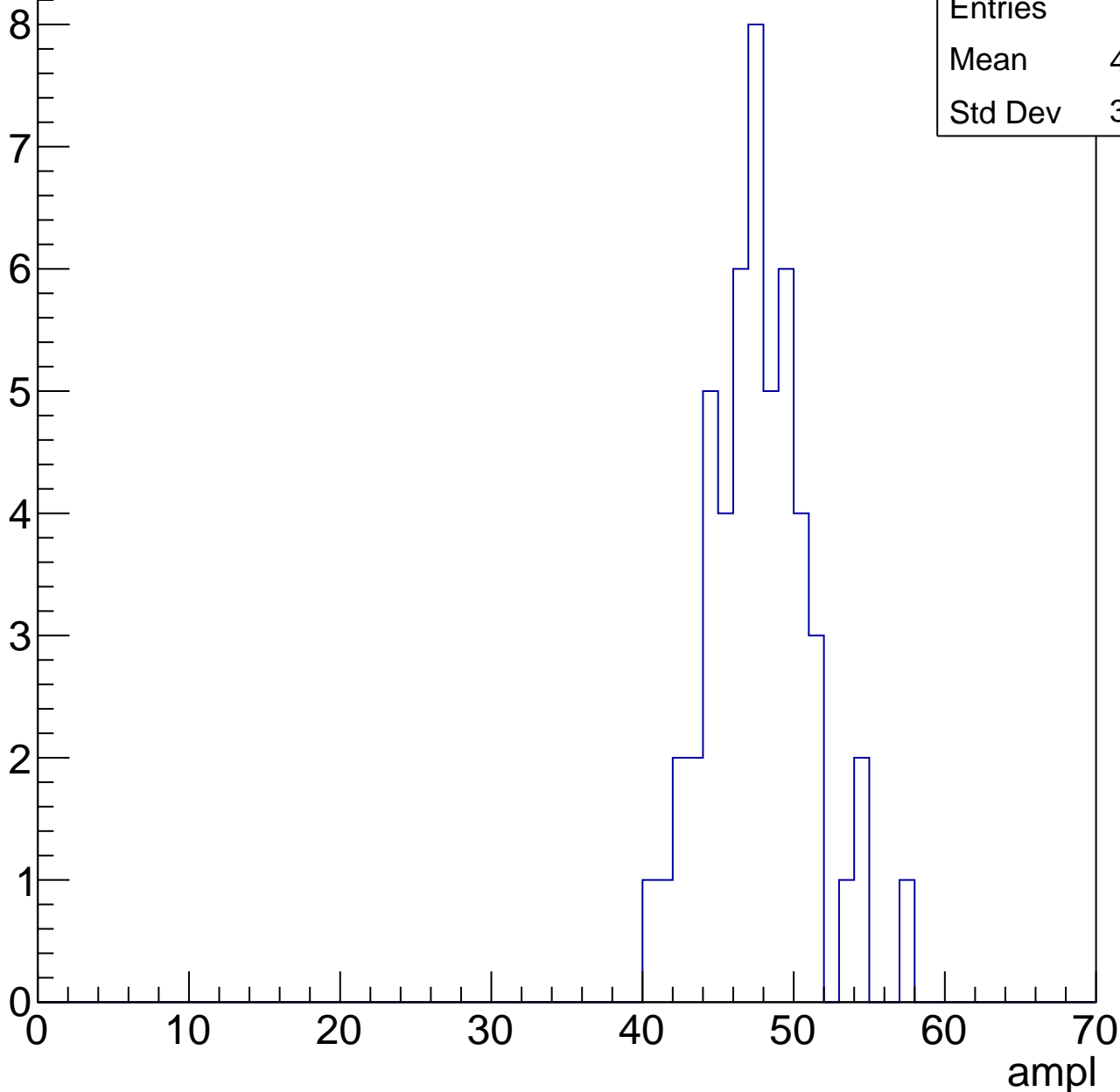


# B1L002S, U18-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	47.22
Std Dev	3.368

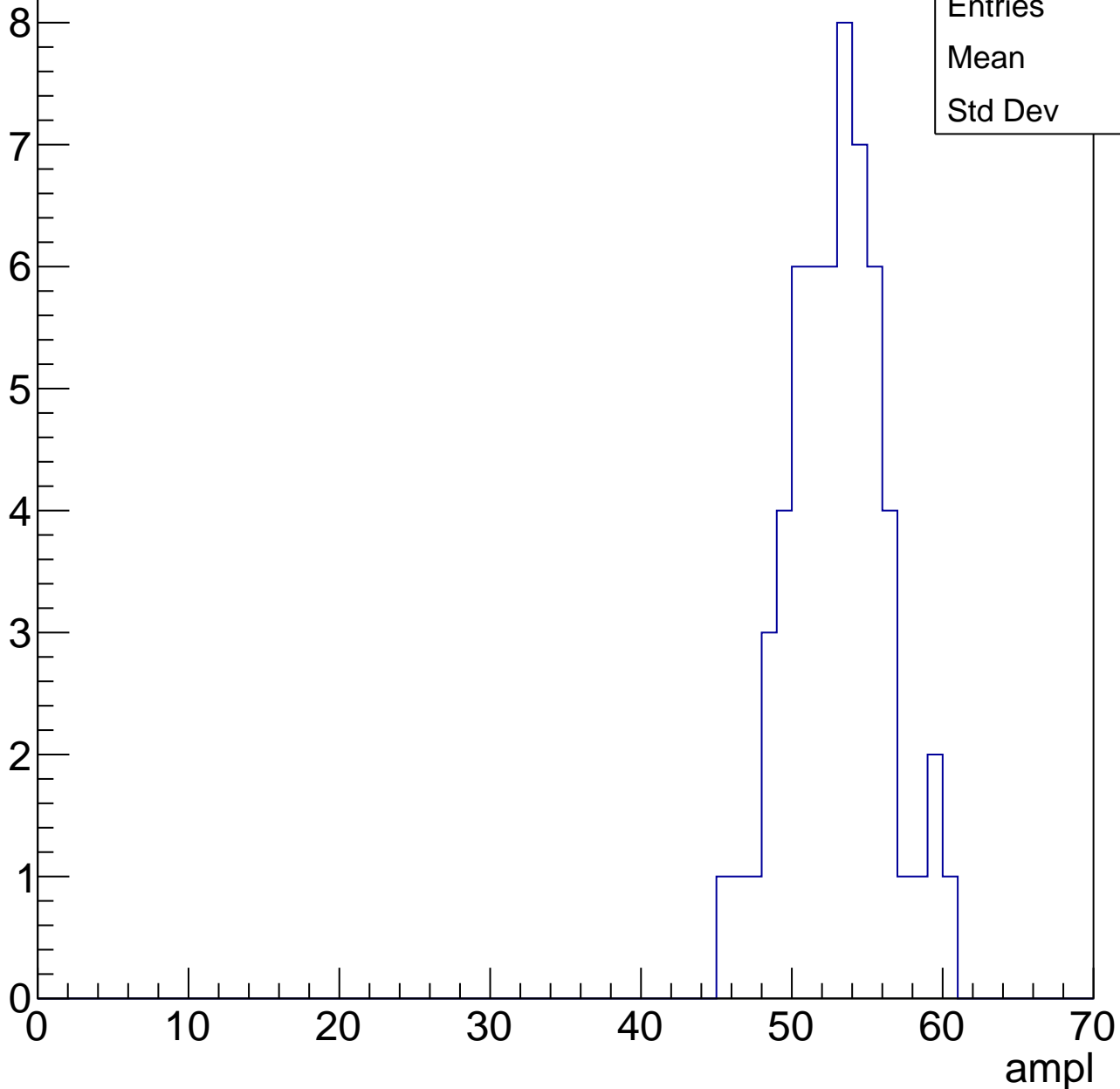


# B1L002S, U18-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.5
Std Dev	3.18

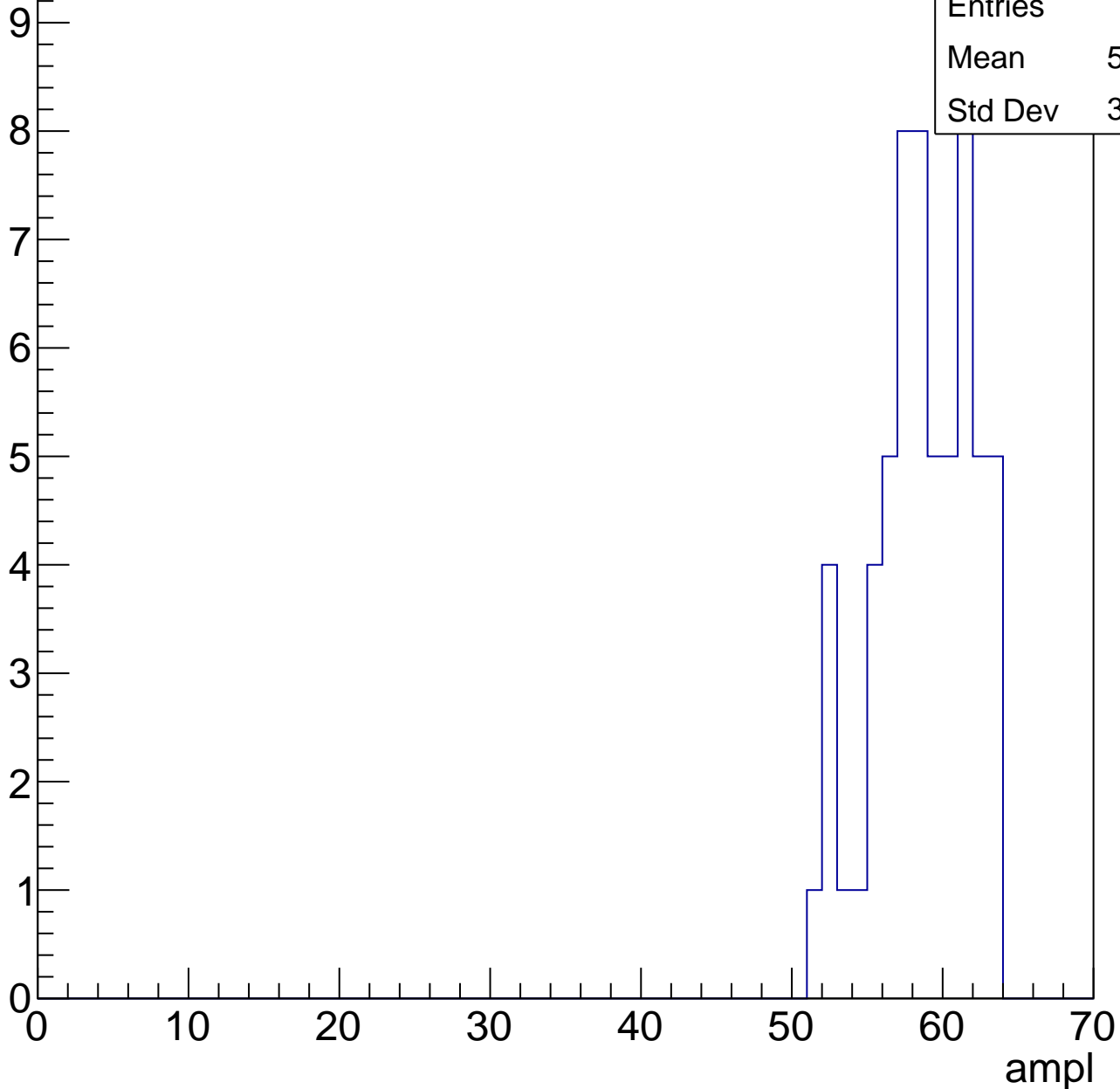


# B1L002S, U18-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

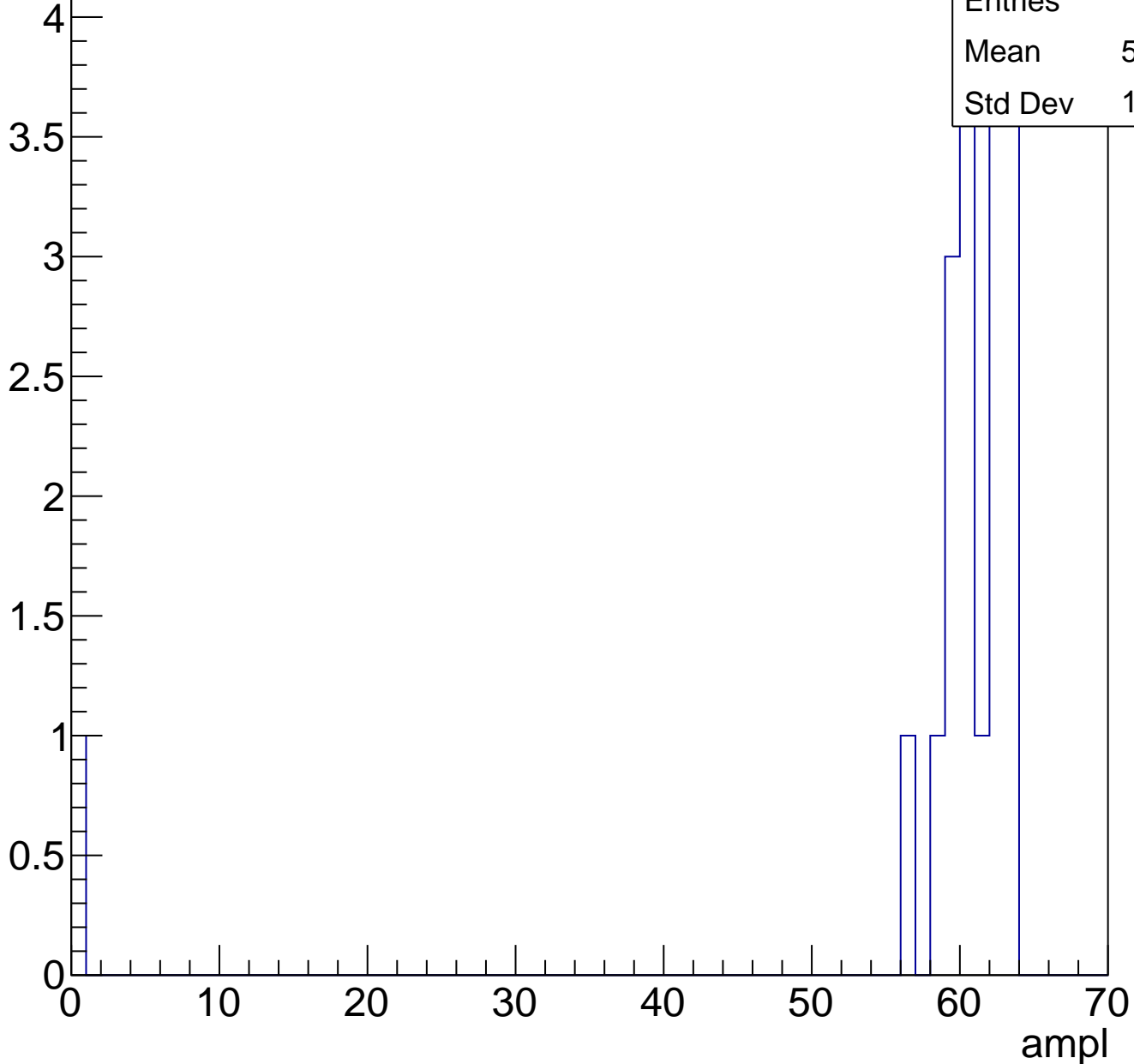
Entries	61
Mean	58.28
Std Dev	3.147



# B1L002S, U18-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U18-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch111, adc0

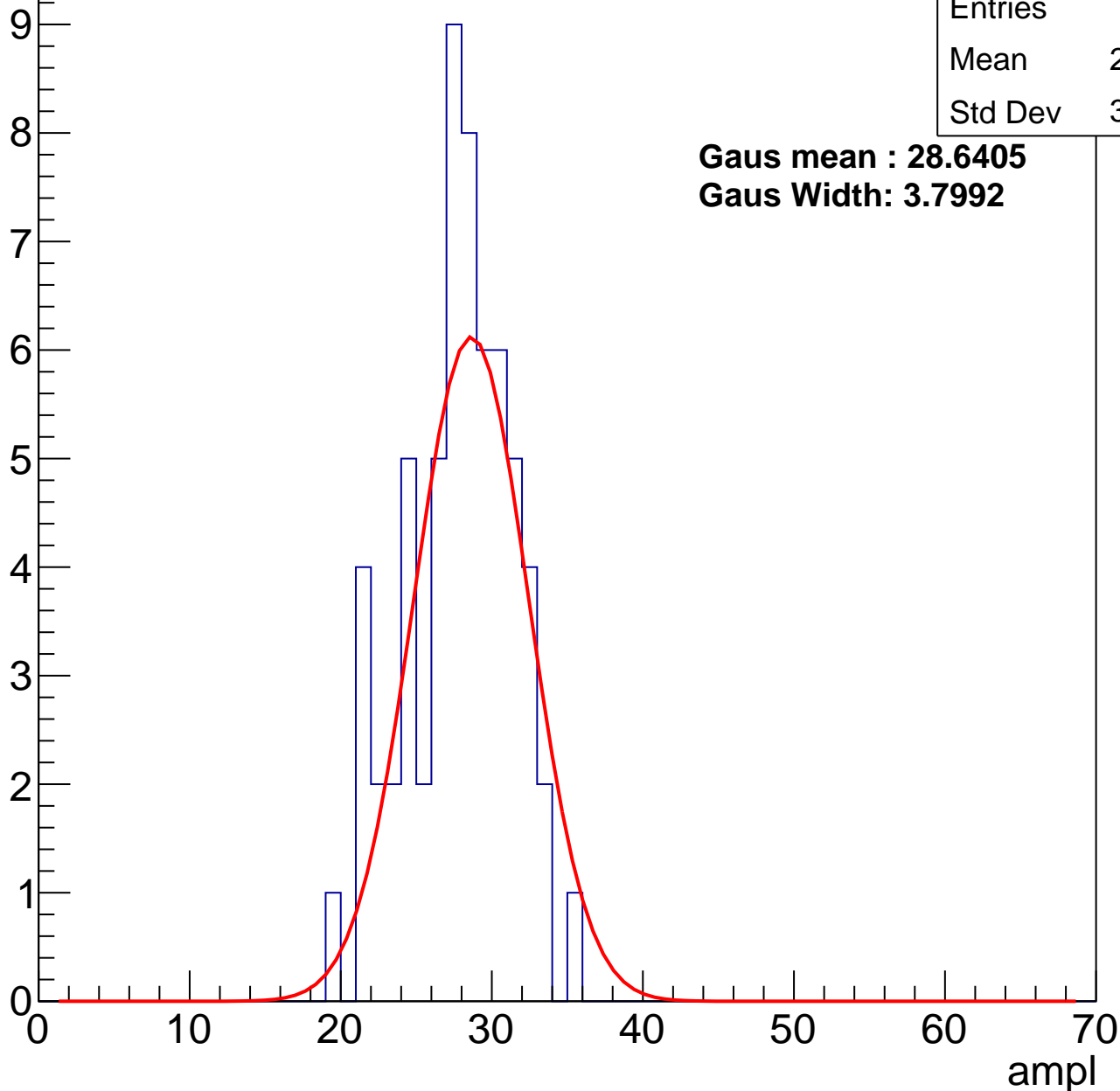
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	27.39
Std Dev	3.456

**Gaus mean : 28.6405**

**Gaus Width: 3.7992**



# B1L002S, U18-ch111, adc1

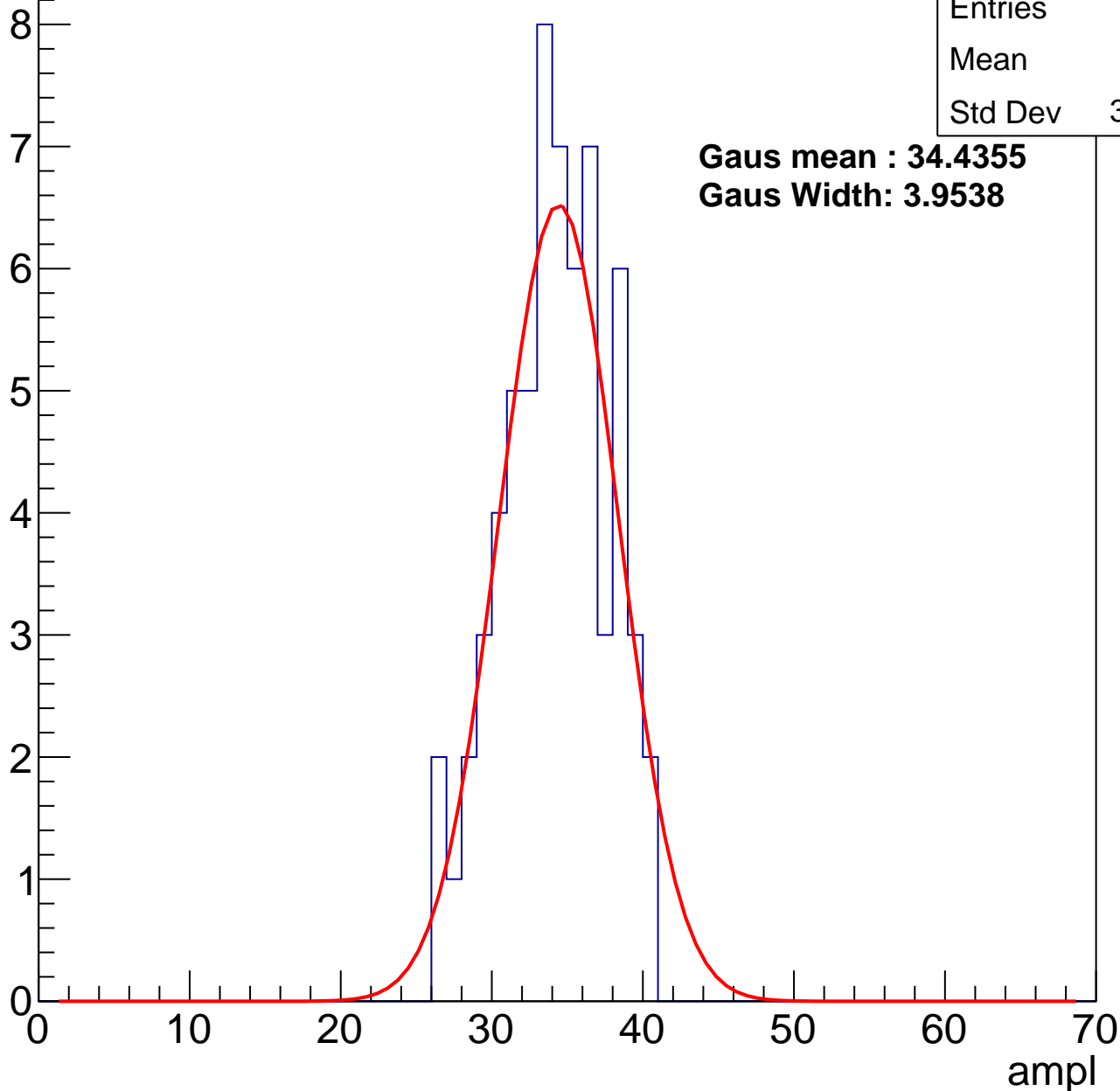
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.7
Std Dev	3.445

**Gaus mean : 34.4355**

**Gaus Width: 3.9538**



# B1L002S, U18-ch111, adc2

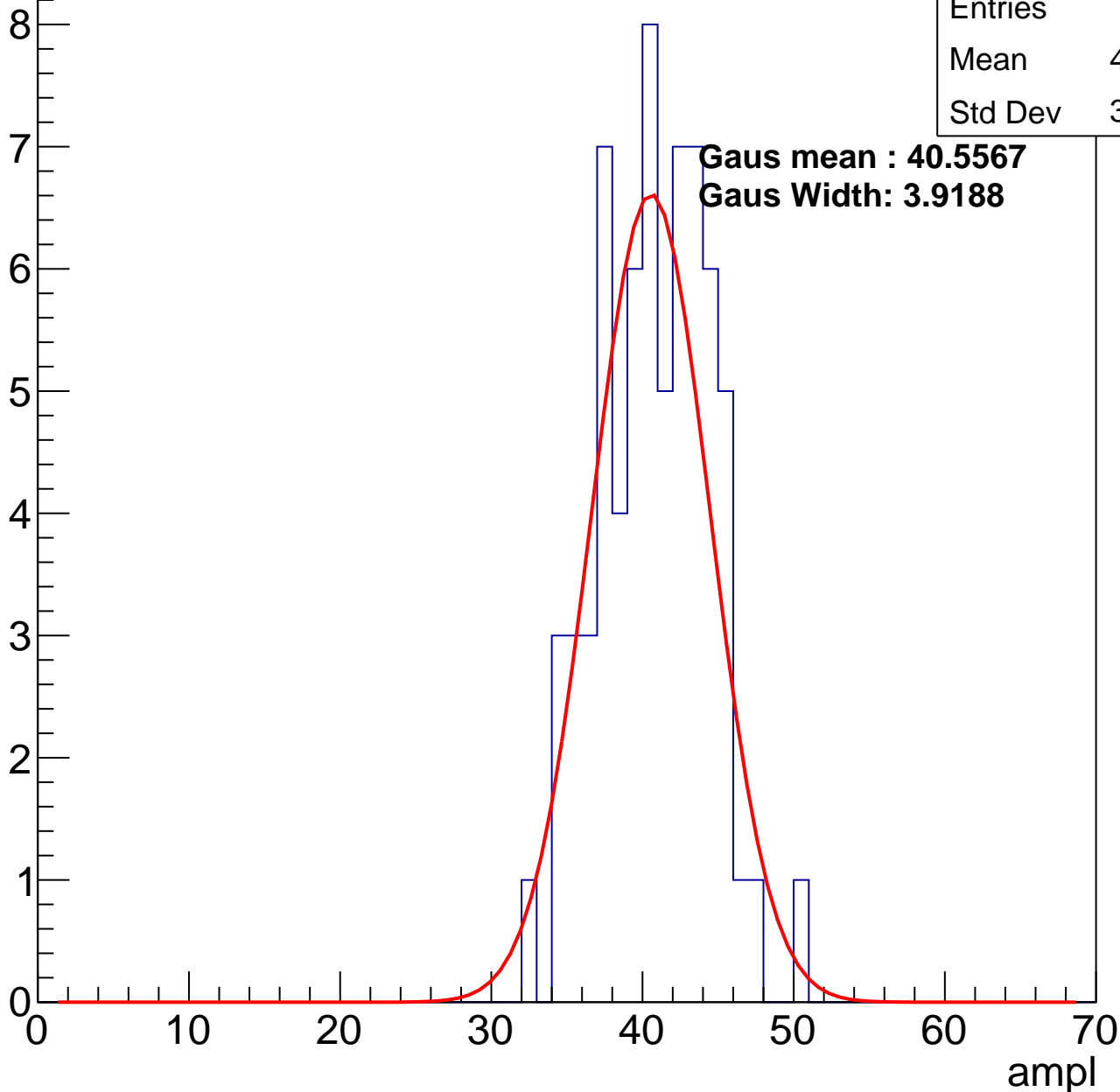
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.35
Std Dev	3.584

**Gaus mean : 40.5567**

**Gaus Width: 3.9188**

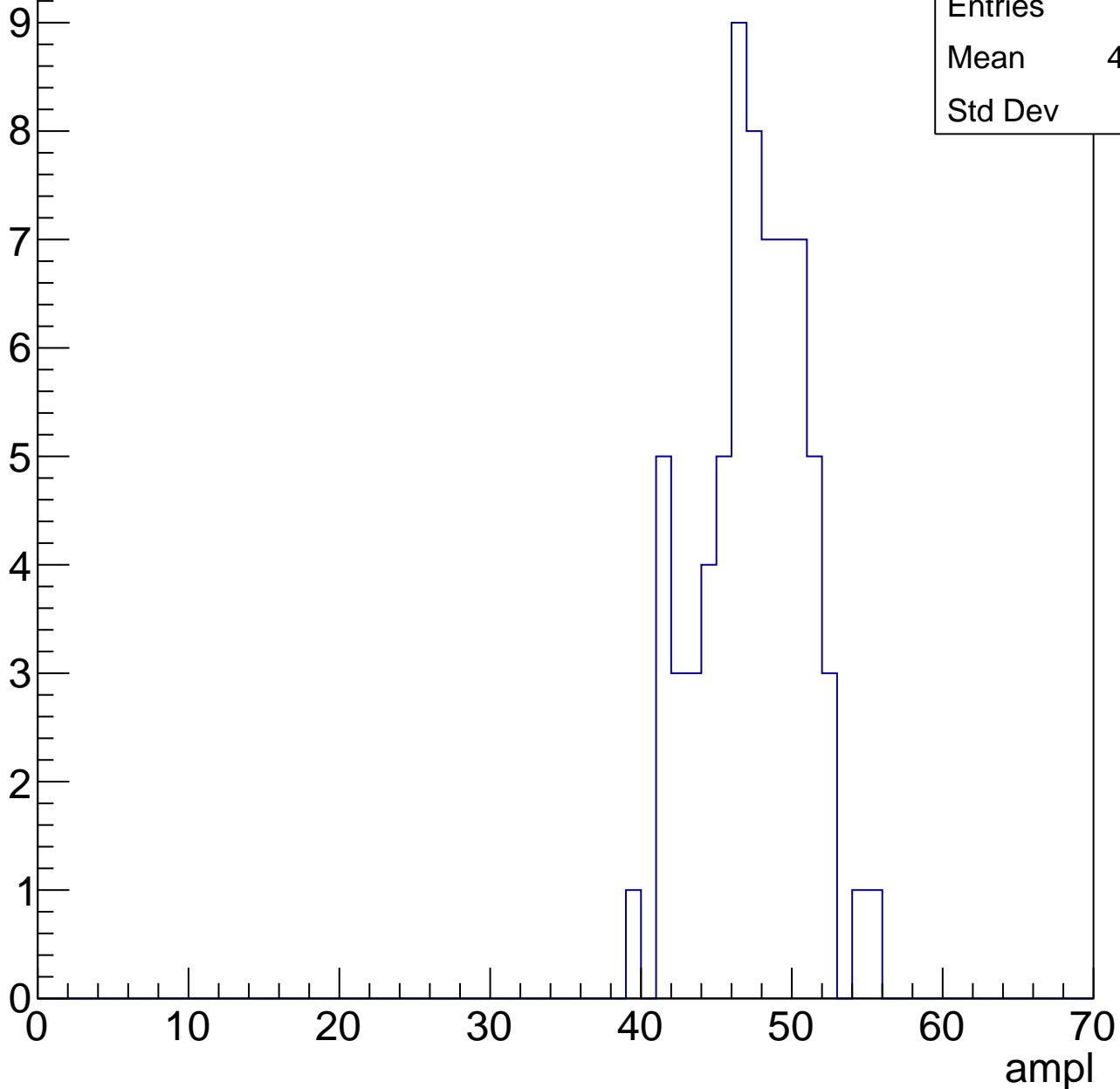


# B1L002S, U18-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.94
Std Dev	3.4

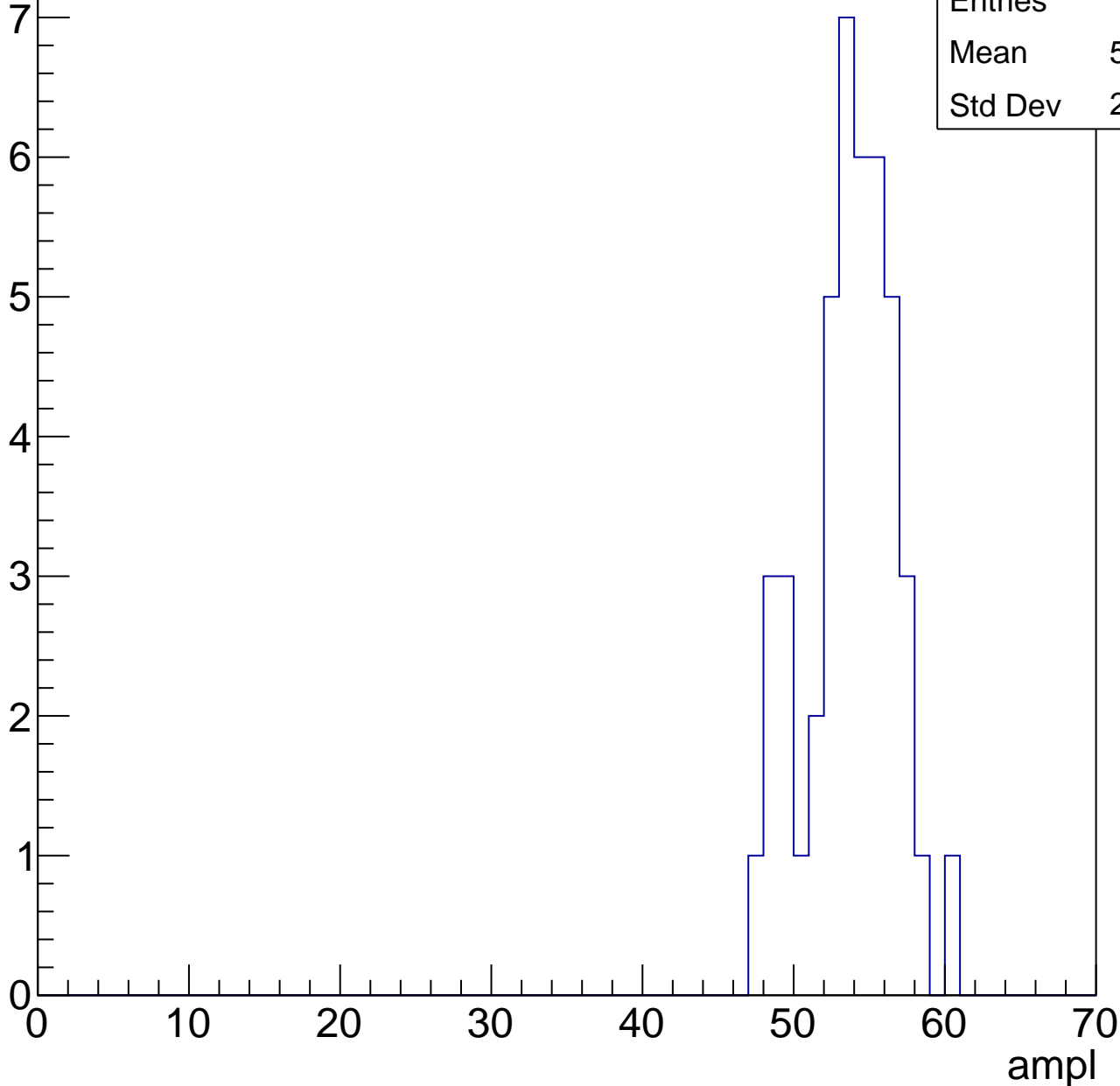


# B1L002S, U18-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	53.27
Std Dev	2.918

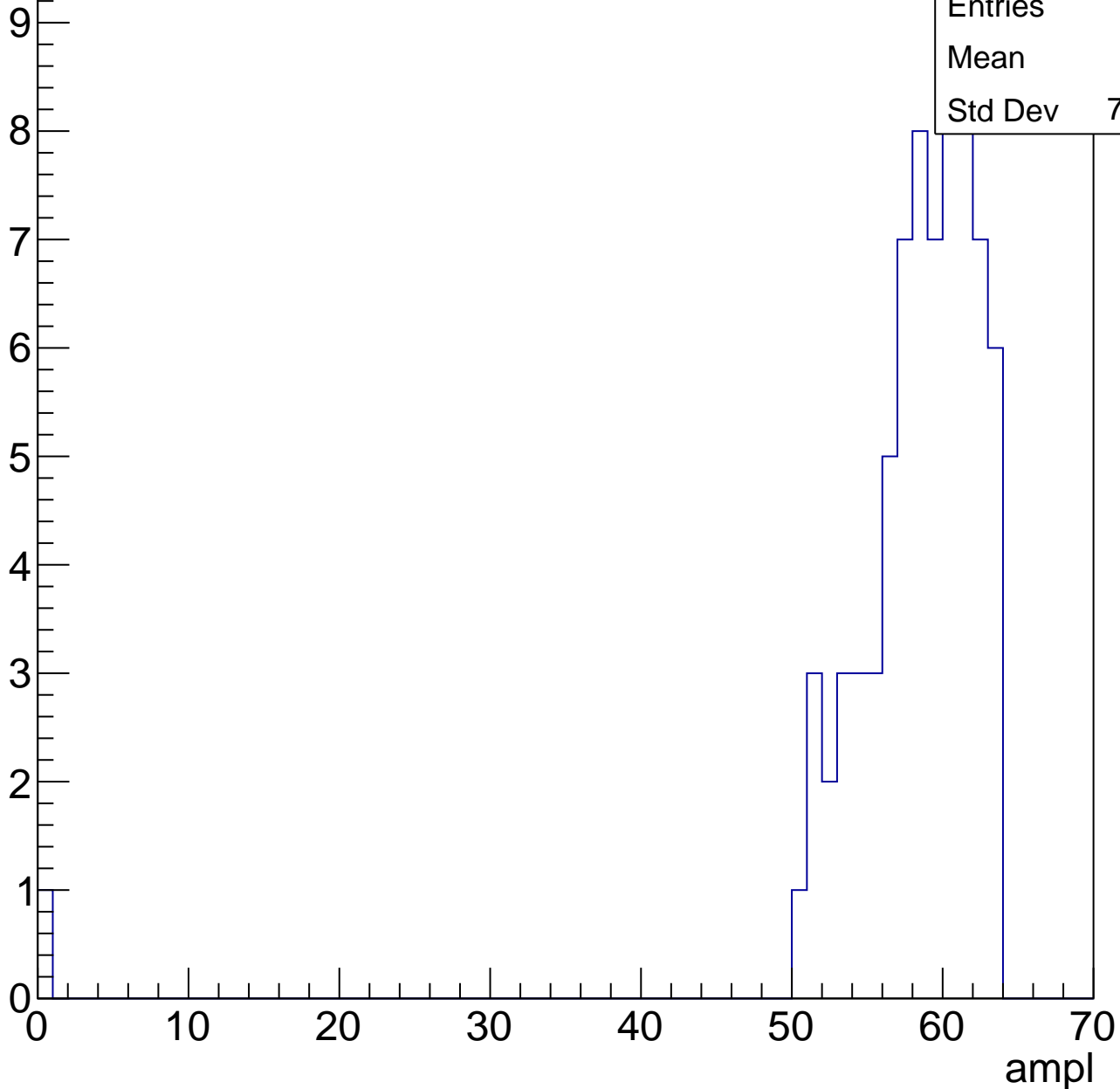


# B1L002S, U18-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

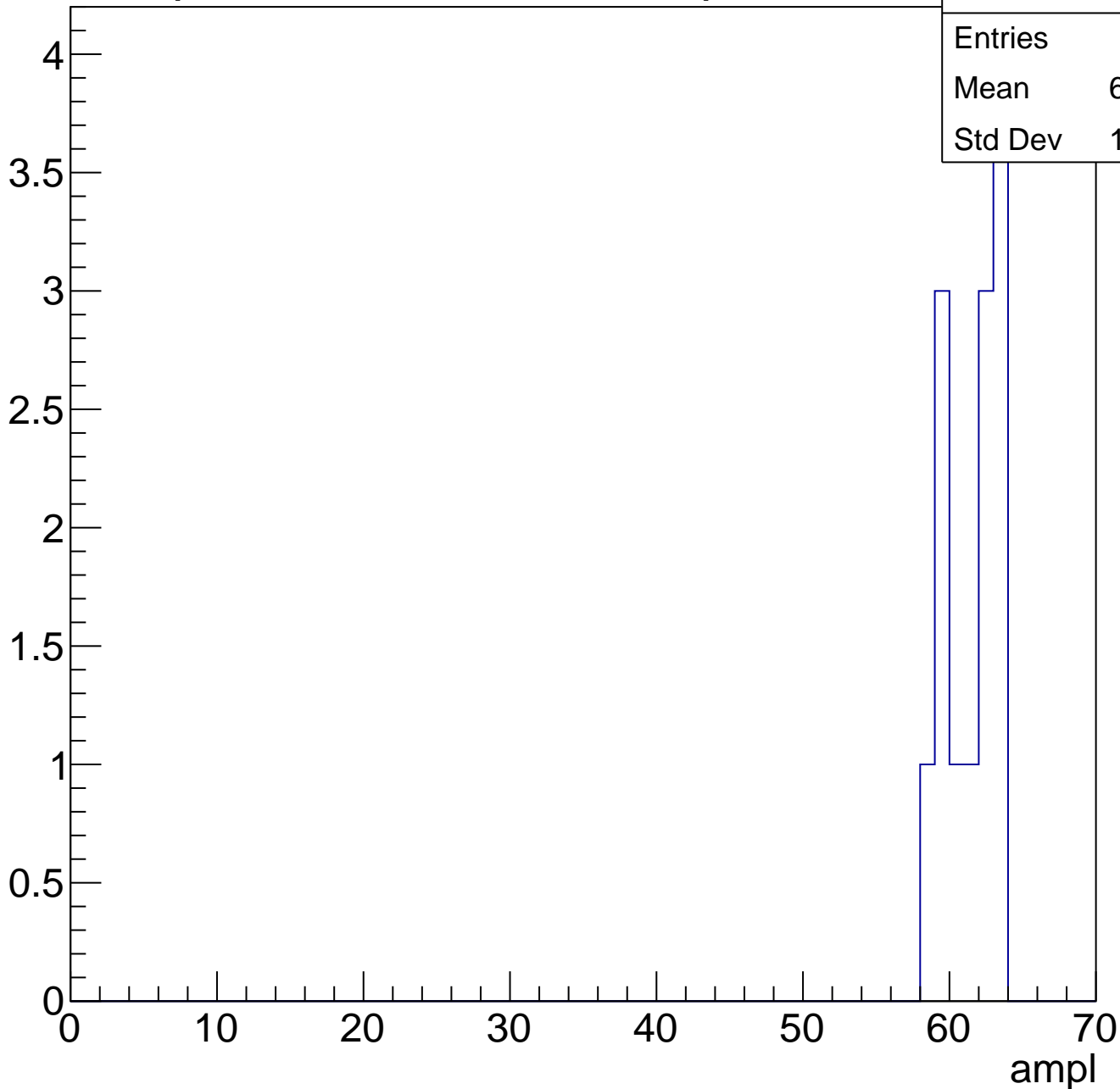
Entries	73
Mean	57.4
Std Dev	7.567



# B1L002S, U18-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U18-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U18-ch112, adc0

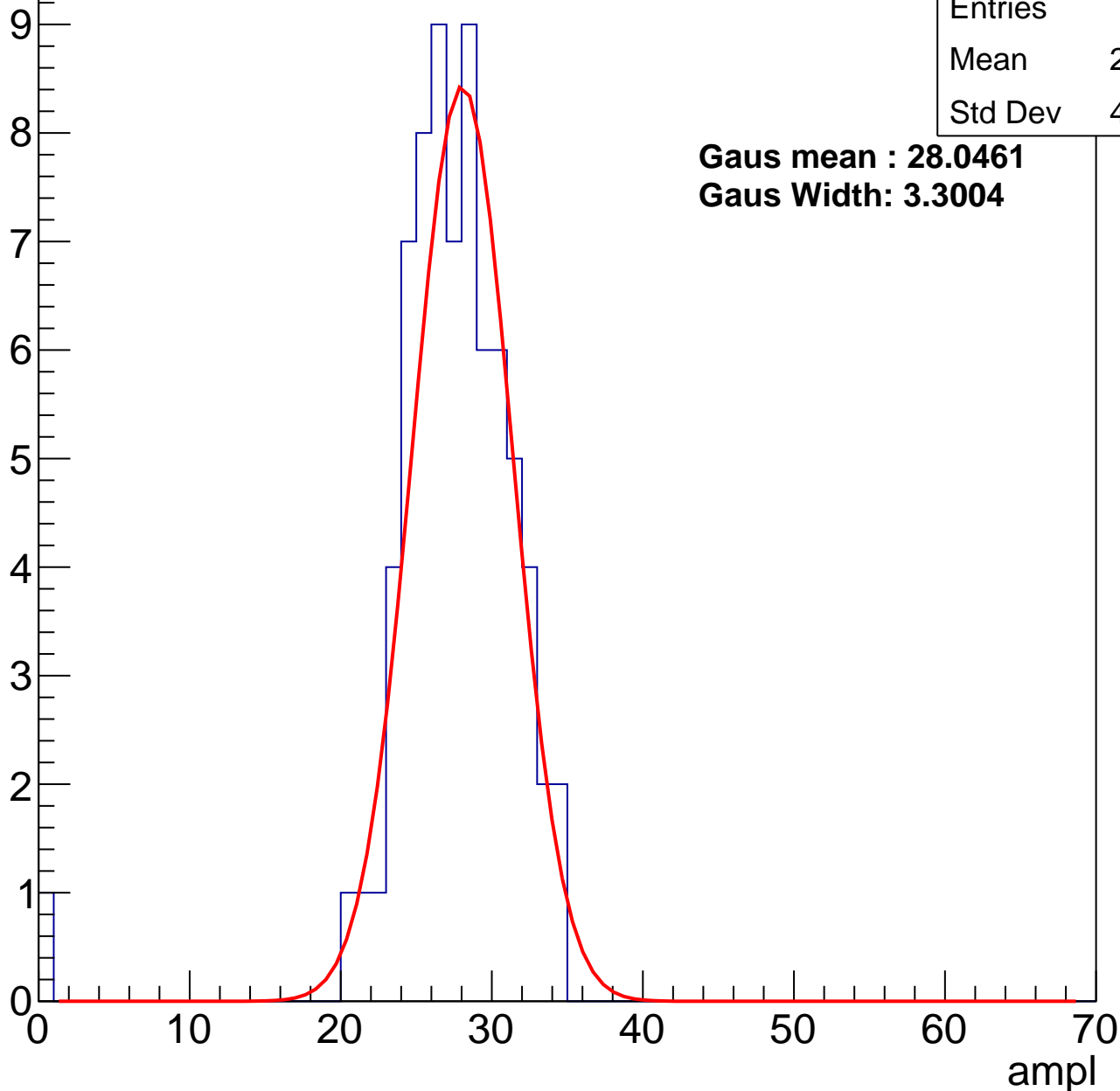
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	26.97
Std Dev	4.447

**Gaus mean : 28.0461**

**Gaus Width: 3.3004**



# B1L002S, U18-ch112, adc1

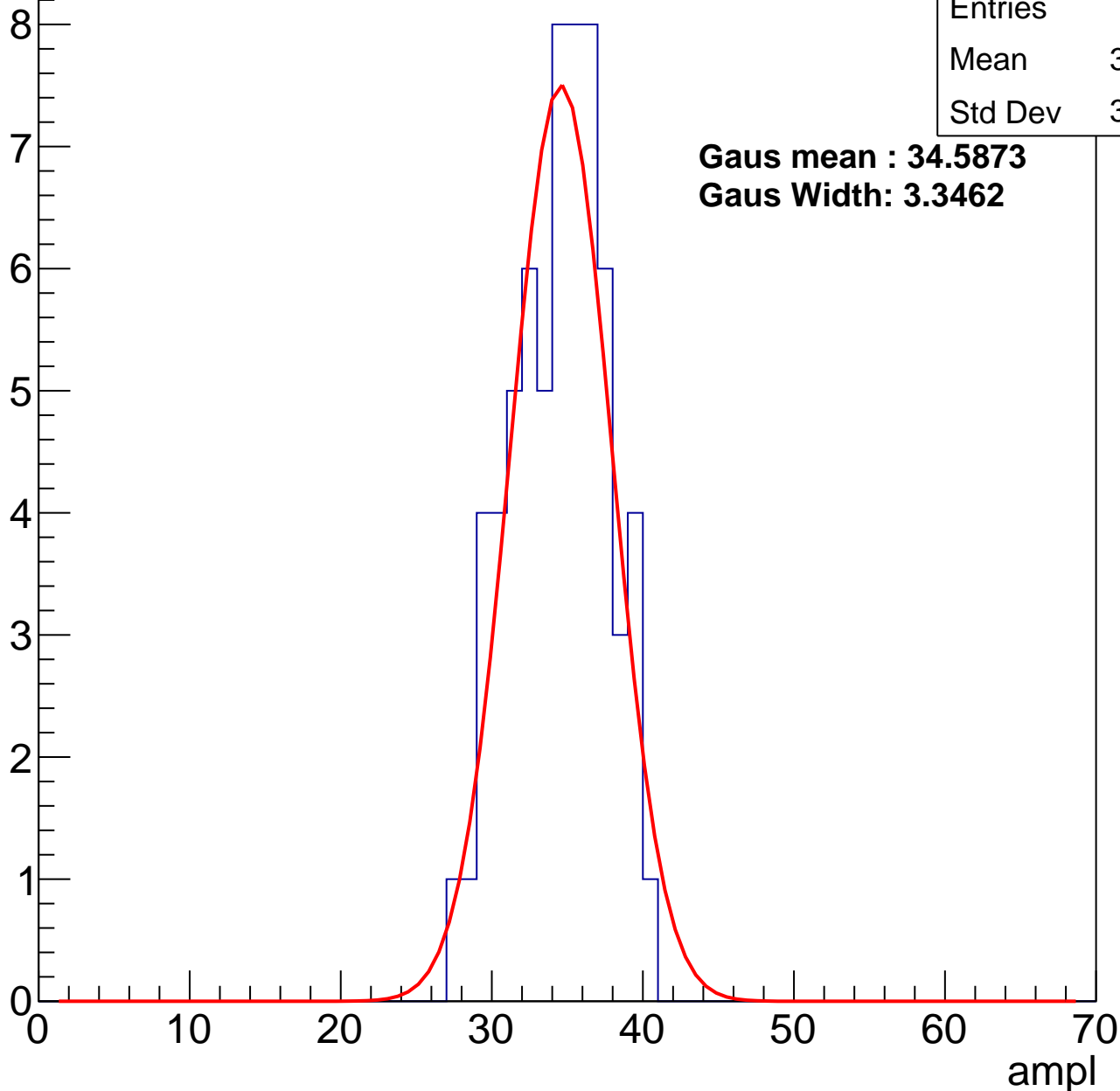
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.98
Std Dev	3.064

**Gaus mean : 34.5873**

**Gaus Width: 3.3462**



# B1L002S, U18-ch112, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	40.48
Std Dev	3.296

**Gaus mean : 40.8544**

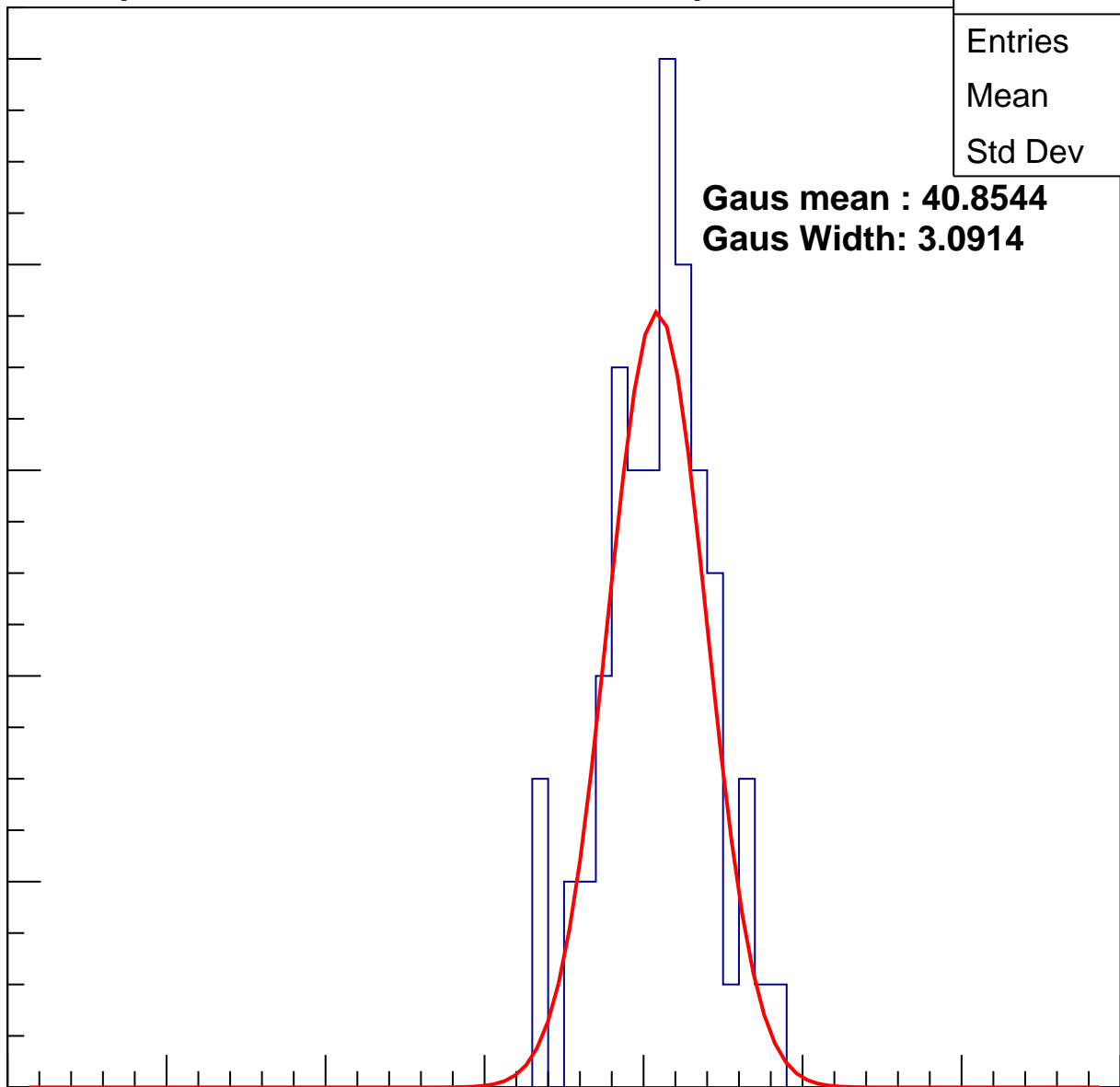
**Gaus Width: 3.0914**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

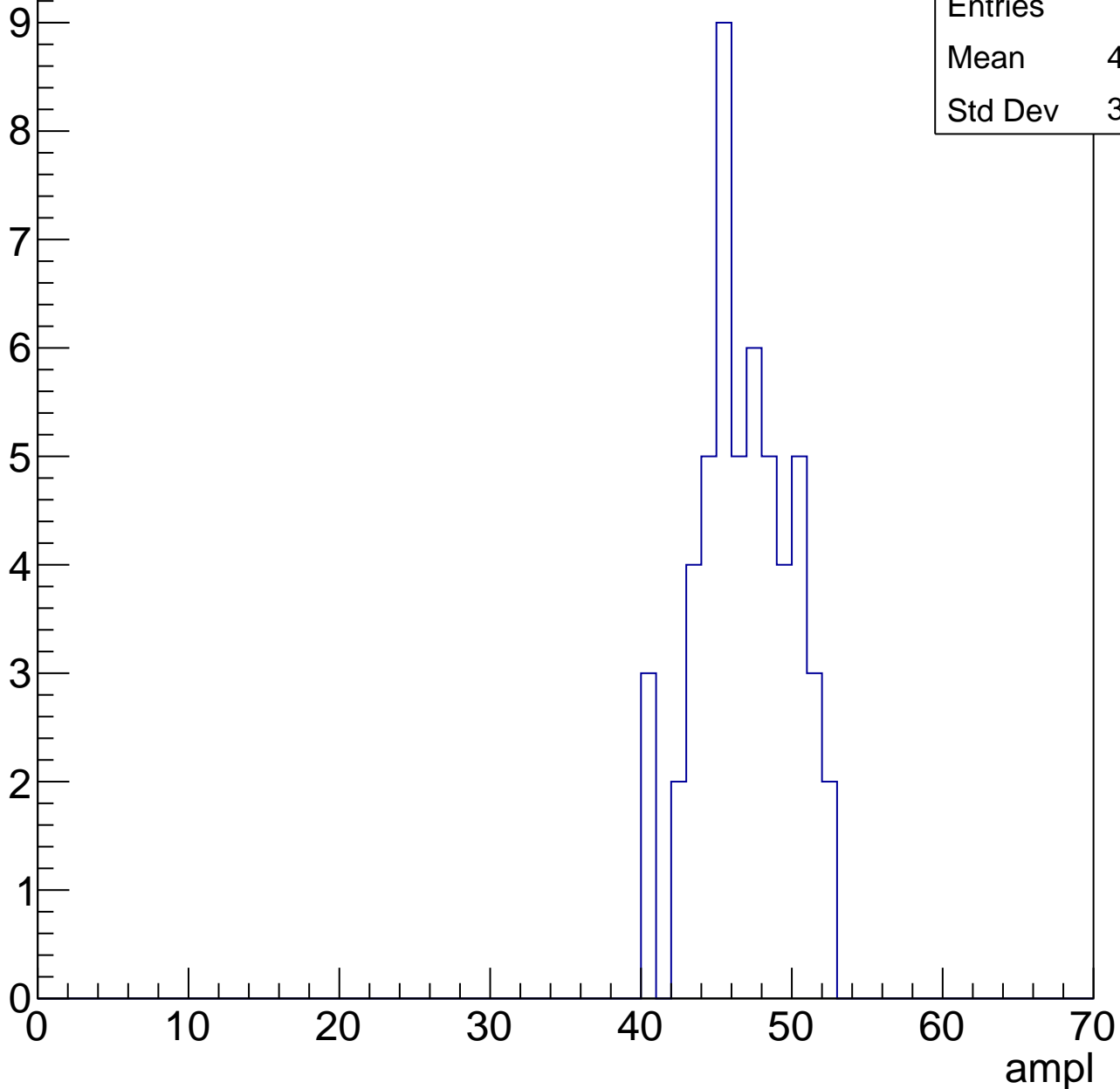


# B1L002S, U18-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.34
Std Dev	3.034

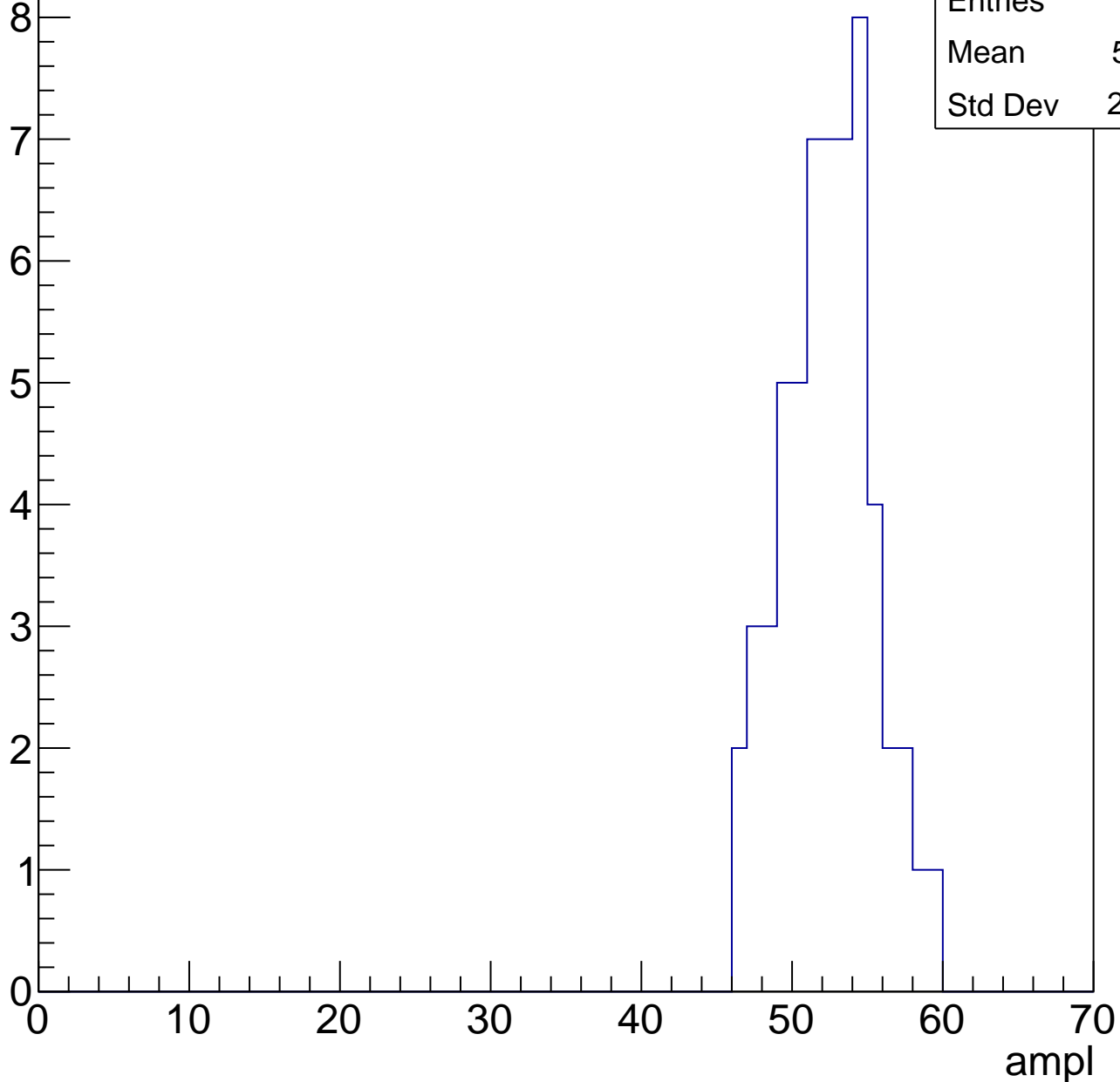


# B1L002S, U18-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	51.91
Std Dev	2.987

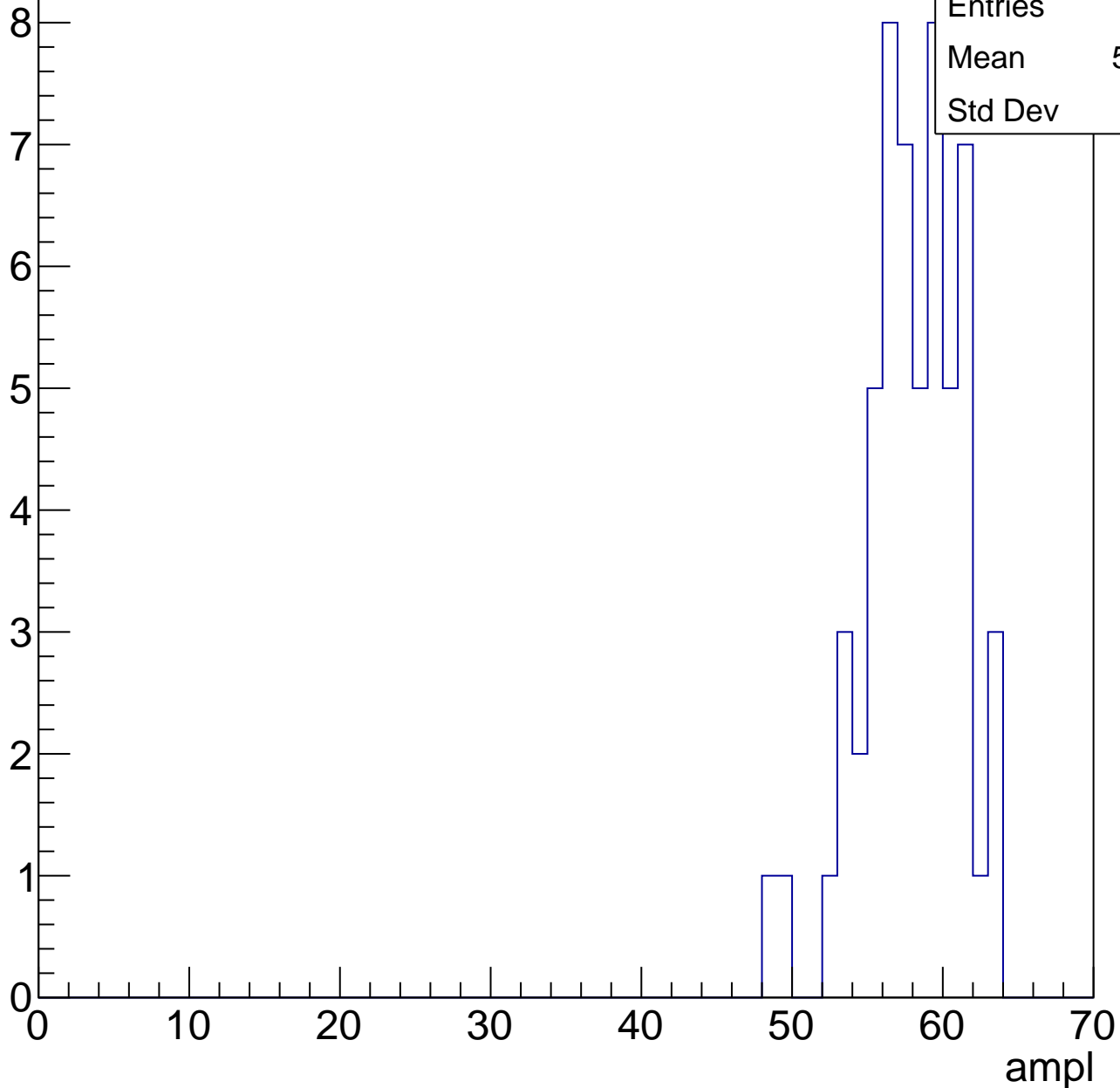


# B1L002S, U18-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.51
Std Dev	3.18

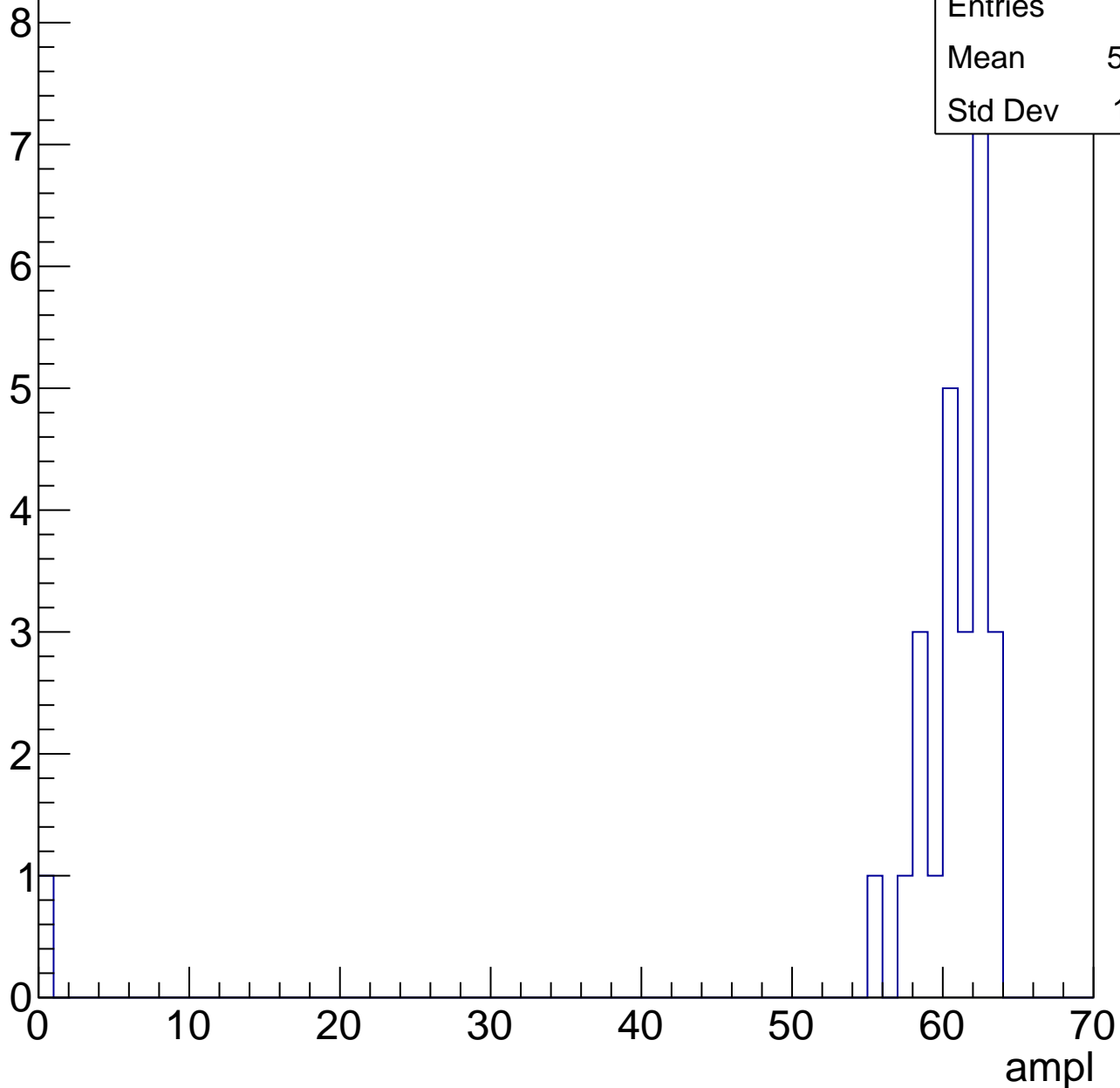


# B1L002S, U18-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.19
Std Dev	11.81

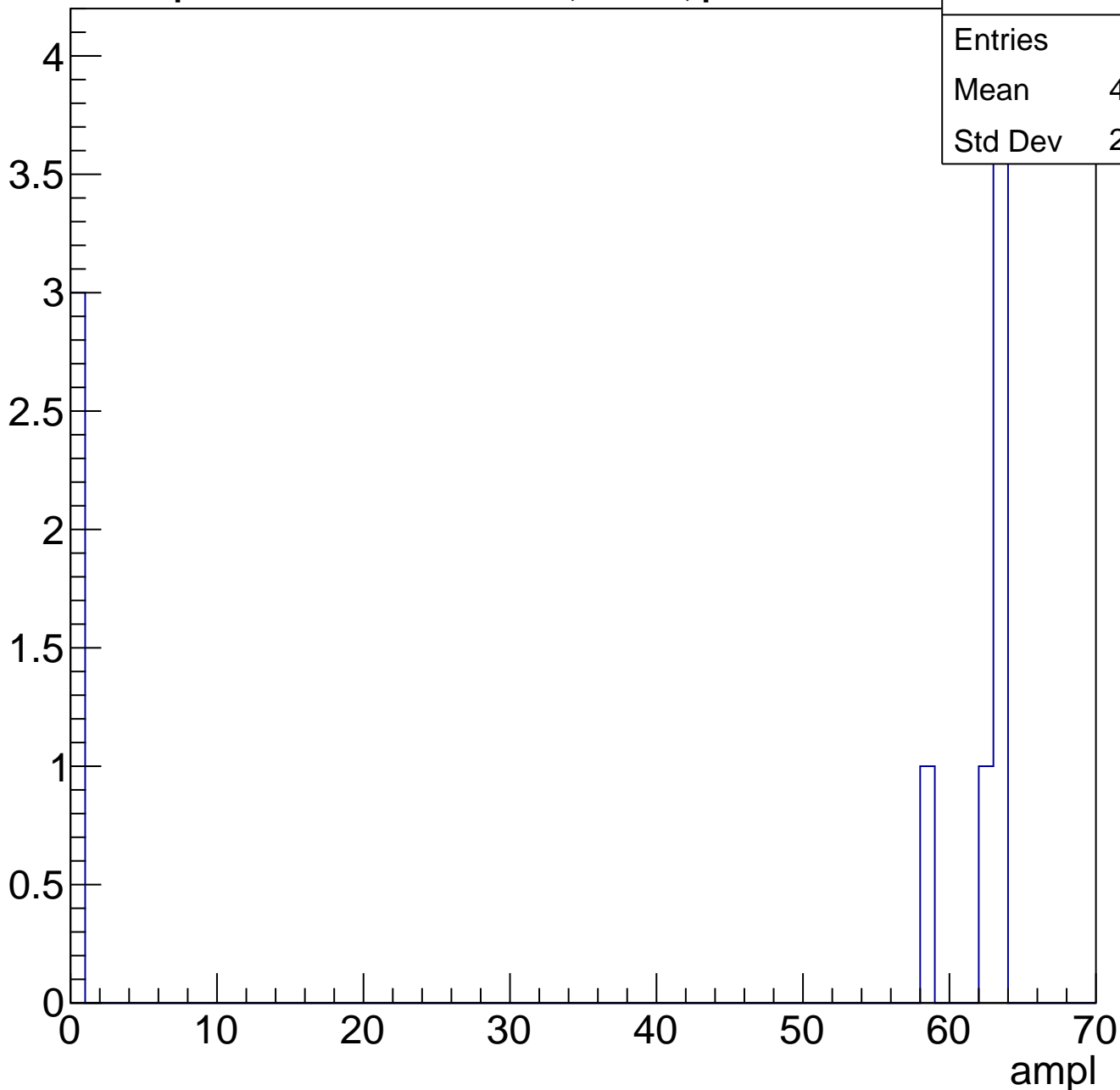




# B1L002S, U18-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch113, adc0

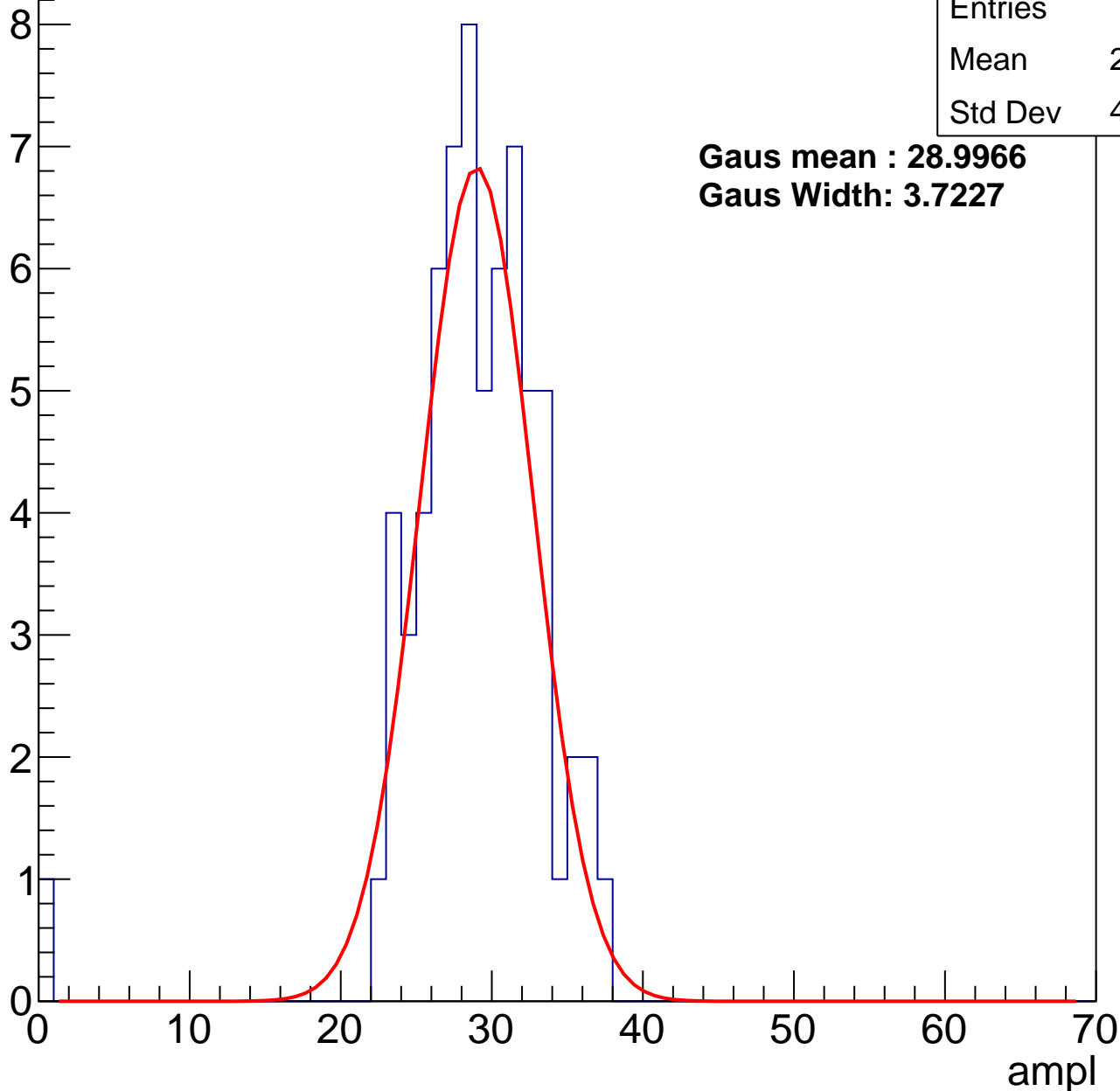
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	28.46
Std Dev	4.942

**Gaus mean : 28.9966**

**Gaus Width: 3.7227**



# B1L002S, U18-ch113, adc1

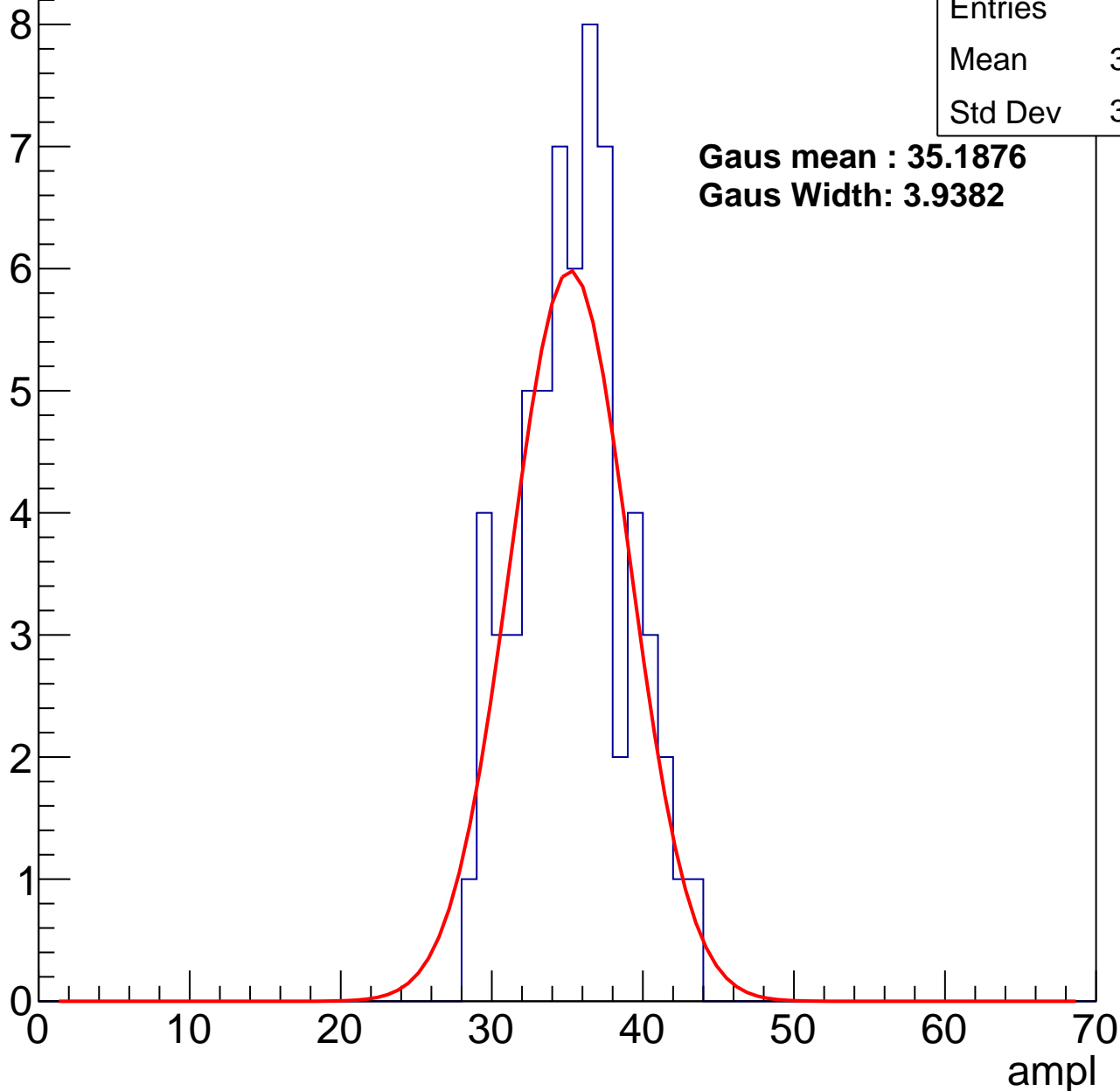
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.94
Std Dev	3.514

**Gaus mean : 35.1876**

**Gaus Width: 3.9382**



# B1L002S, U18-ch113, adc2

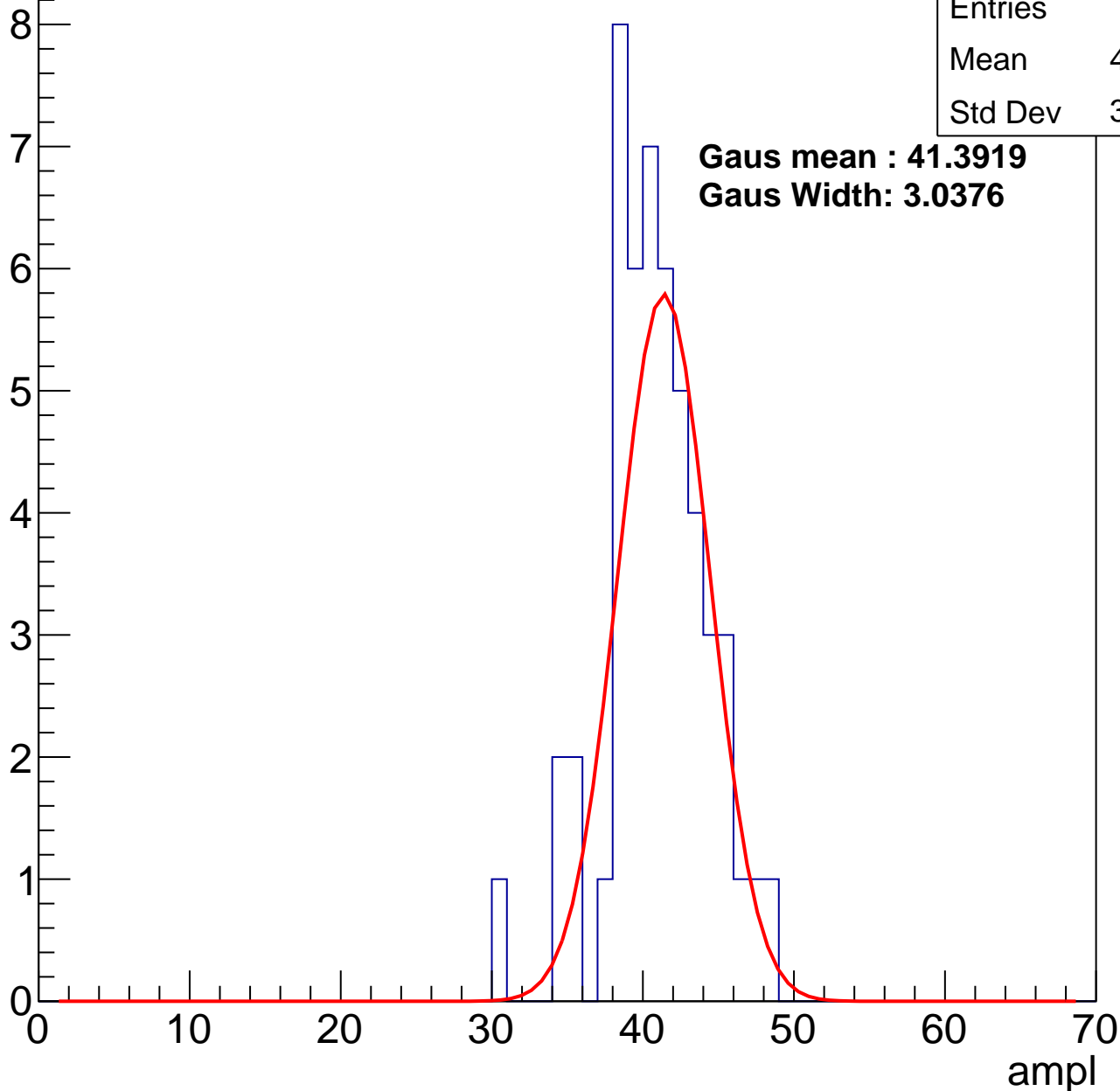
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.37
Std Dev	3.418

**Gaus mean : 41.3919**

**Gaus Width: 3.0376**

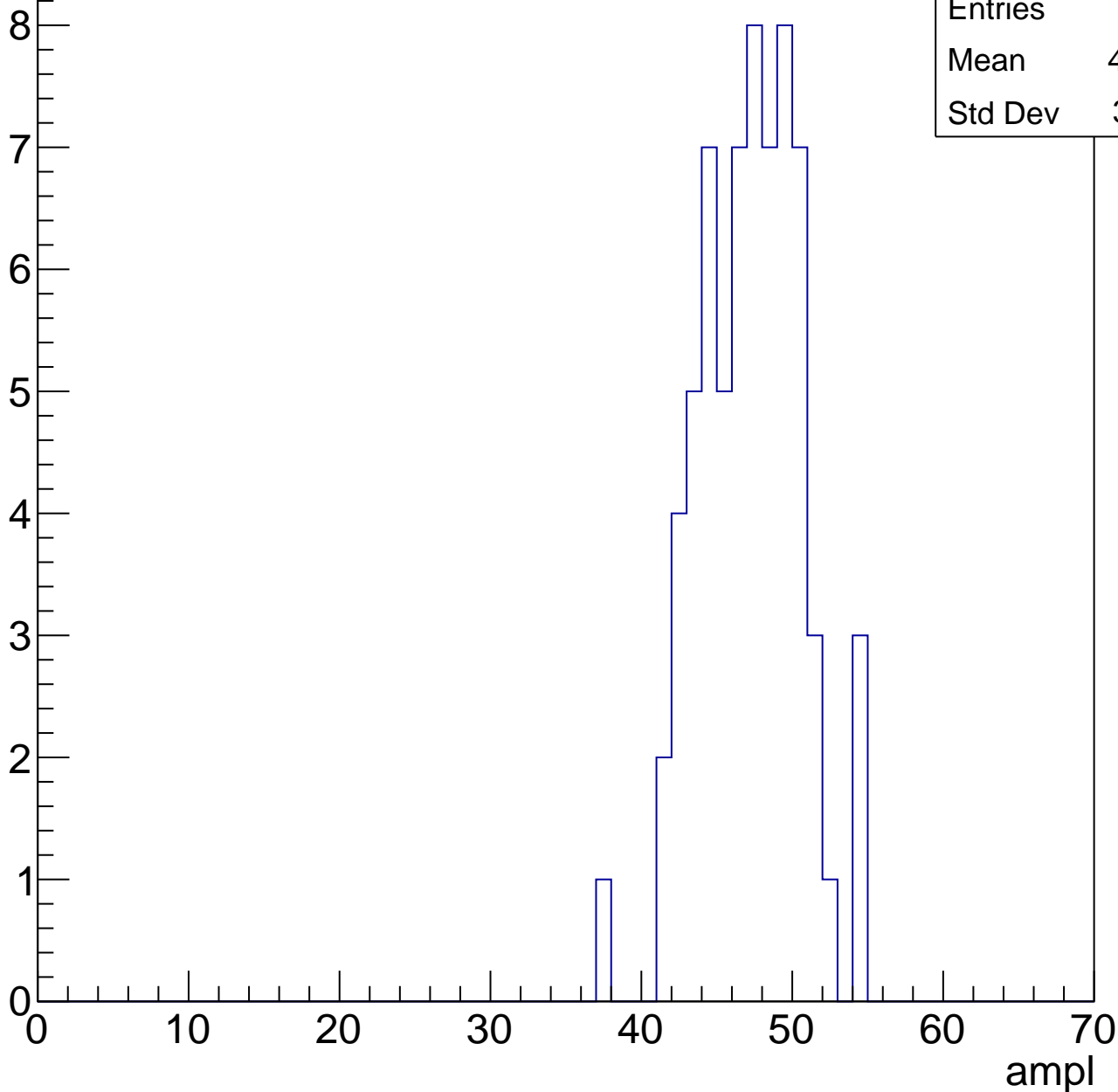


# B1L002S, U18-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	46.74
Std Dev	3.341

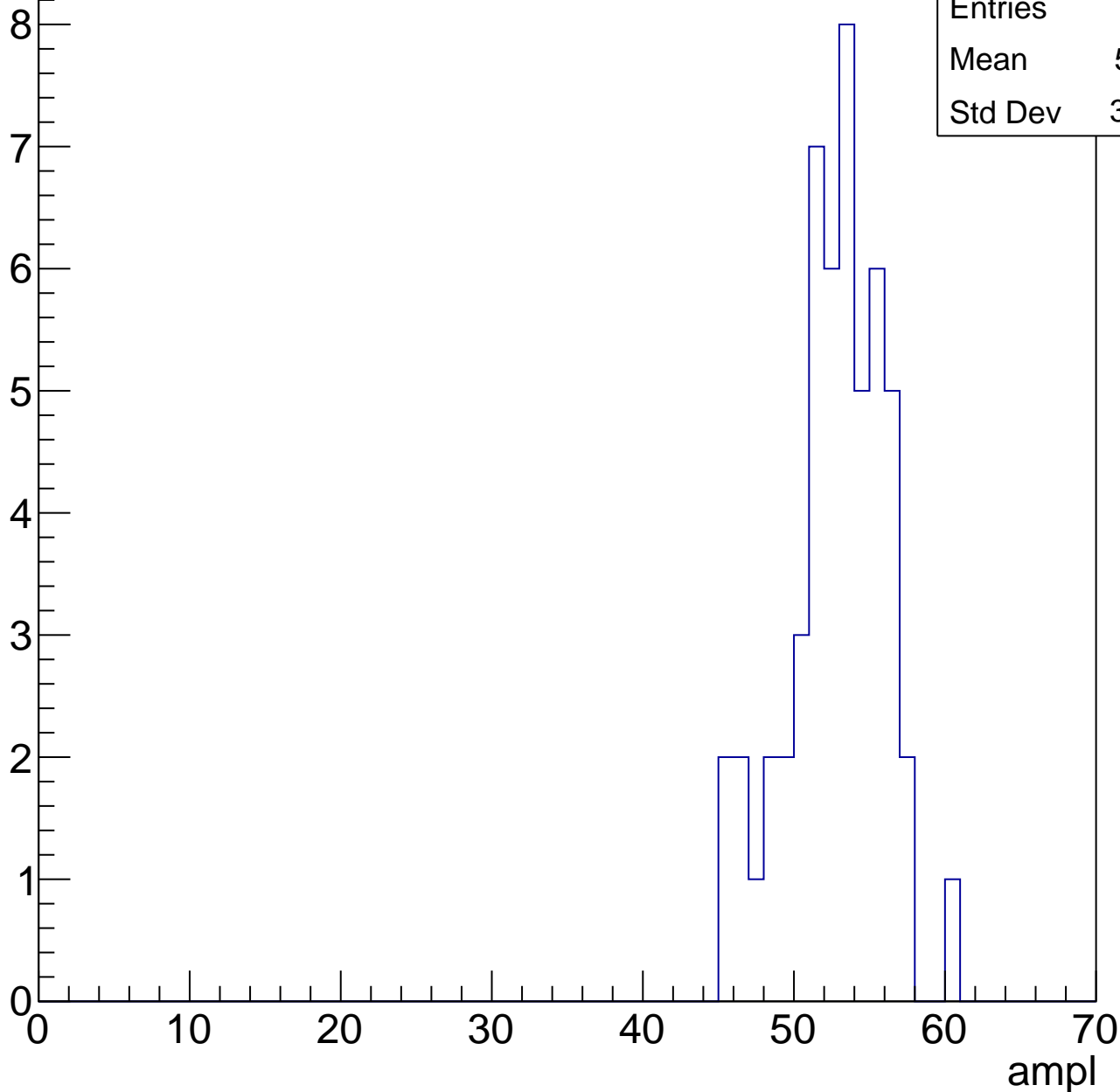


# B1L002S, U18-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.31
Std Dev	3.208



# B1L002S, U18-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

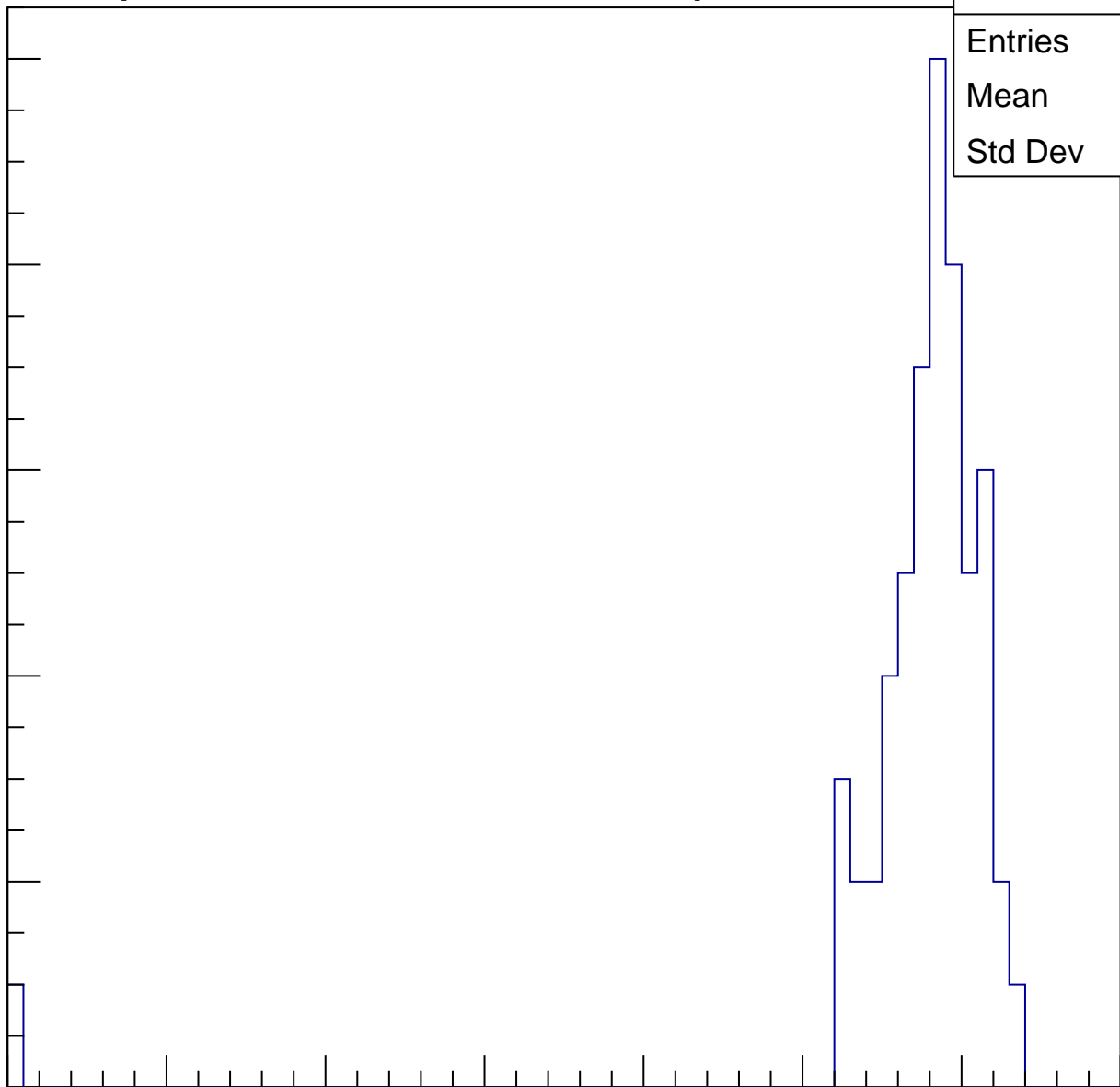
Entries	56
Mean	56.68
Std Dev	8.082

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

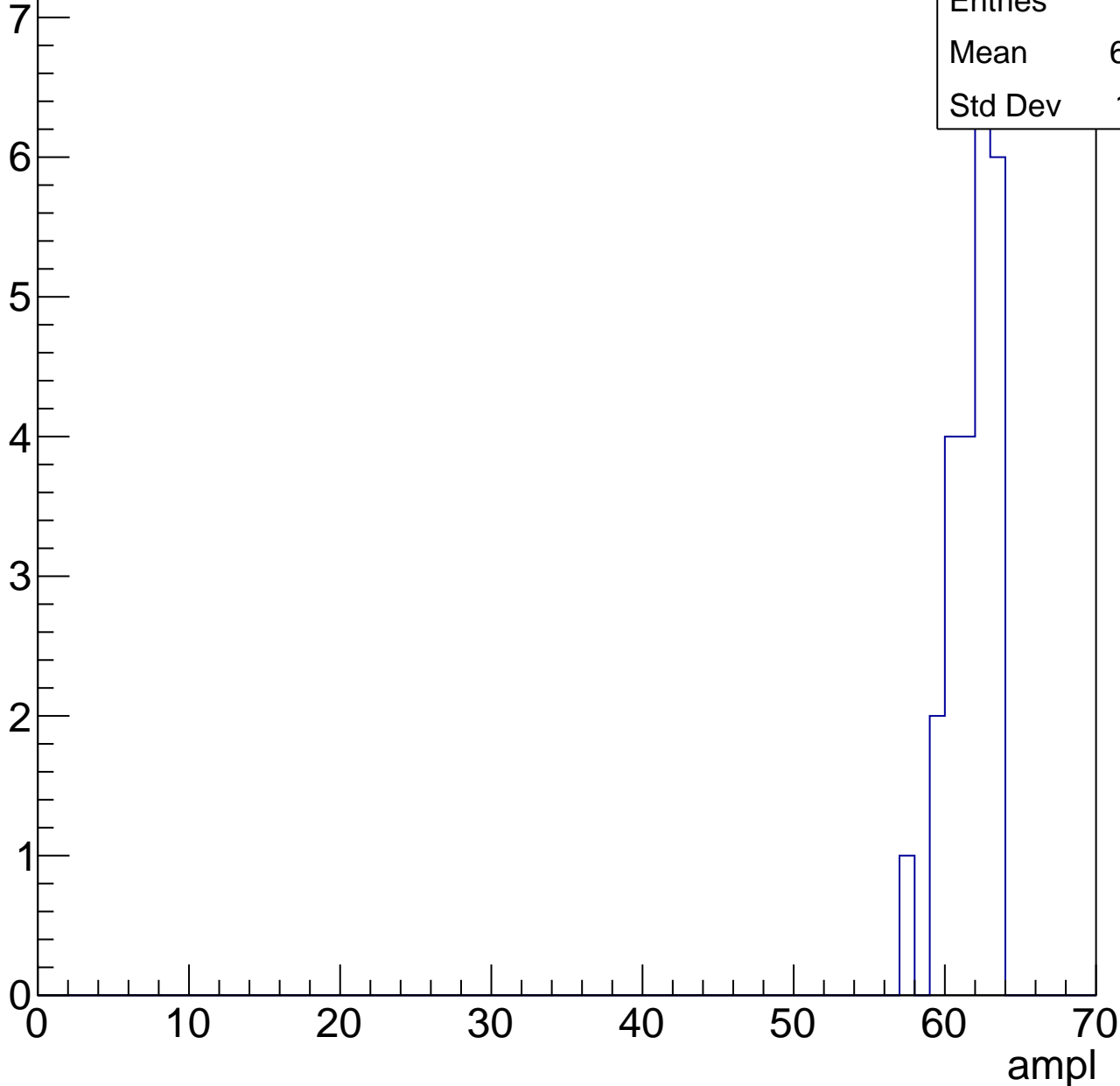


# B1L002S, U18-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	61.29
Std Dev	1.541

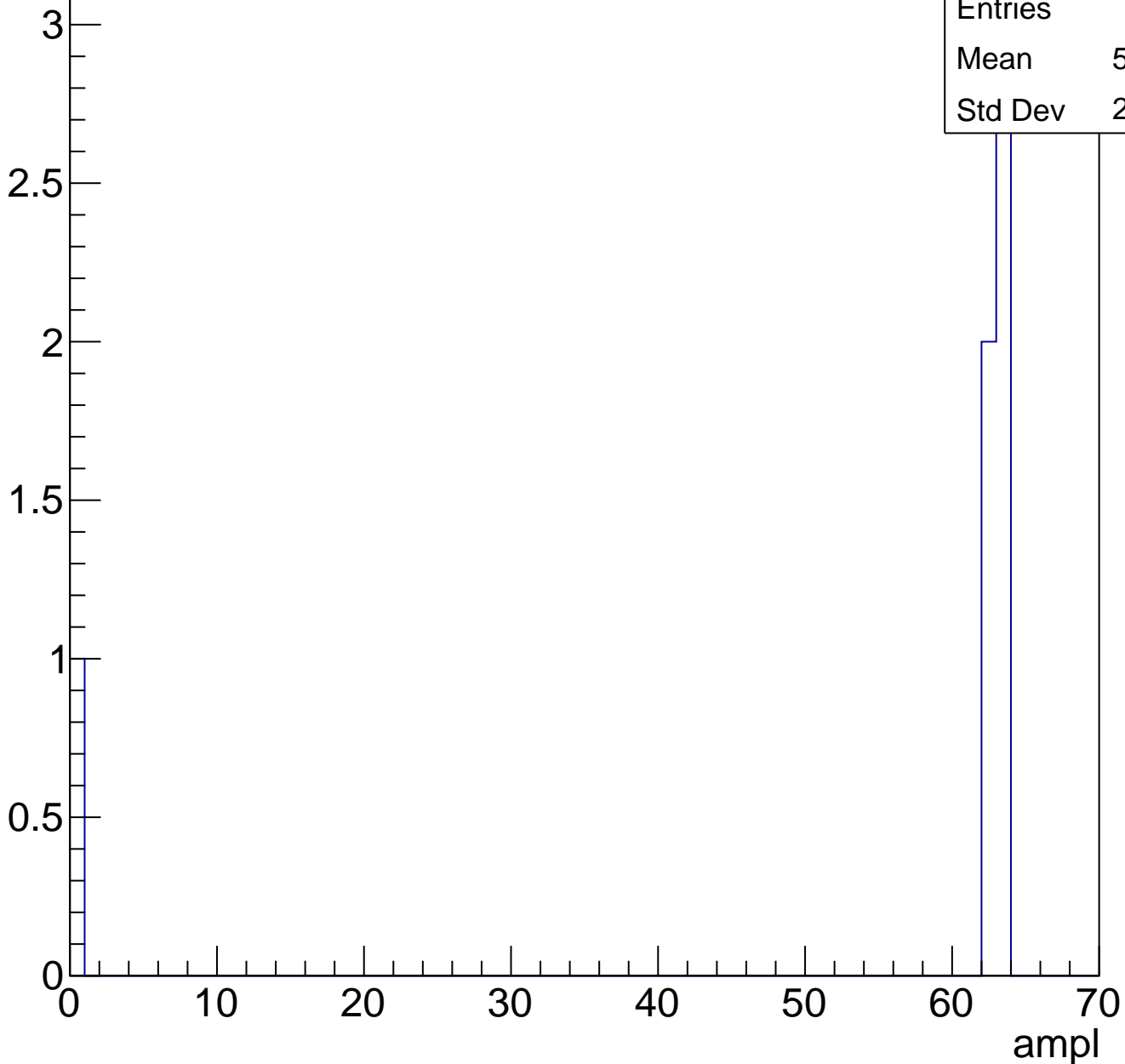




# B1L002S, U18-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch114, adc0

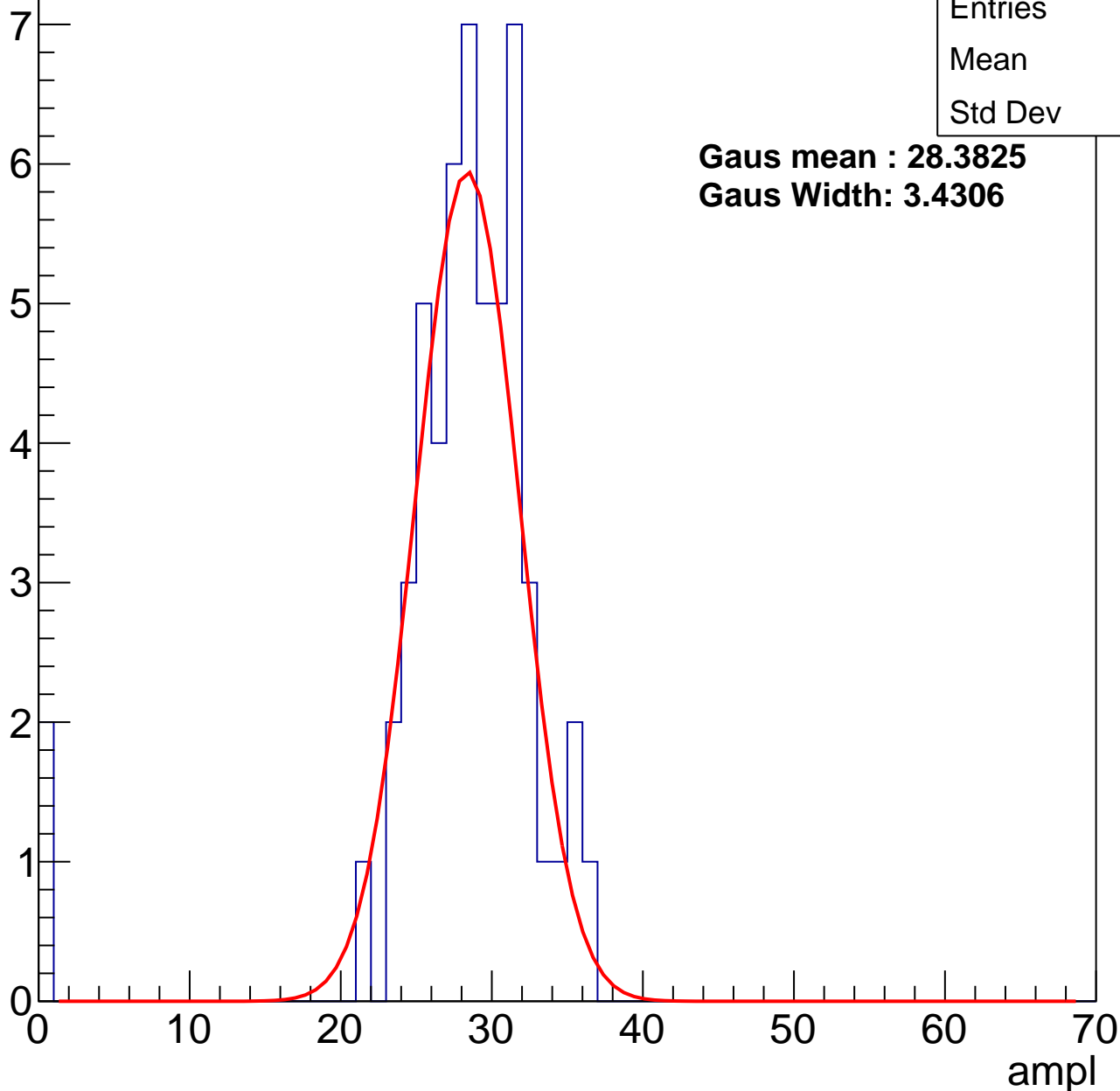
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.4
Std Dev	6.21

**Gaus mean : 28.3825**

**Gaus Width: 3.4306**



# B1L002S, U18-ch114, adc1

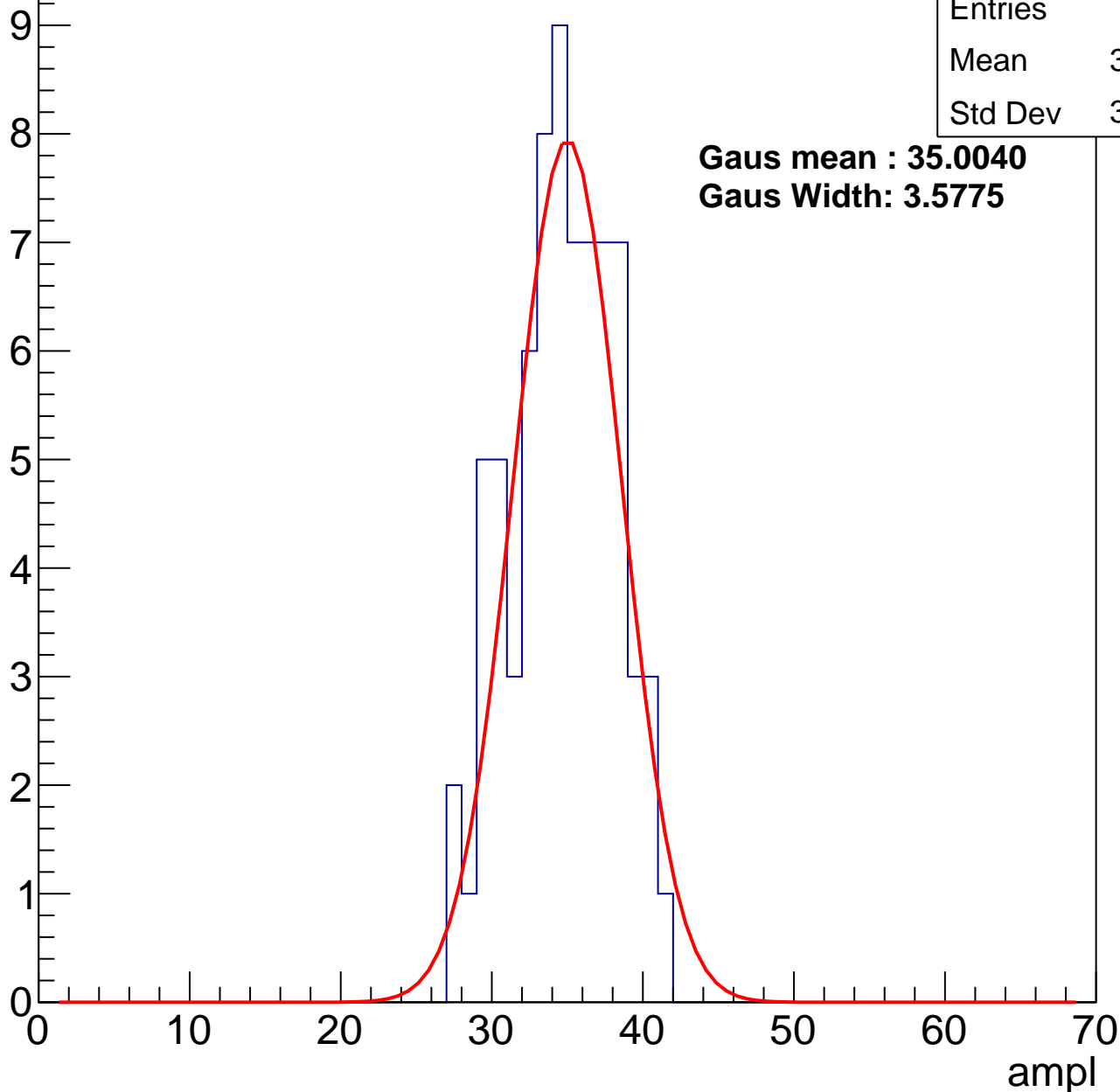
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	34.22
Std Dev	3.362

**Gaus mean : 35.0040**

**Gaus Width: 3.5775**



# B1L002S, U18-ch114, adc2

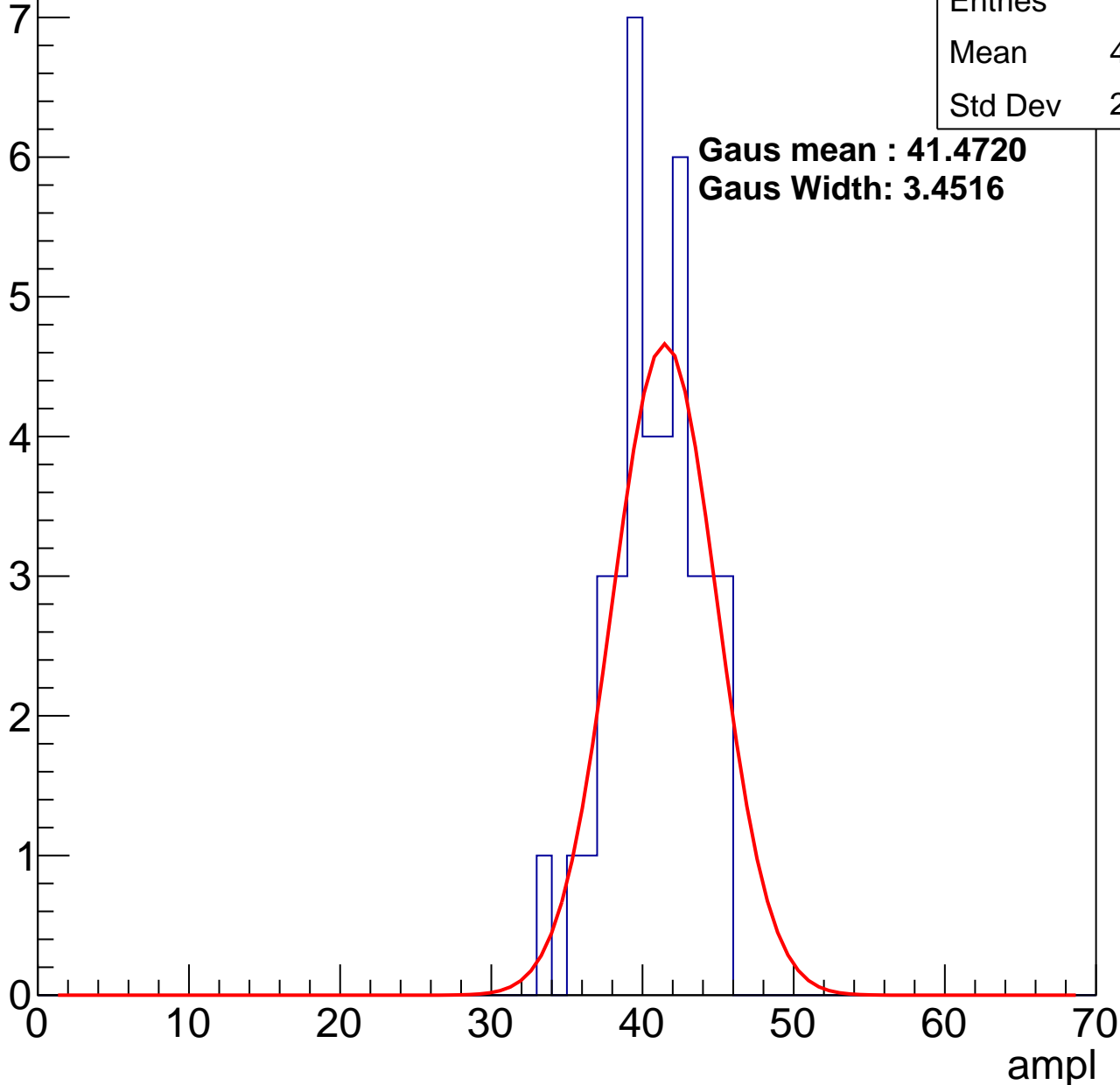
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	40.36
Std Dev	2.815

**Gaus mean : 41.4720**

**Gaus Width: 3.4516**



# B1L002S, U18-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	45.23
Std Dev	3.176

Entry

10

8

6

4

2

0

0

10

20

30

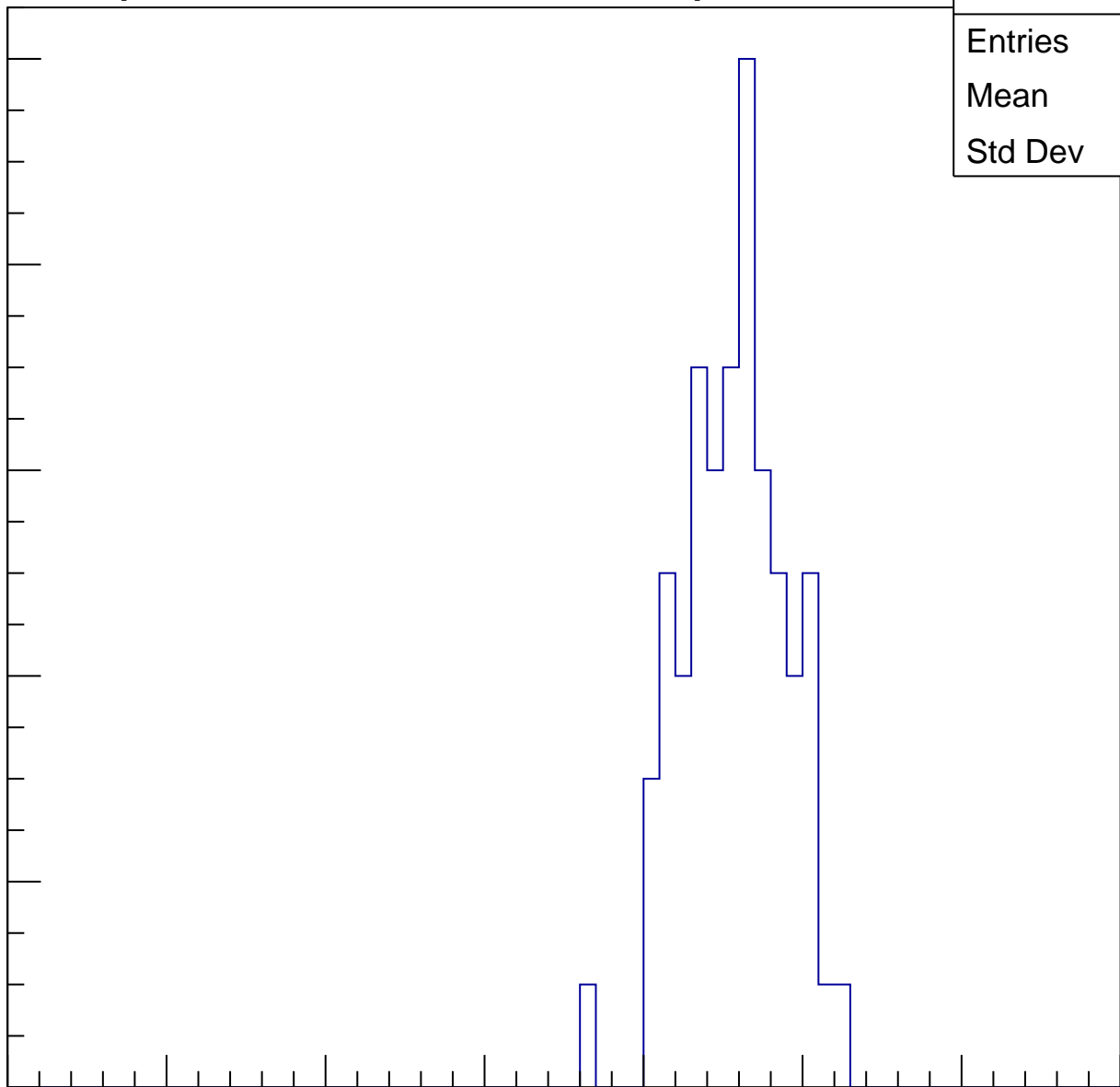
40

50

60

70

ampl

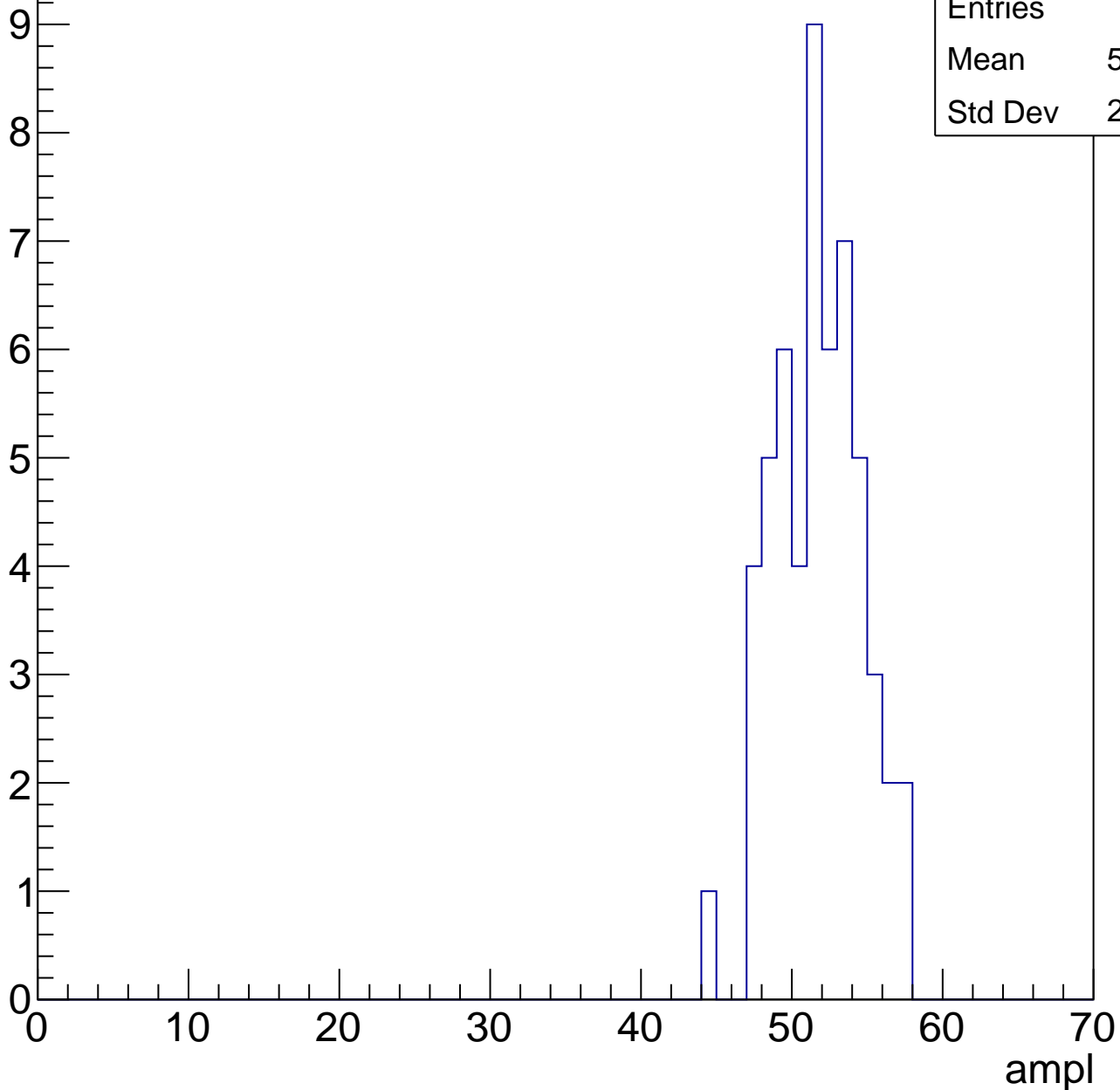


# B1L002S, U18-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.28
Std Dev	2.825

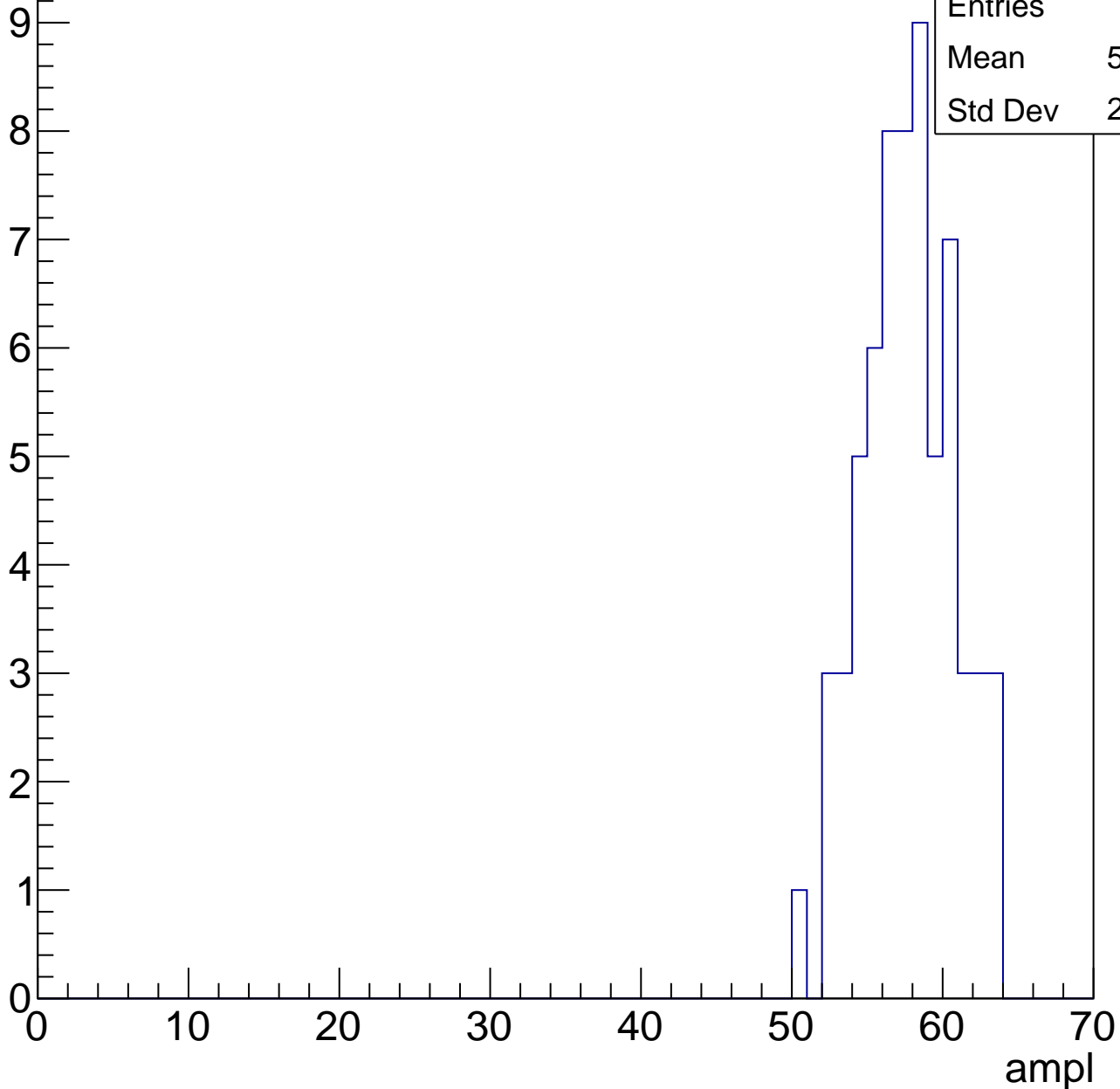


# B1L002S, U18-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.25
Std Dev	2.979

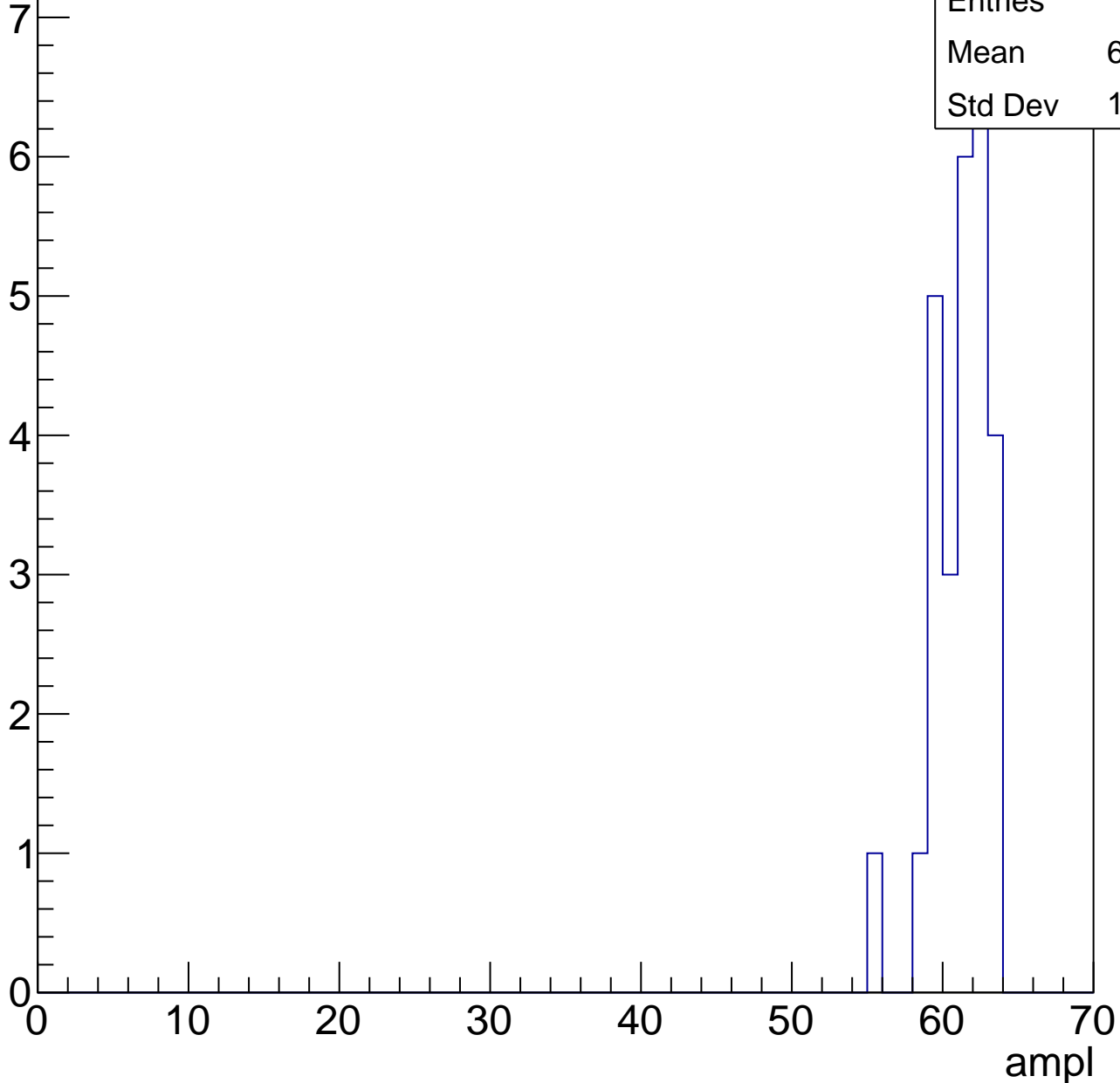


# B1L002S, U18-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.74
Std Dev	1.817

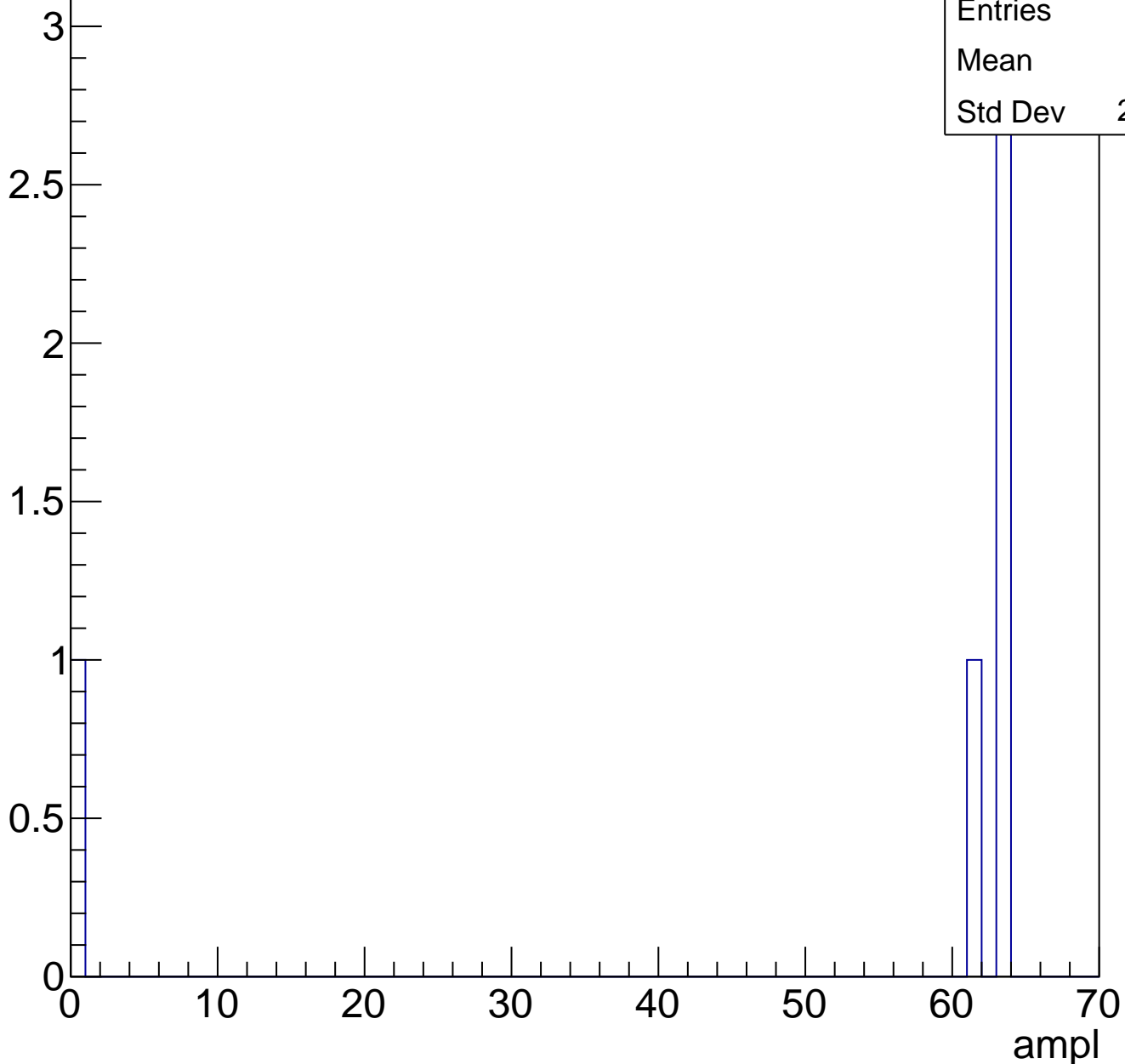




# B1L002S, U18-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch115, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	29.2
Std Dev	6.089

**Gaus mean : 30.5382**

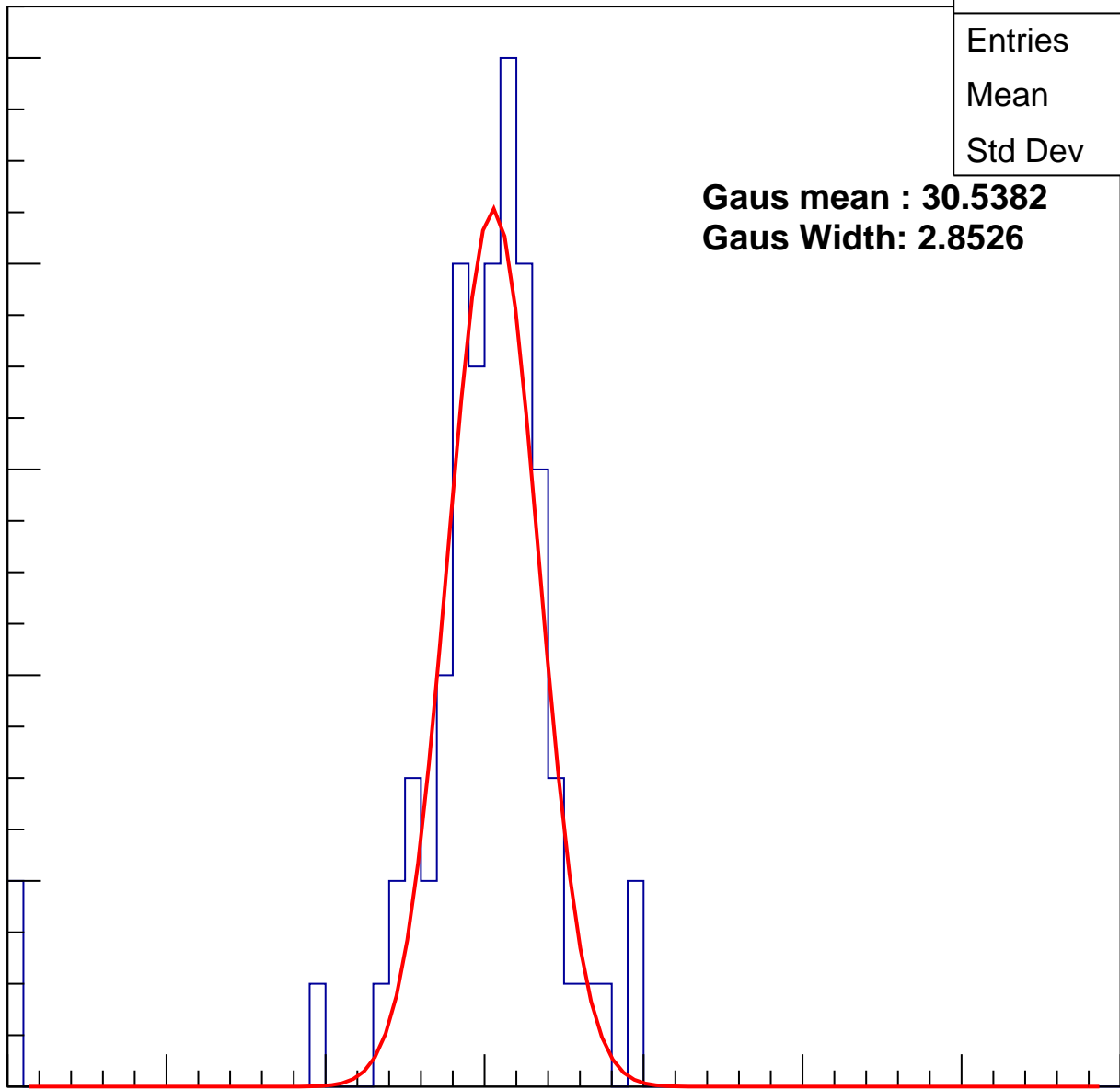
**Gaus Width: 2.8526**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U18-ch115, adc1

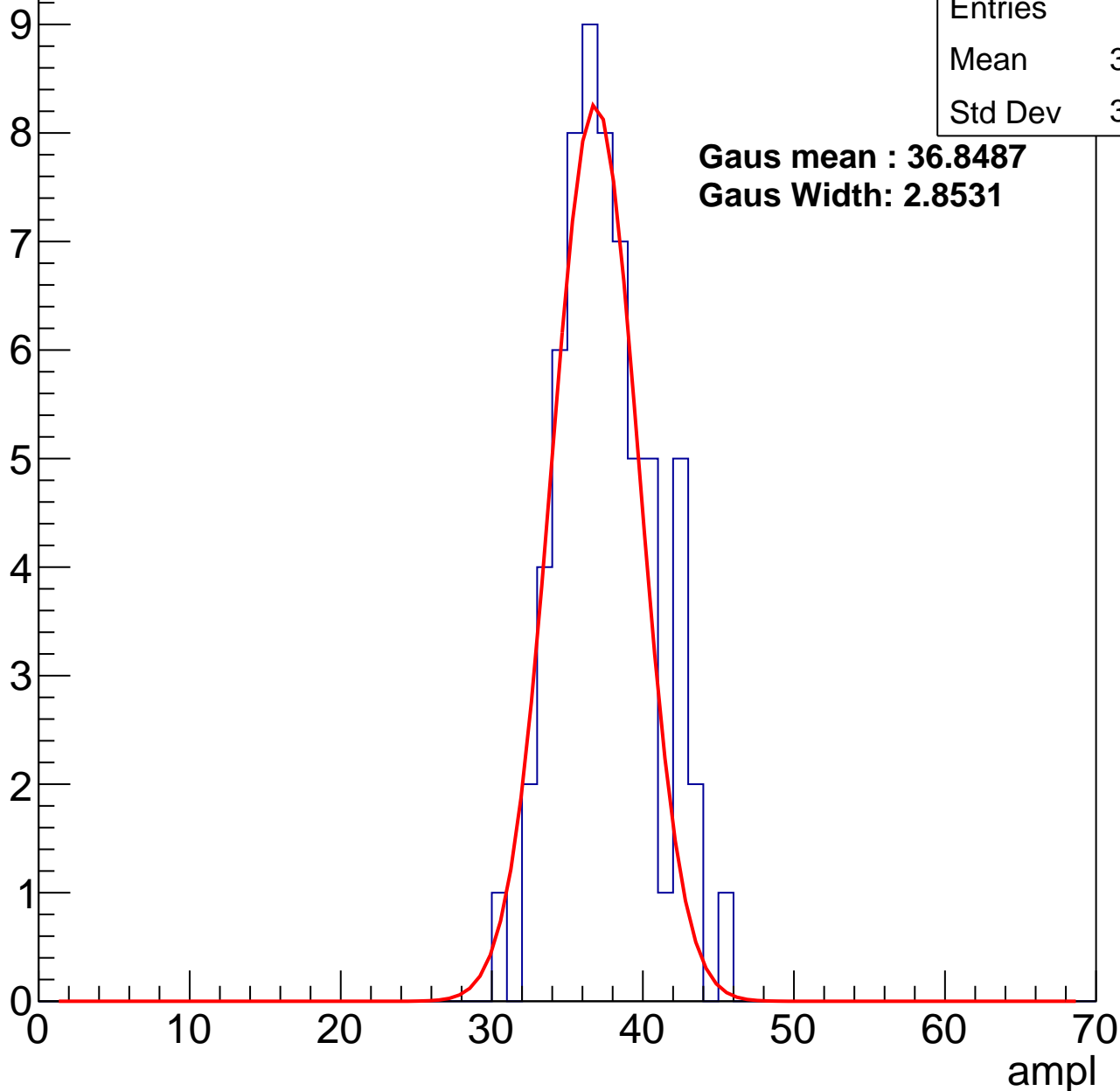
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	37.08
Std Dev	3.079

**Gaus mean : 36.8487**

**Gaus Width: 2.8531**



# B1L002S, U18-ch115, adc2

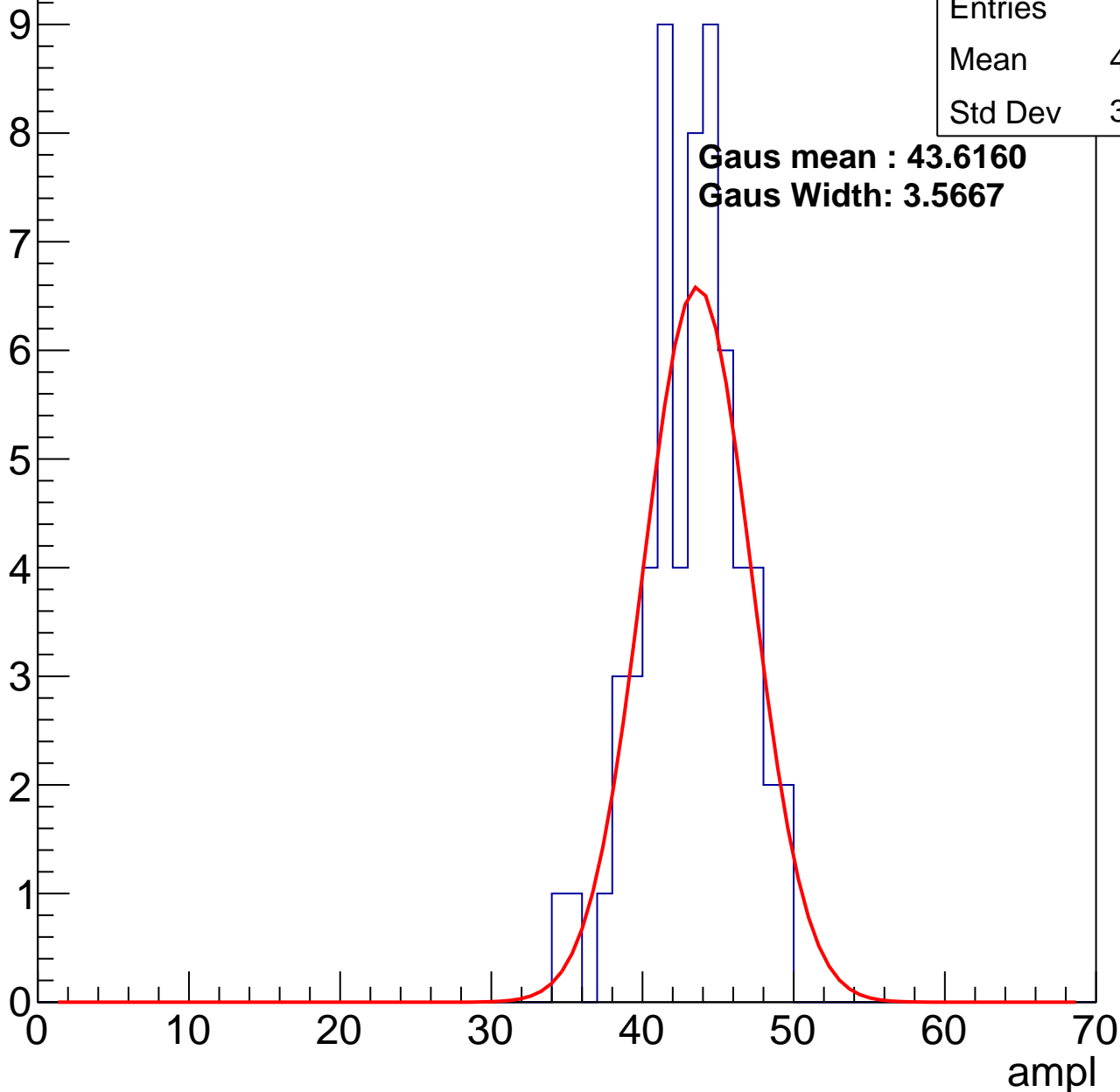
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	42.79
Std Dev	3.219

**Gaus mean : 43.6160**

**Gaus Width: 3.5667**

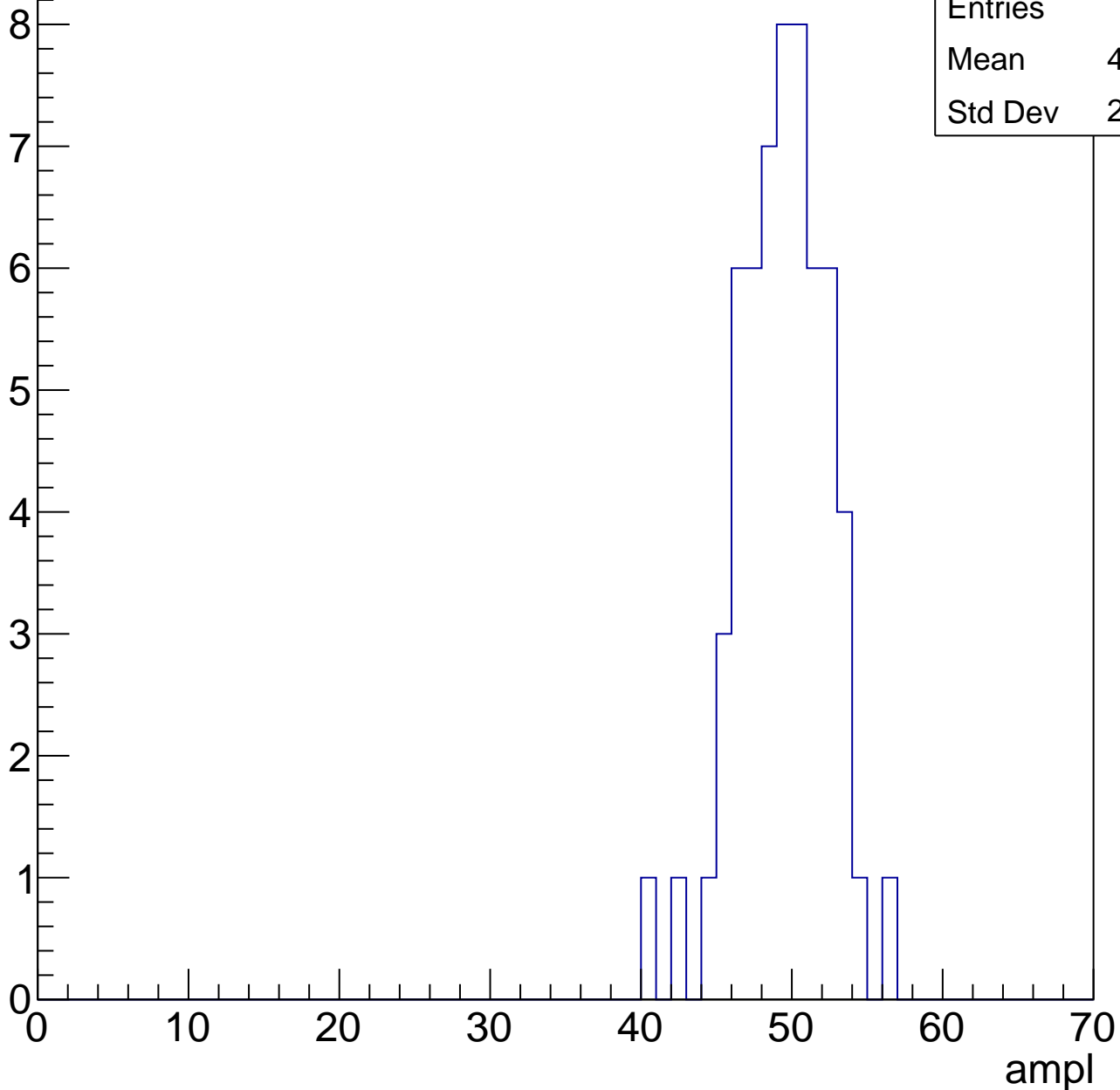


# B1L002S, U18-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	48.93
Std Dev	2.945

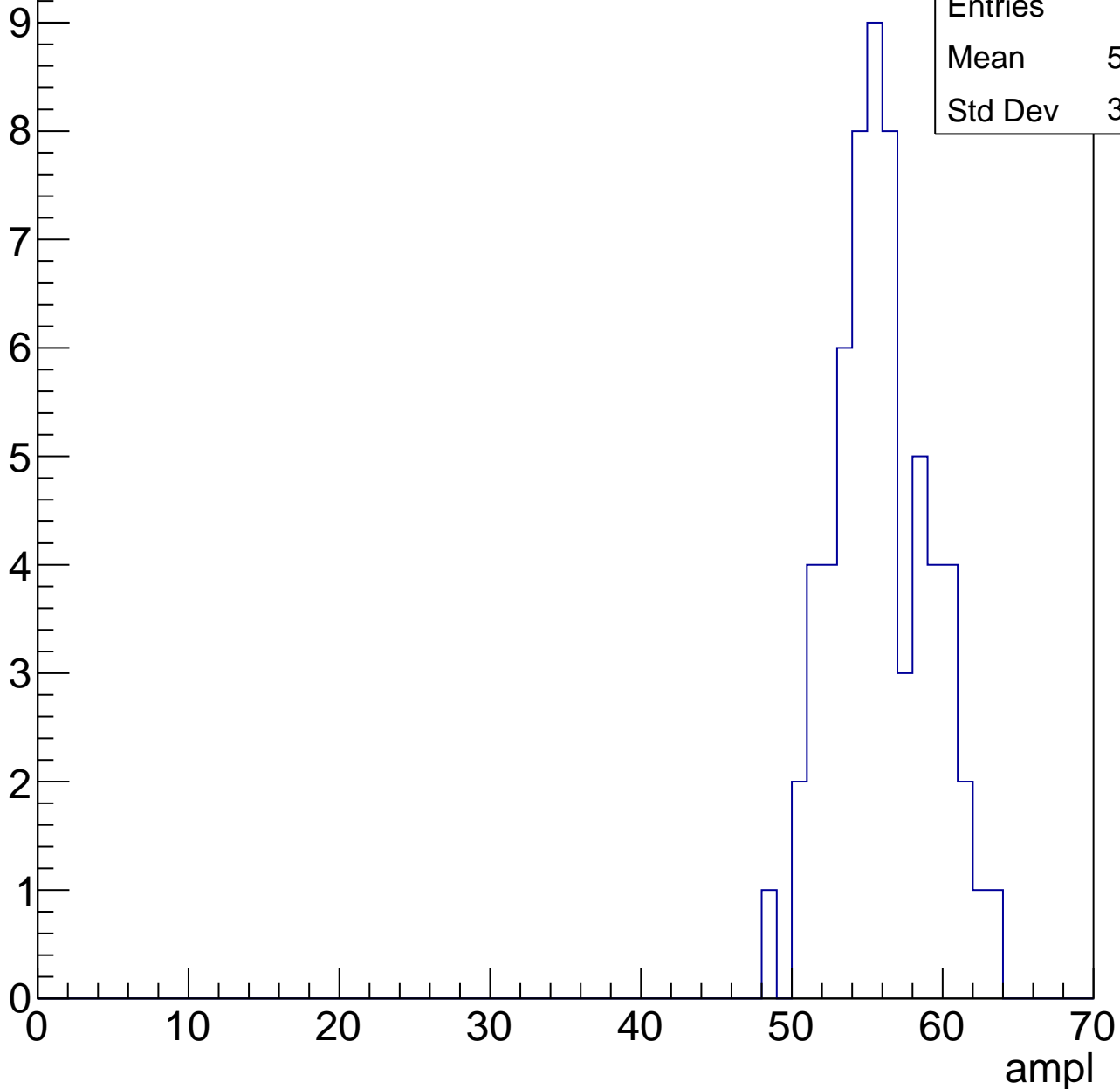


# B1L002S, U18-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	55.44
Std Dev	3.196

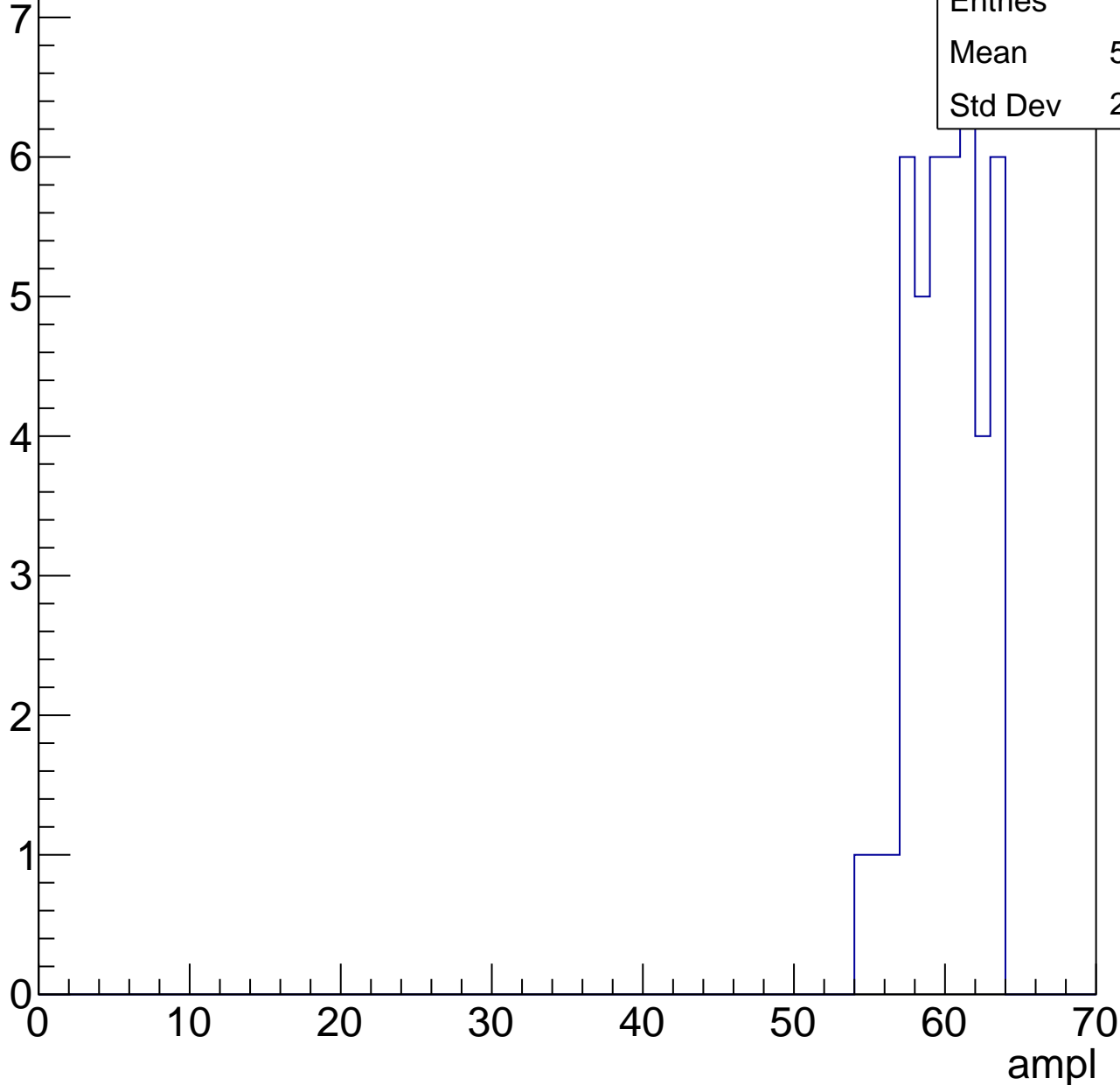


# B1L002S, U18-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	59.63
Std Dev	2.303

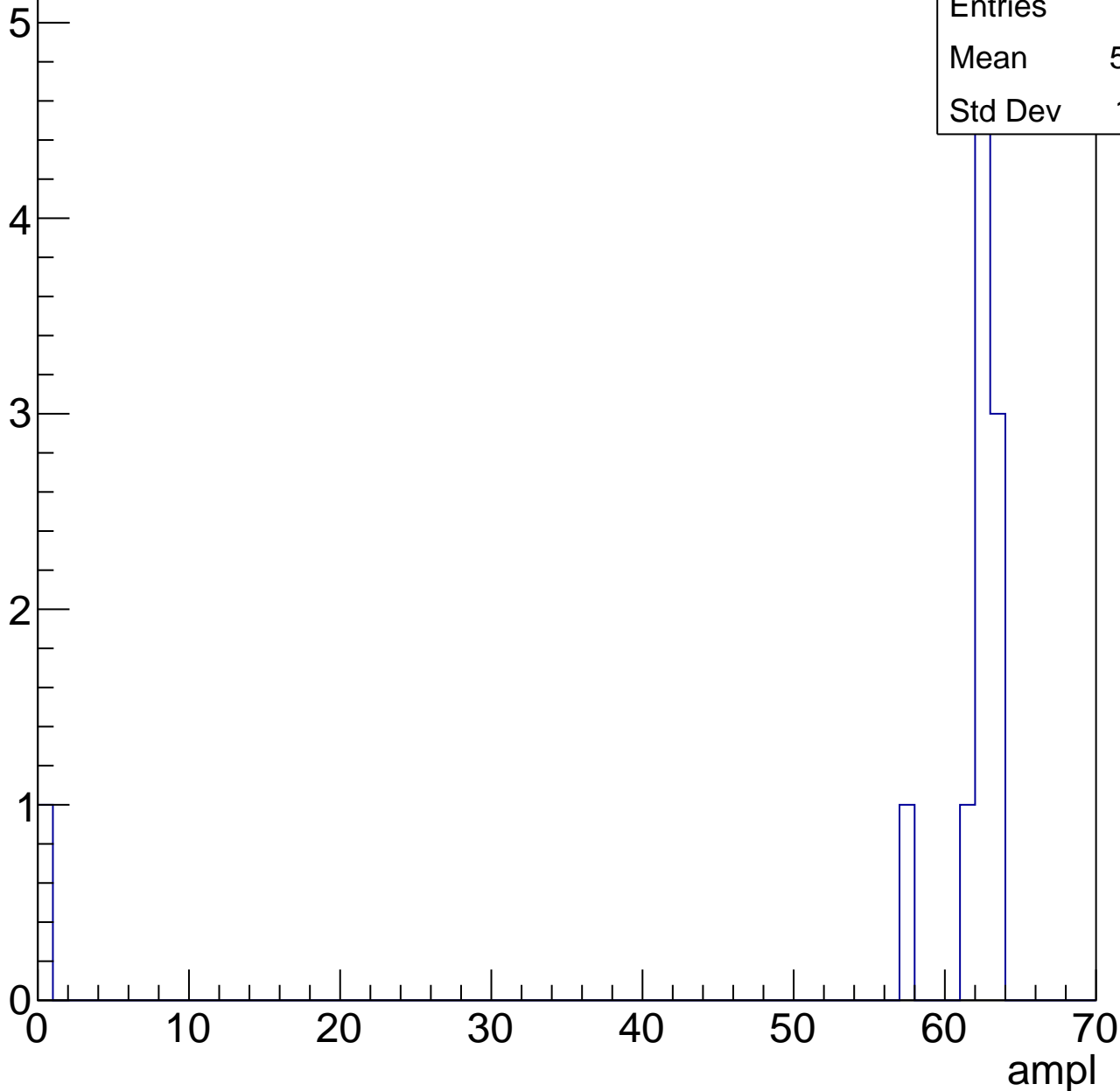


# B1L002S, U18-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	56.09
Std Dev	17.81





# B1L002S, U18-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch116, adc0

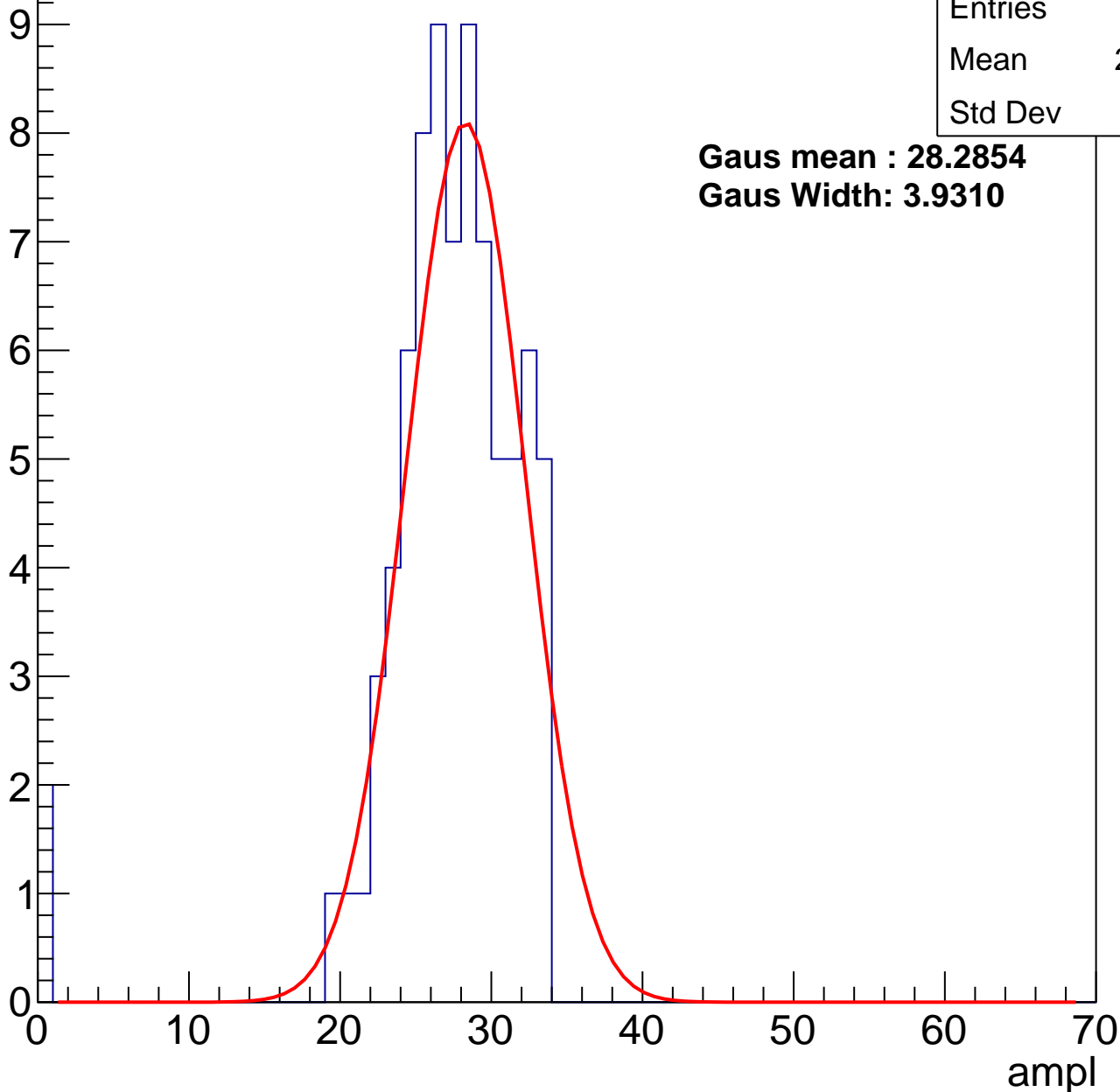
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	26.61
Std Dev	5.42

**Gaus mean : 28.2854**

**Gaus Width: 3.9310**



# B1L002S, U18-ch116, adc1

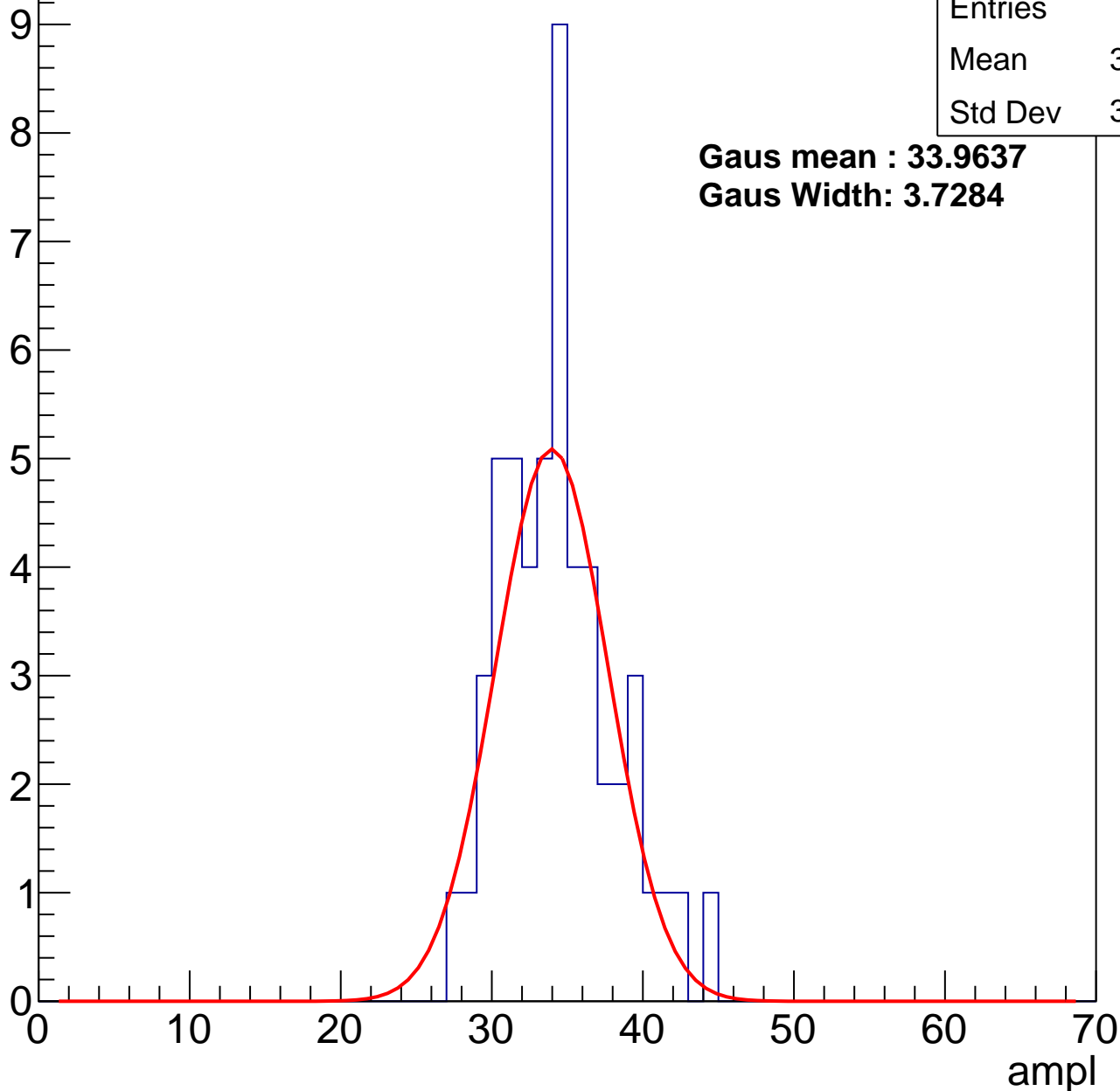
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.92
Std Dev	3.668

**Gaus mean : 33.9637**

**Gaus Width: 3.7284**



# B1L002S, U18-ch116, adc2

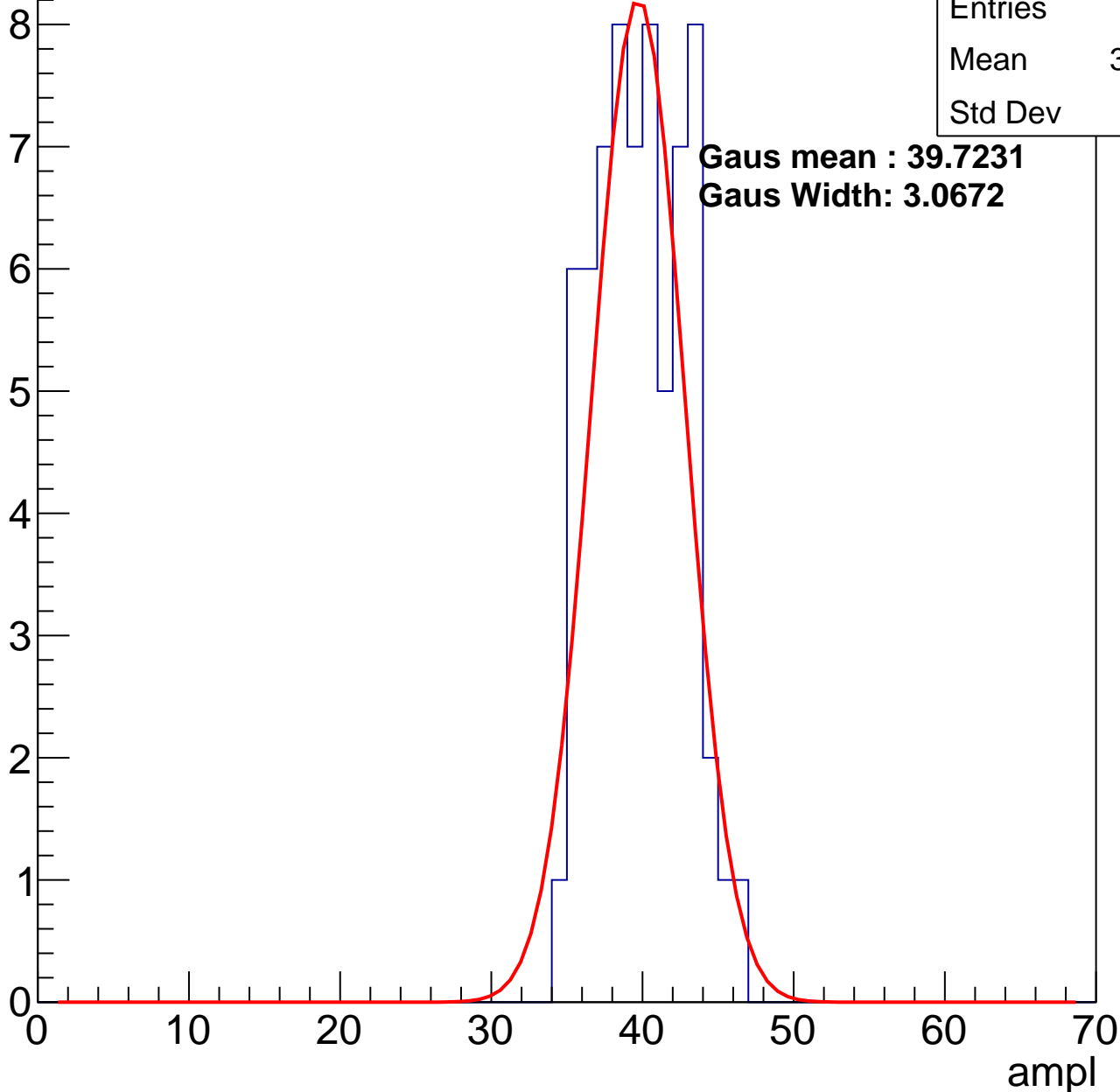
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.37
Std Dev	2.88

**Gaus mean : 39.7231**

**Gaus Width: 3.0672**

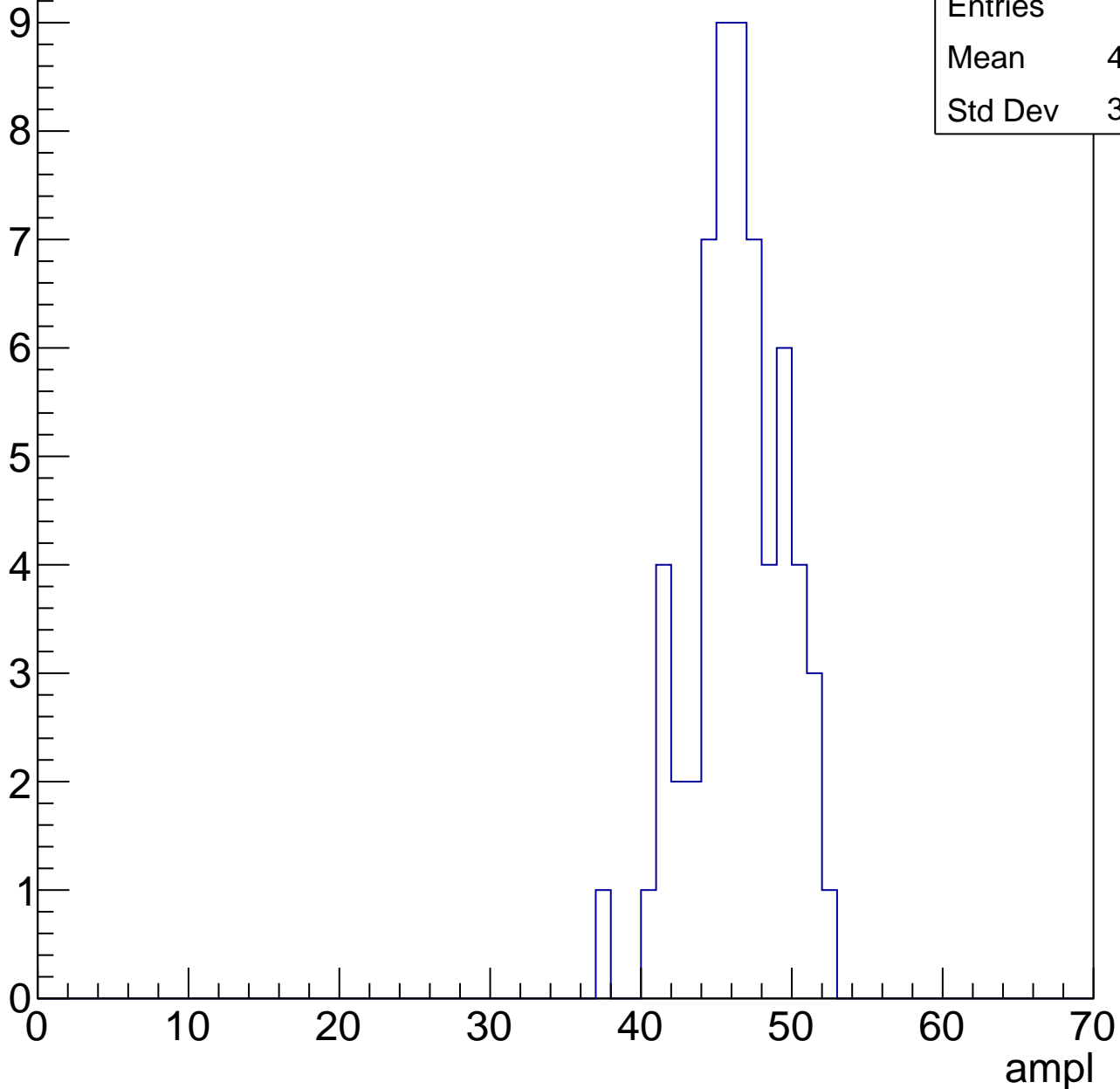


# B1L002S, U18-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.97
Std Dev	3.044

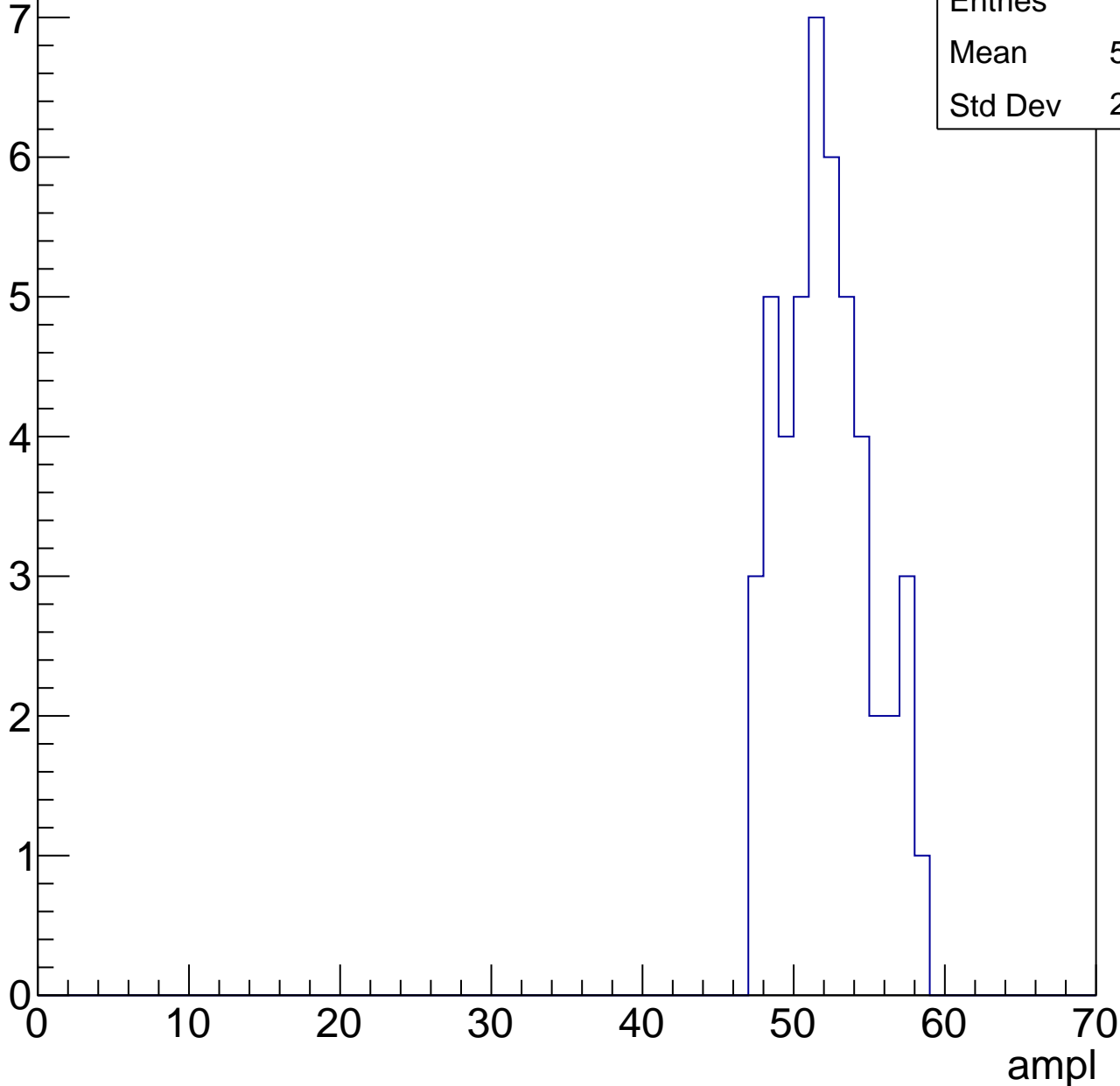


# B1L002S, U18-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	51.66
Std Dev	2.897

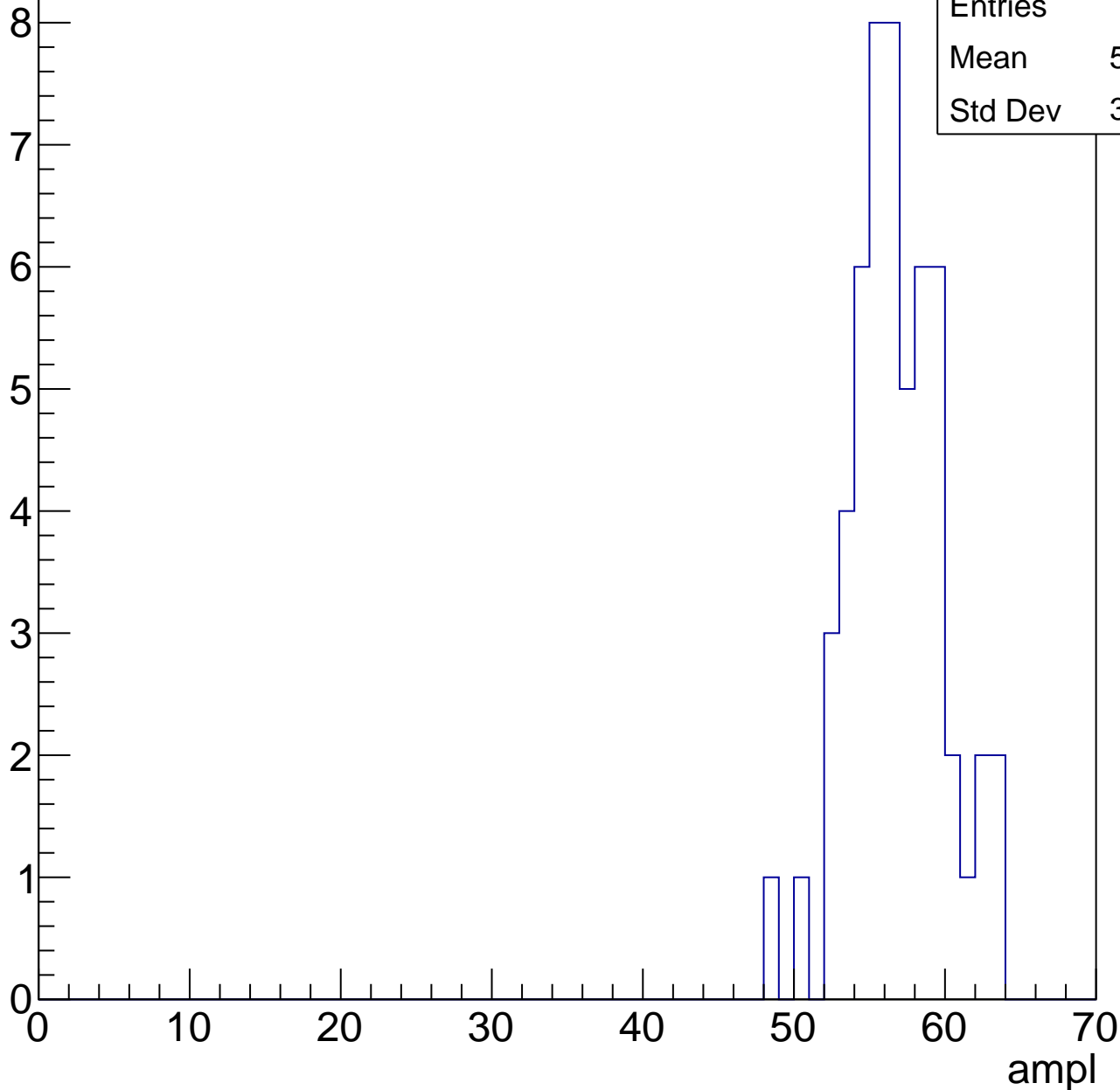


# B1L002S, U18-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	56.29
Std Dev	3.079

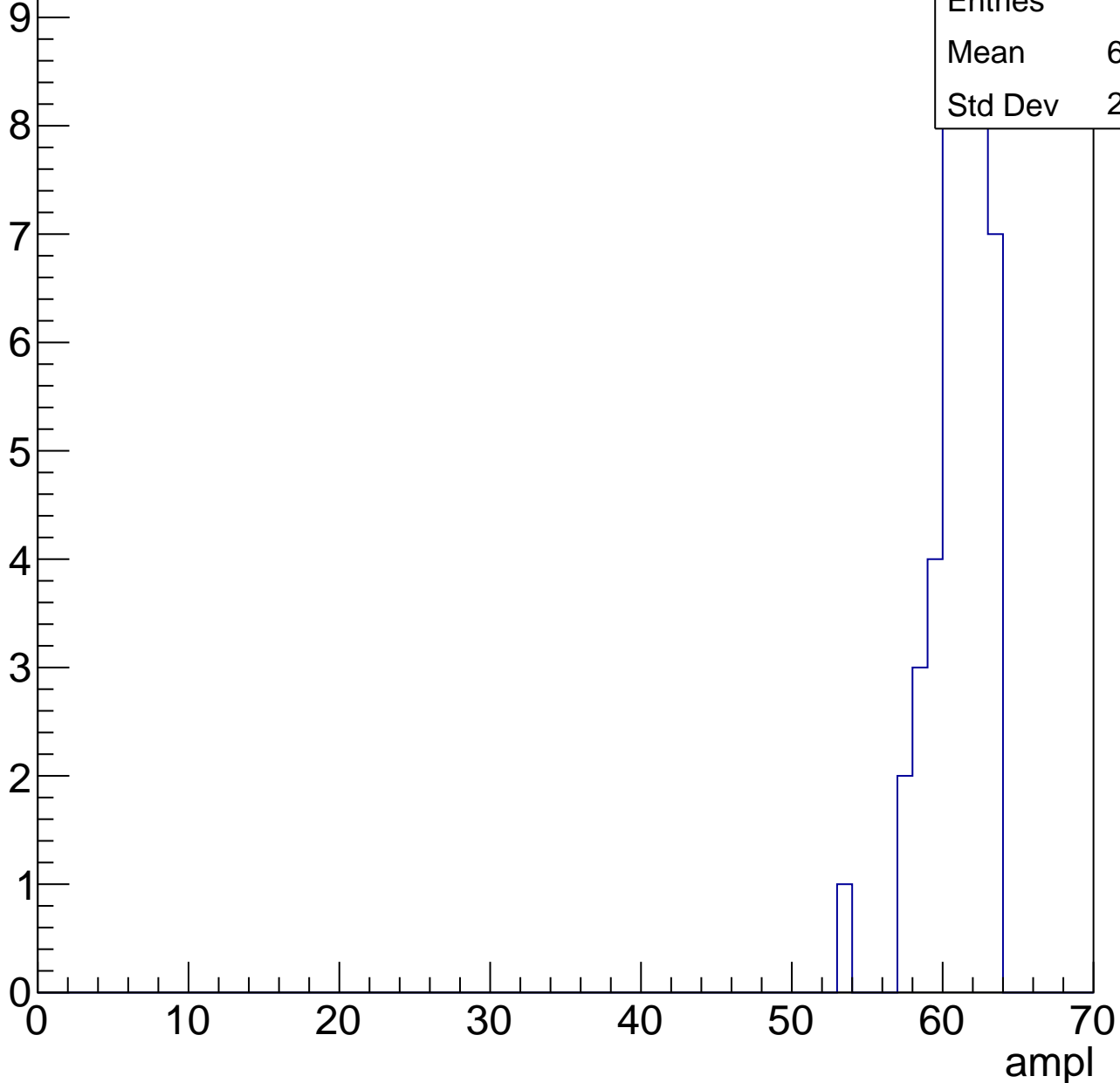


# B1L002S, U18-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	60.55
Std Dev	2.038

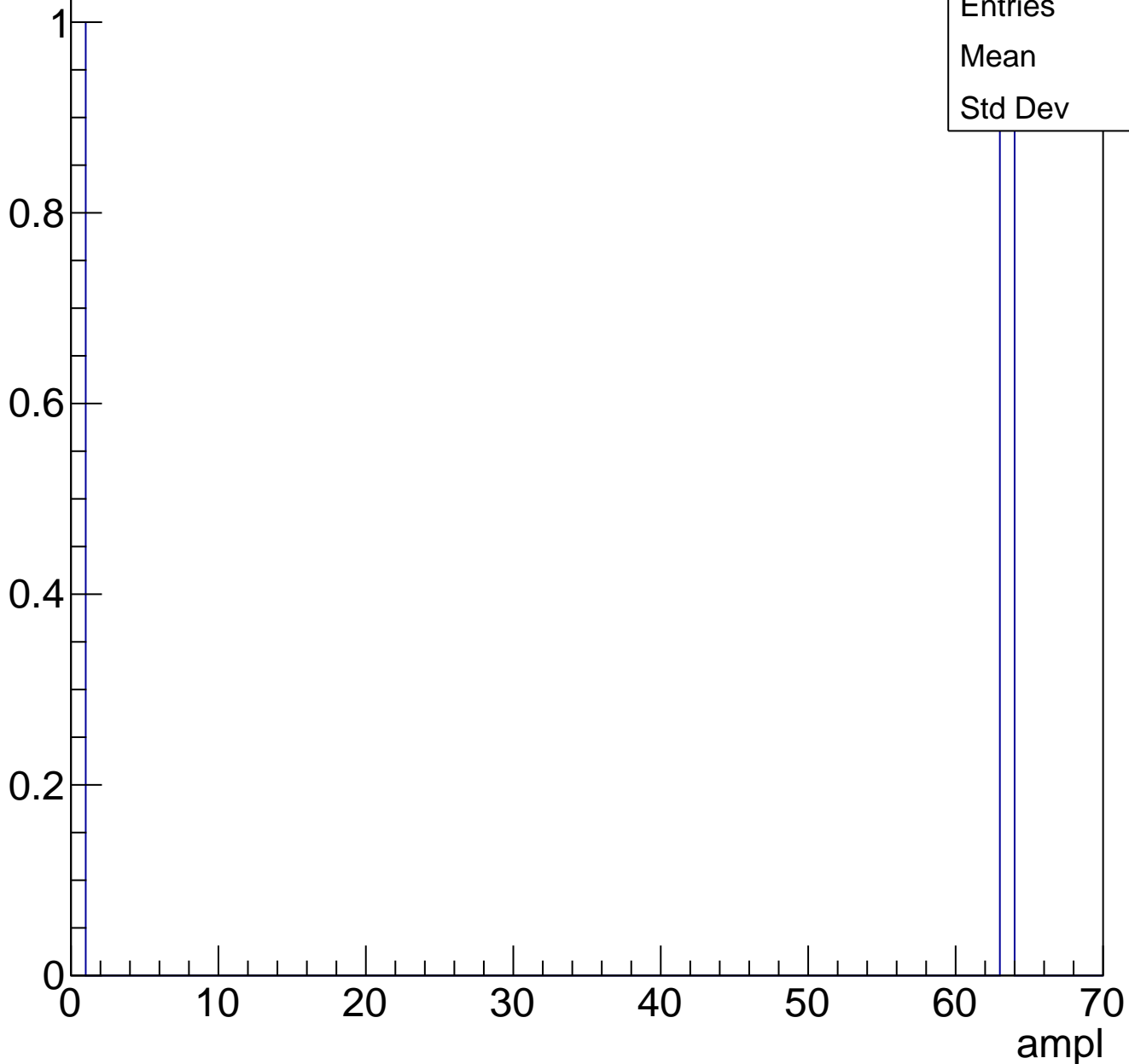




# B1L002S, U18-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch117, adc0

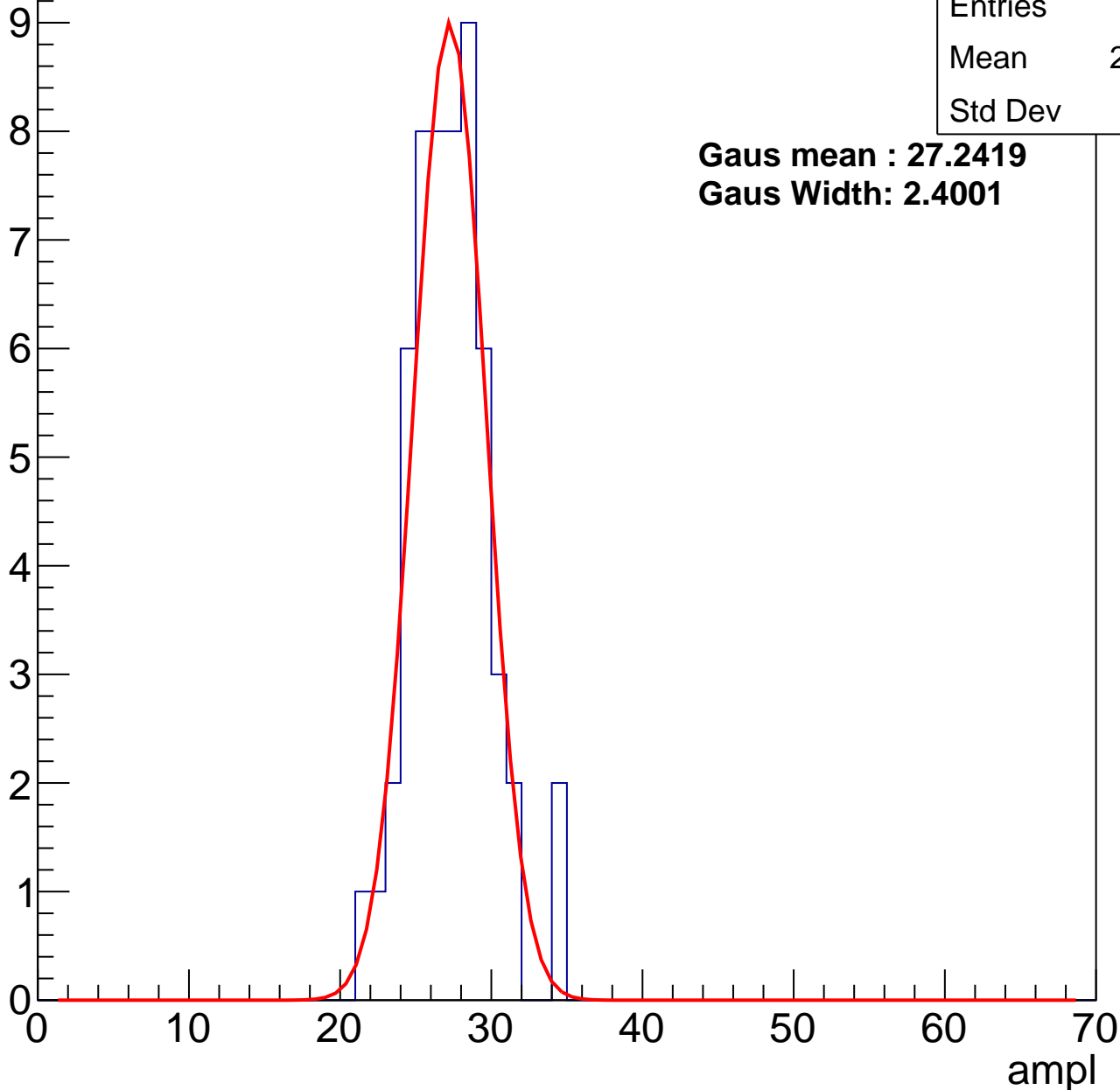
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	26.84
Std Dev	2.59

**Gaus mean : 27.2419**

**Gaus Width: 2.4001**



# B1L002S, U18-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	33.26
Std Dev	2.786

**Gaus mean : 33.4335**

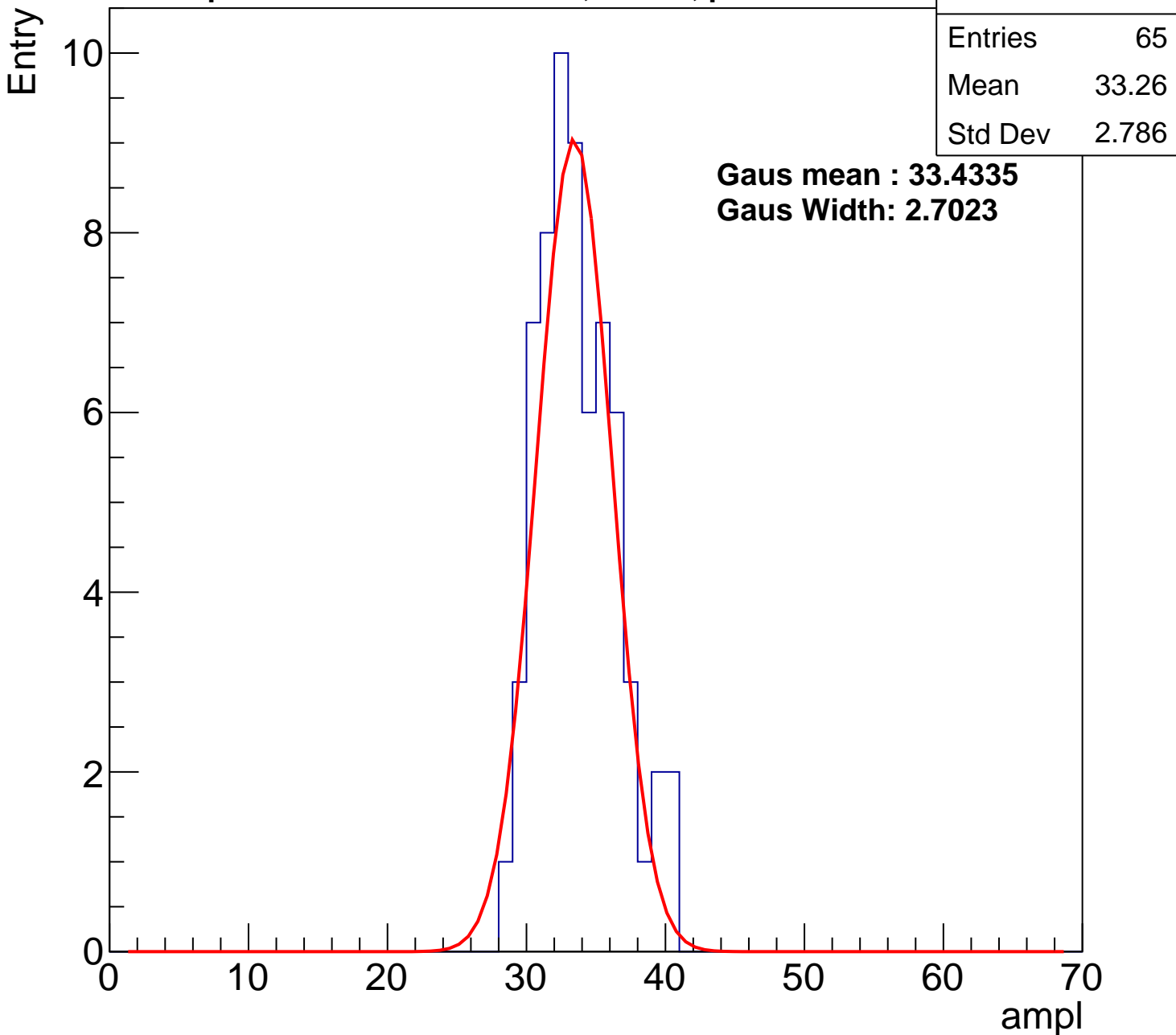
**Gaus Width: 2.7023**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U18-ch117, adc2

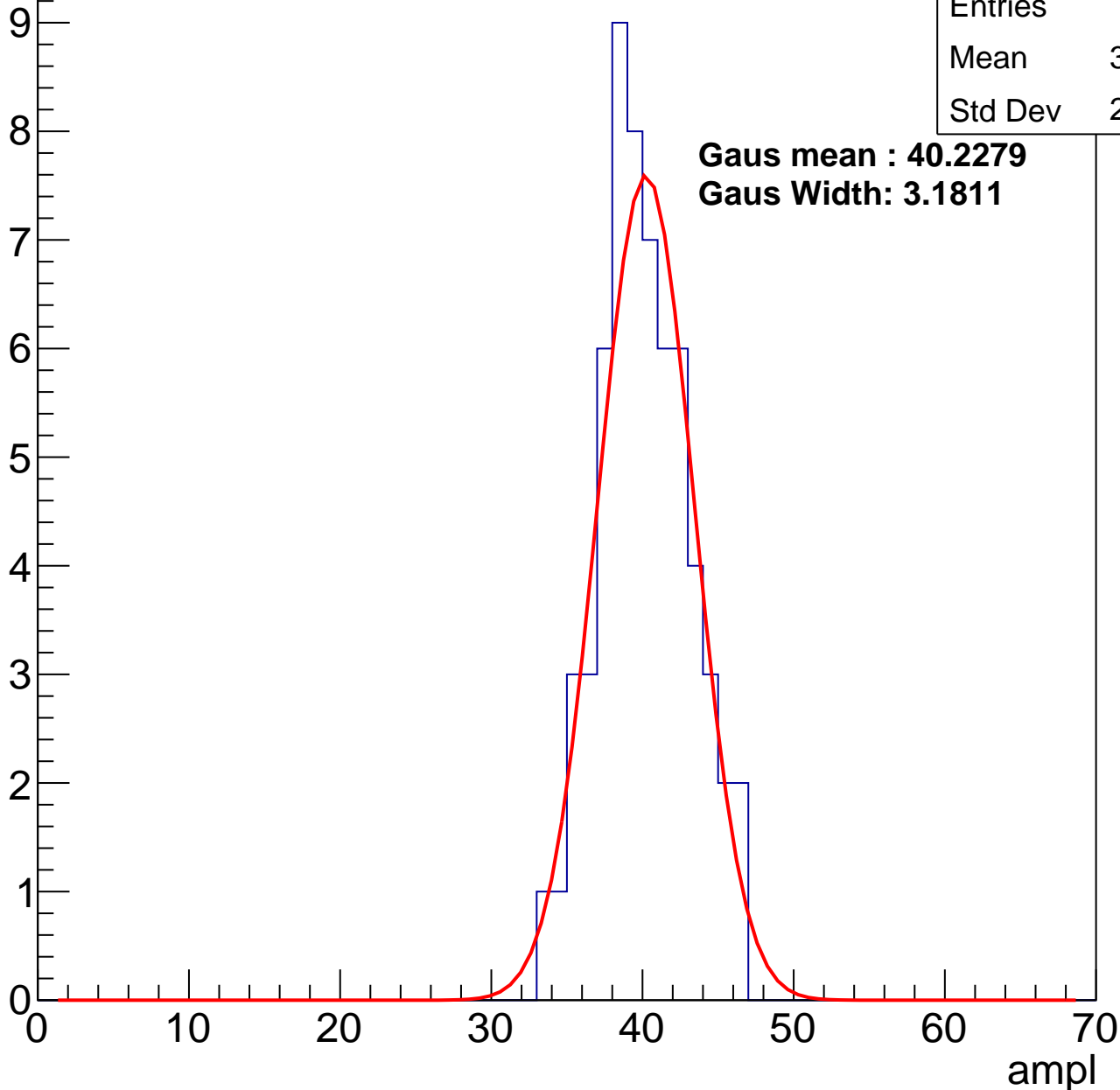
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.67
Std Dev	2.963

**Gaus mean : 40.2279**

**Gaus Width: 3.1811**

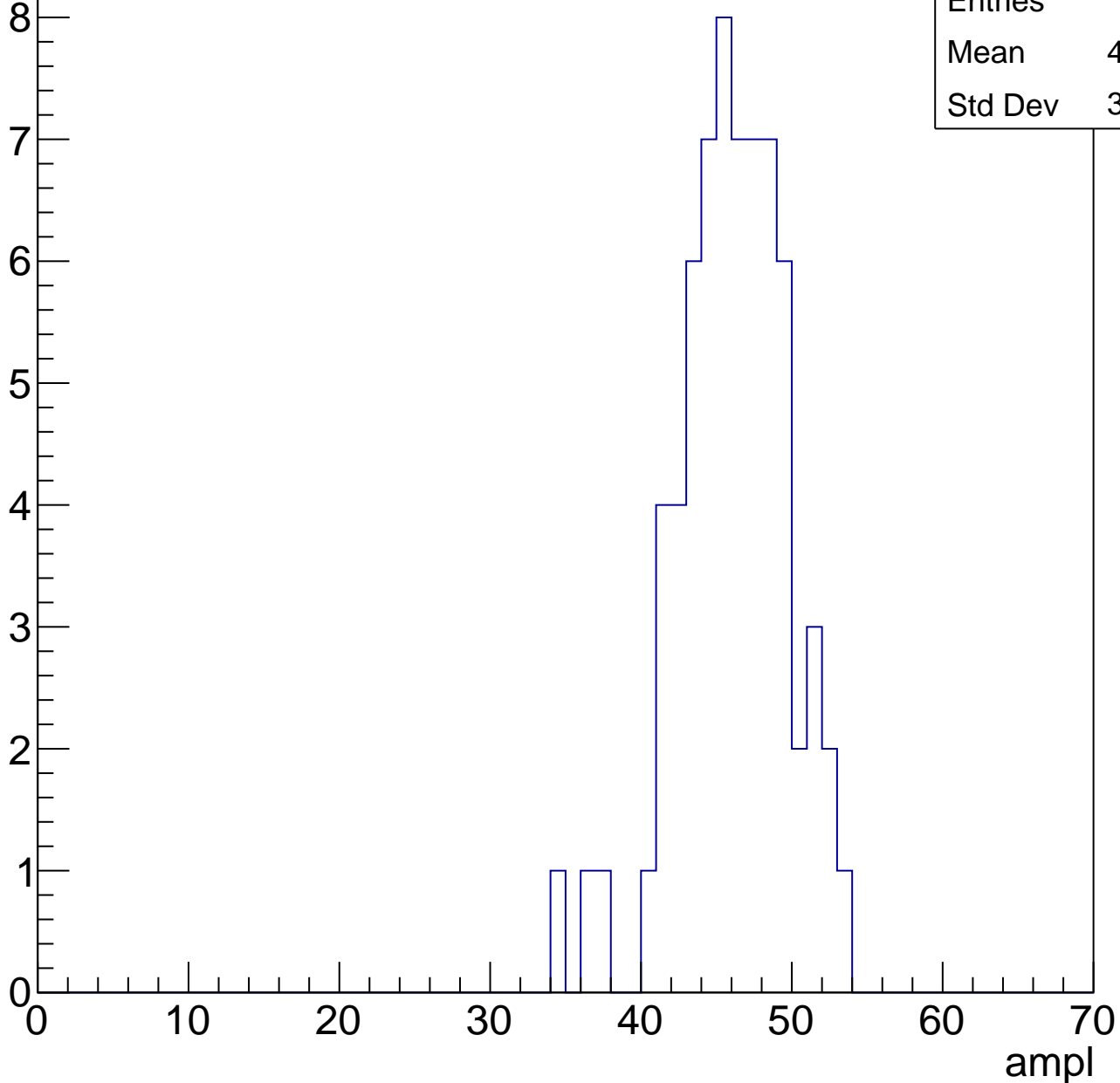


# B1L002S, U18-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	45.53
Std Dev	3.672

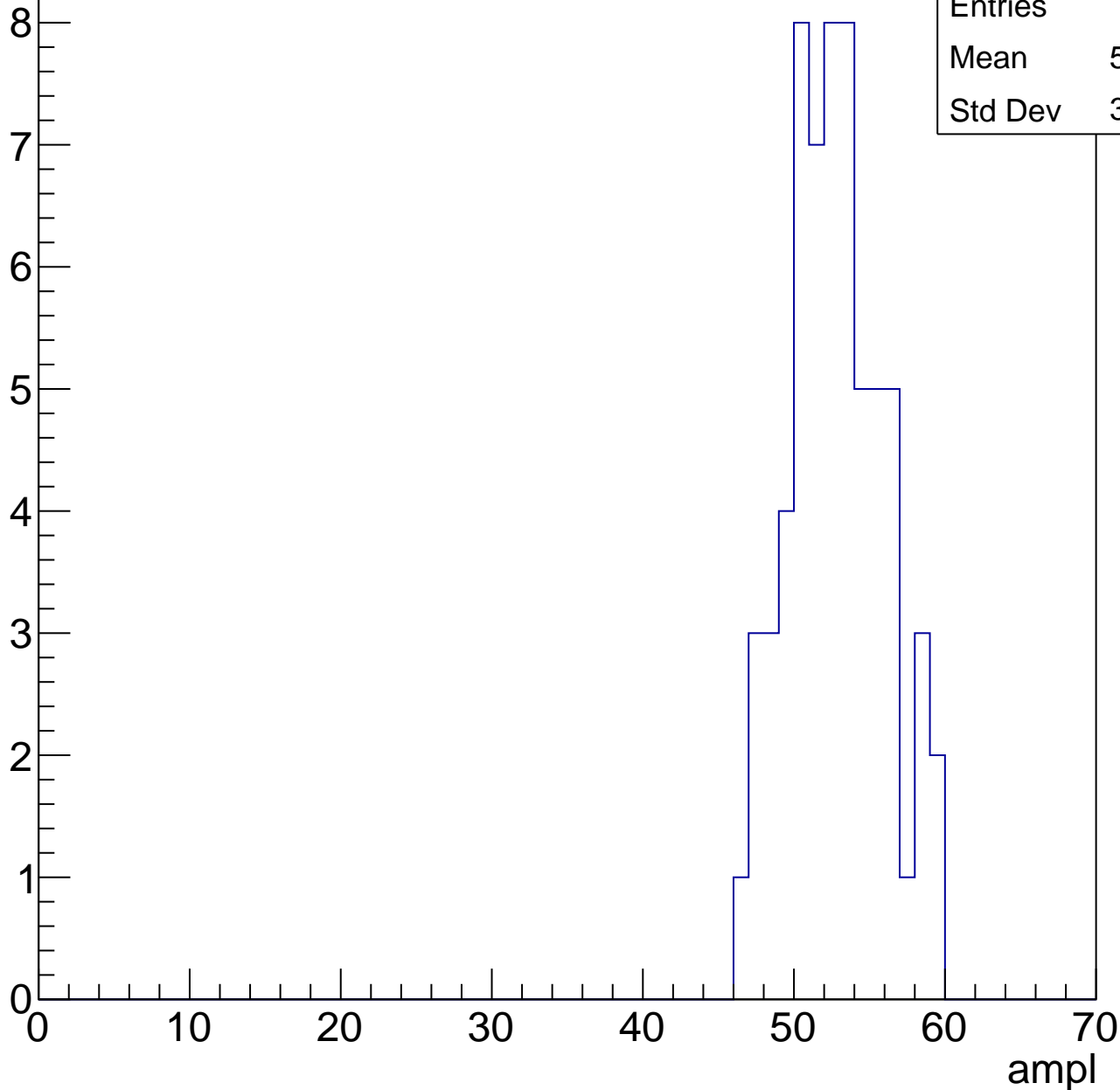


# B1L002S, U18-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	52.35
Std Dev	3.112

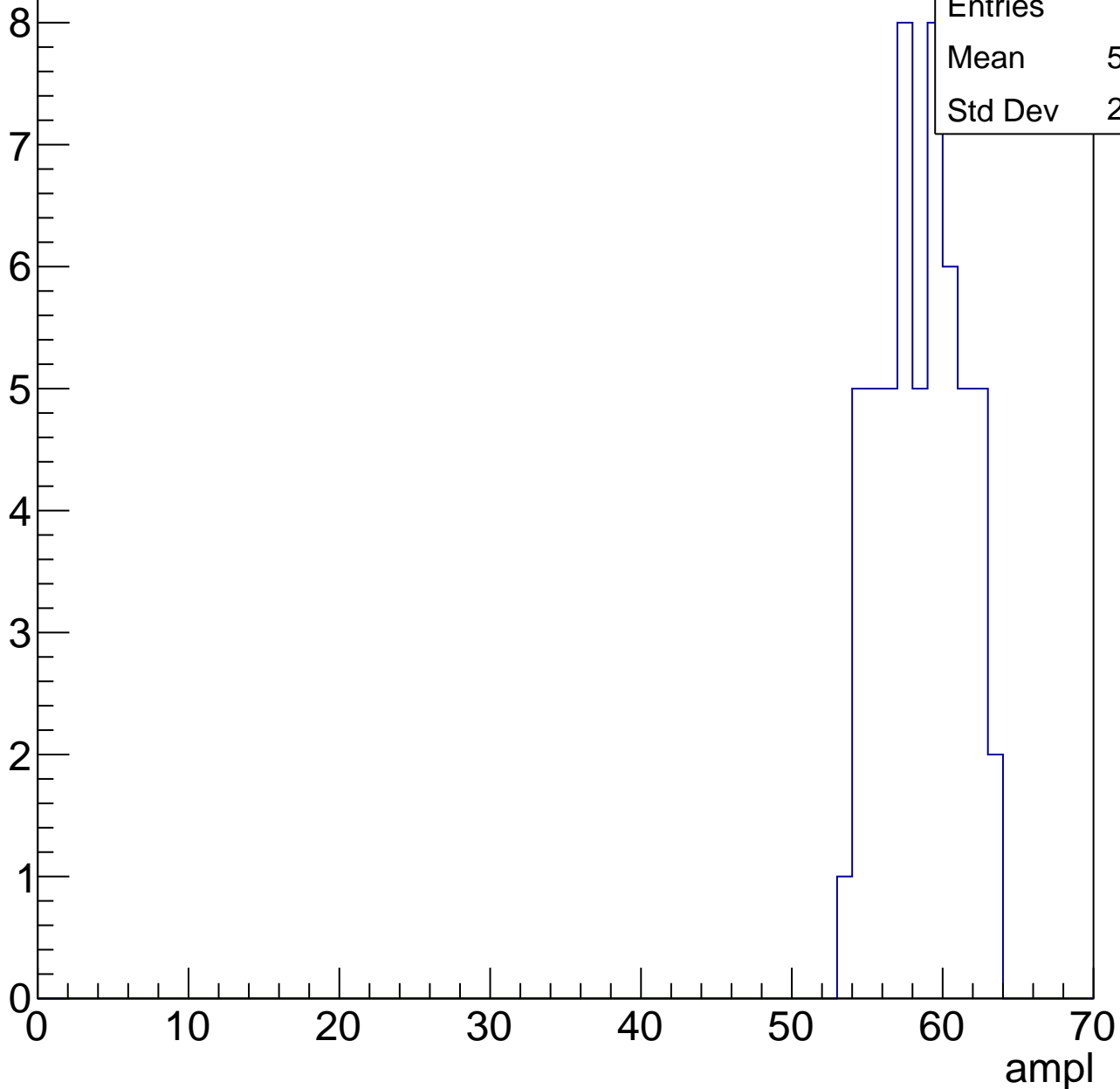


# B1L002S, U18-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	58.13
Std Dev	2.643

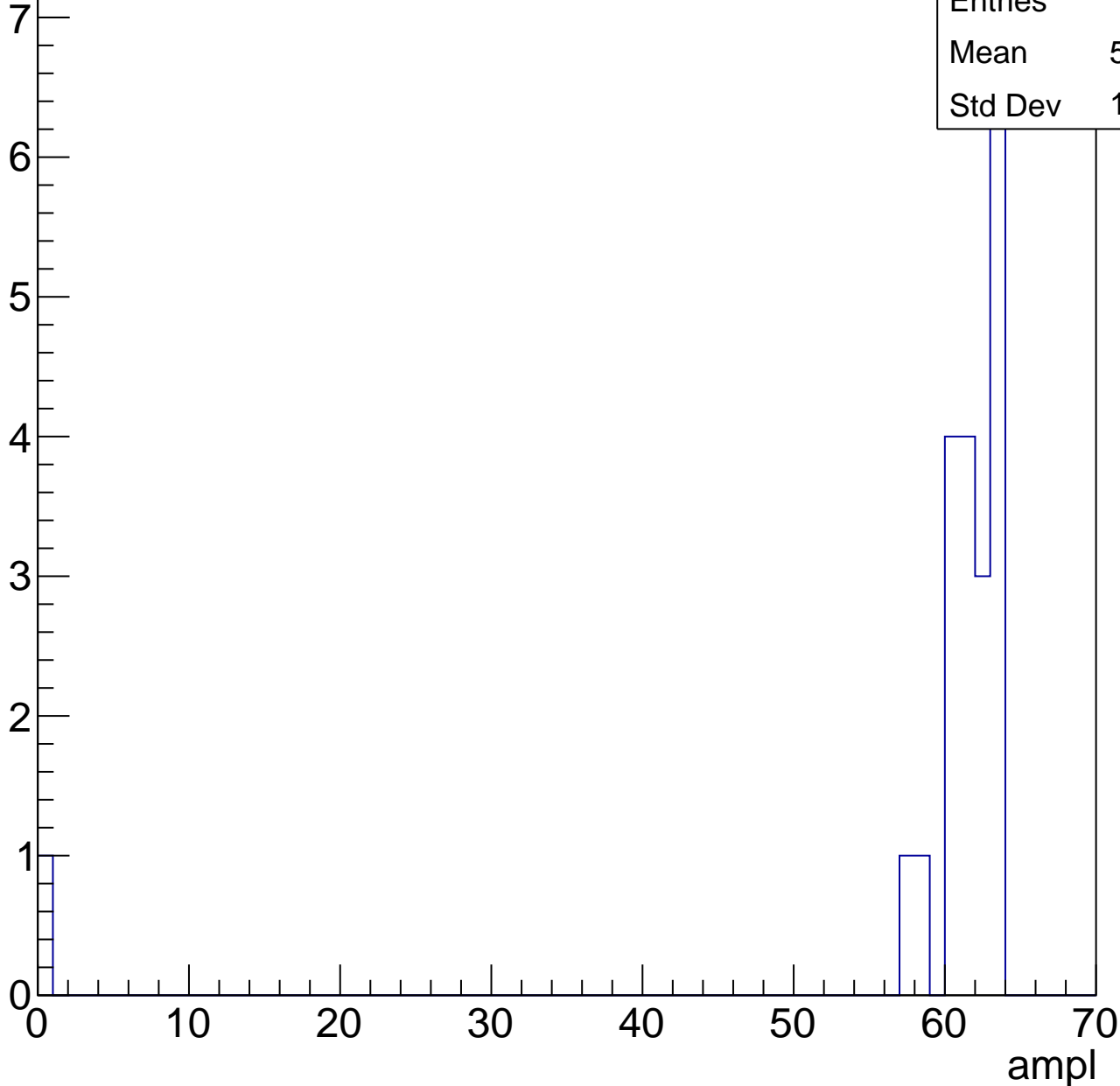


# B1L002S, U18-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.38
Std Dev	13.16

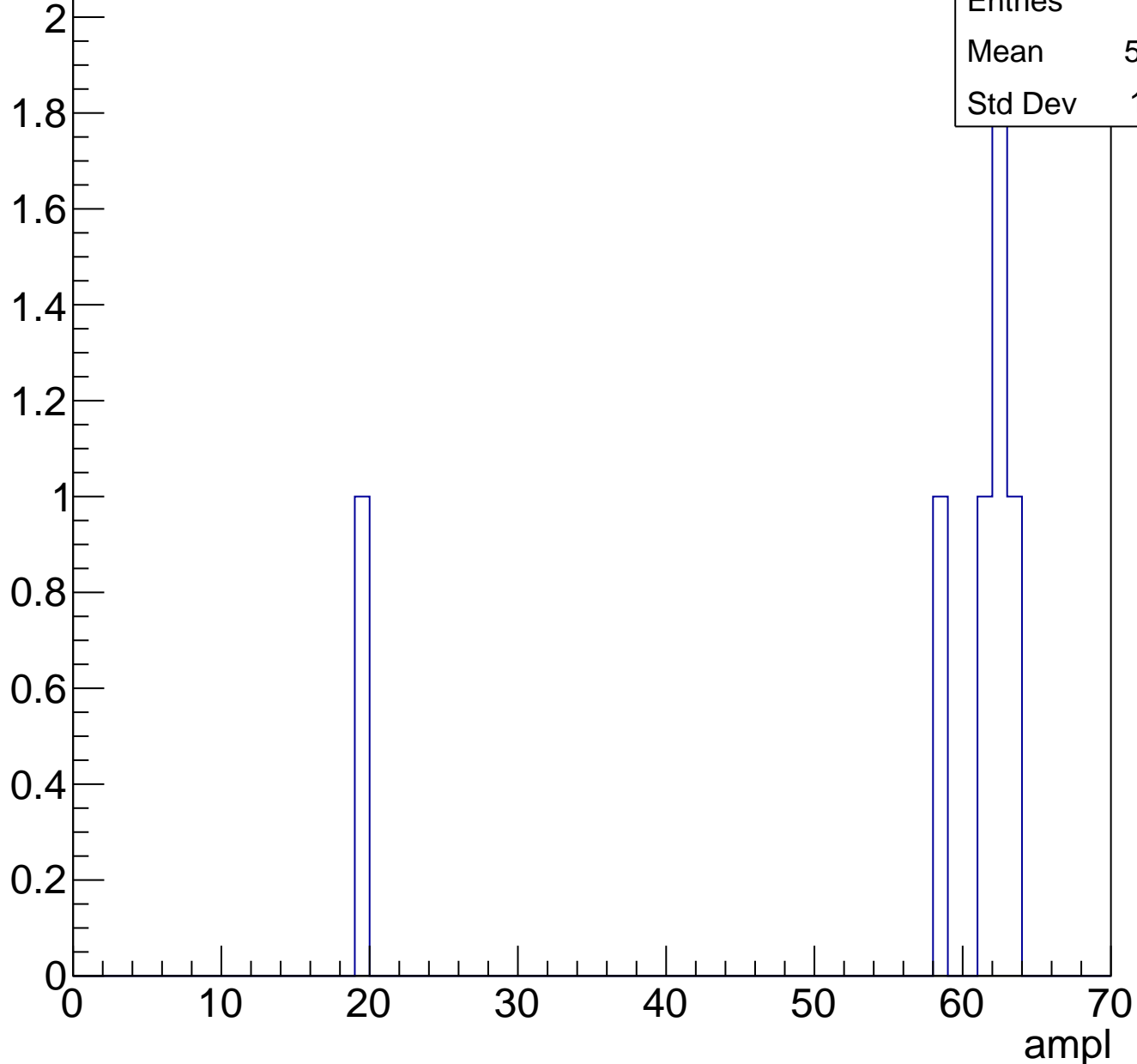




# B1L002S, U18-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch118, adc0

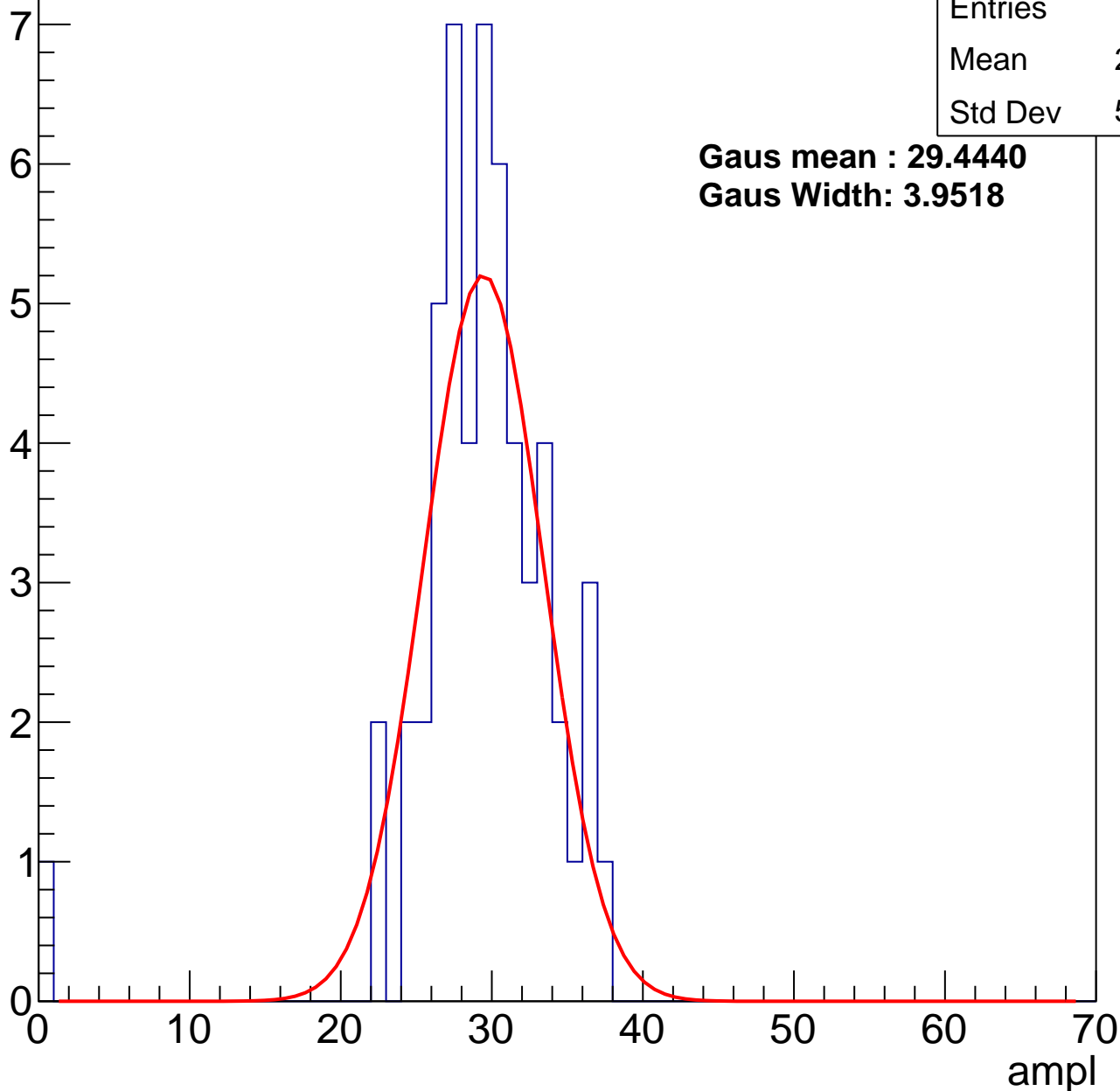
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.81
Std Dev	5.281

**Gaus mean : 29.4440**

**Gaus Width: 3.9518**



# B1L002S, U18-ch118, adc1

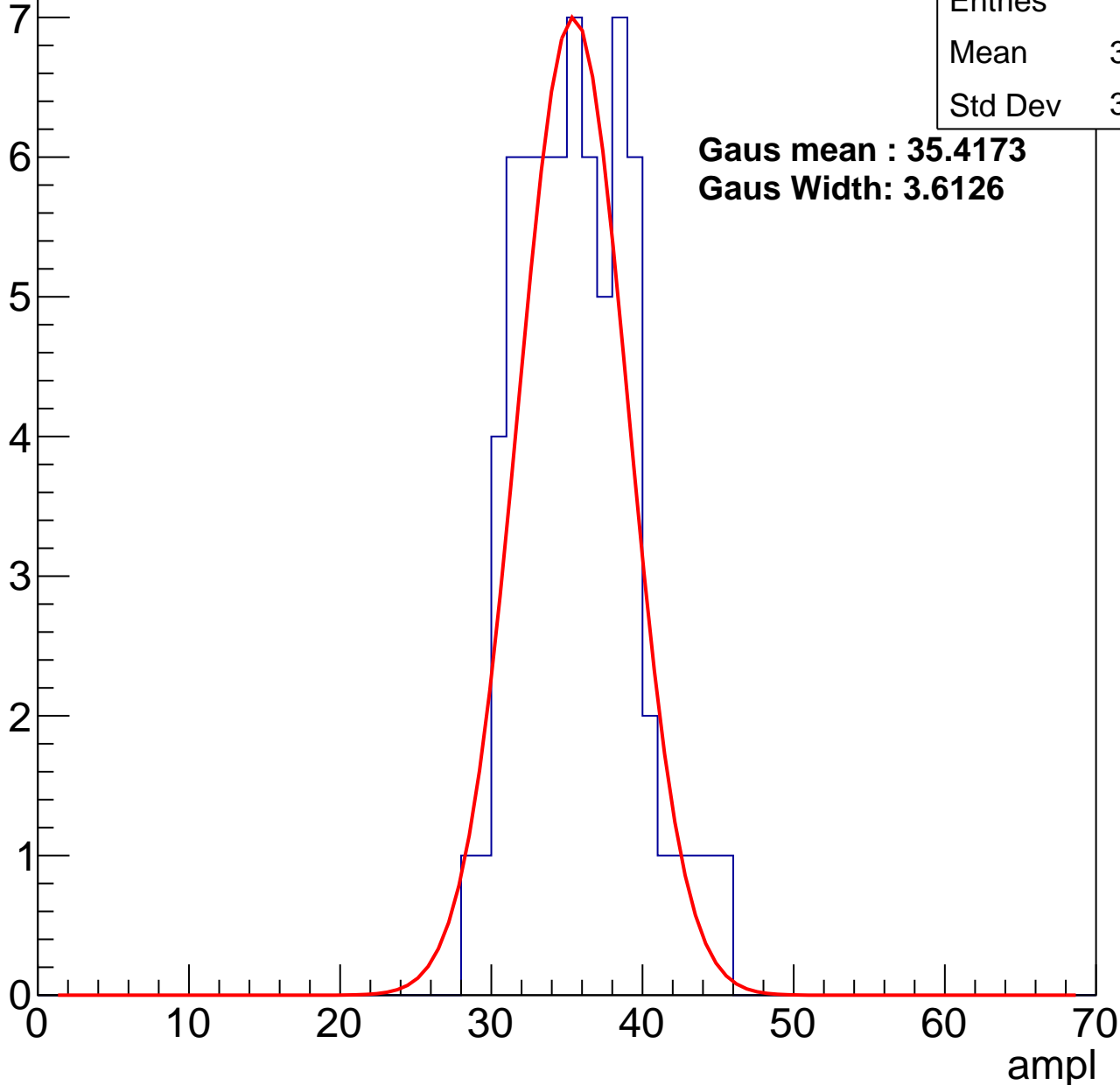
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.26
Std Dev	3.689

**Gaus mean : 35.4173**

**Gaus Width: 3.6126**



# B1L002S, U18-ch118, adc2

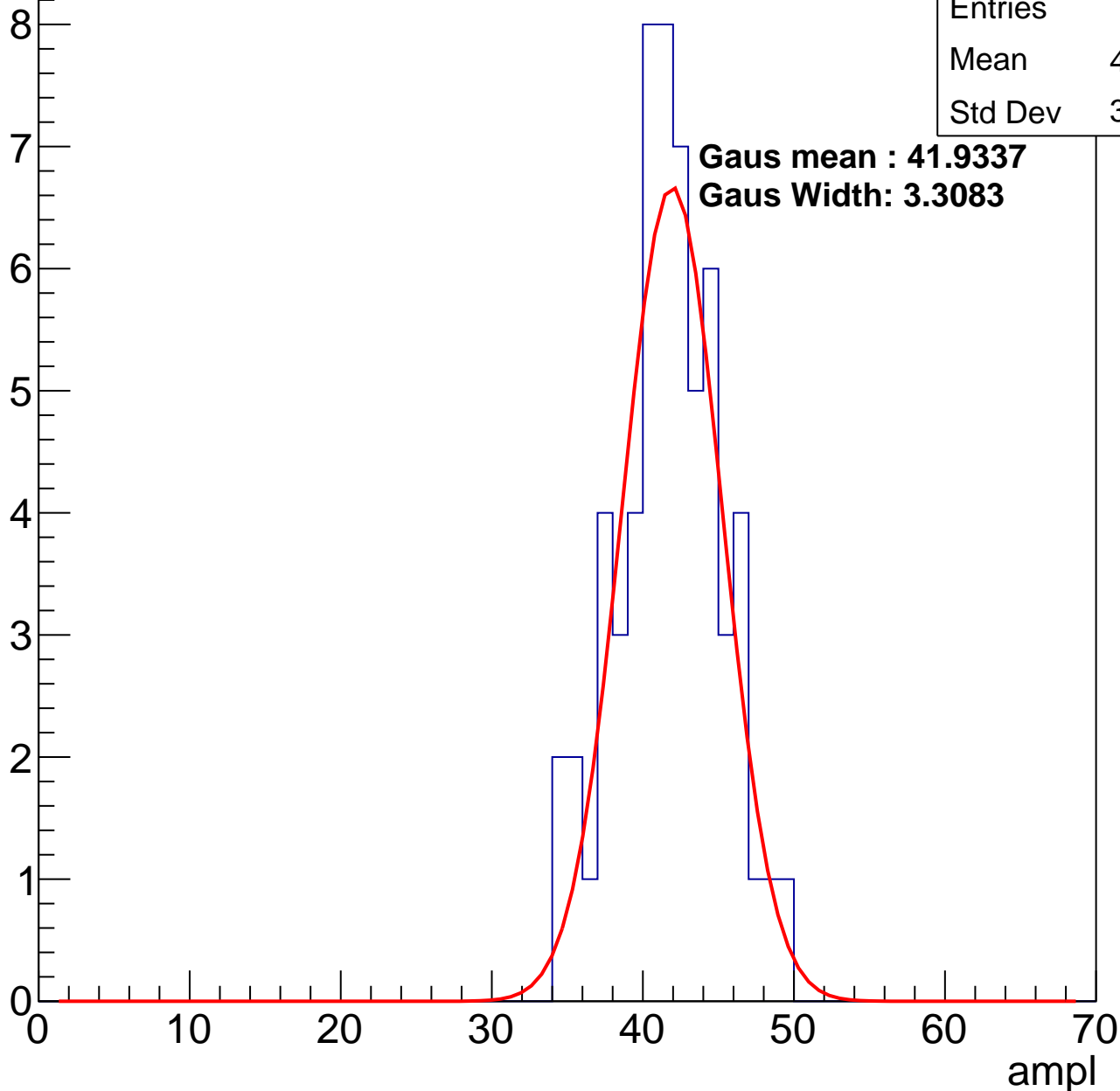
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.27
Std Dev	3.376

**Gaus mean : 41.9337**

**Gaus Width: 3.3083**

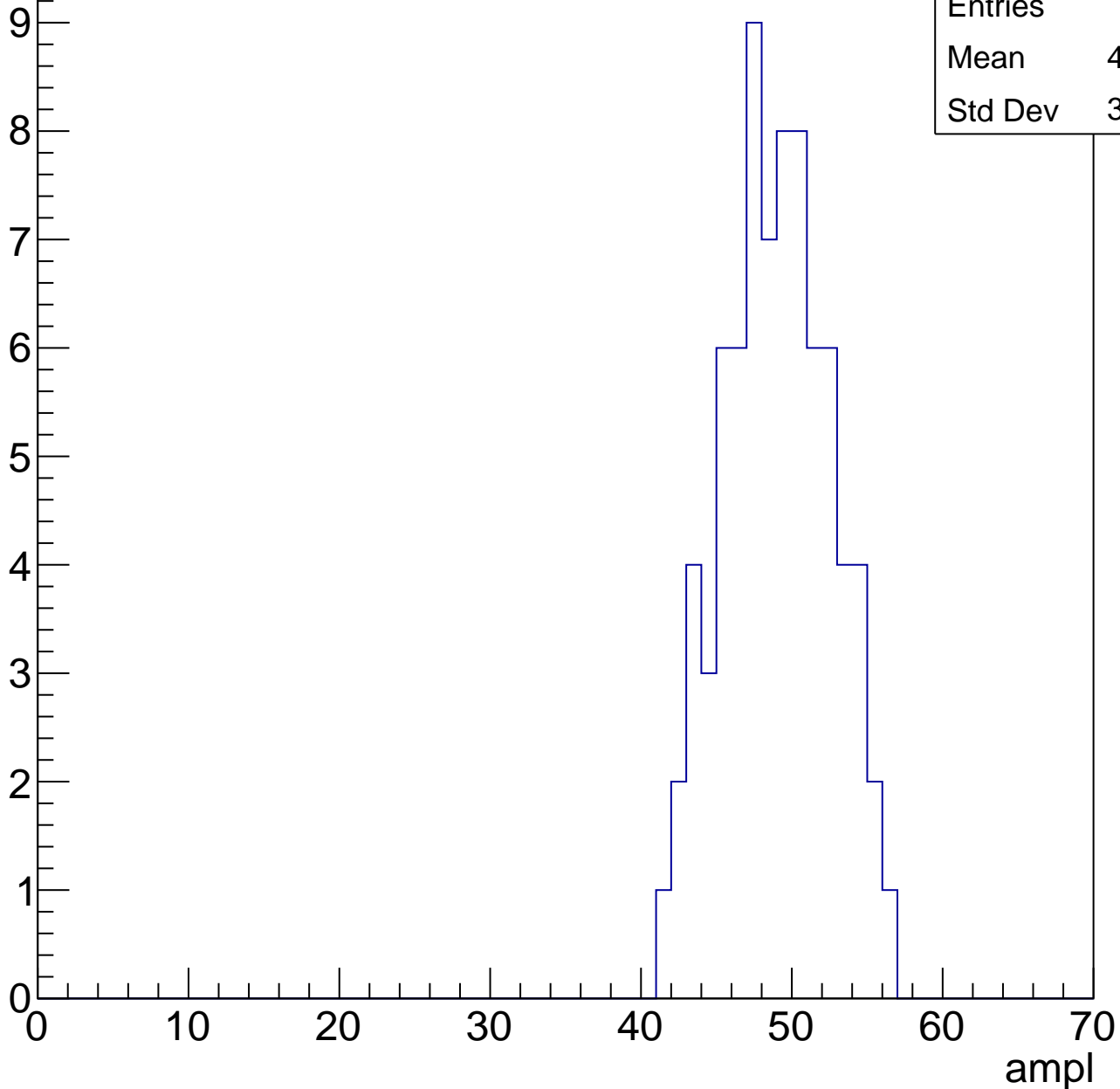


# B1L002S, U18-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	48.55
Std Dev	3.474

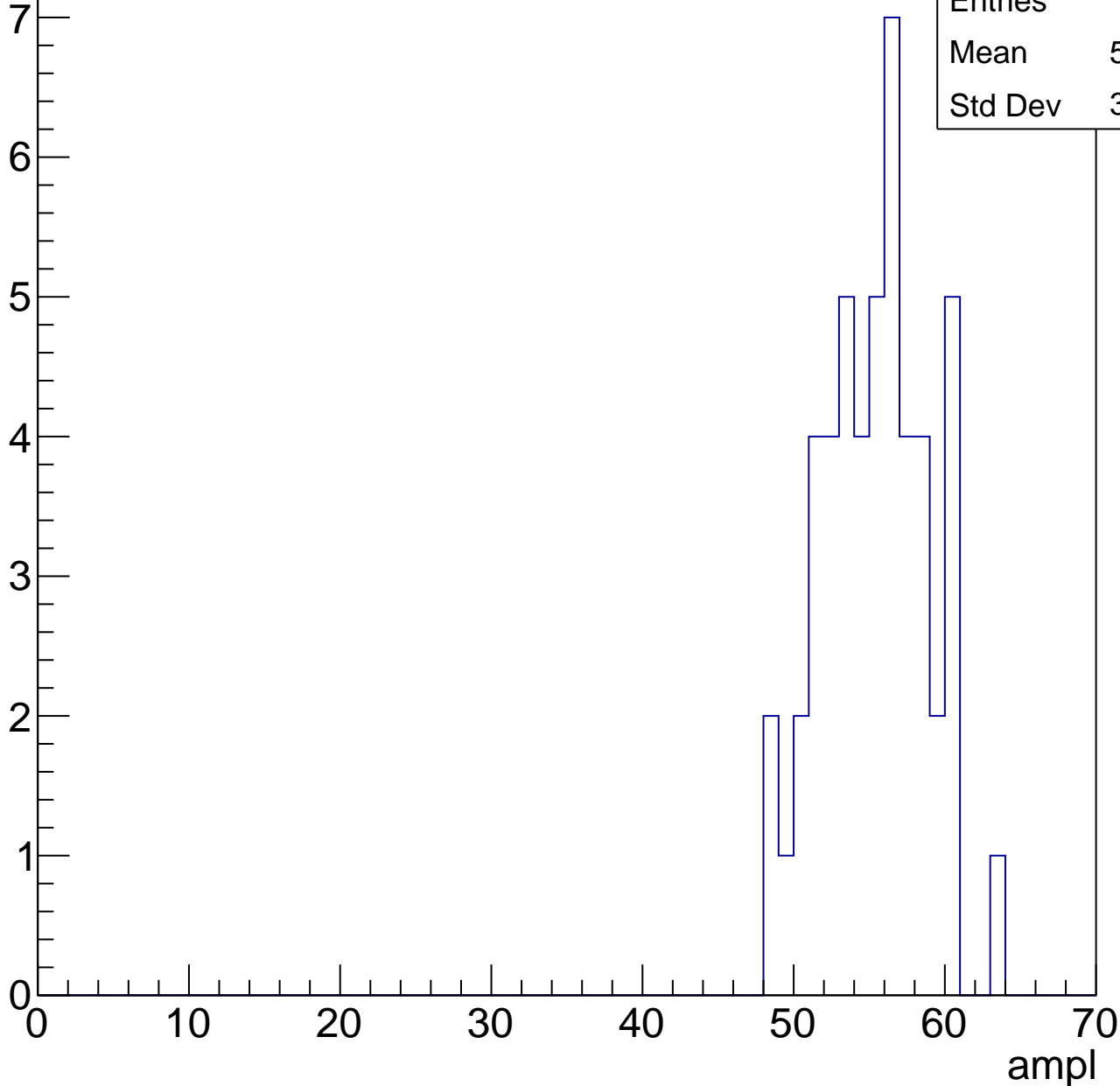


# B1L002S, U18-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	54.92
Std Dev	3.428

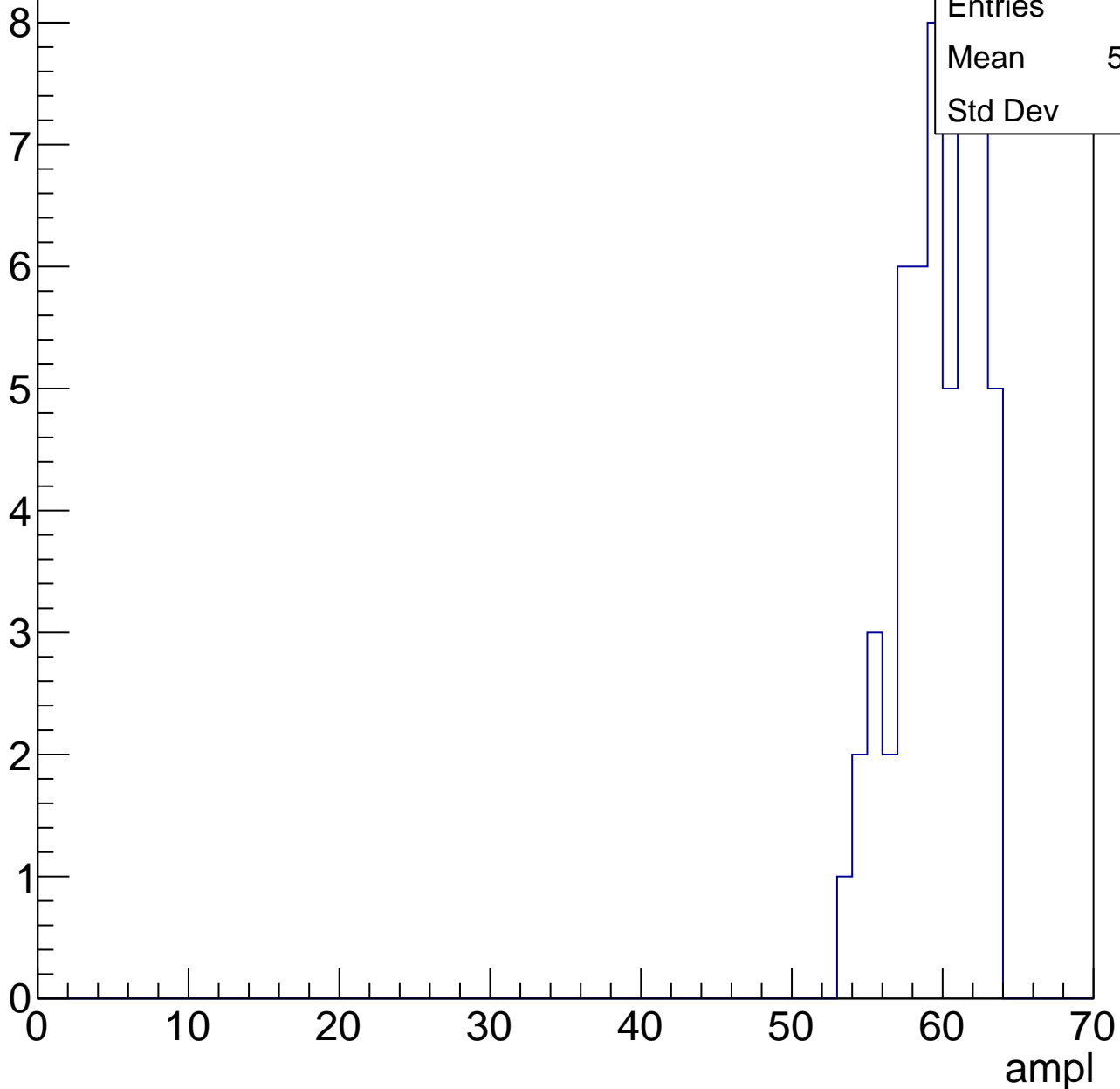


# B1L002S, U18-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

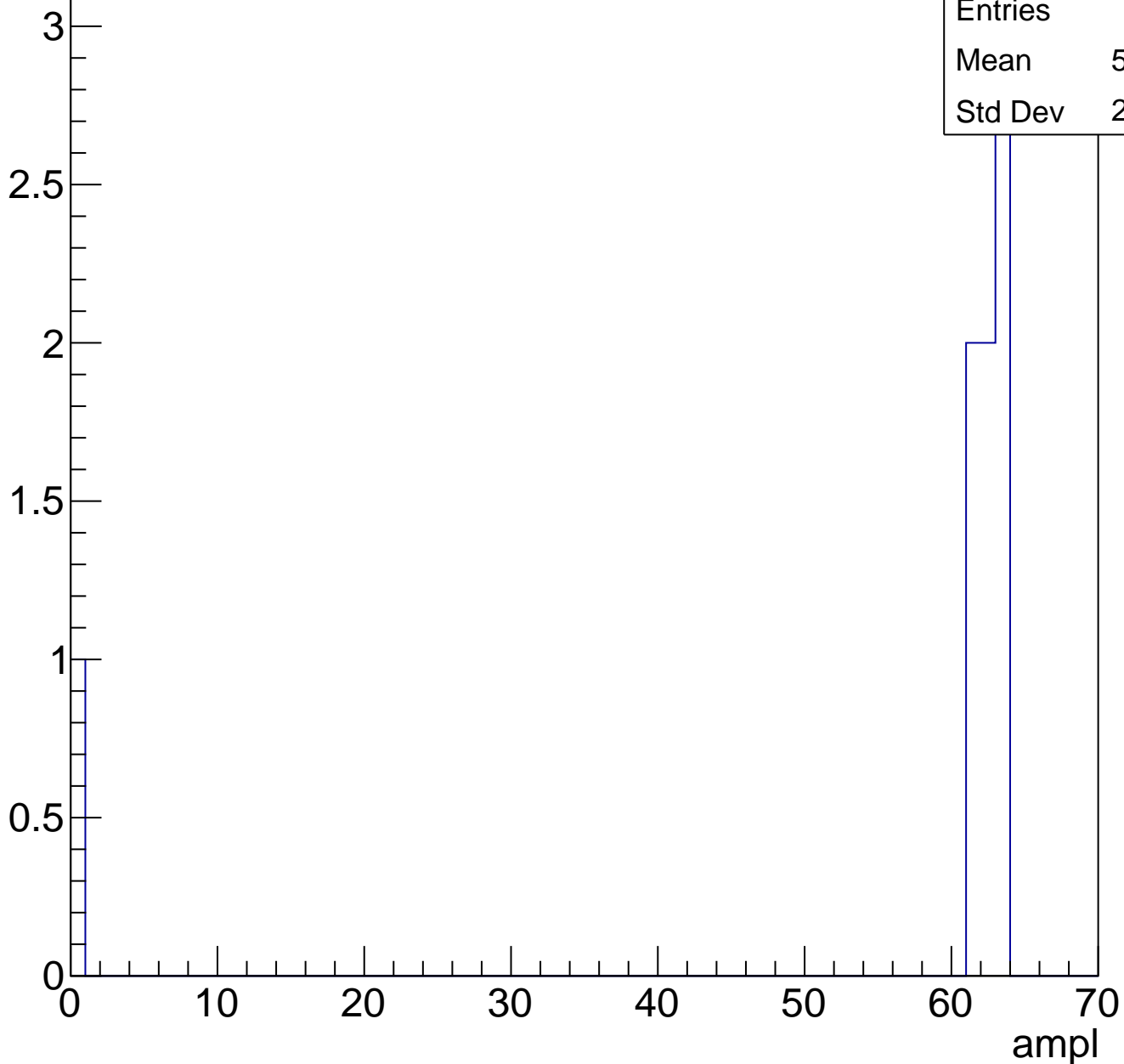
Entries	54
Mean	59.24
Std Dev	2.61



# B1L002S, U18-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

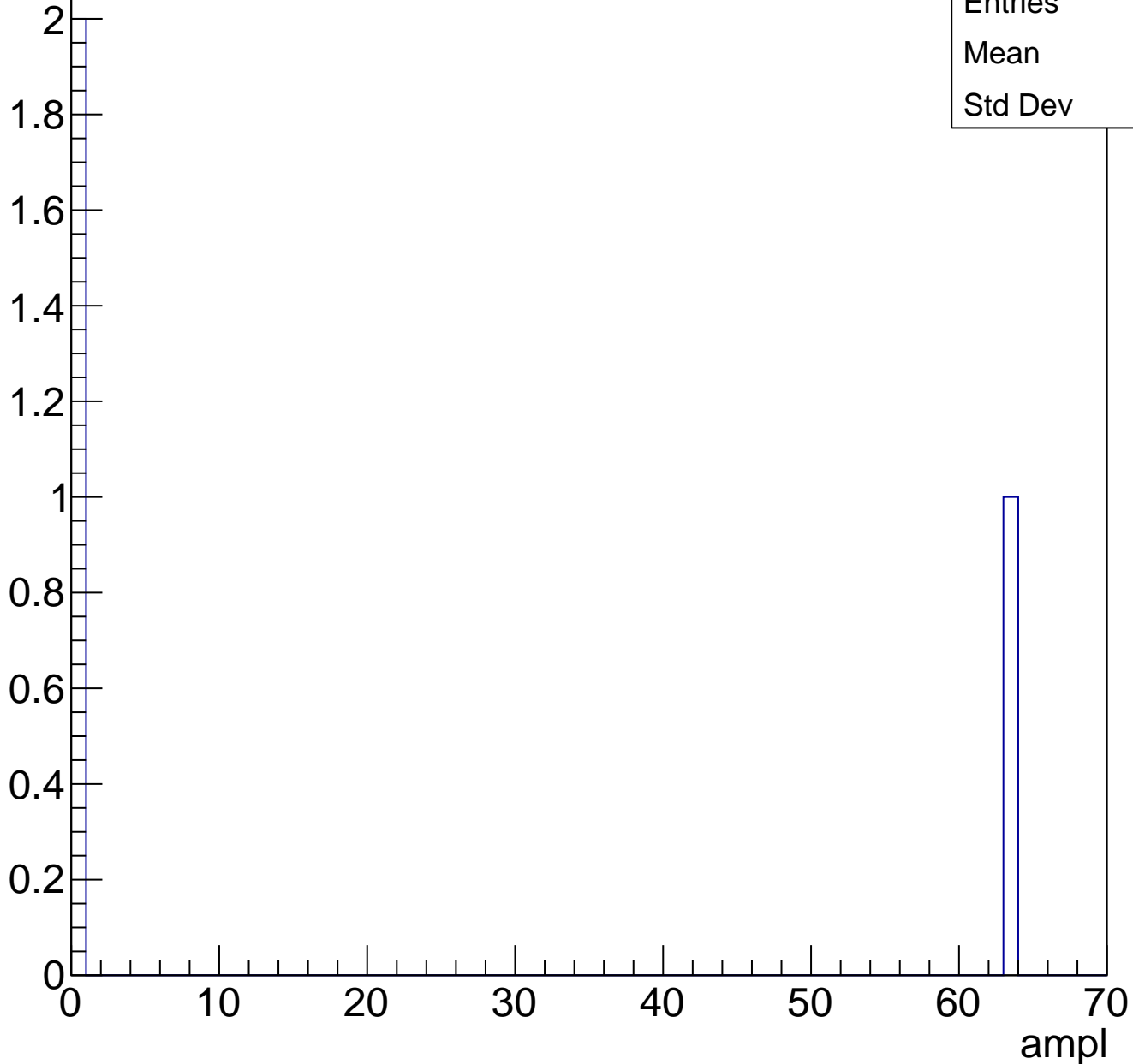




# B1L002S, U18-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch119, adc0

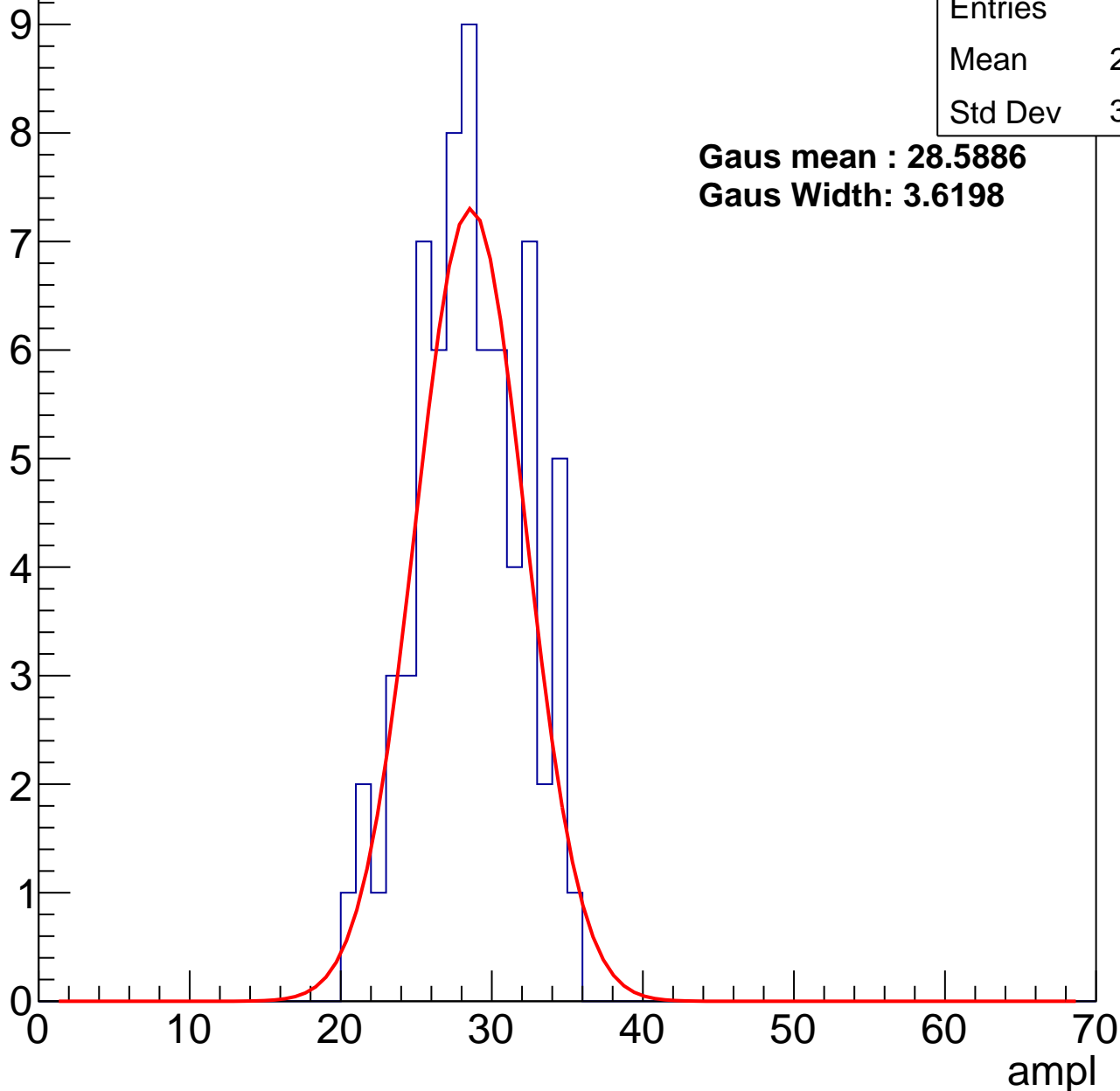
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.13
Std Dev	3.504

**Gaus mean : 28.5886**

**Gaus Width: 3.6198**



# B1L002S, U18-ch119, adc1

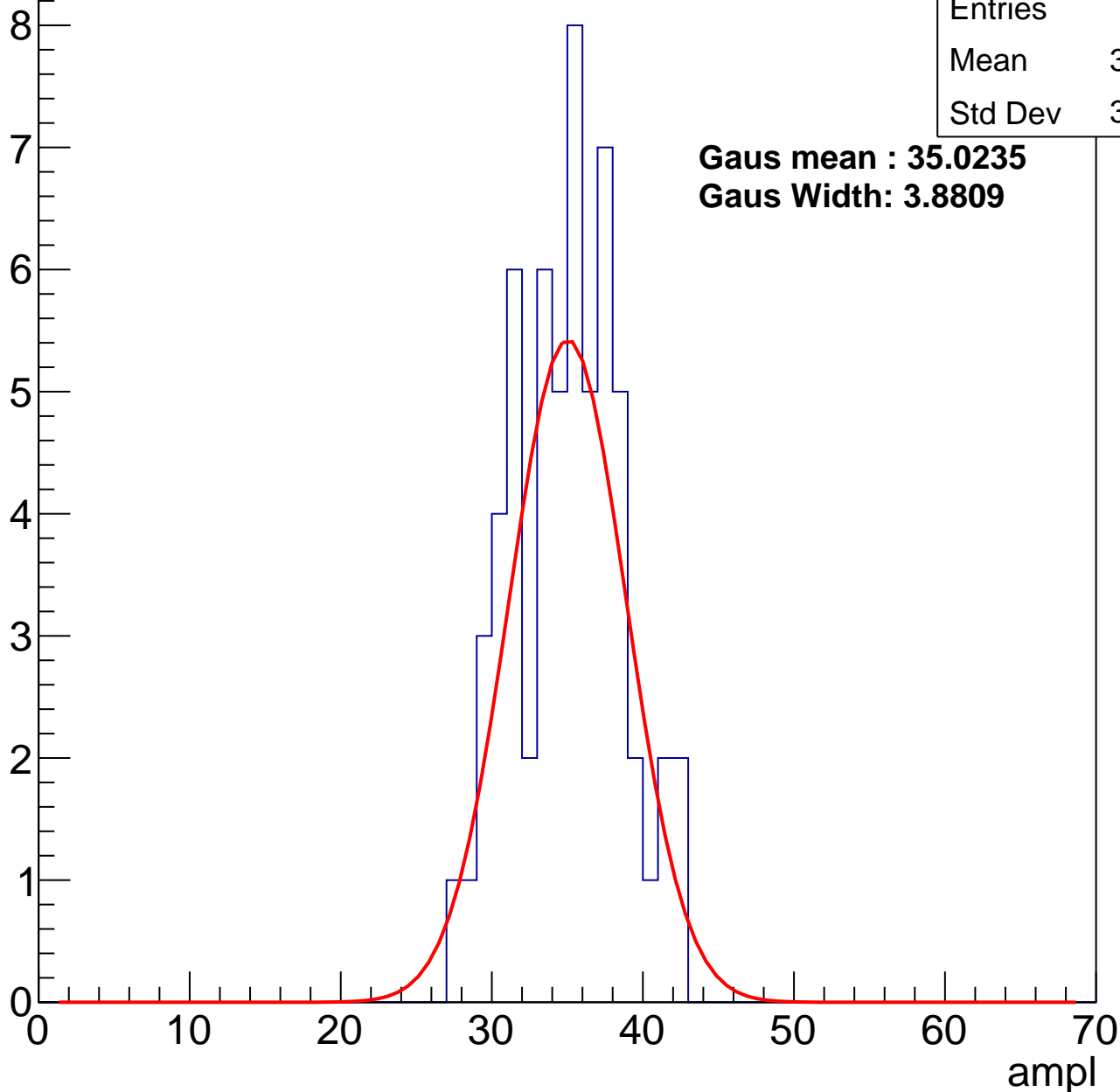
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34.55
Std Dev	3.552

**Gaus mean : 35.0235**

**Gaus Width: 3.8809**



# B1L002S, U18-ch119, adc2

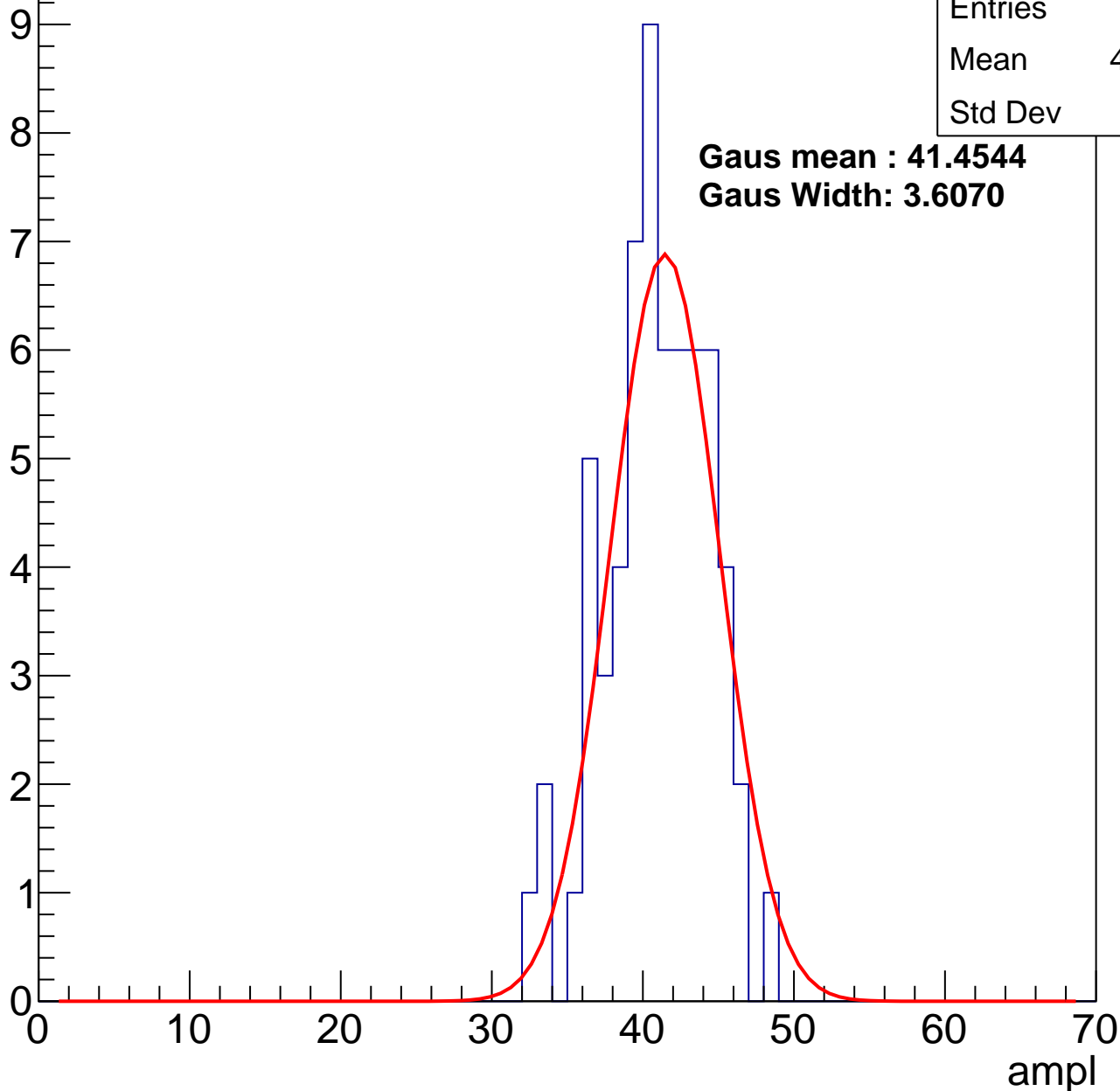
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.46
Std Dev	3.38

**Gaus mean : 41.4544**

**Gaus Width: 3.6070**

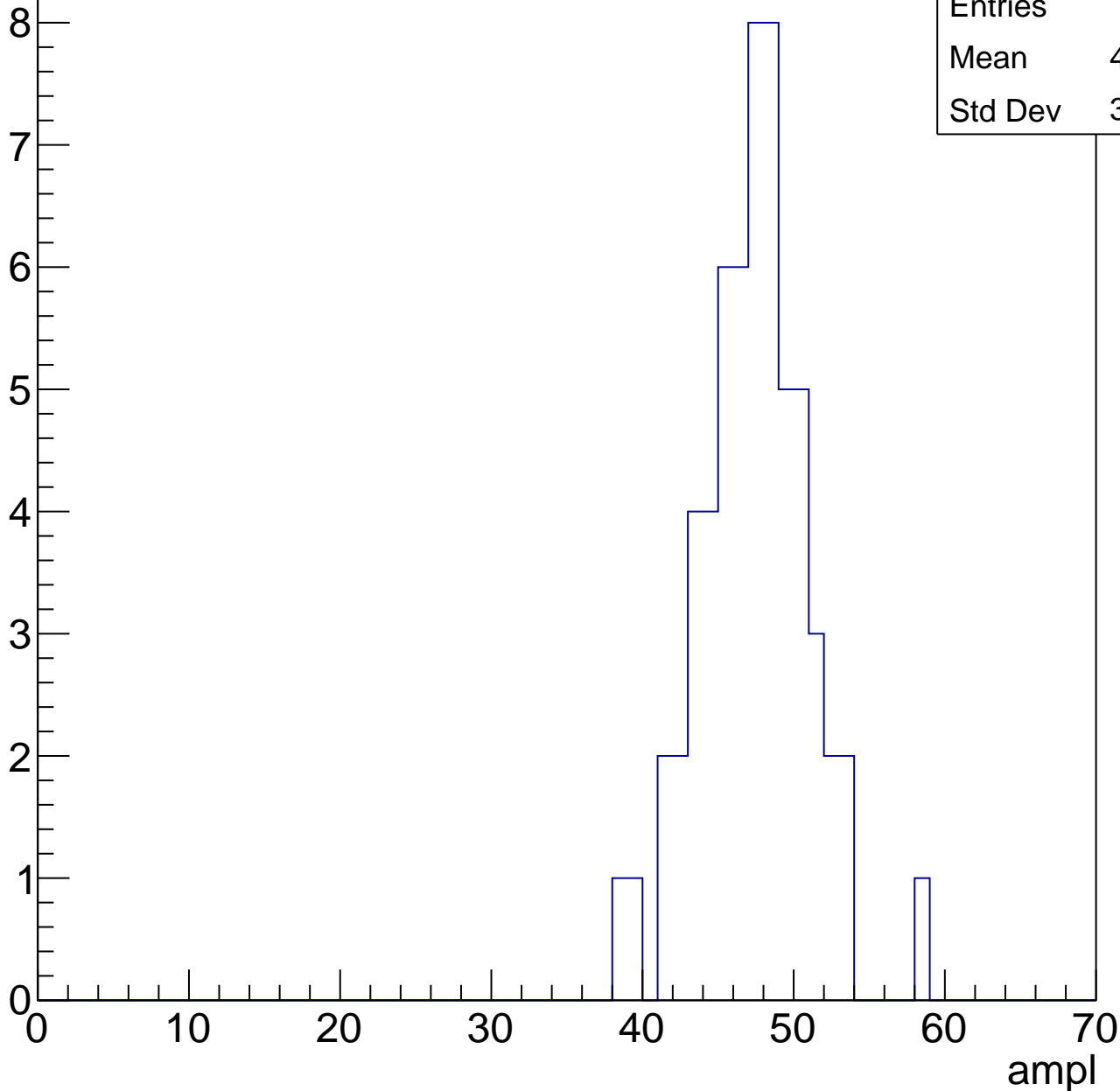


# B1L002S, U18-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

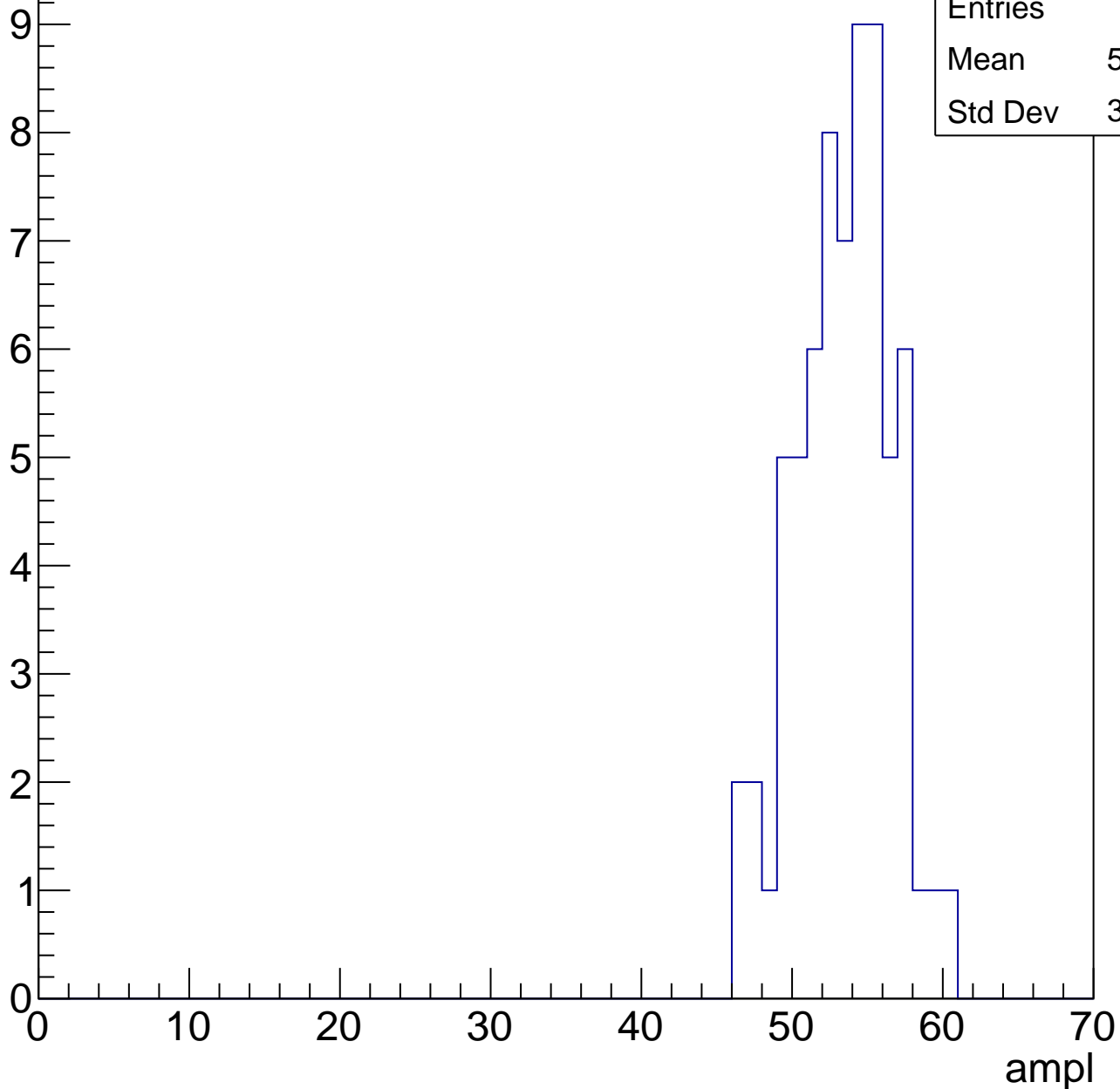
Entries	60
Mean	46.88
Std Dev	3.559



# B1L002S, U18-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

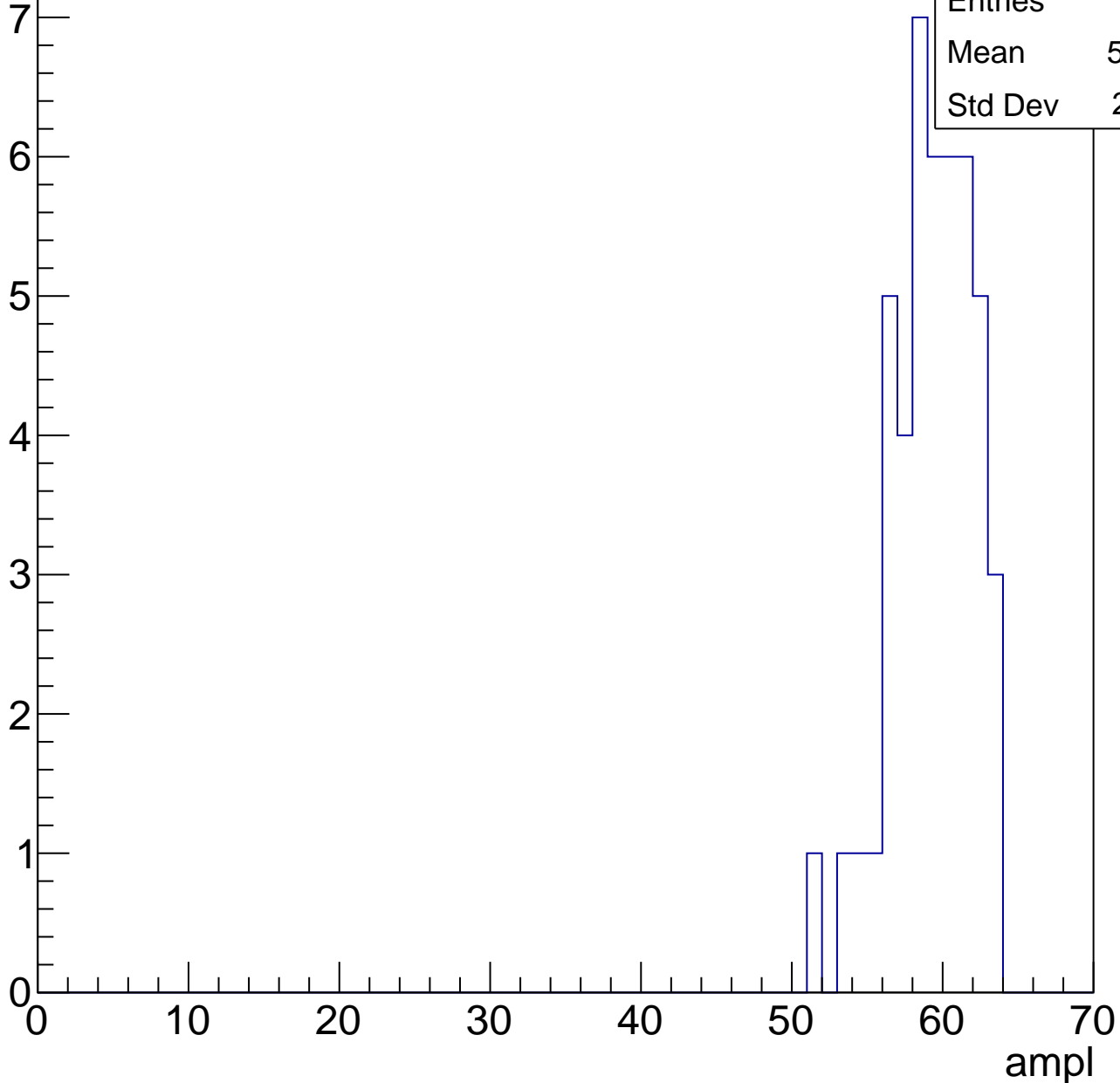


# B1L002S, U18-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	58.83
Std Dev	2.681

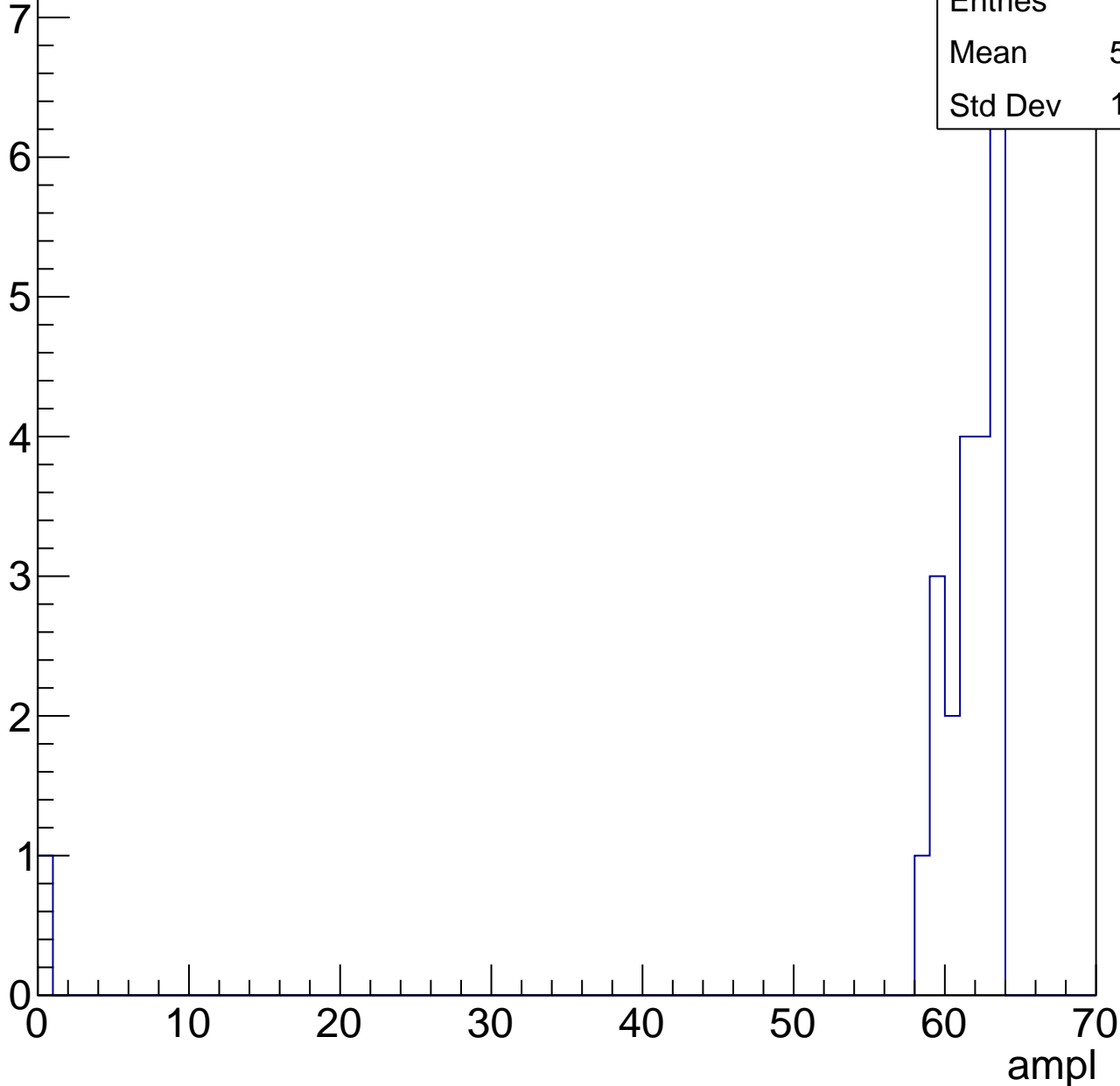


# B1L002S, U18-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.55
Std Dev	12.87

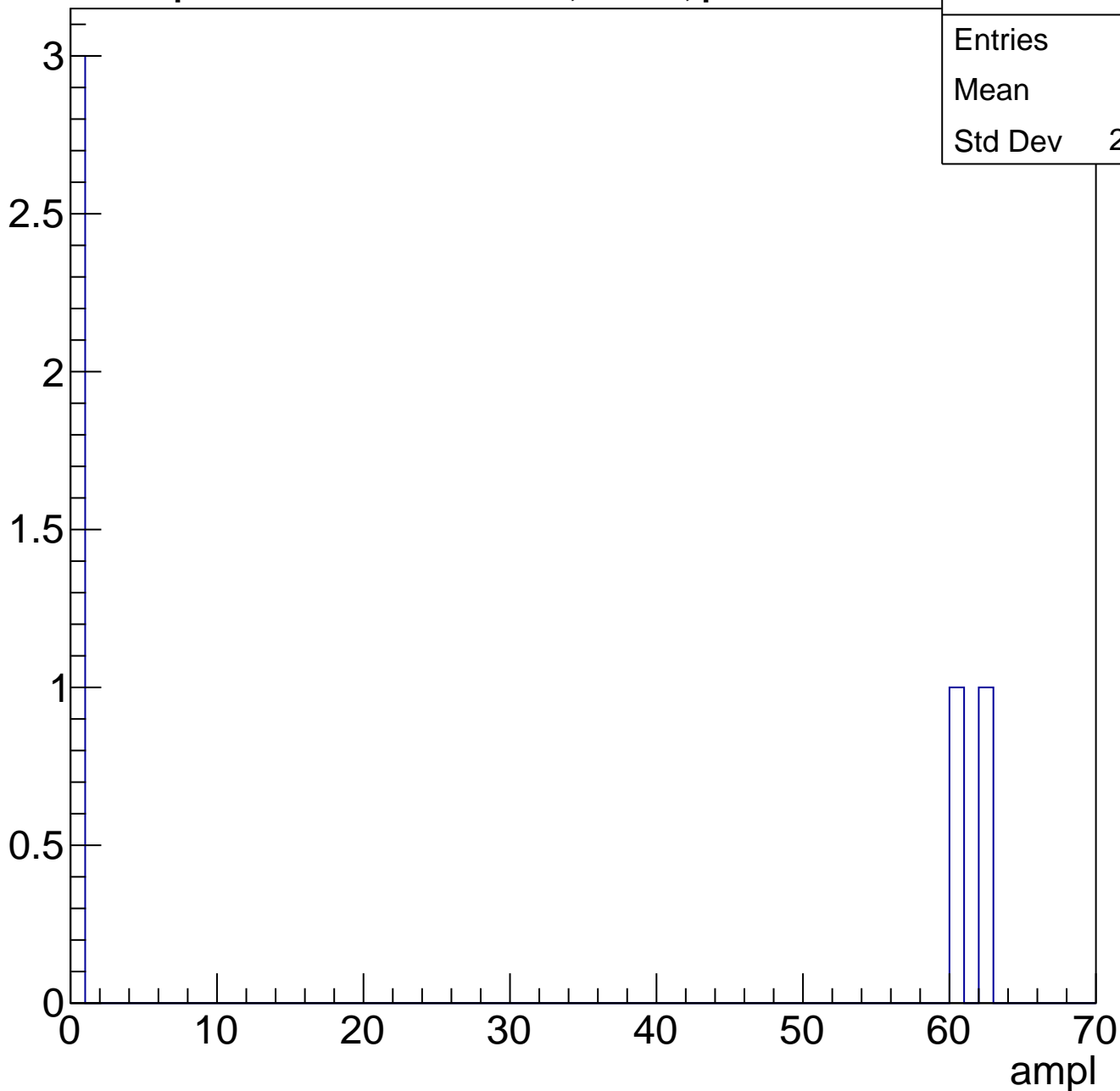




# B1L002S, U18-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch120, adc0

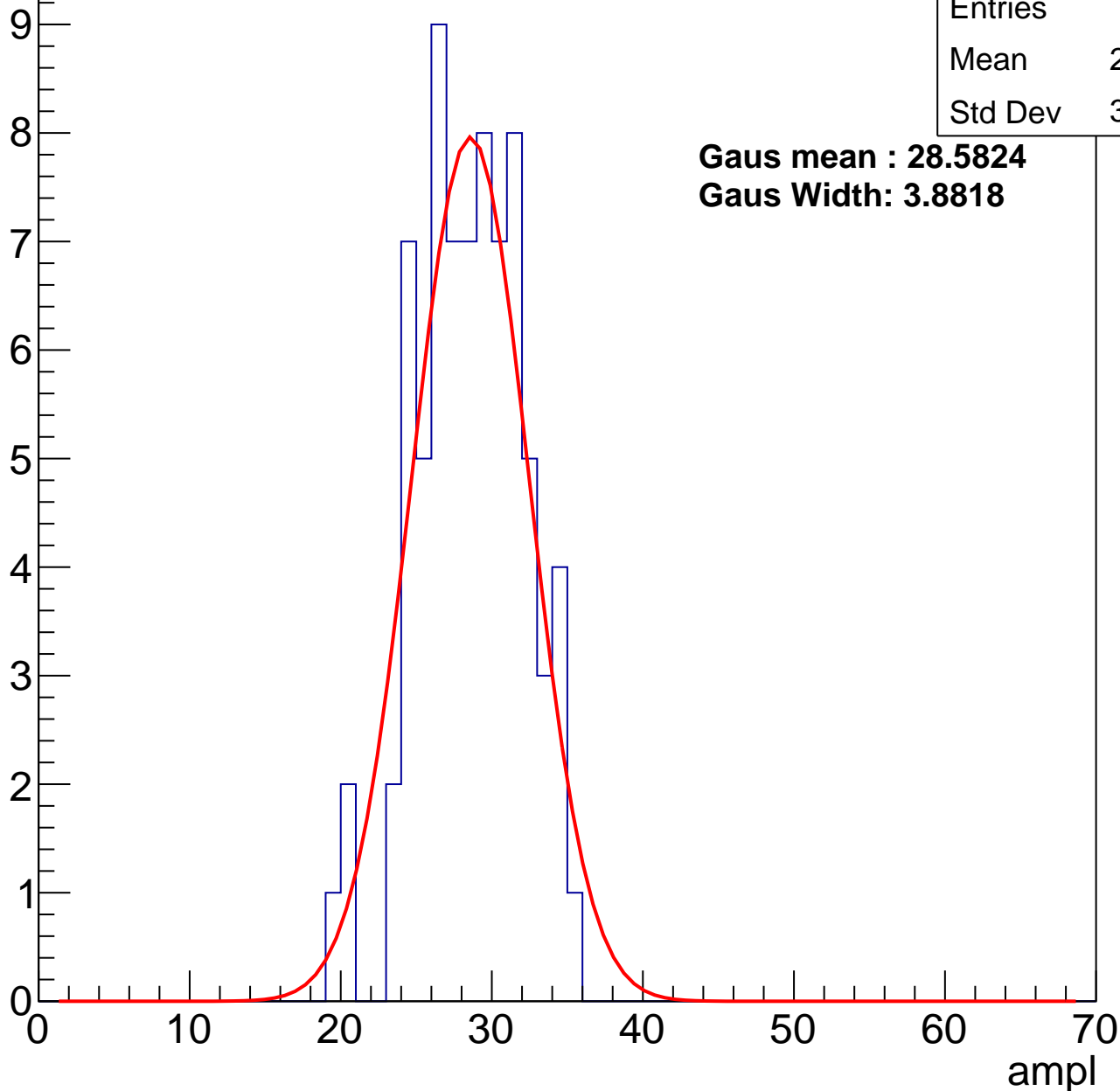
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	28.12
Std Dev	3.468

**Gaus mean : 28.5824**

**Gaus Width: 3.8818**



# B1L002S, U18-ch120, adc1

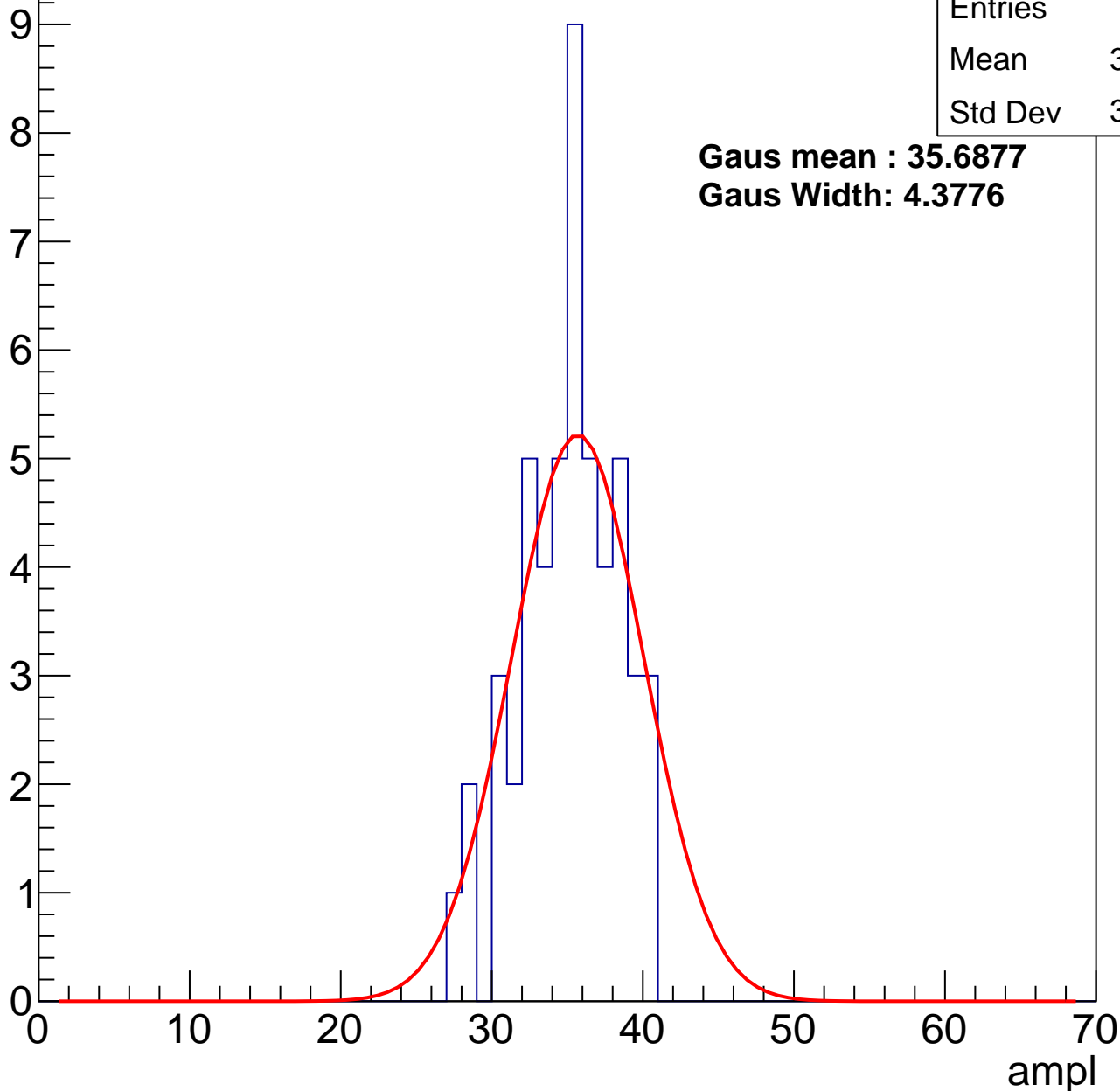
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	34.65
Std Dev	3.186

**Gaus mean : 35.6877**

**Gaus Width: 4.3776**



# B1L002S, U18-ch120, adc2

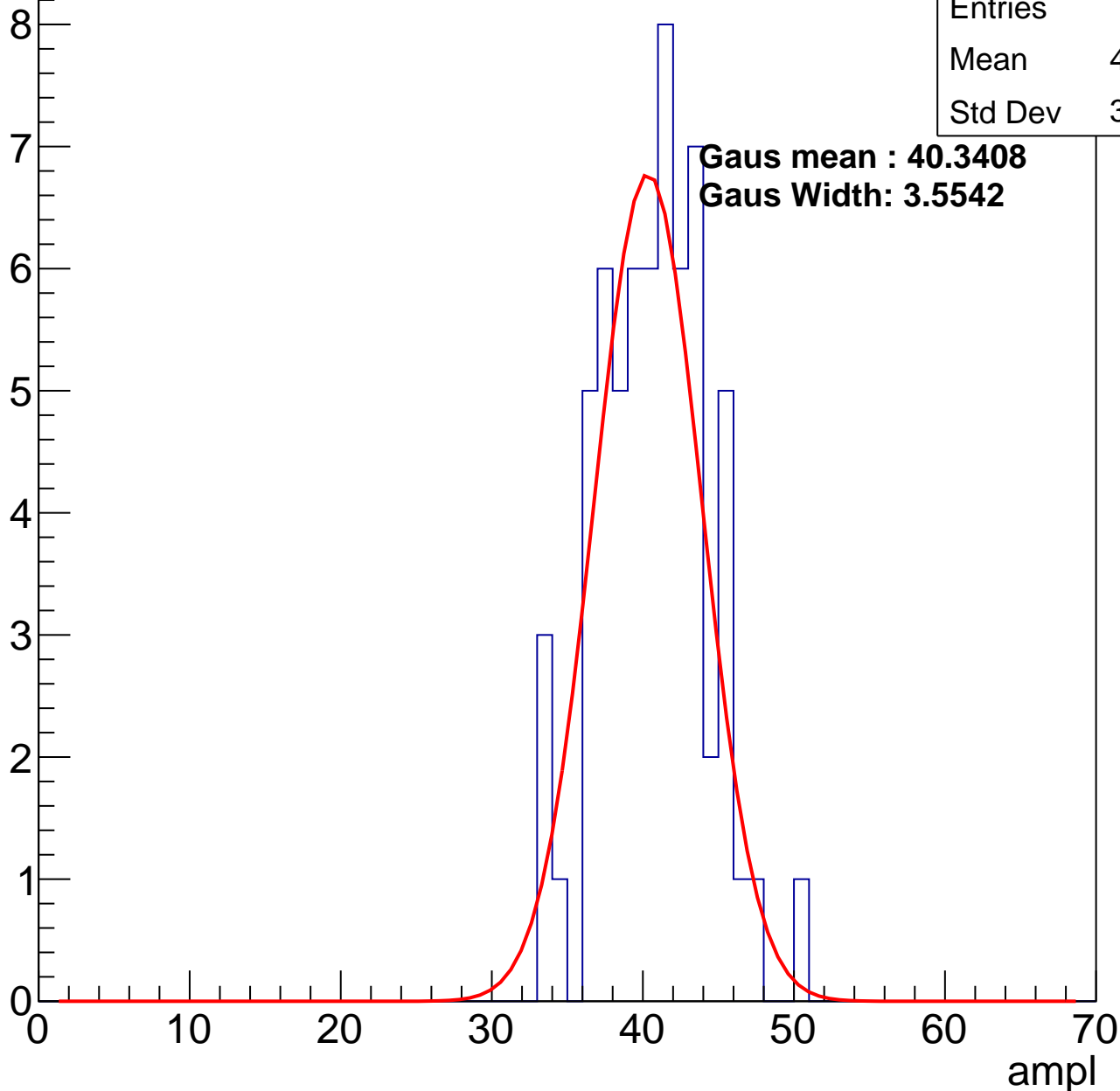
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.25
Std Dev	3.509

**Gaus mean : 40.3408**

**Gaus Width: 3.5542**

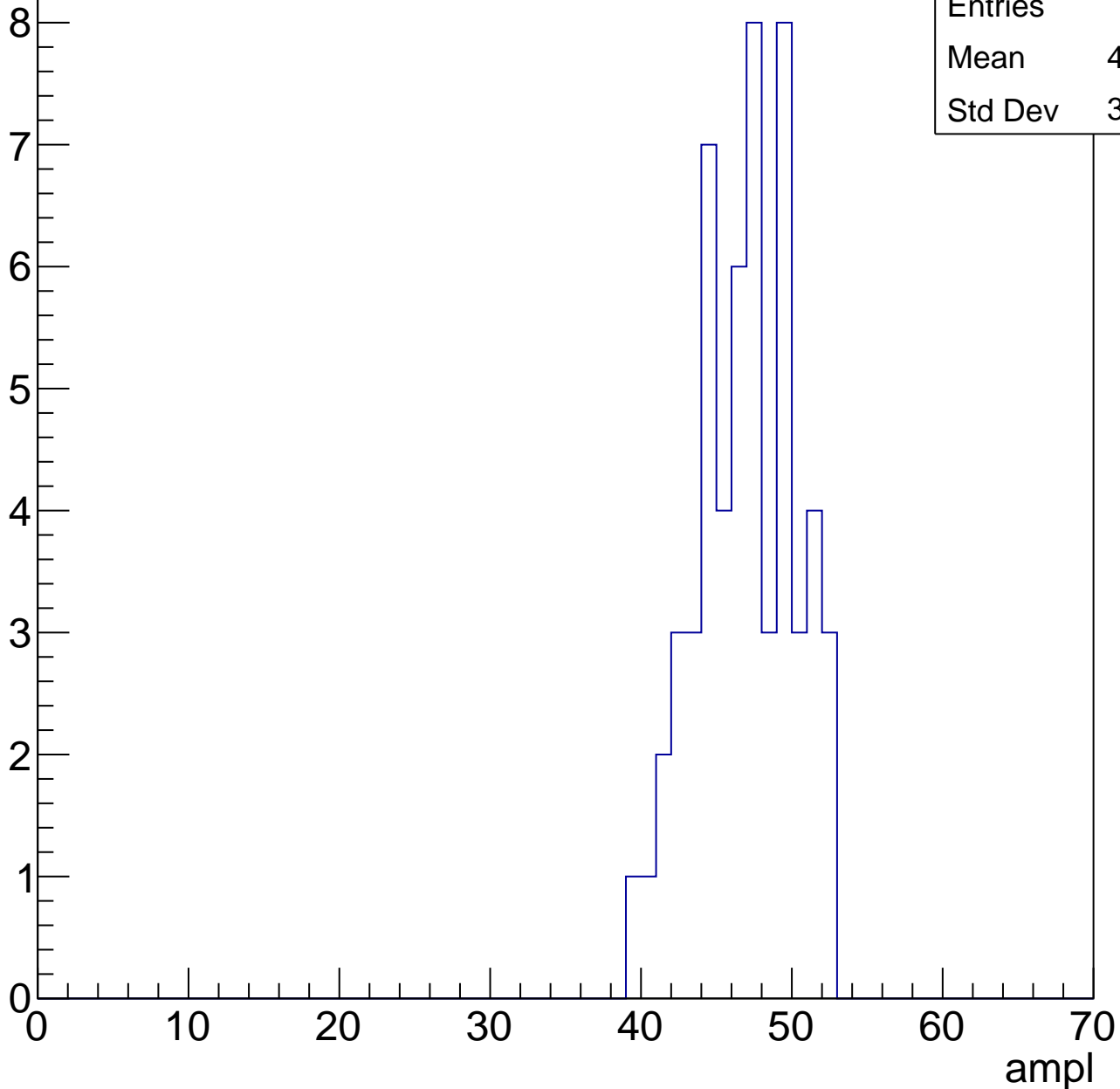


# B1L002S, U18-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	46.46
Std Dev	3.212

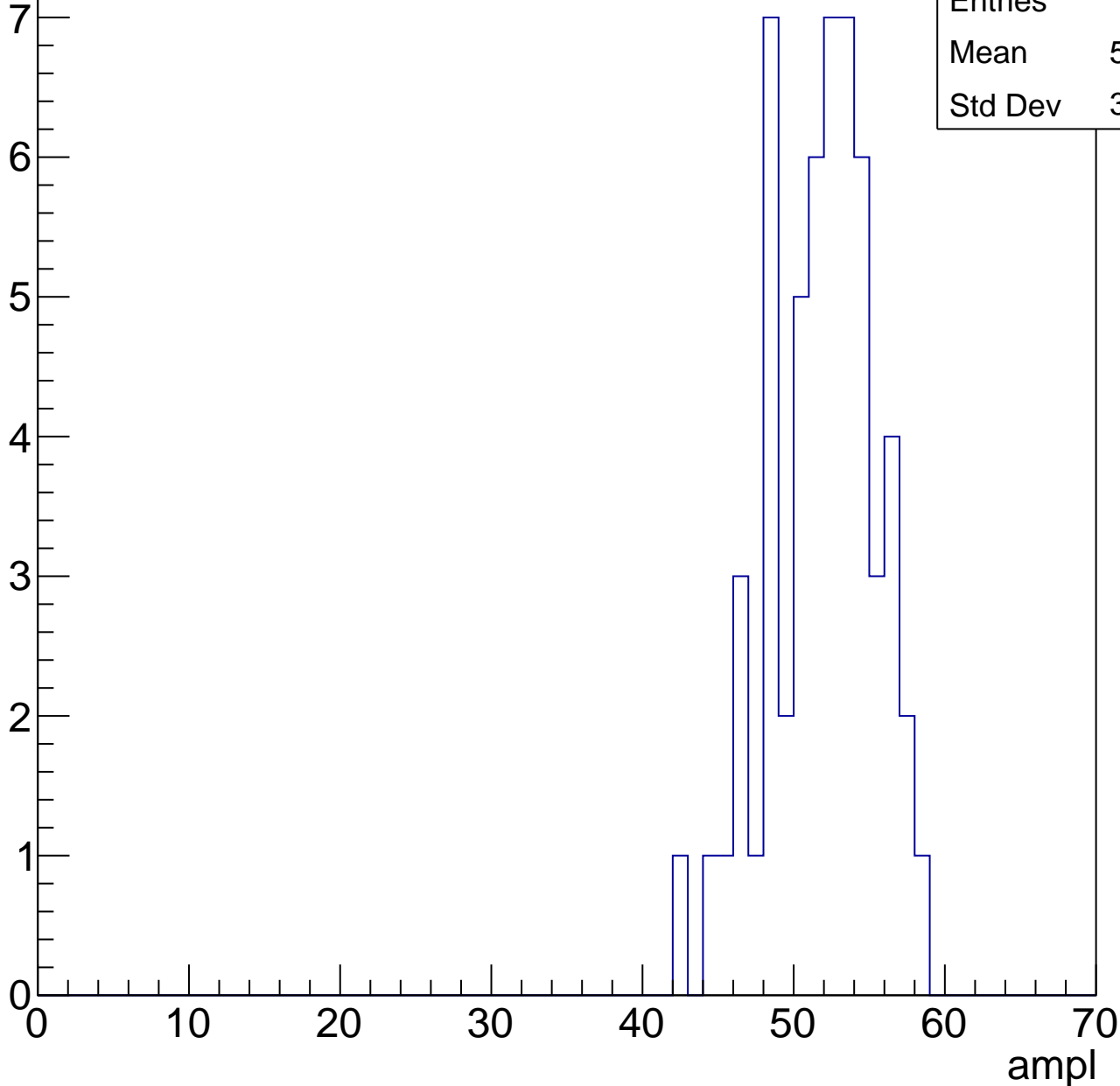


# B1L002S, U18-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	51.33
Std Dev	3.466

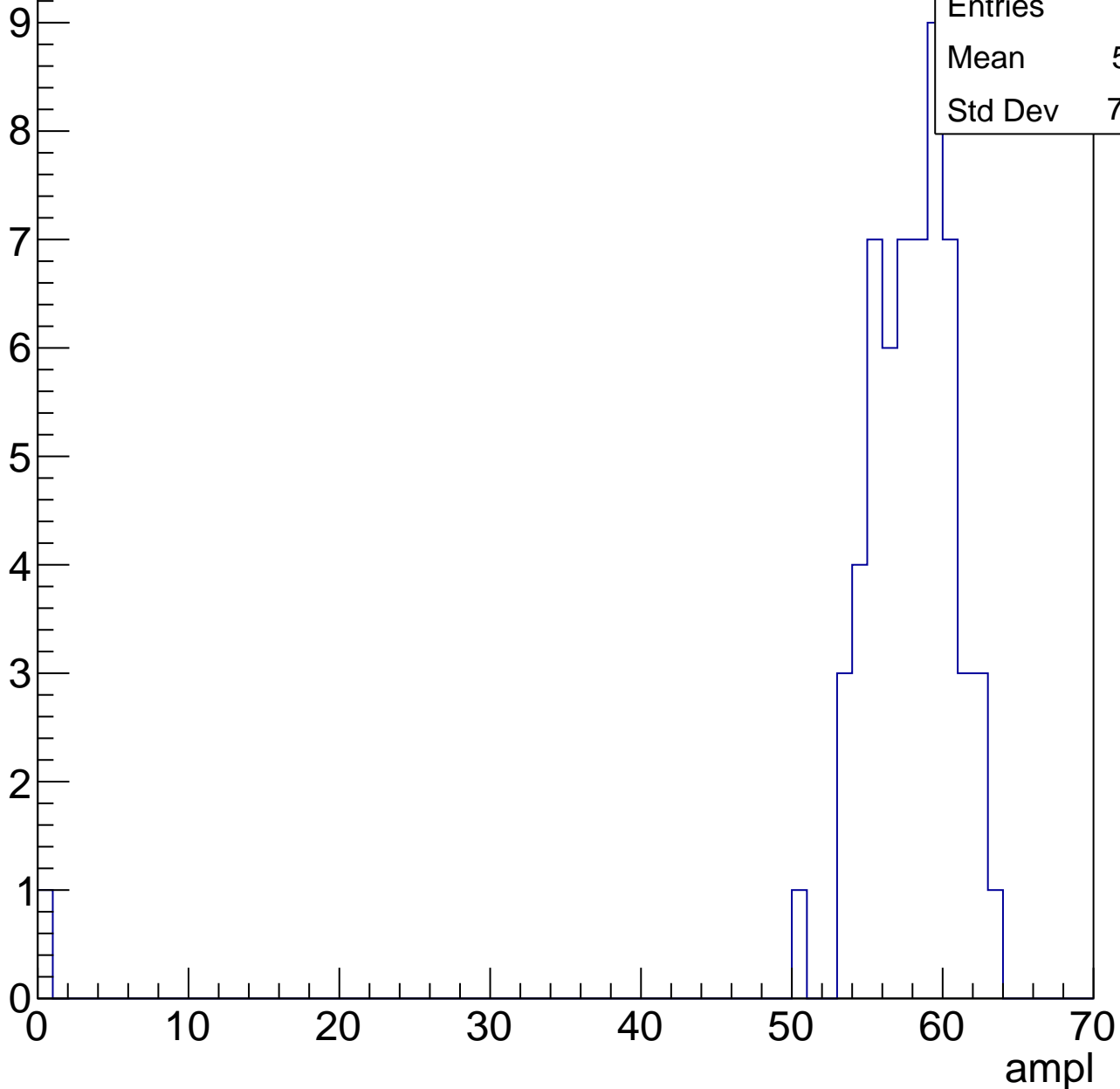


# B1L002S, U18-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	56.51
Std Dev	7.883

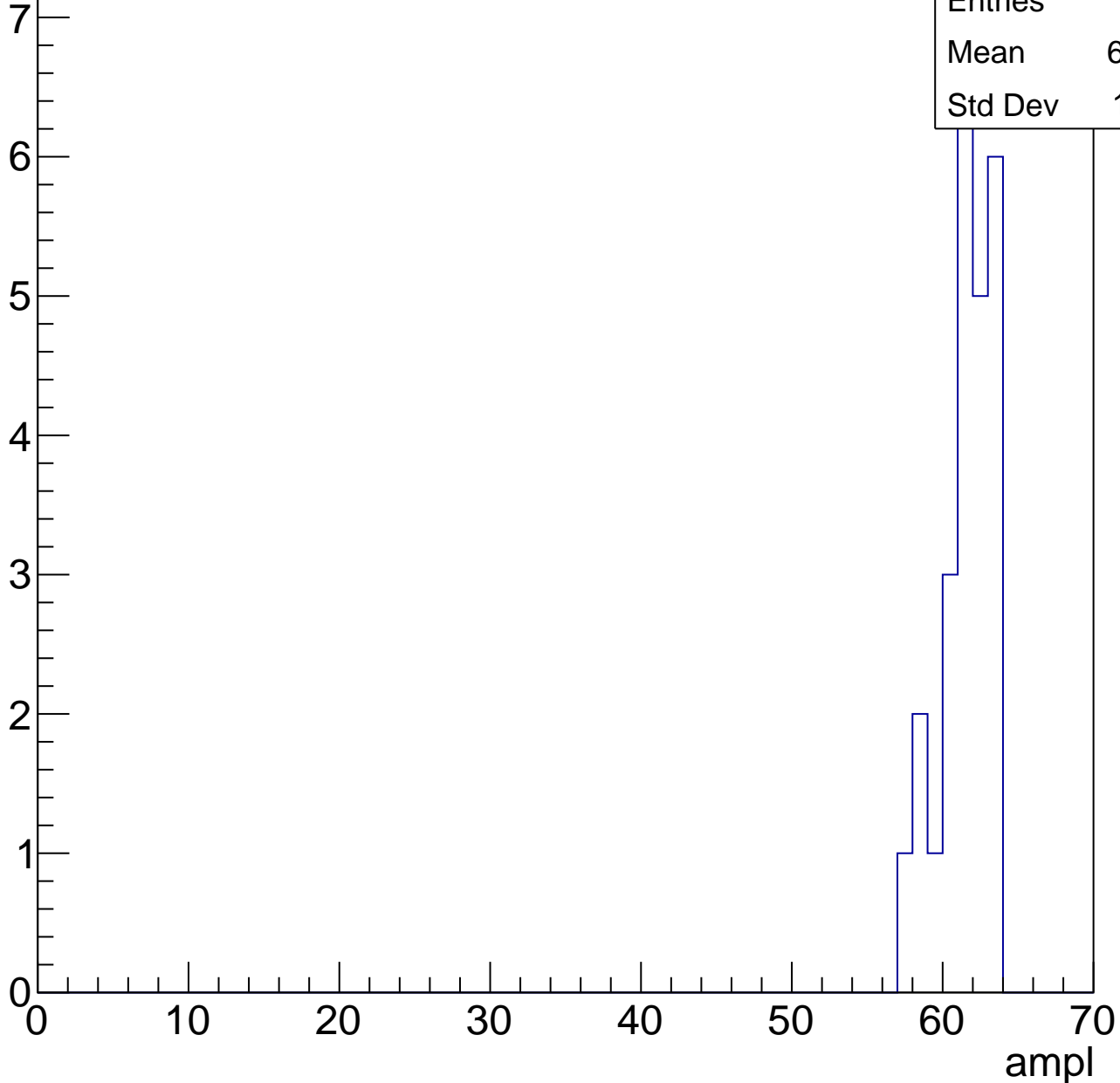


# B1L002S, U18-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.08
Std Dev	1.671

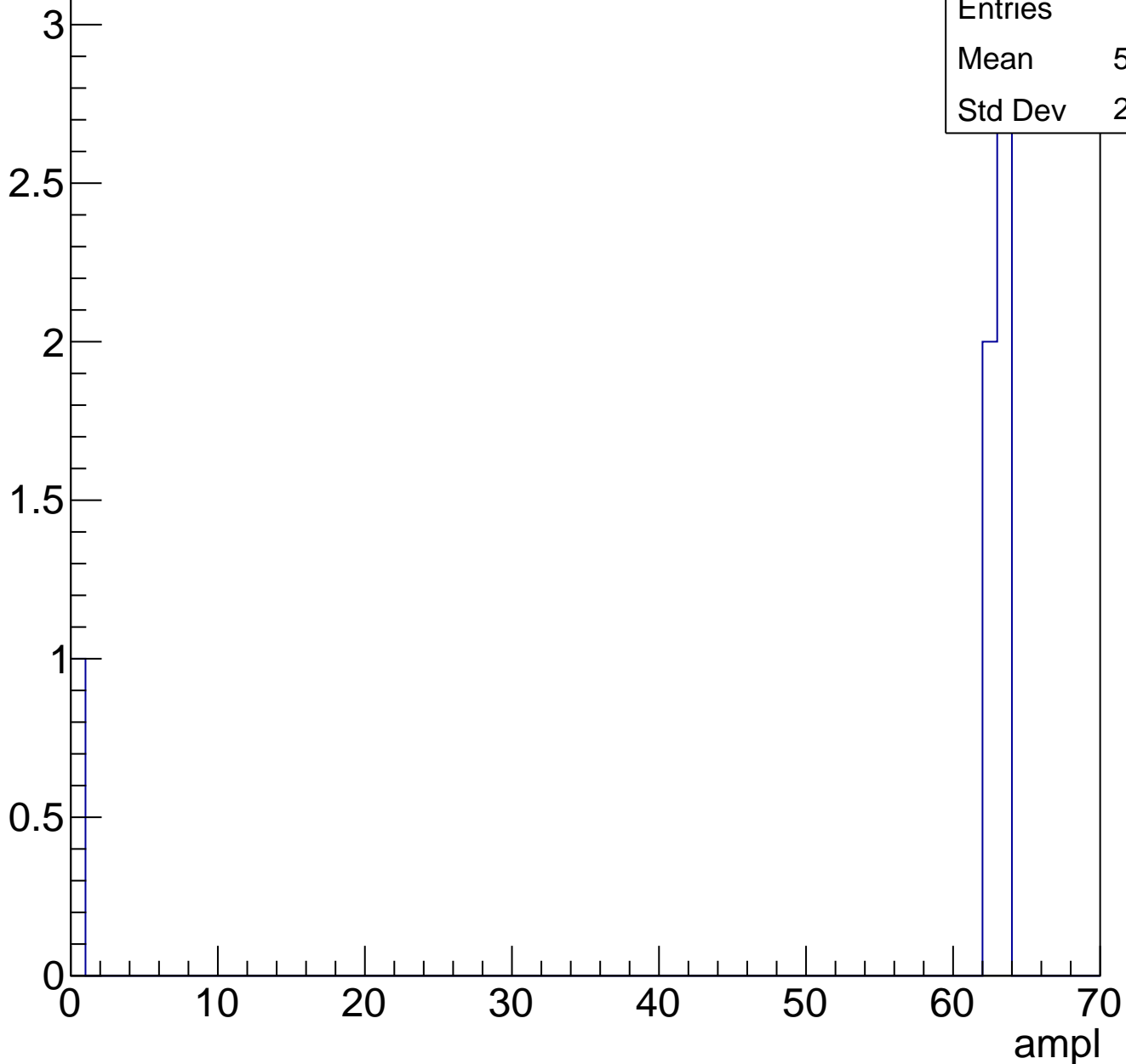




# B1L002S, U18-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch121, adc0

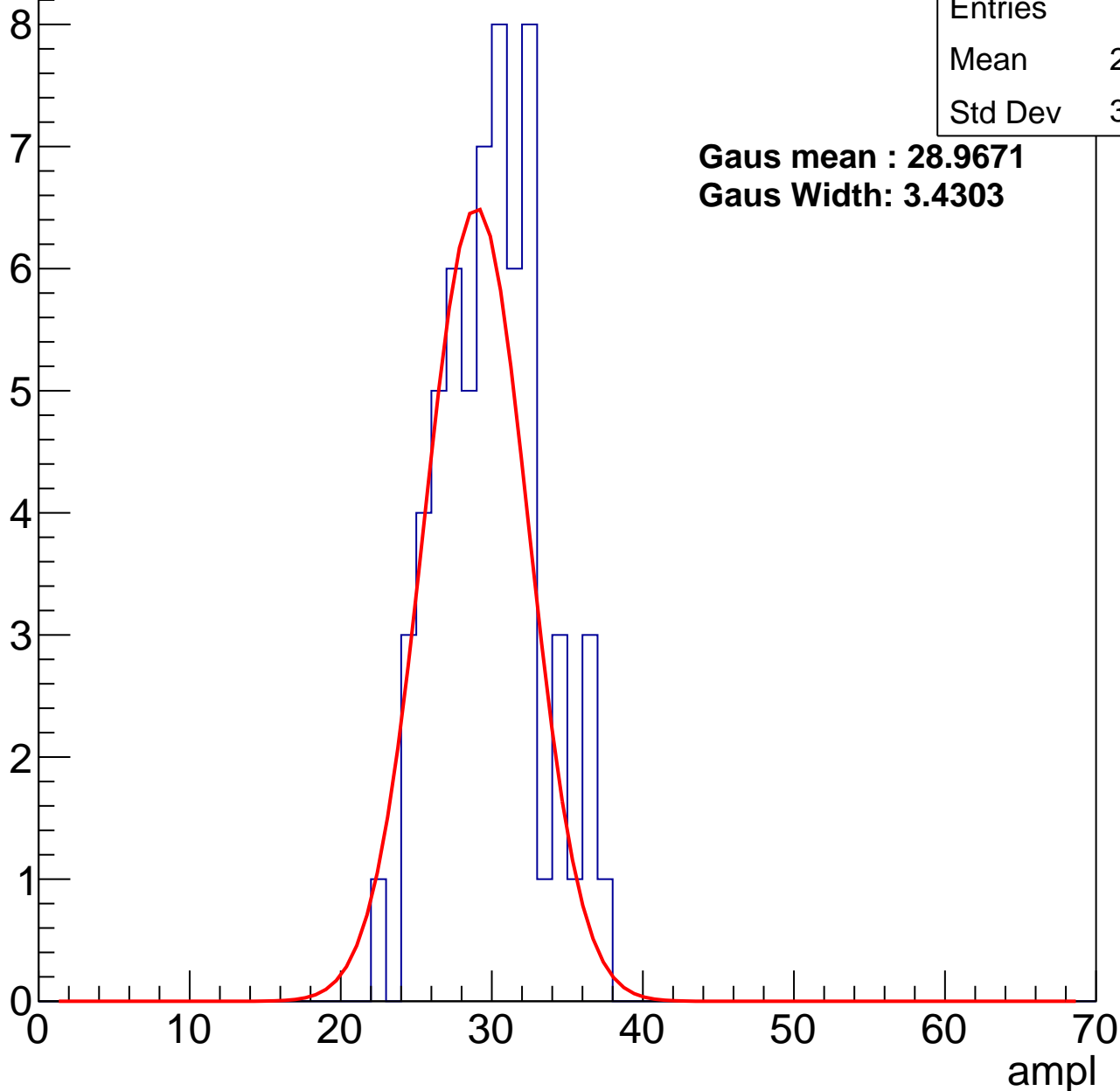
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.45
Std Dev	3.339

**Gaus mean : 28.9671**

**Gaus Width: 3.4303**



# B1L002S, U18-ch121, adc1

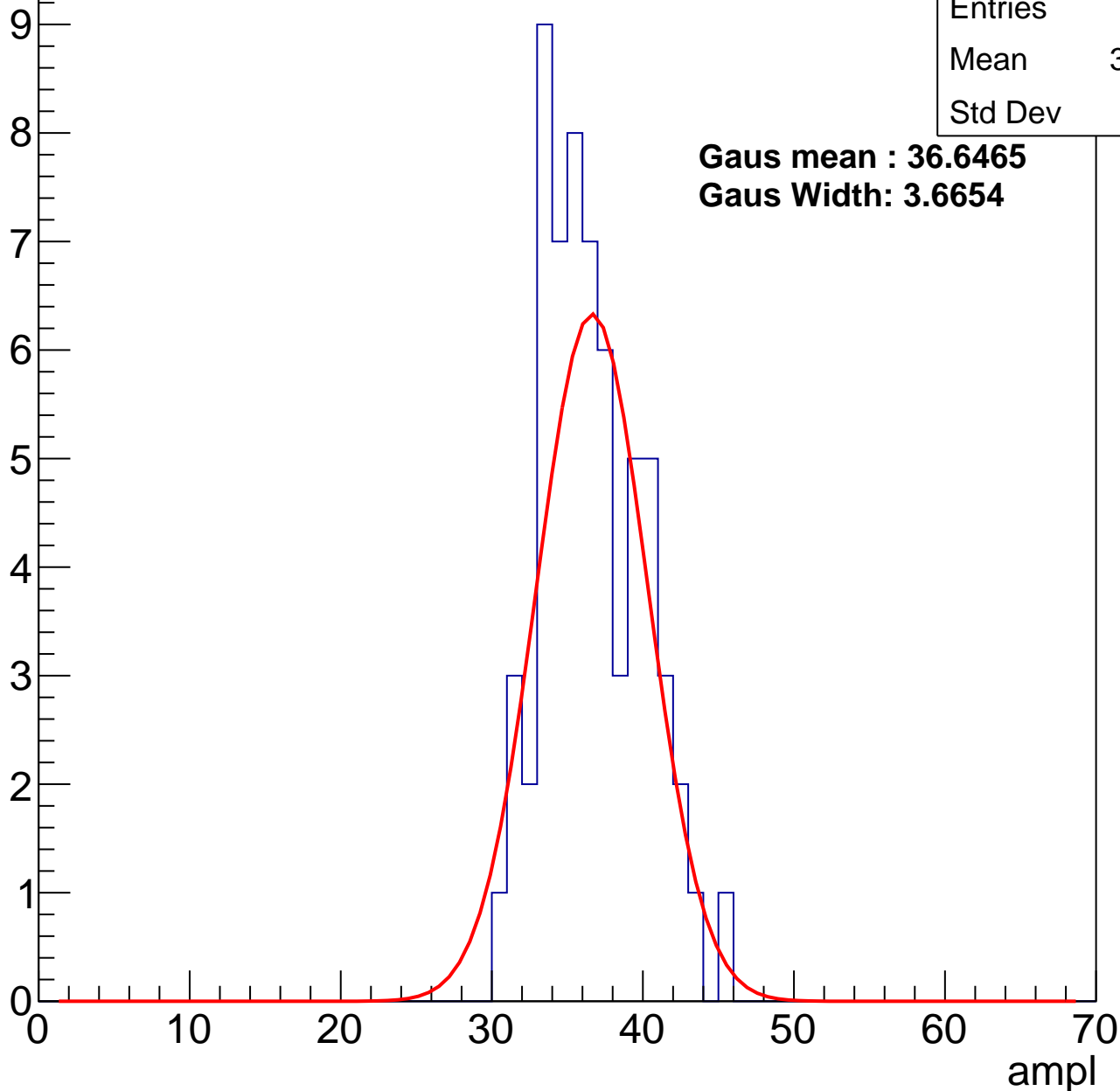
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.19
Std Dev	3.28

**Gaus mean : 36.6465**

**Gaus Width: 3.6654**

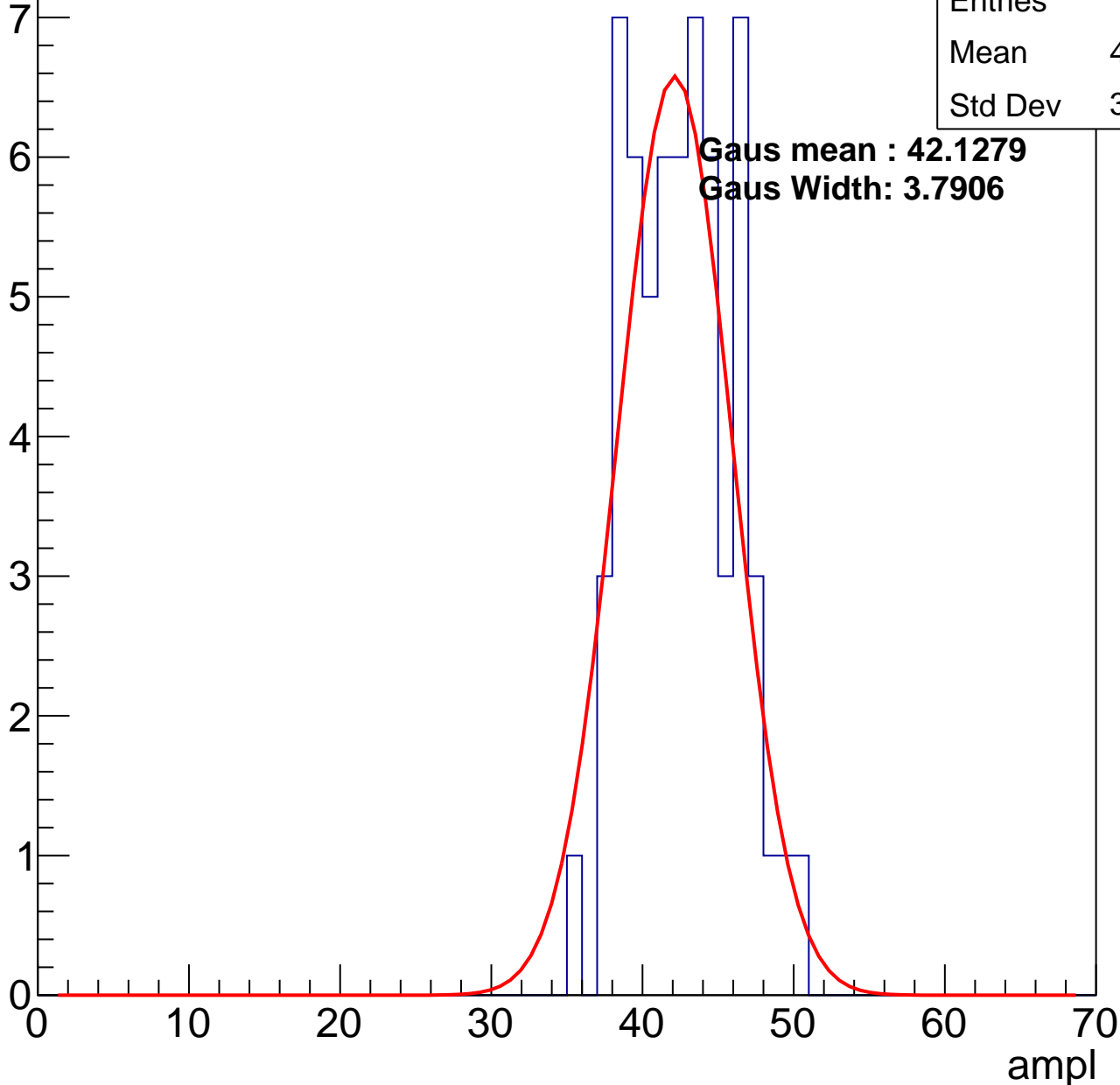


# B1L002S, U18-ch121, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	42.13
Std Dev	3.355

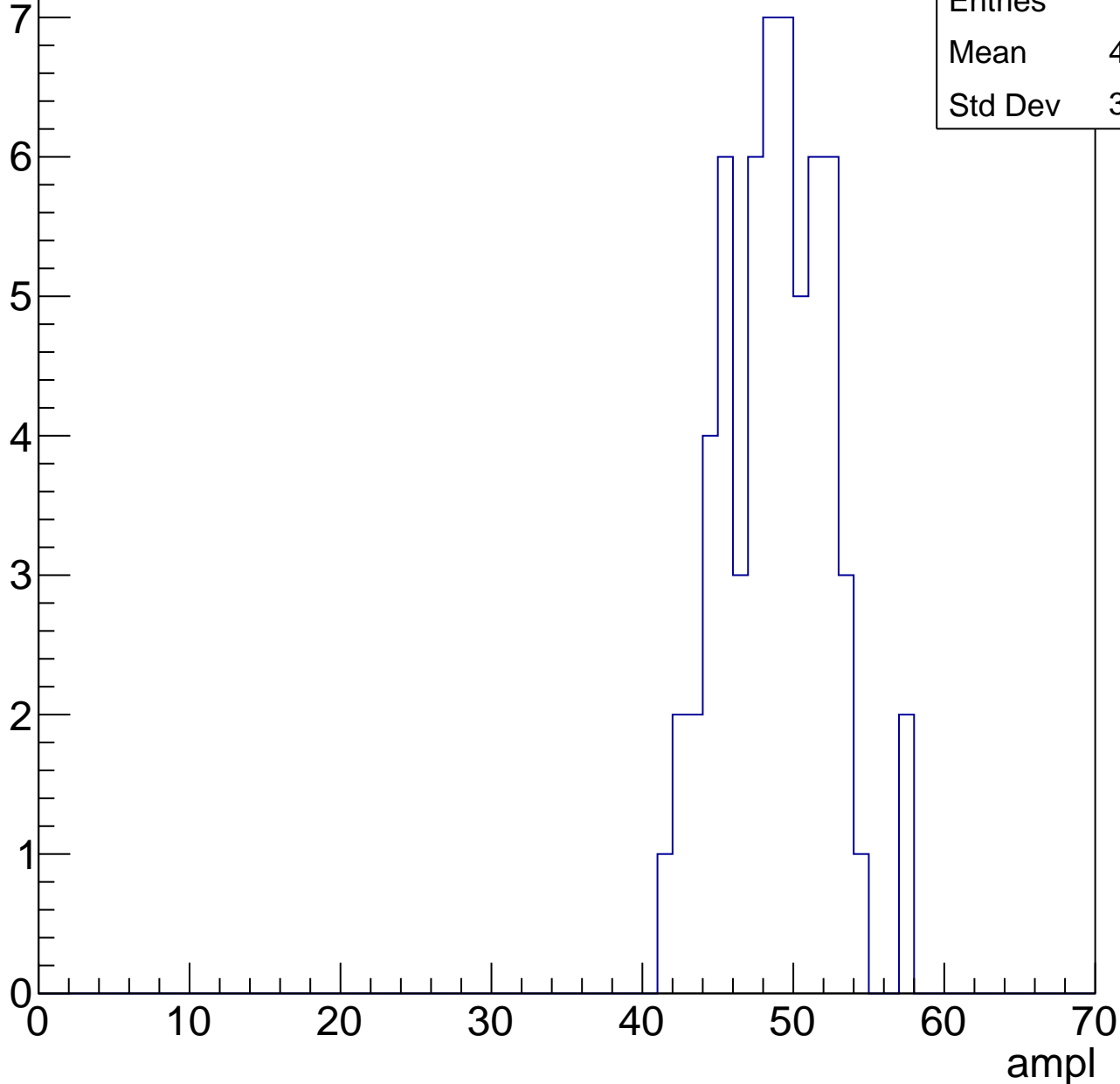


# B1L002S, U18-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	48.38
Std Dev	3.503

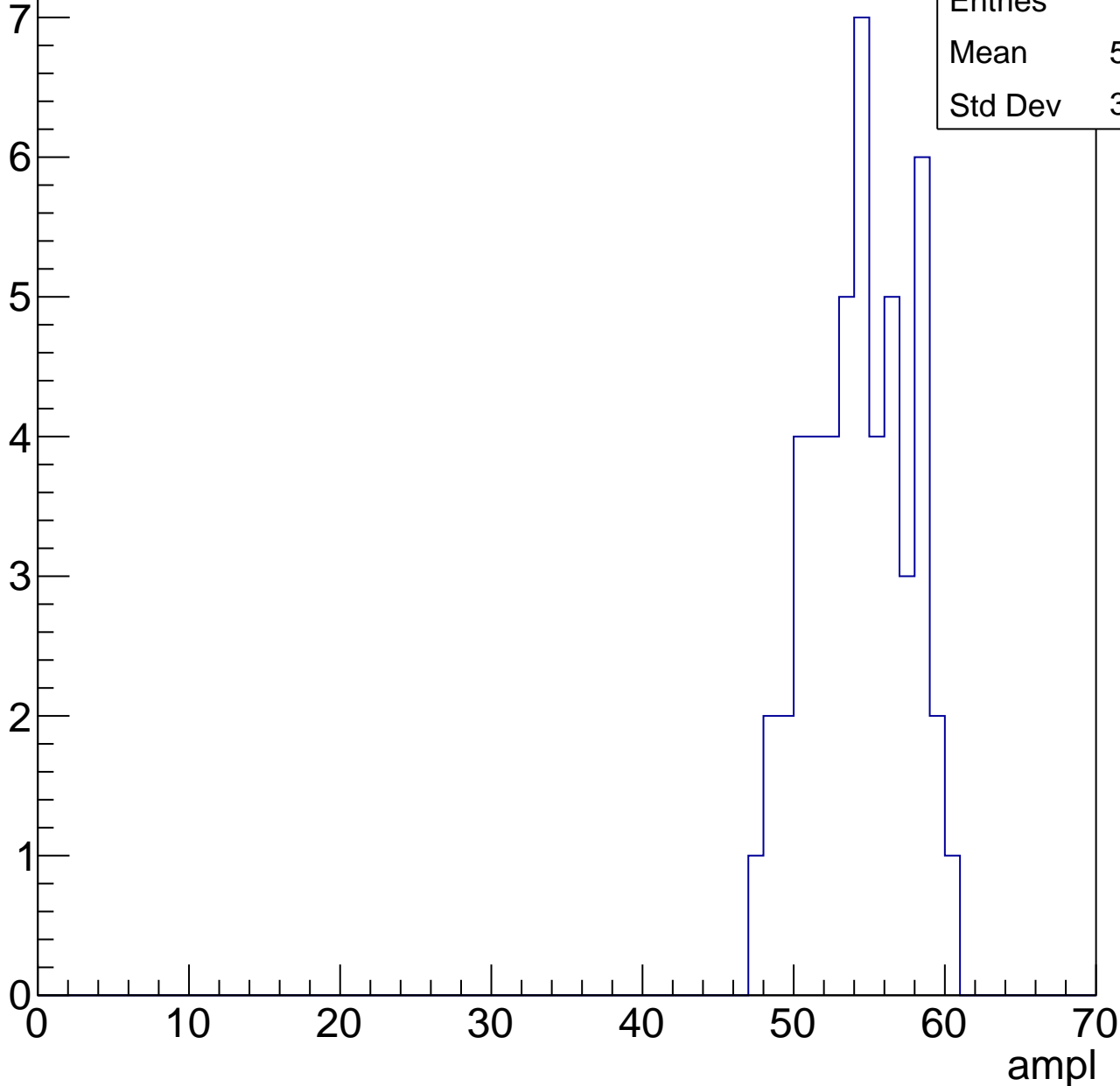


# B1L002S, U18-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	53.86
Std Dev	3.237

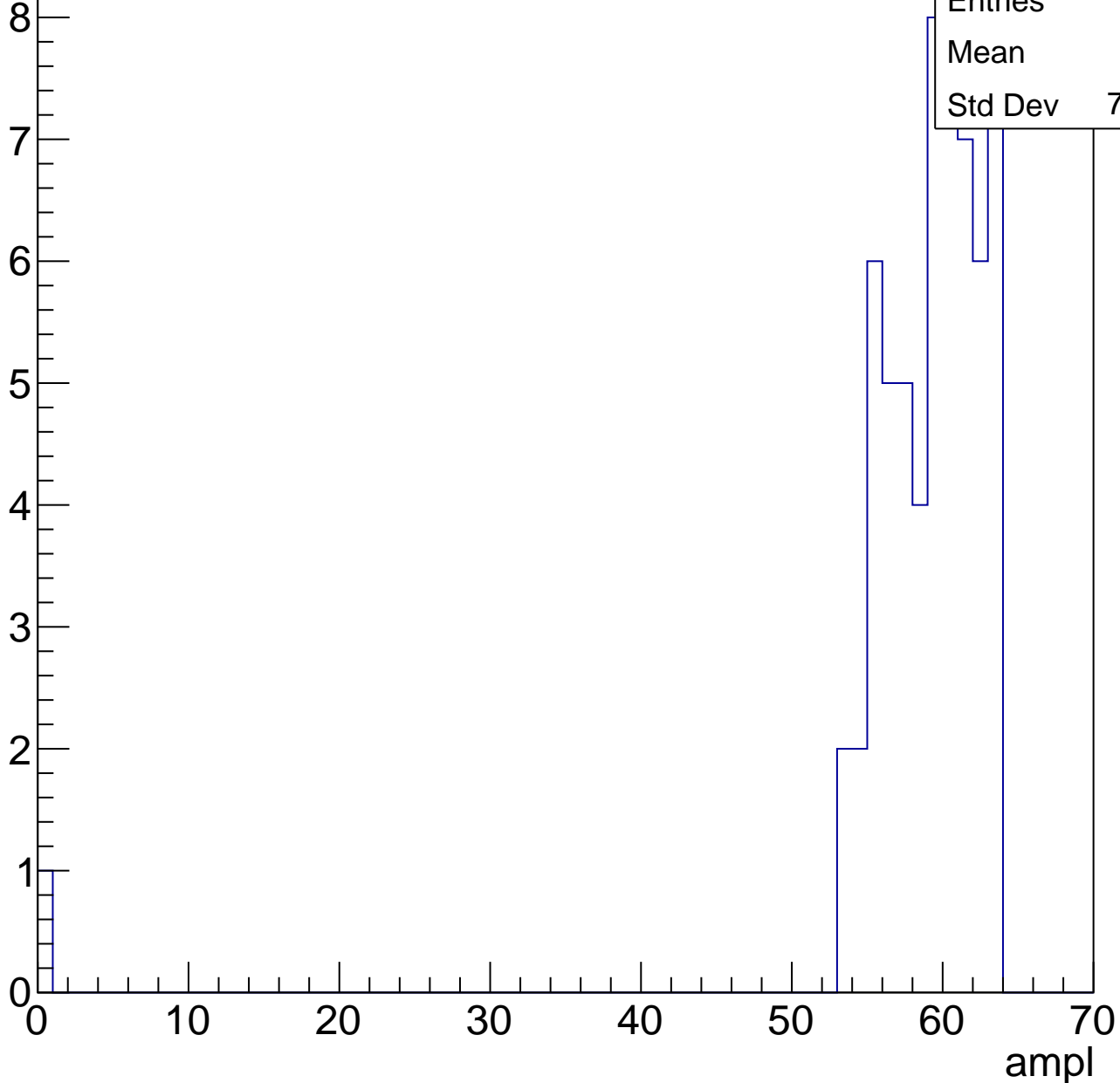


# B1L002S, U18-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

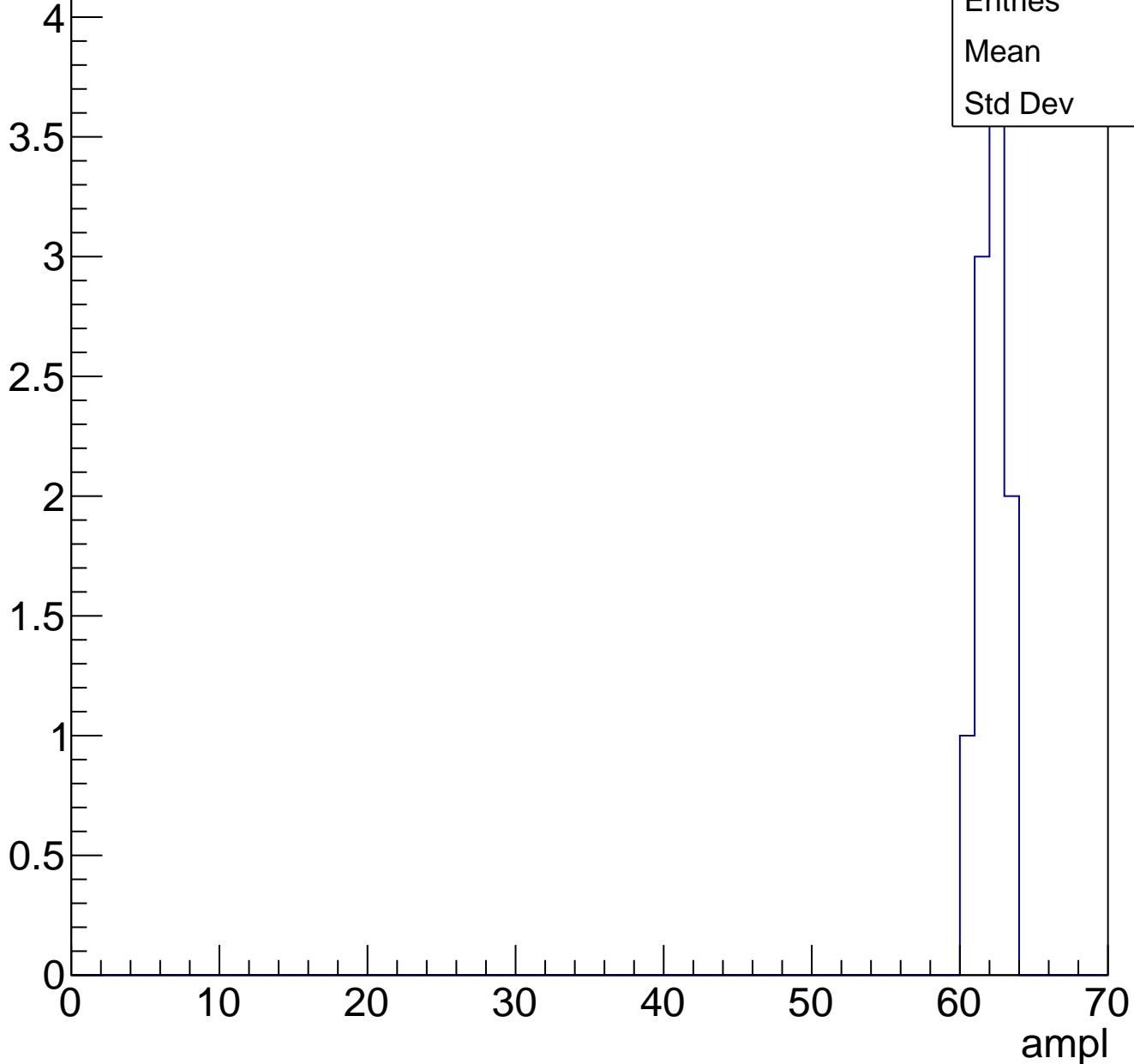
Entries	62
Mean	58
Std Dev	7.956



# B1L002S, U18-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	10
Mean	61.7
Std Dev	0.9



# B1L002S, U18-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L002S, U18-ch122, adc0

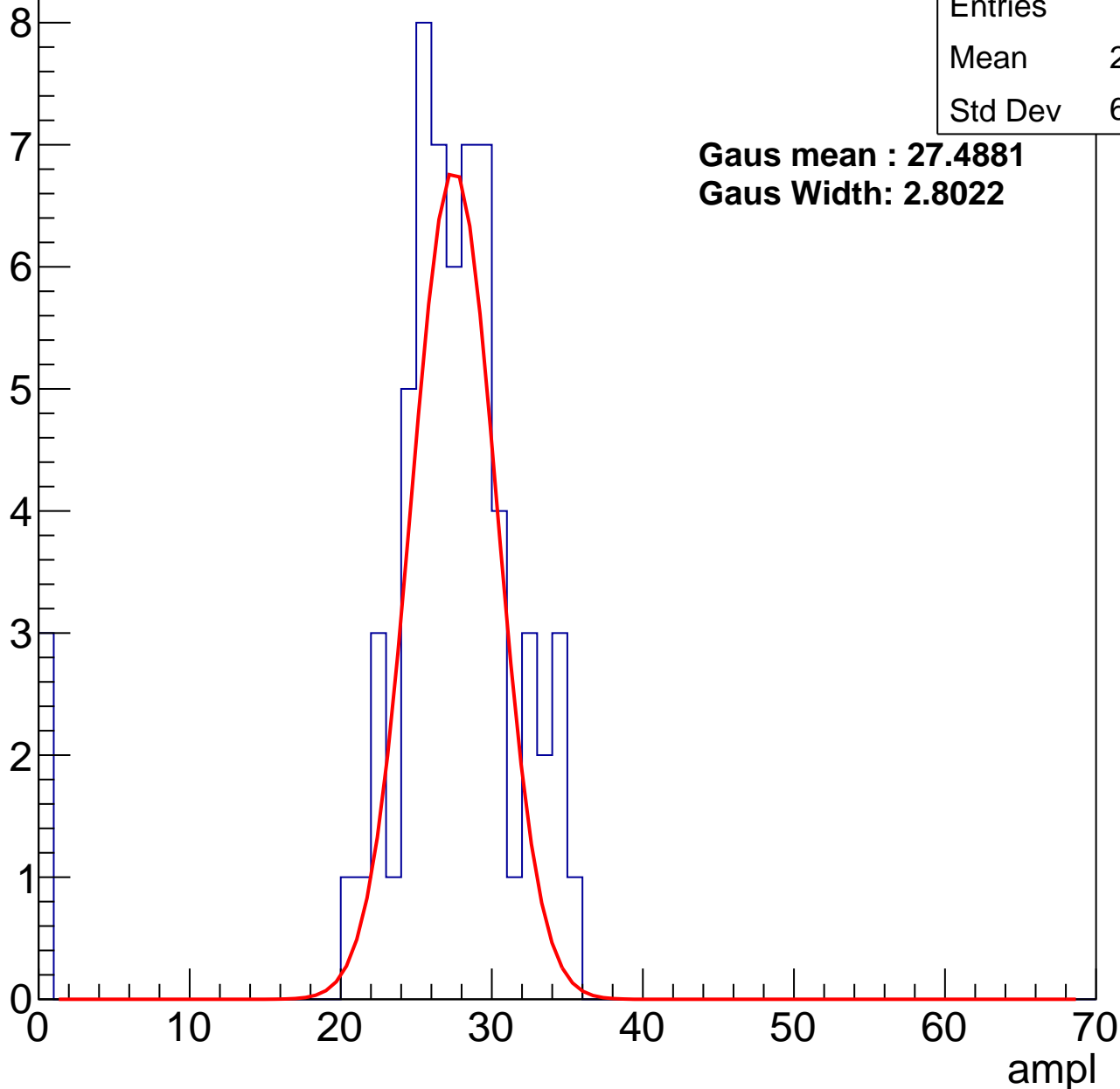
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	26.08
Std Dev	6.712

**Gaus mean : 27.4881**

**Gaus Width: 2.8022**



# B1L002S, U18-ch122, adc1

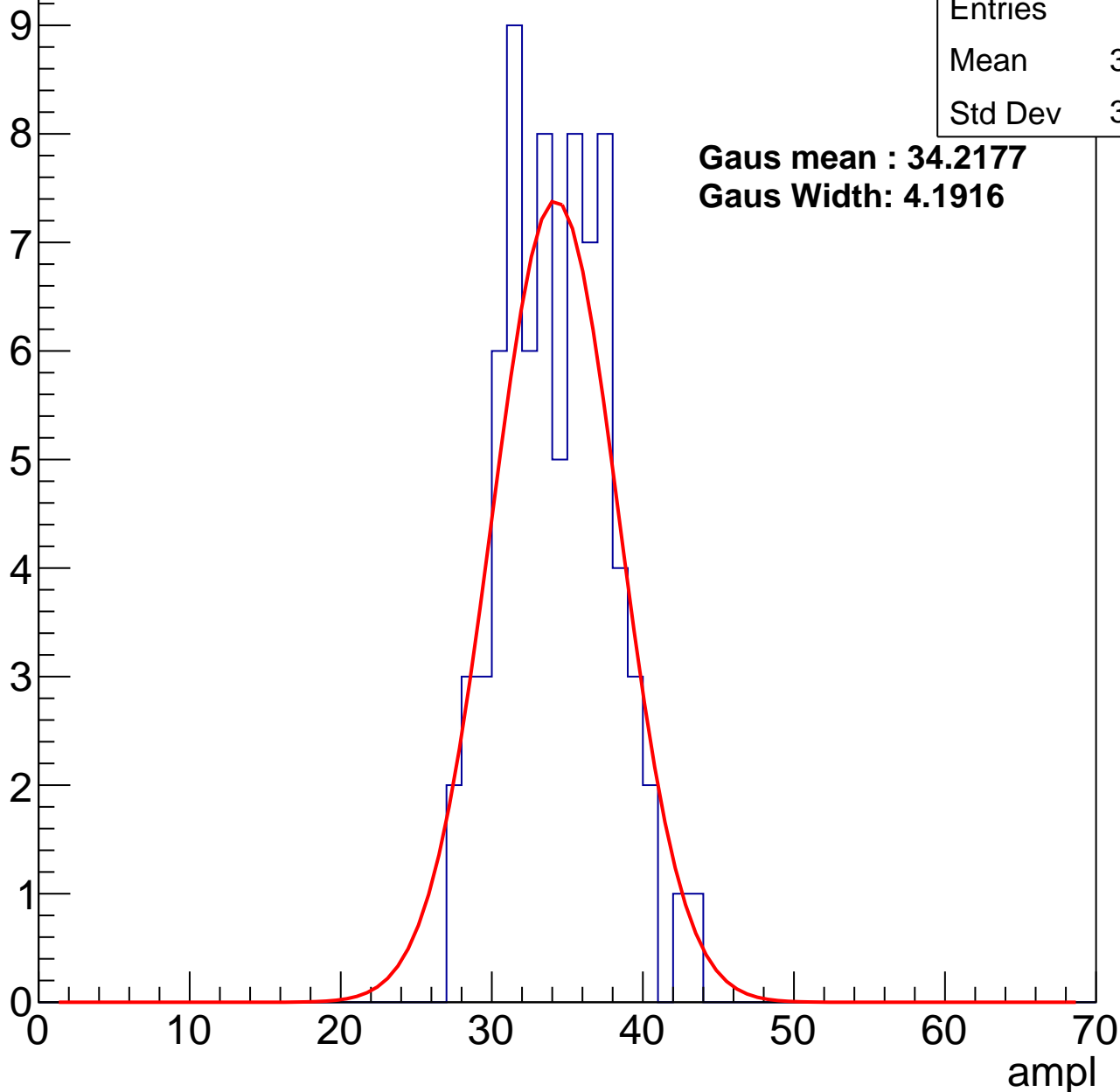
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	33.84
Std Dev	3.539

**Gaus mean : 34.2177**

**Gaus Width: 4.1916**



# B1L002S, U18-ch122, adc2

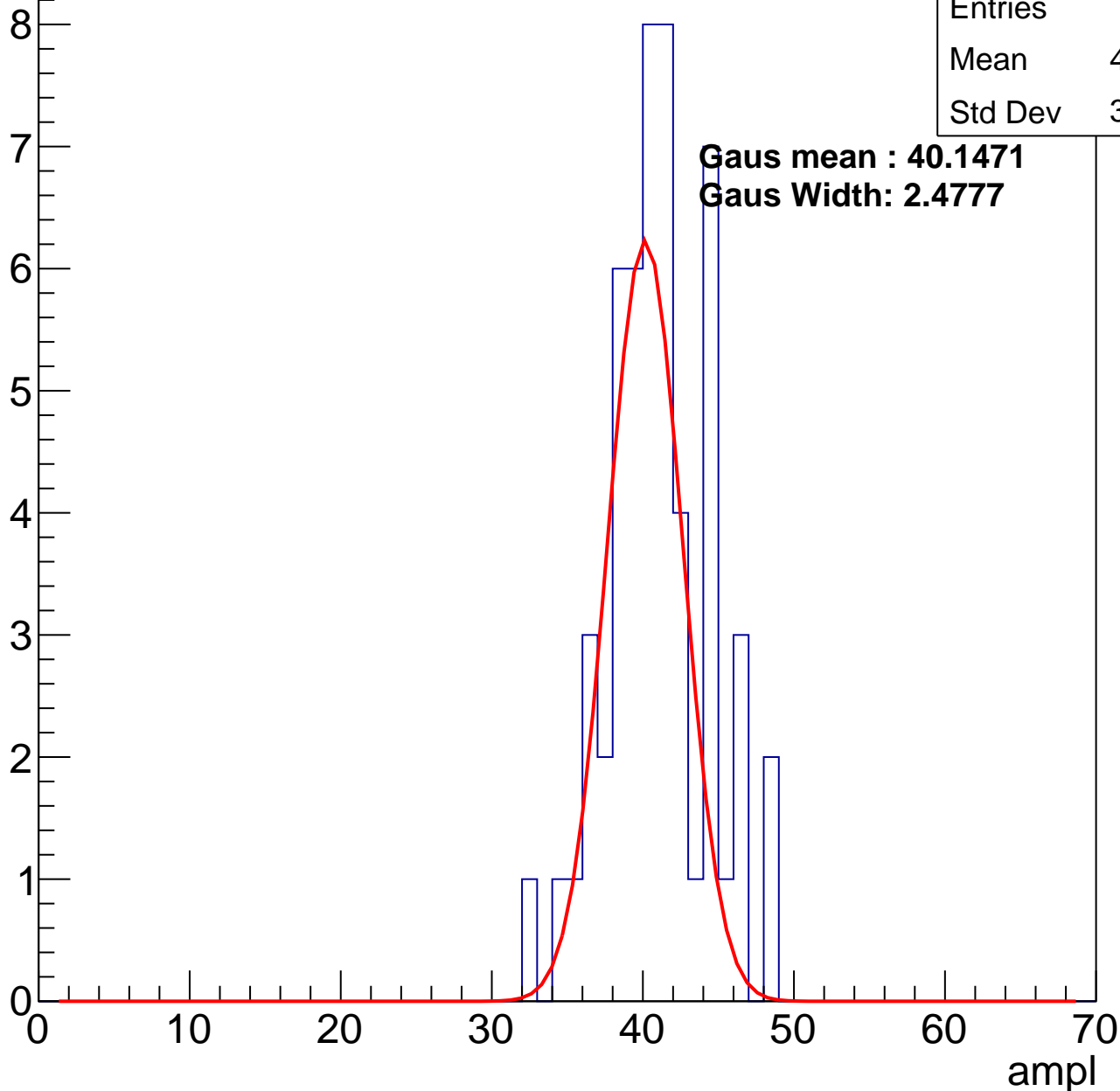
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.57
Std Dev	3.359

**Gaus mean : 40.1471**

**Gaus Width: 2.4777**

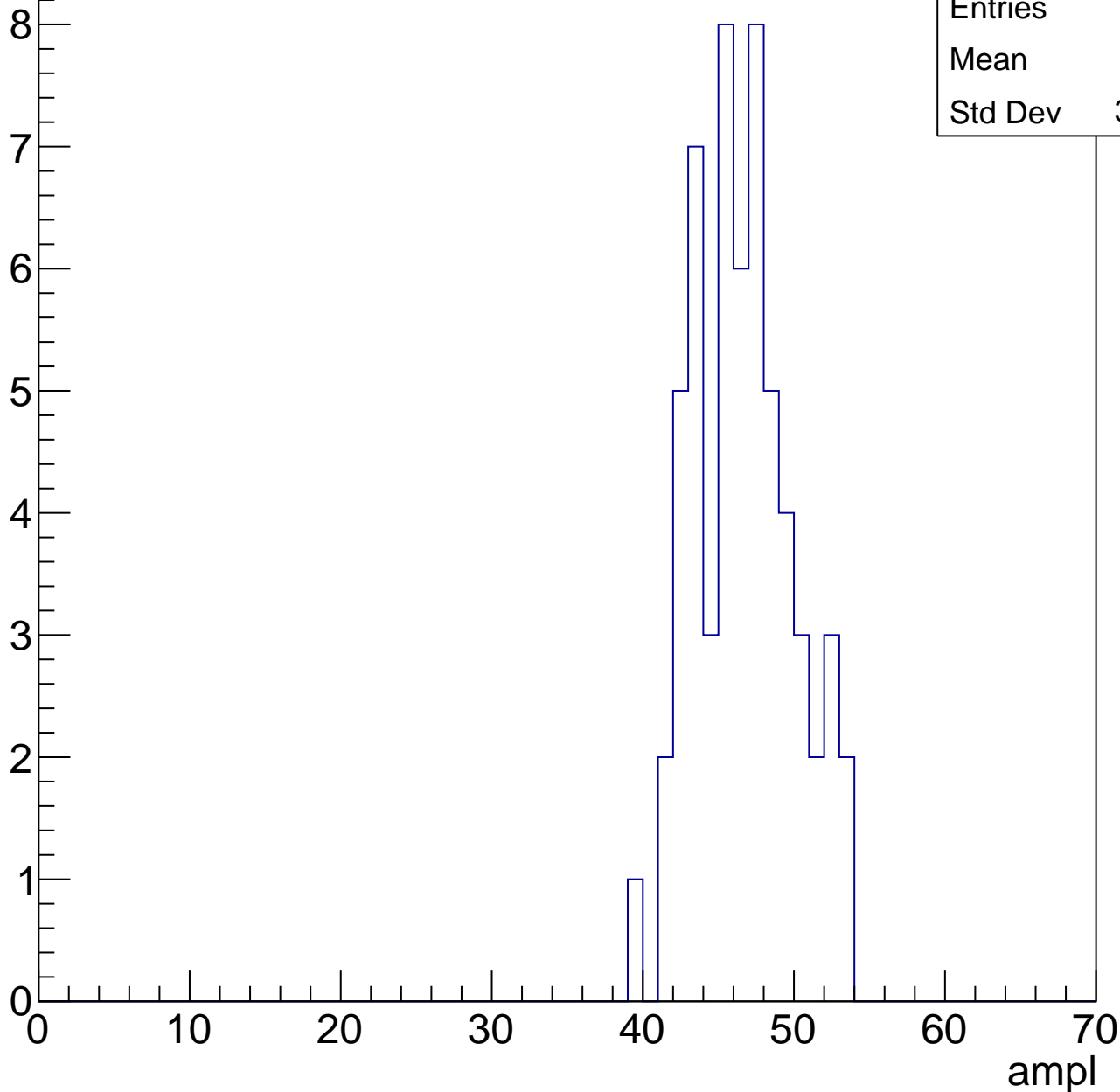


# B1L002S, U18-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	46.2
Std Dev	3.261

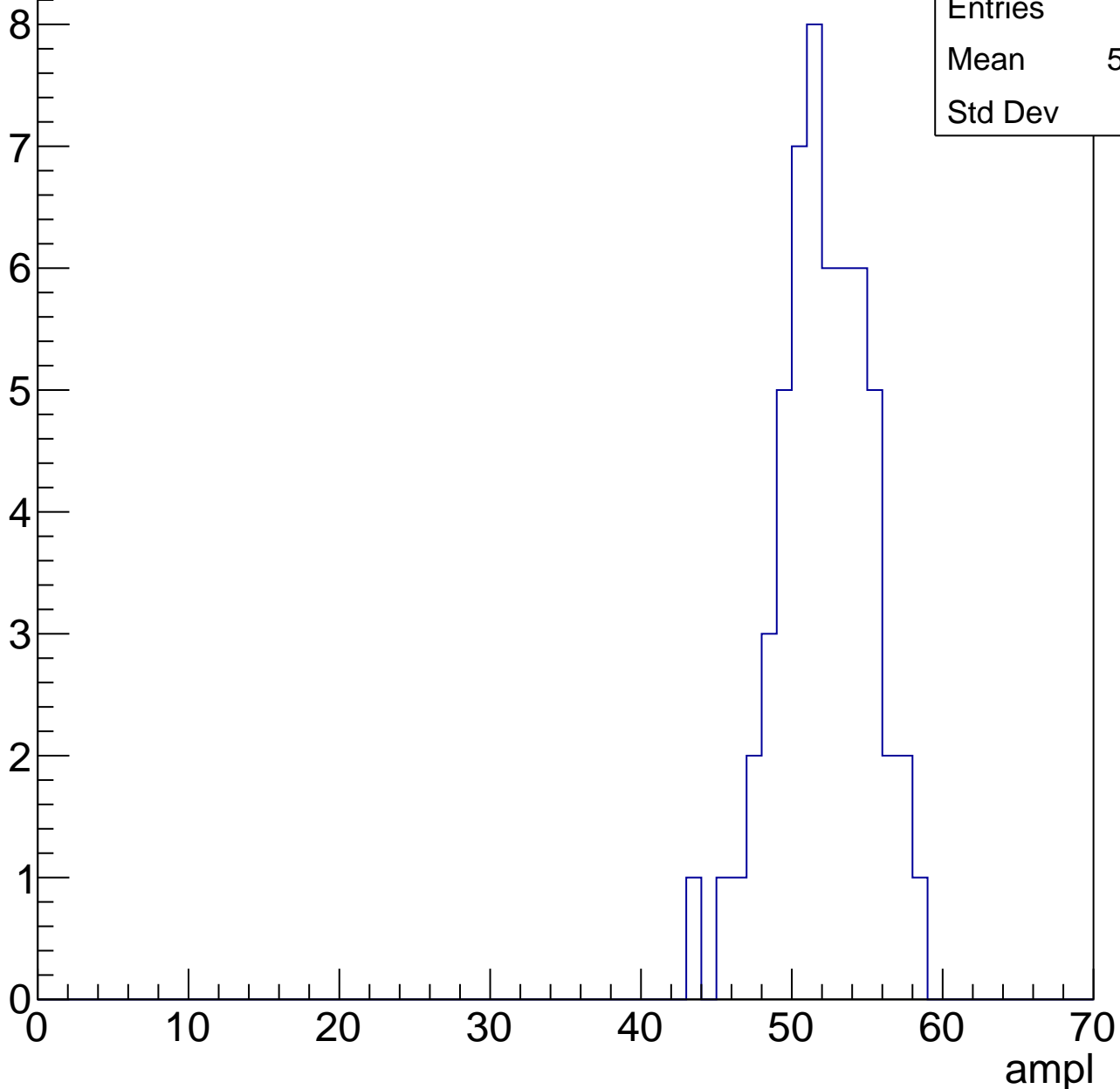


# B1L002S, U18-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	51.57
Std Dev	3.07

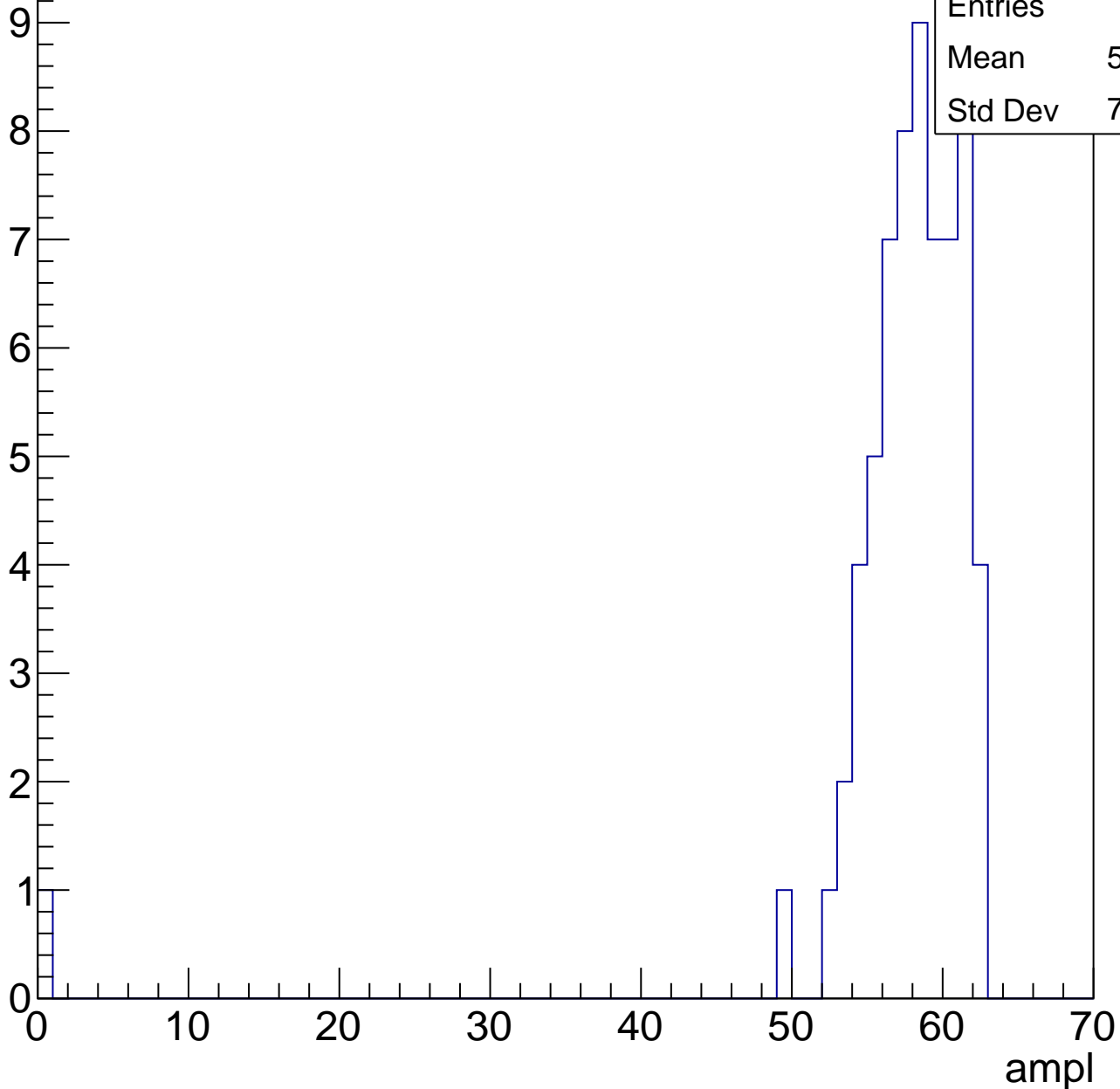


# B1L002S, U18-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	56.83
Std Dev	7.664

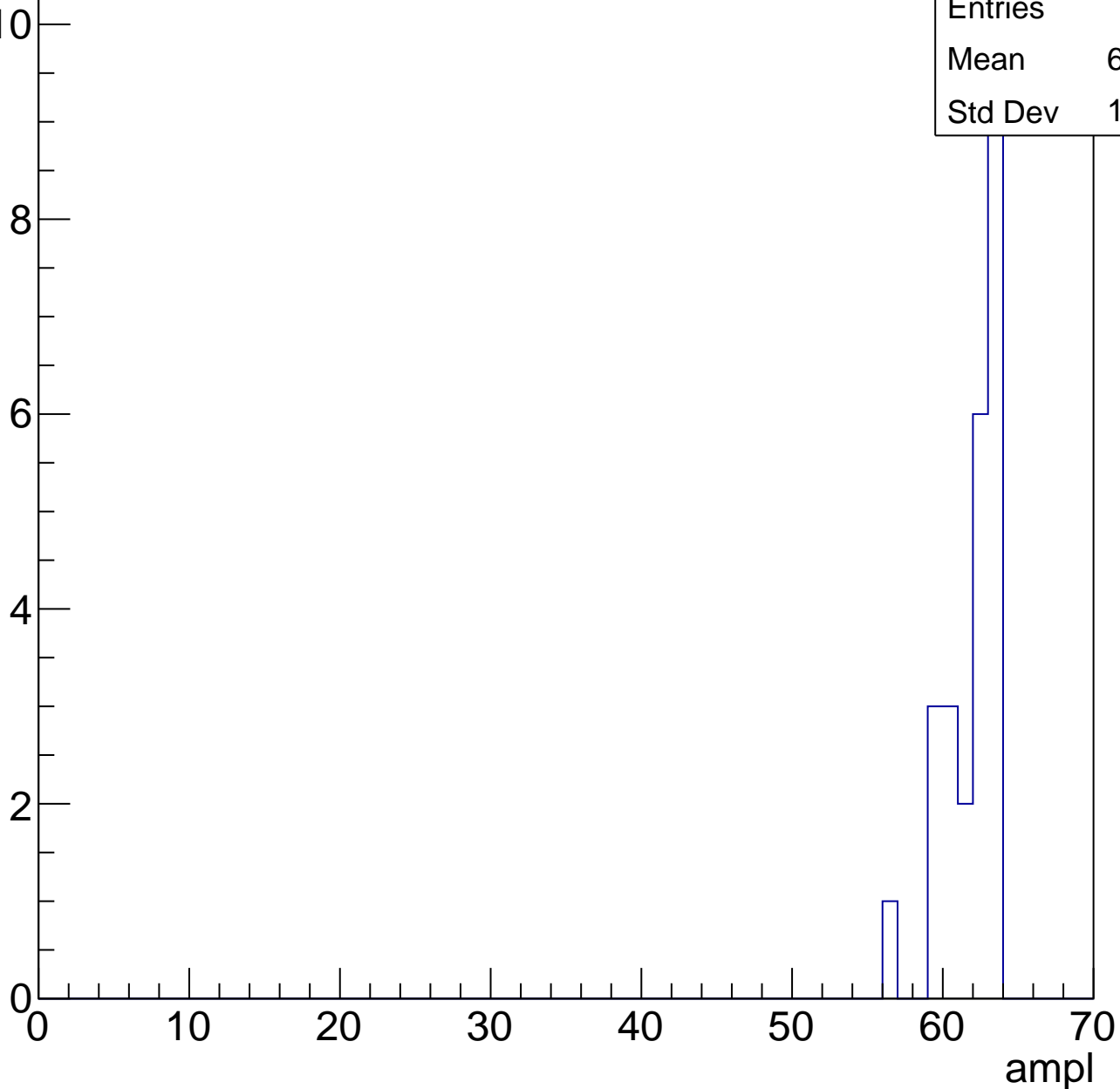


# B1L002S, U18-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.48
Std Dev	1.792





# B1L002S, U18-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U18-ch123, adc0

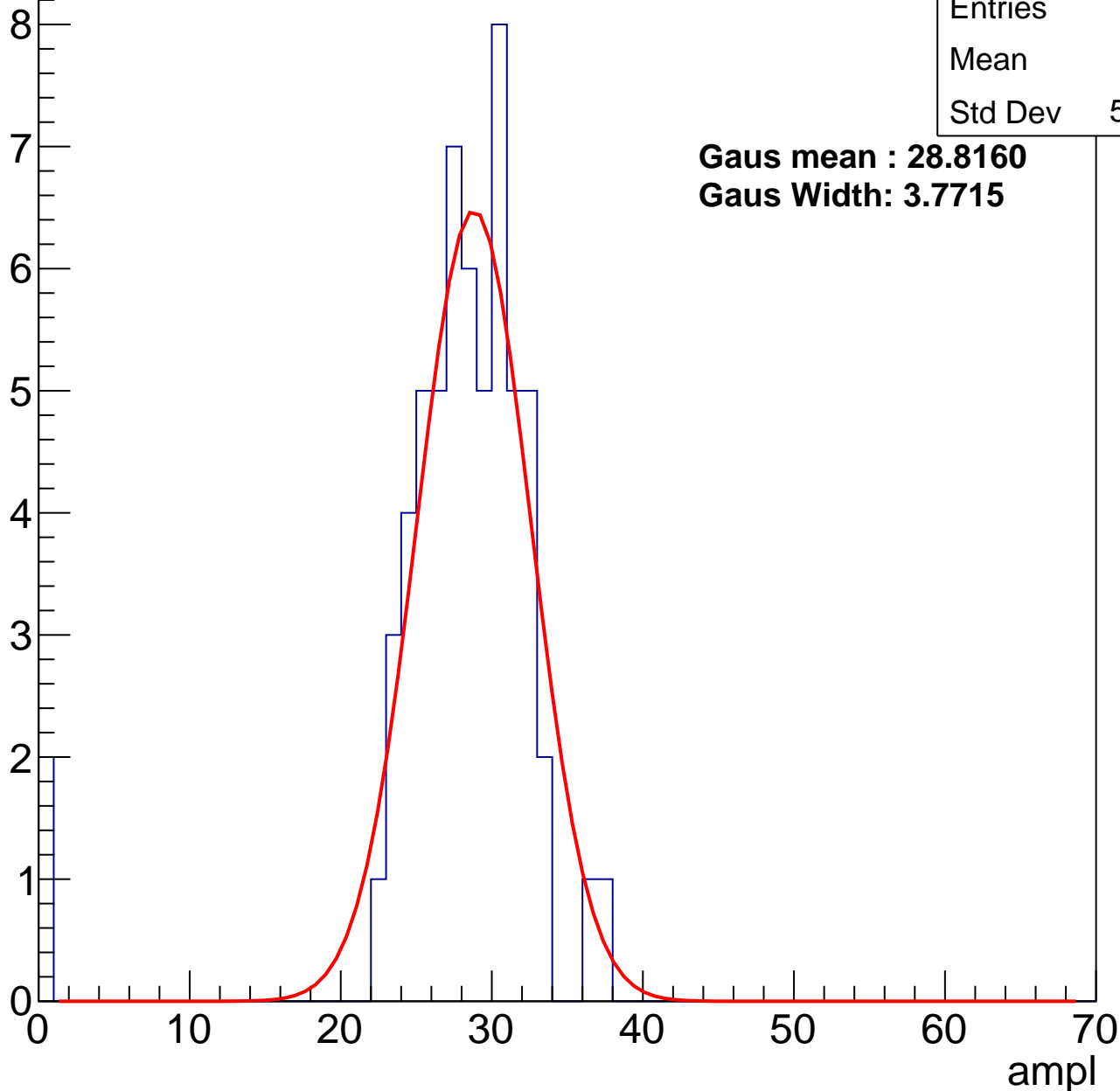
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	27.3
Std Dev	5.976

**Gaus mean : 28.8160**

**Gaus Width: 3.7715**



# B1L002S, U18-ch123, adc1

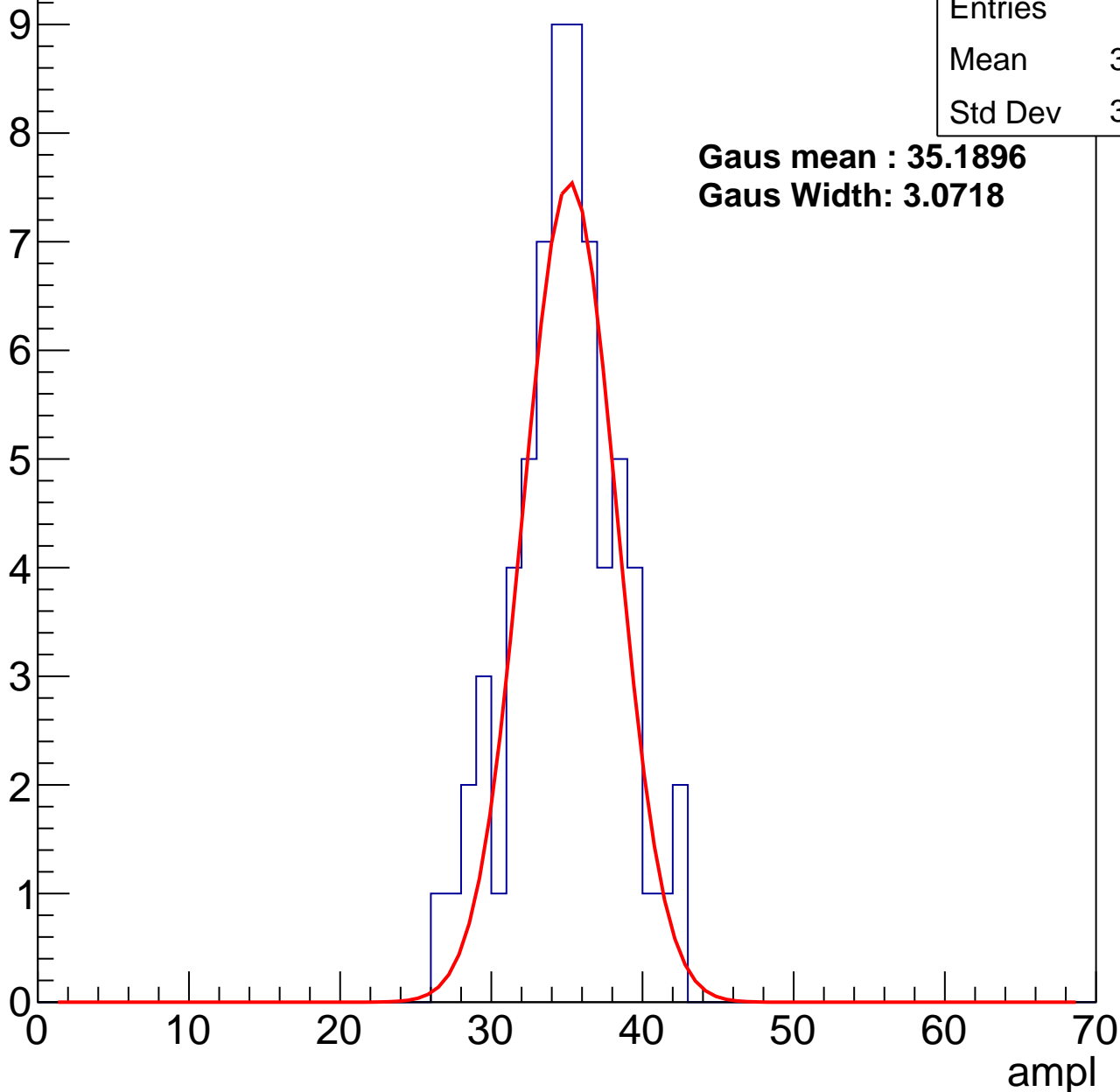
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.44
Std Dev	3.465

**Gaus mean : 35.1896**

**Gaus Width: 3.0718**



# B1L002S, U18-ch123, adc2

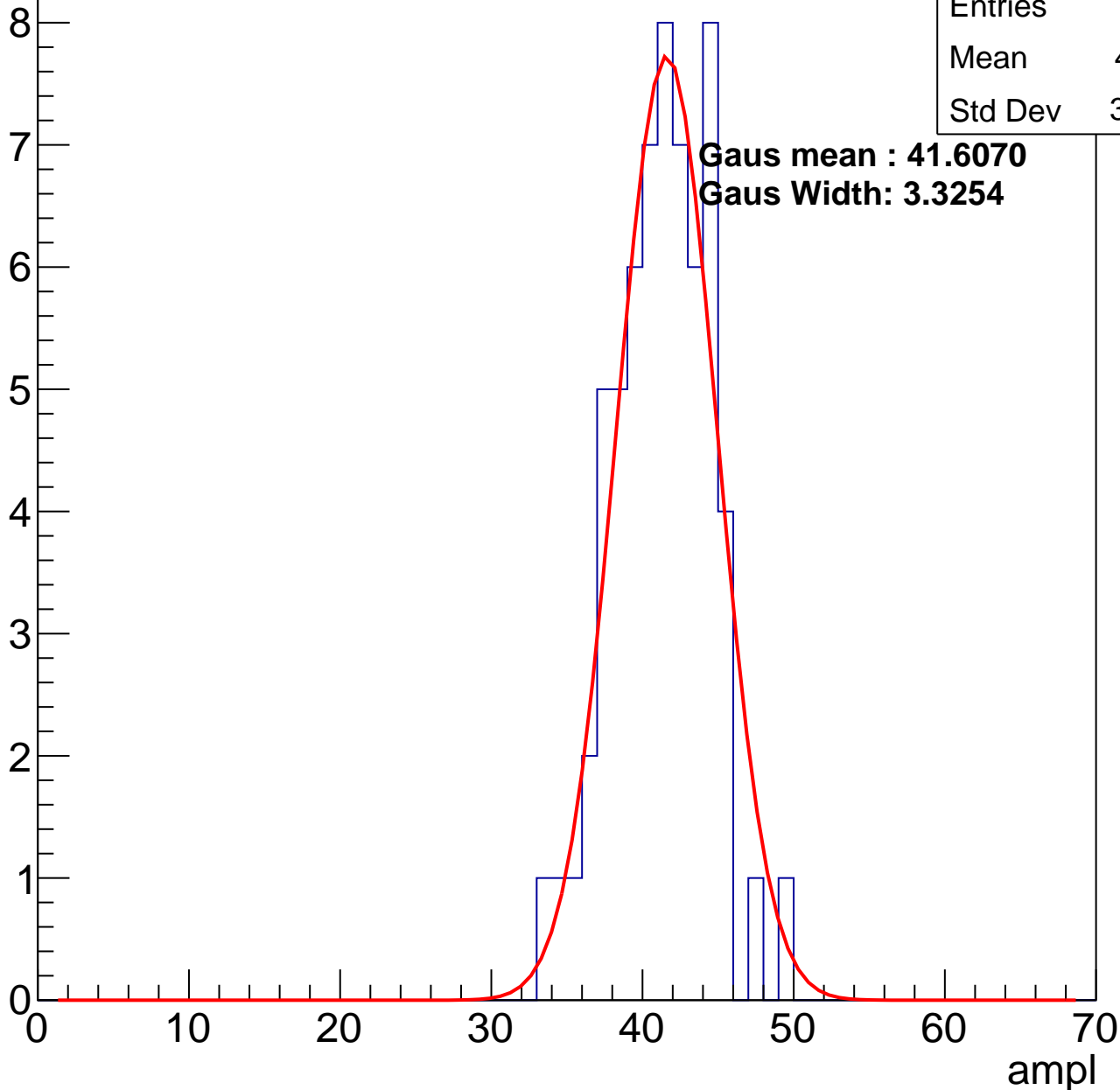
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.81
Std Dev	3.136

**Gaus mean : 41.6070**

**Gaus Width: 3.3254**

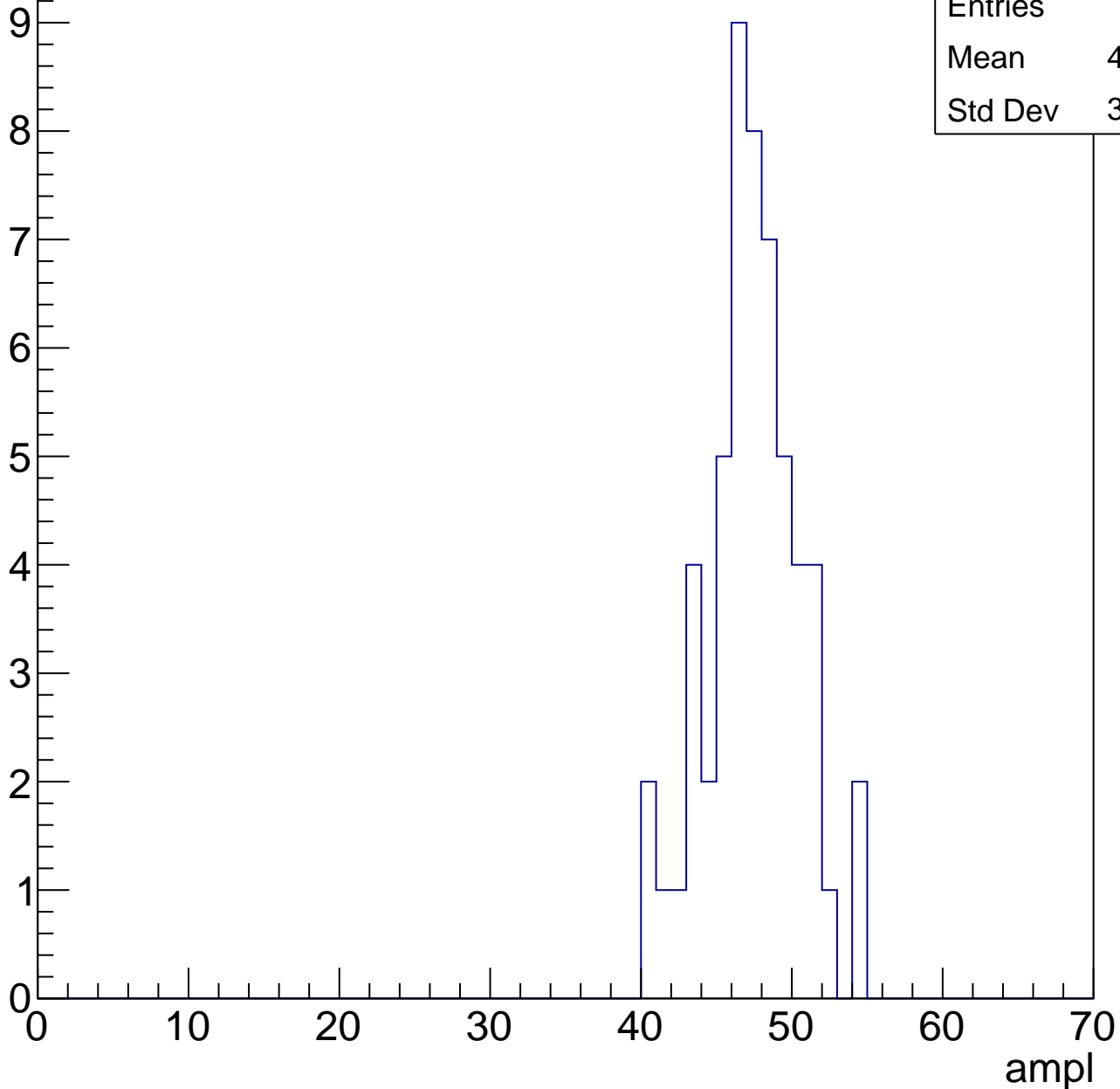


# B1L002S, U18-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.96
Std Dev	3.075

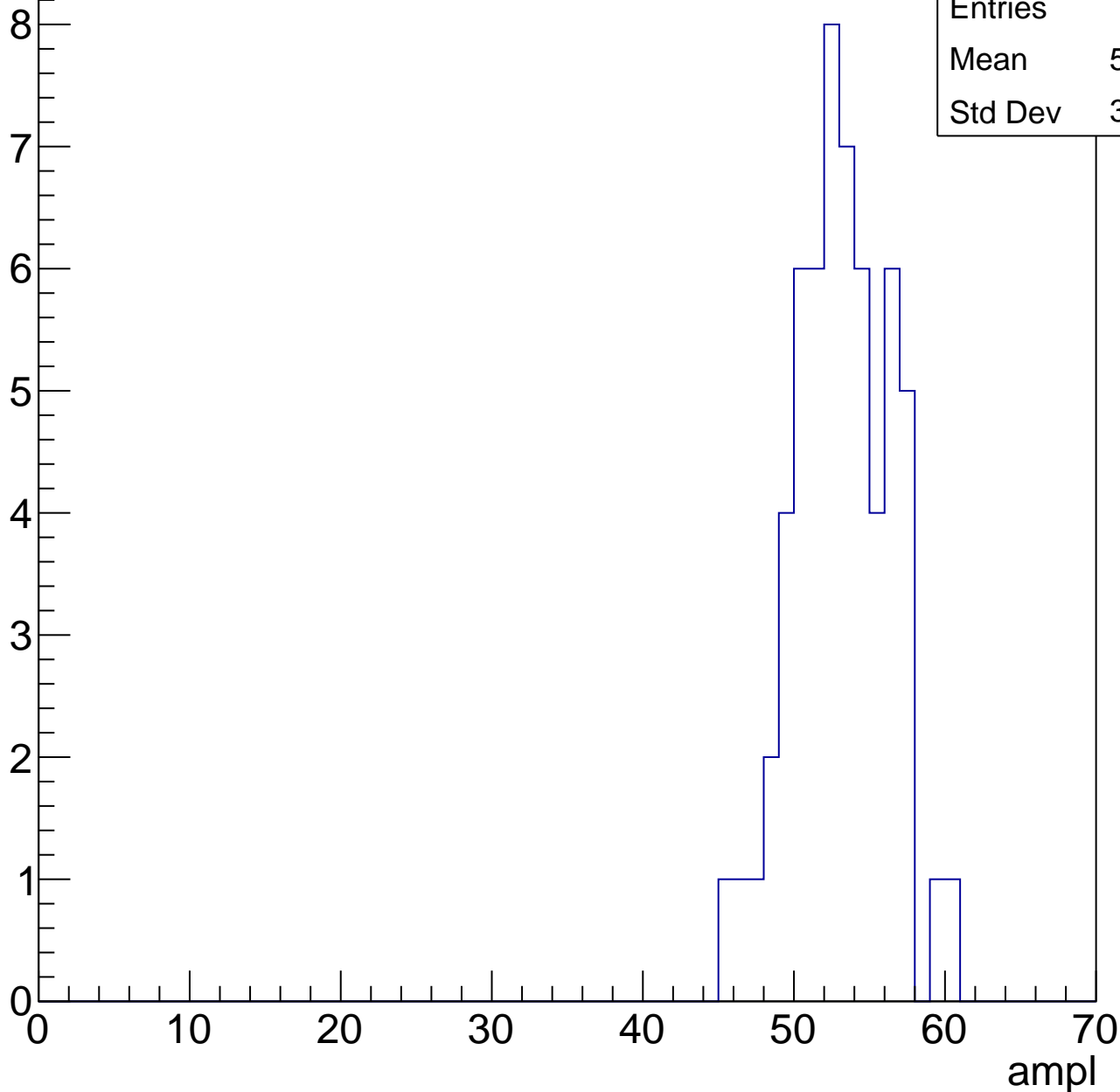


# B1L002S, U18-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	52.66
Std Dev	3.144

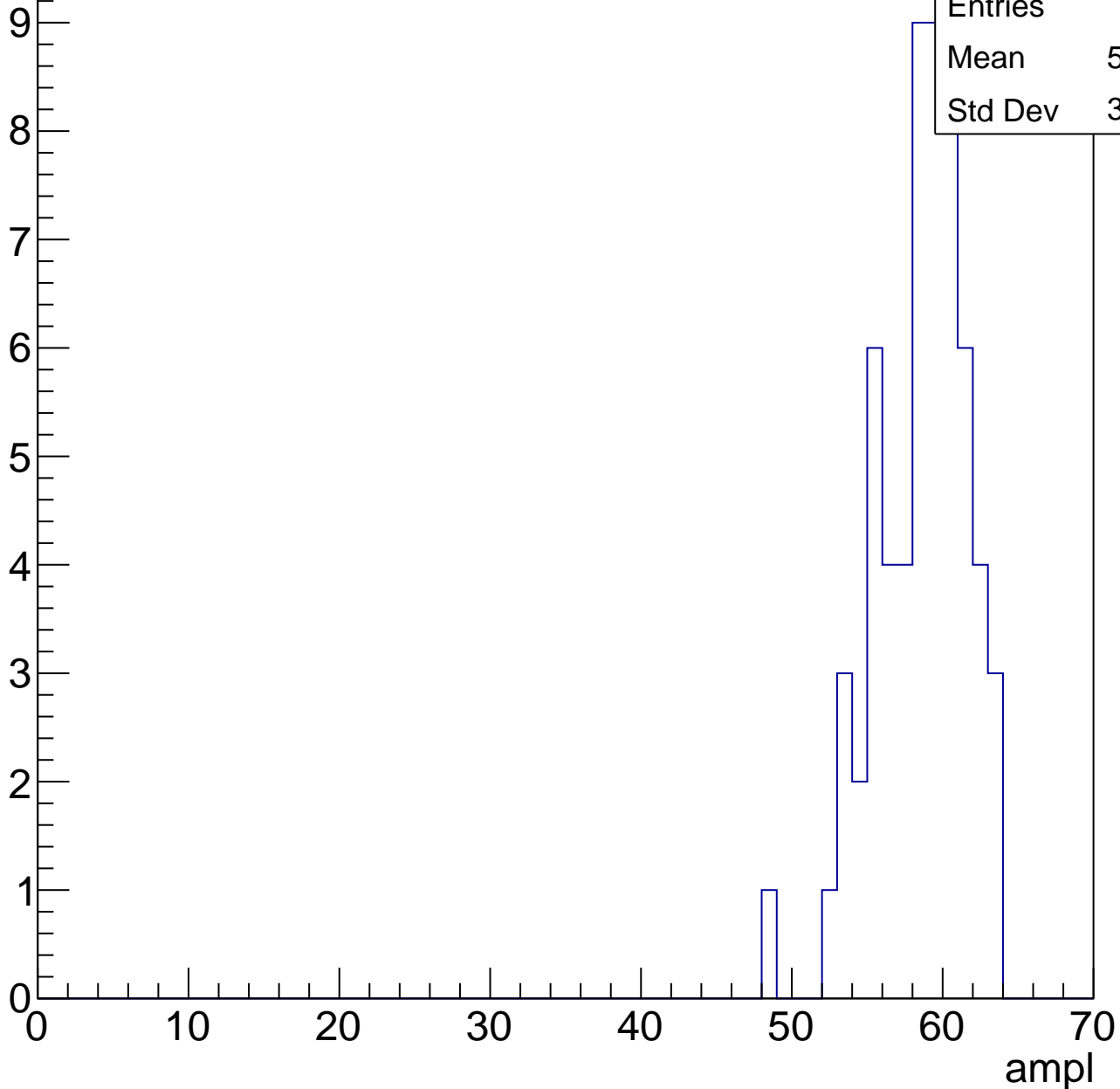


# B1L002S, U18-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.08
Std Dev	3.029

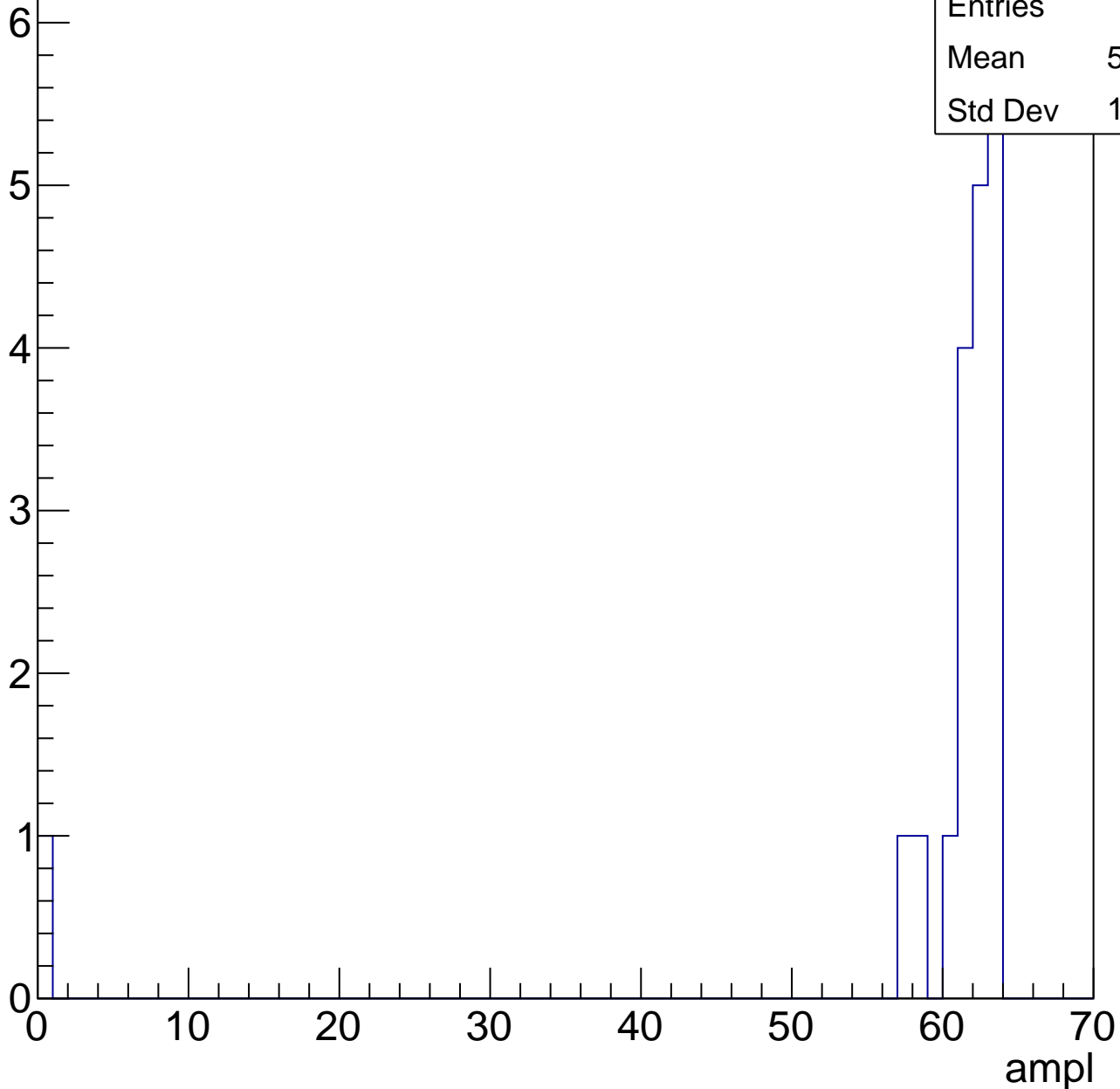


# B1L002S, U18-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.26
Std Dev	13.83





# B1L002S, U18-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch124, adc0

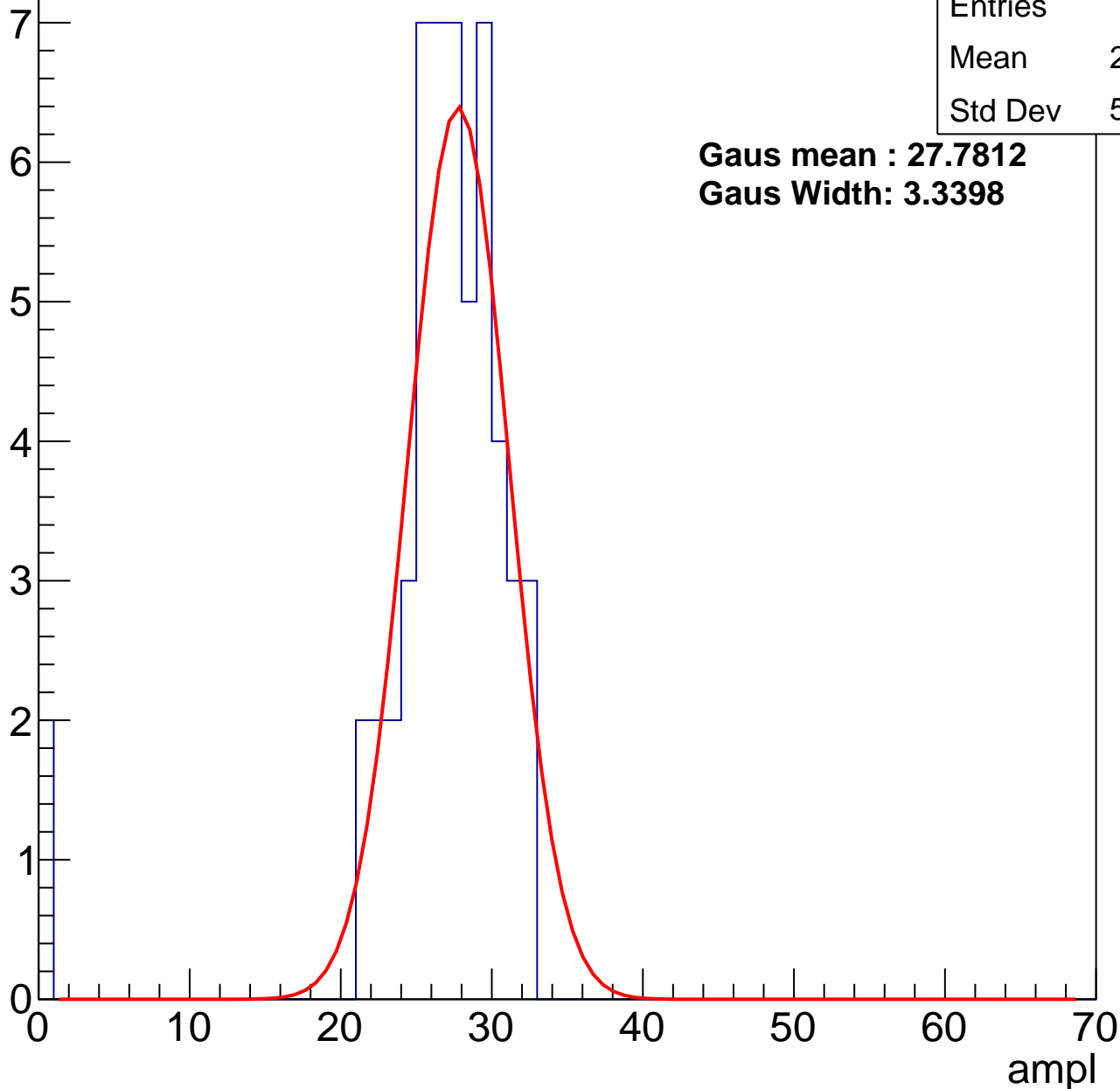
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	25.96
Std Dev	5.786

**Gaus mean : 27.7812**

**Gaus Width: 3.3398**



# B1L002S, U18-ch124, adc1

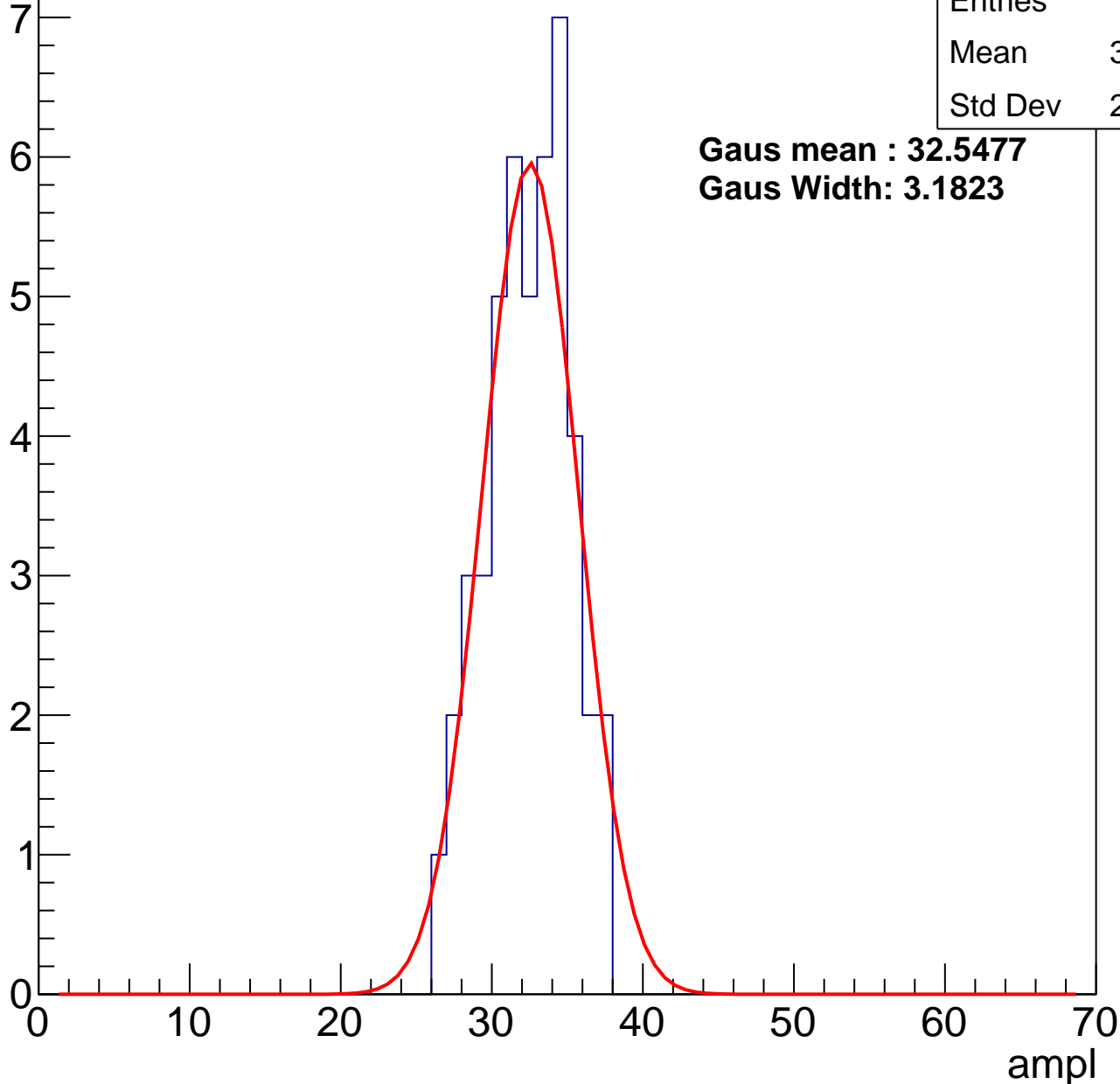
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	31.93
Std Dev	2.714

**Gaus mean : 32.5477**

**Gaus Width: 3.1823**



# B1L002S, U18-ch124, adc2

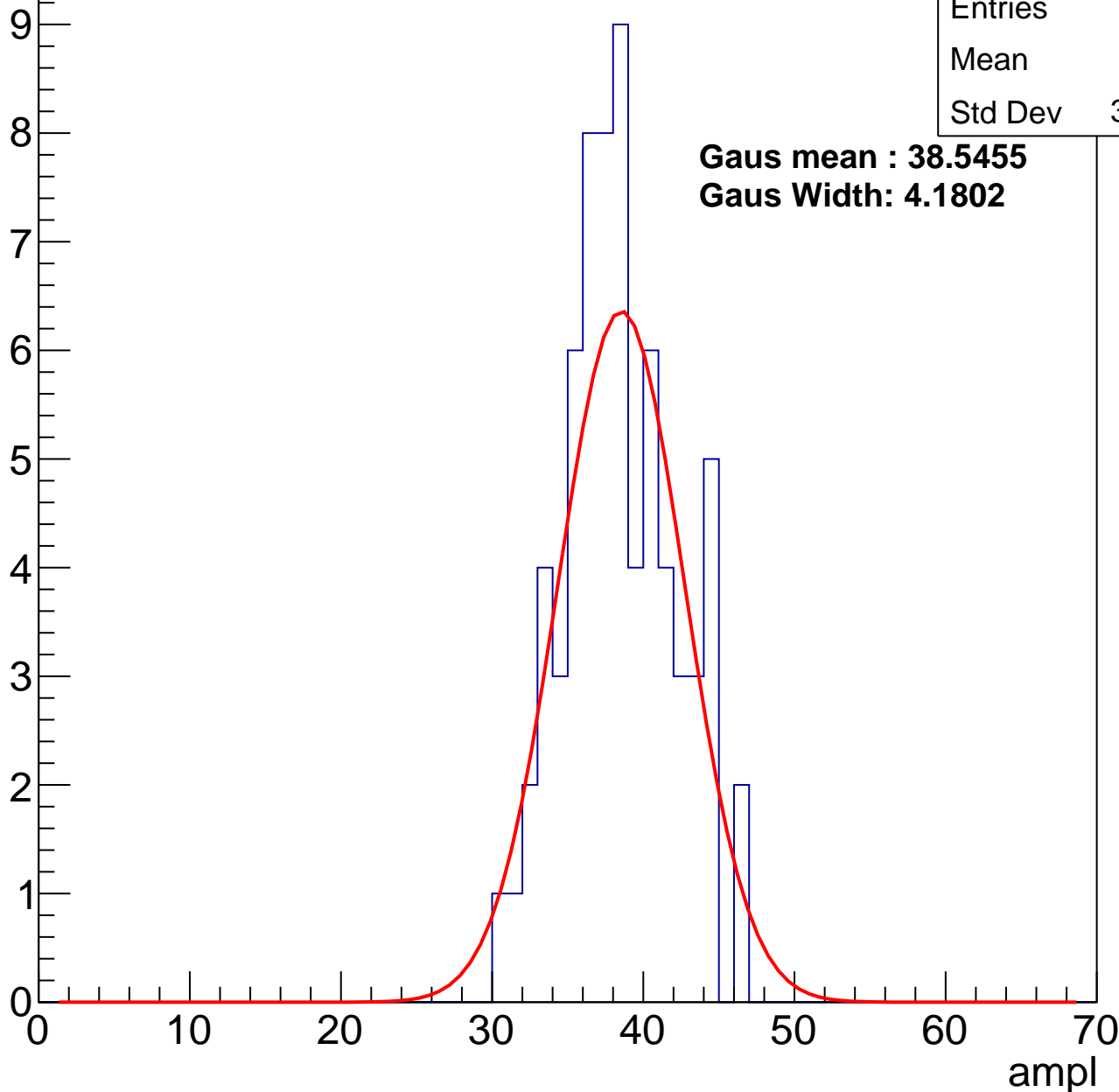
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	38
Std Dev	3.655

**Gaus mean : 38.5455**

**Gaus Width: 4.1802**

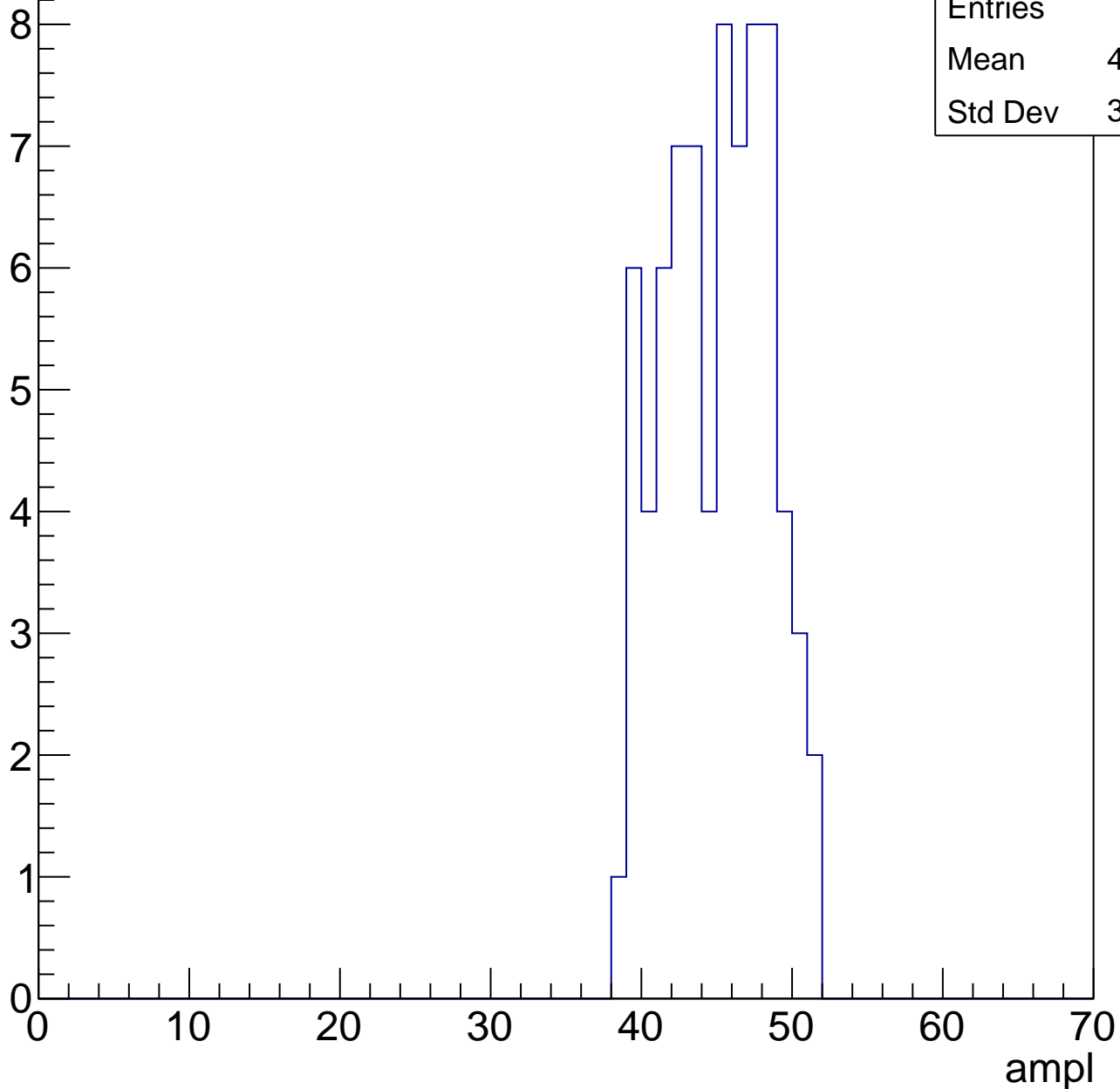


# B1L002S, U18-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	44.52
Std Dev	3.388

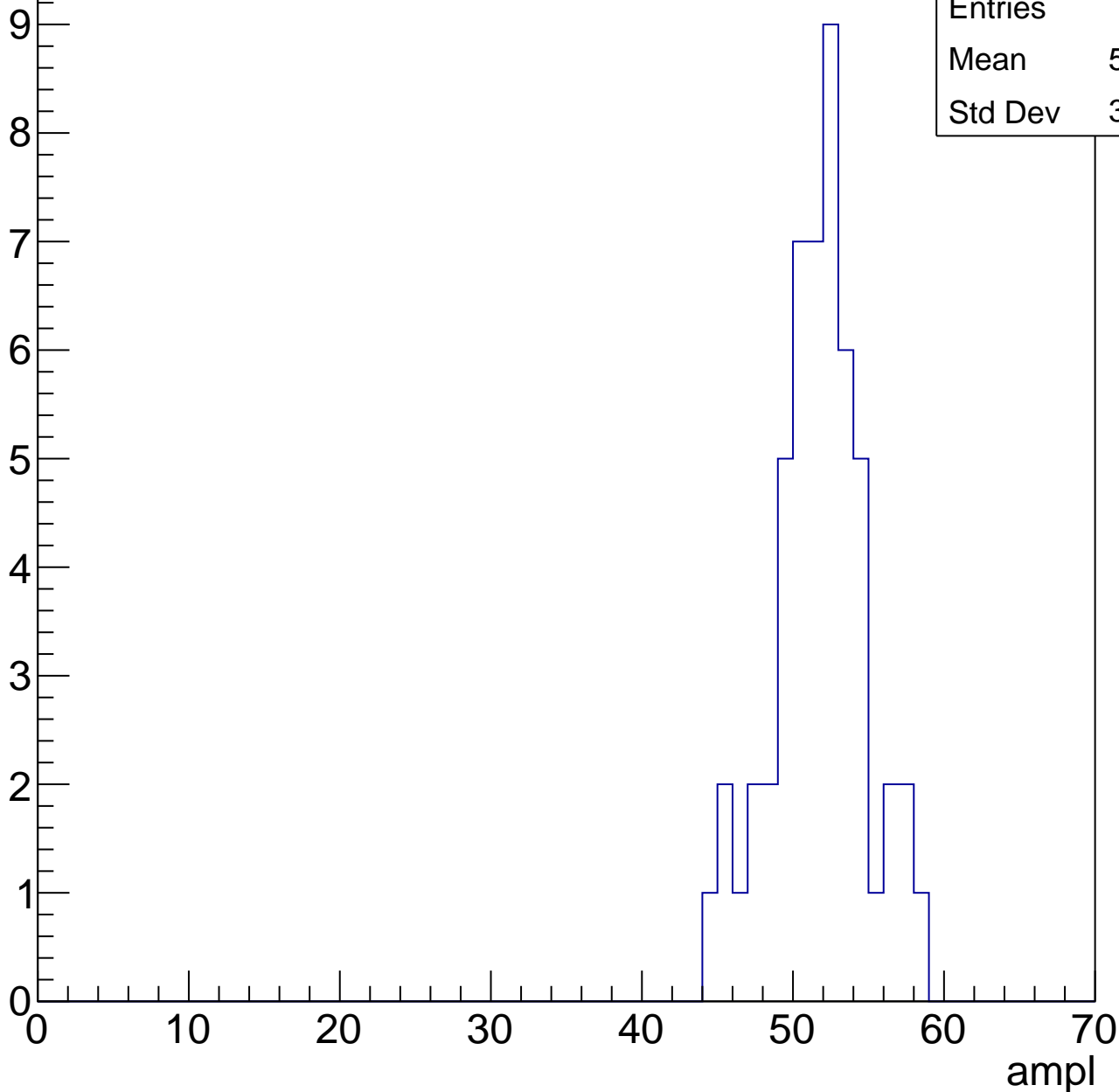


# B1L002S, U18-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	51.26
Std Dev	3.023

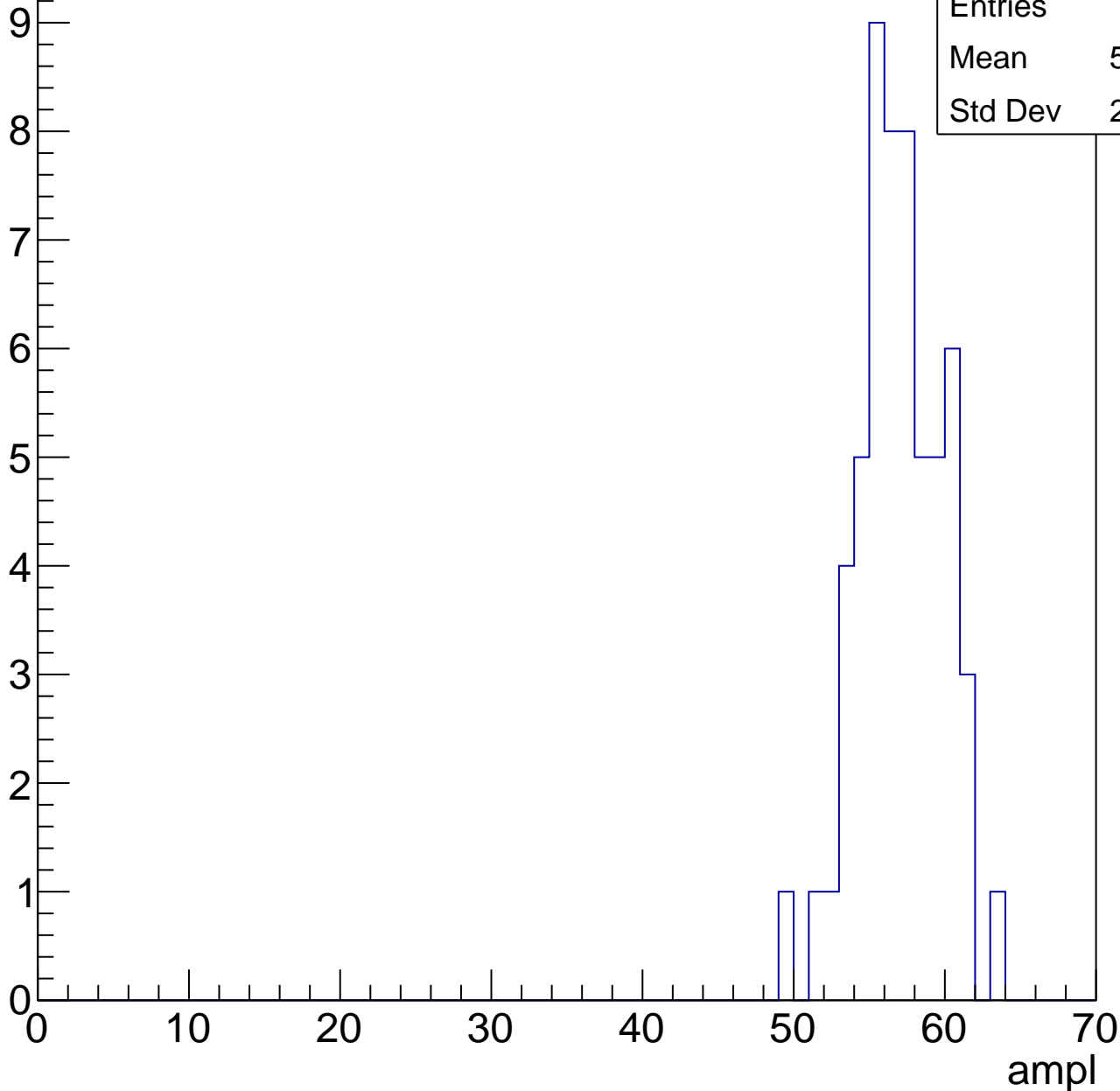


# B1L002S, U18-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	56.56
Std Dev	2.747

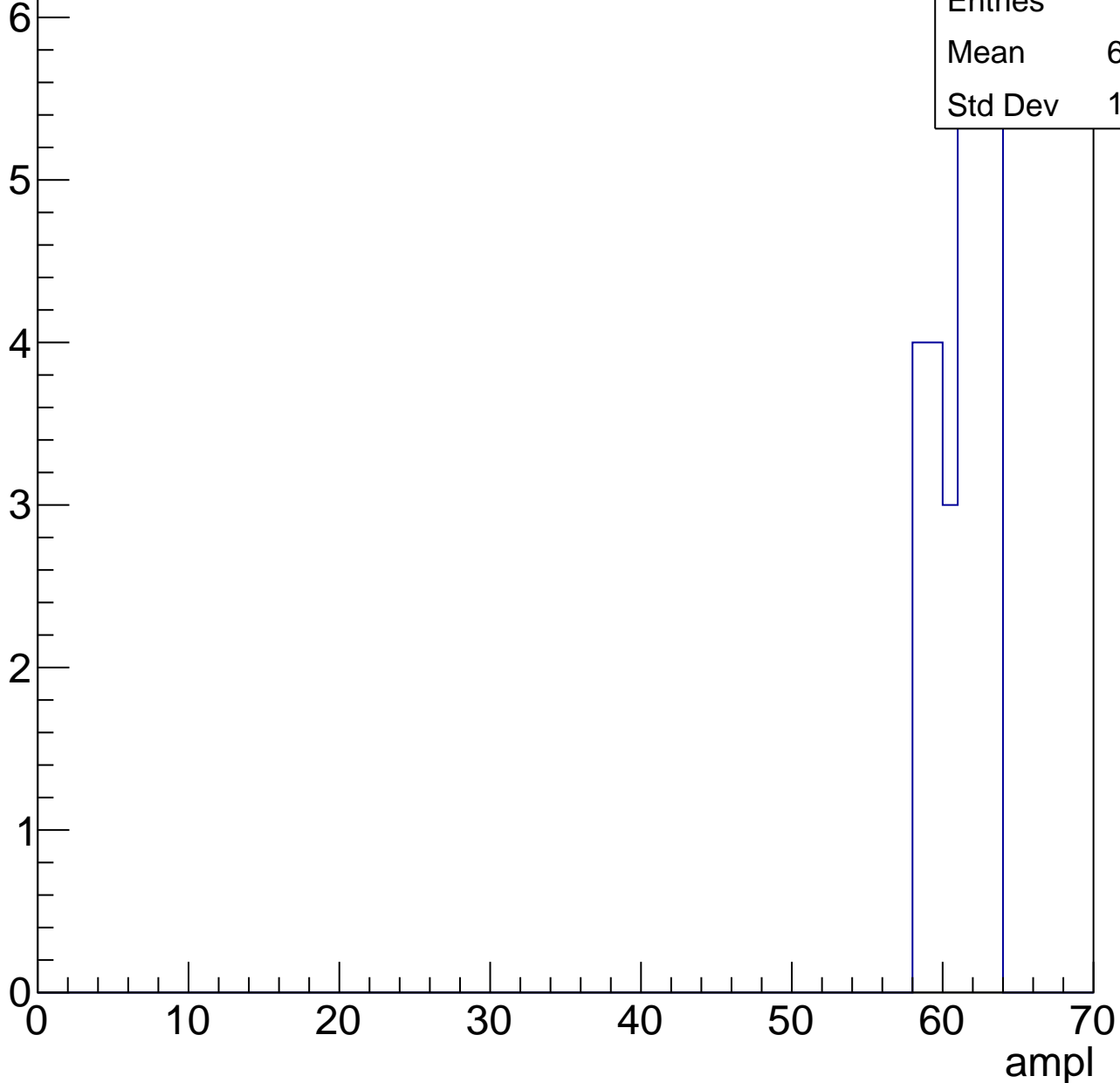


# B1L002S, U18-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	60.83
Std Dev	1.703

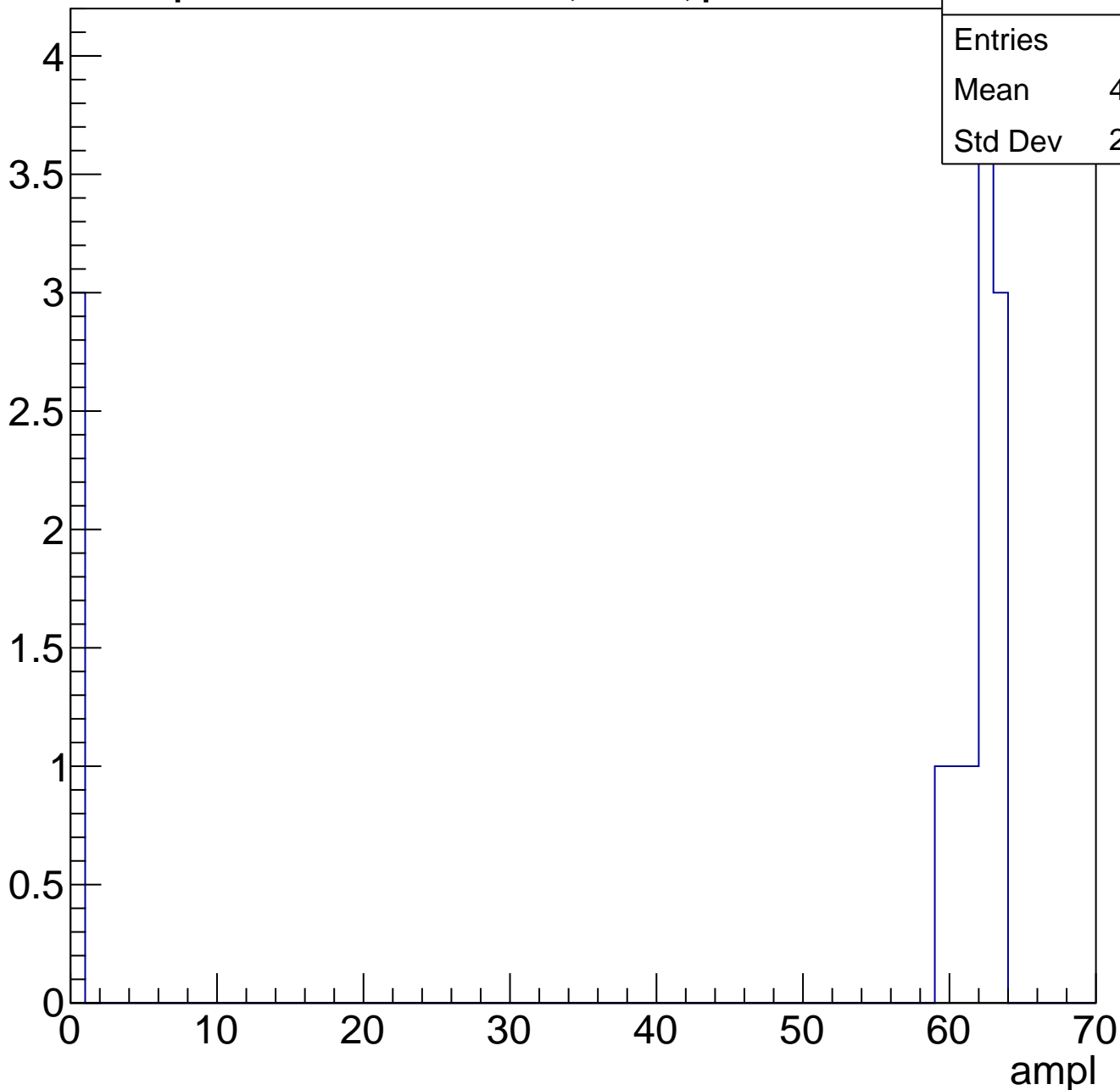




# B1L002S, U18-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch125, adc0

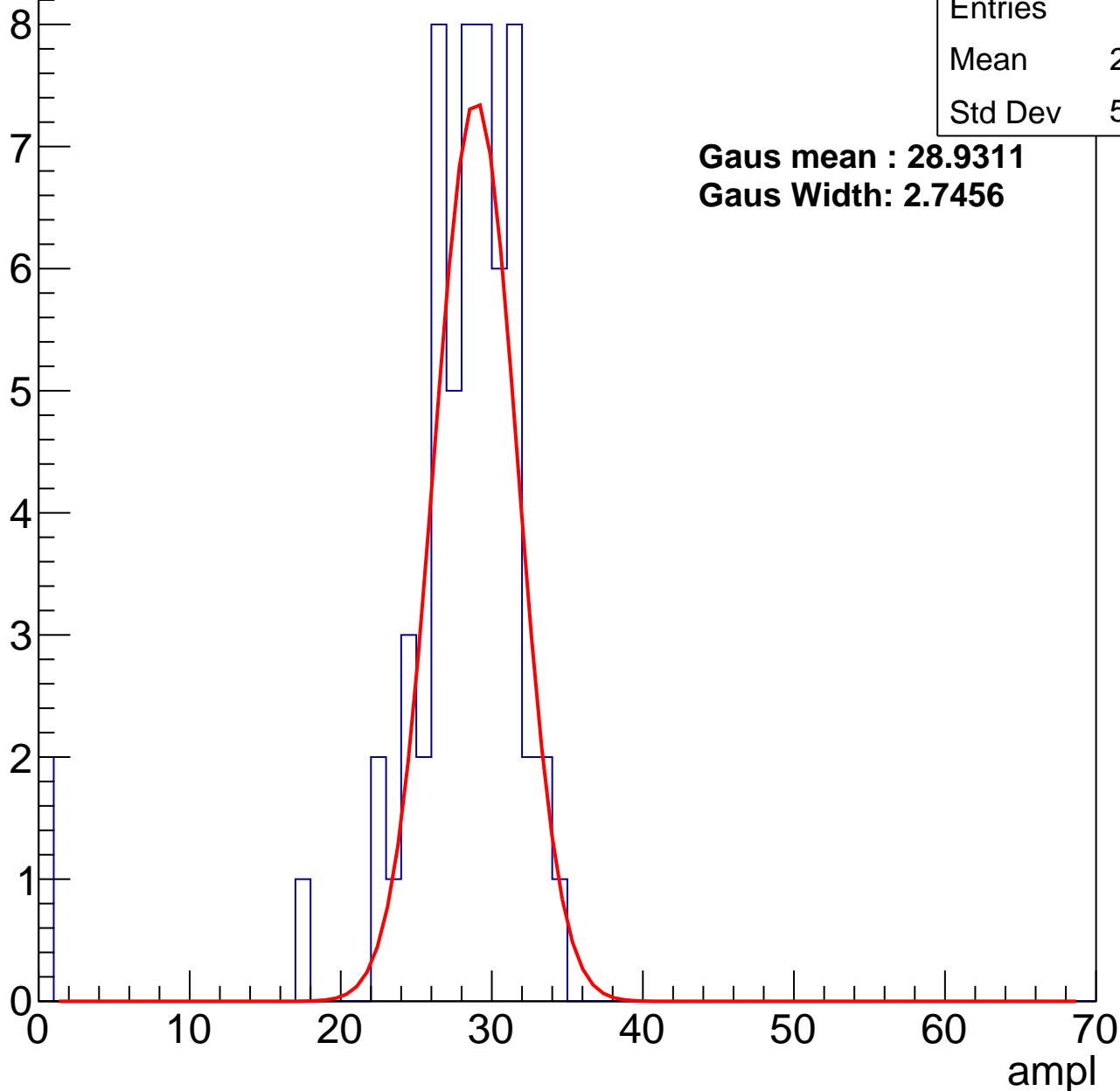
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.07
Std Dev	5.909

**Gaus mean : 28.9311**

**Gaus Width: 2.7456**



# B1L002S, U18-ch125, adc1

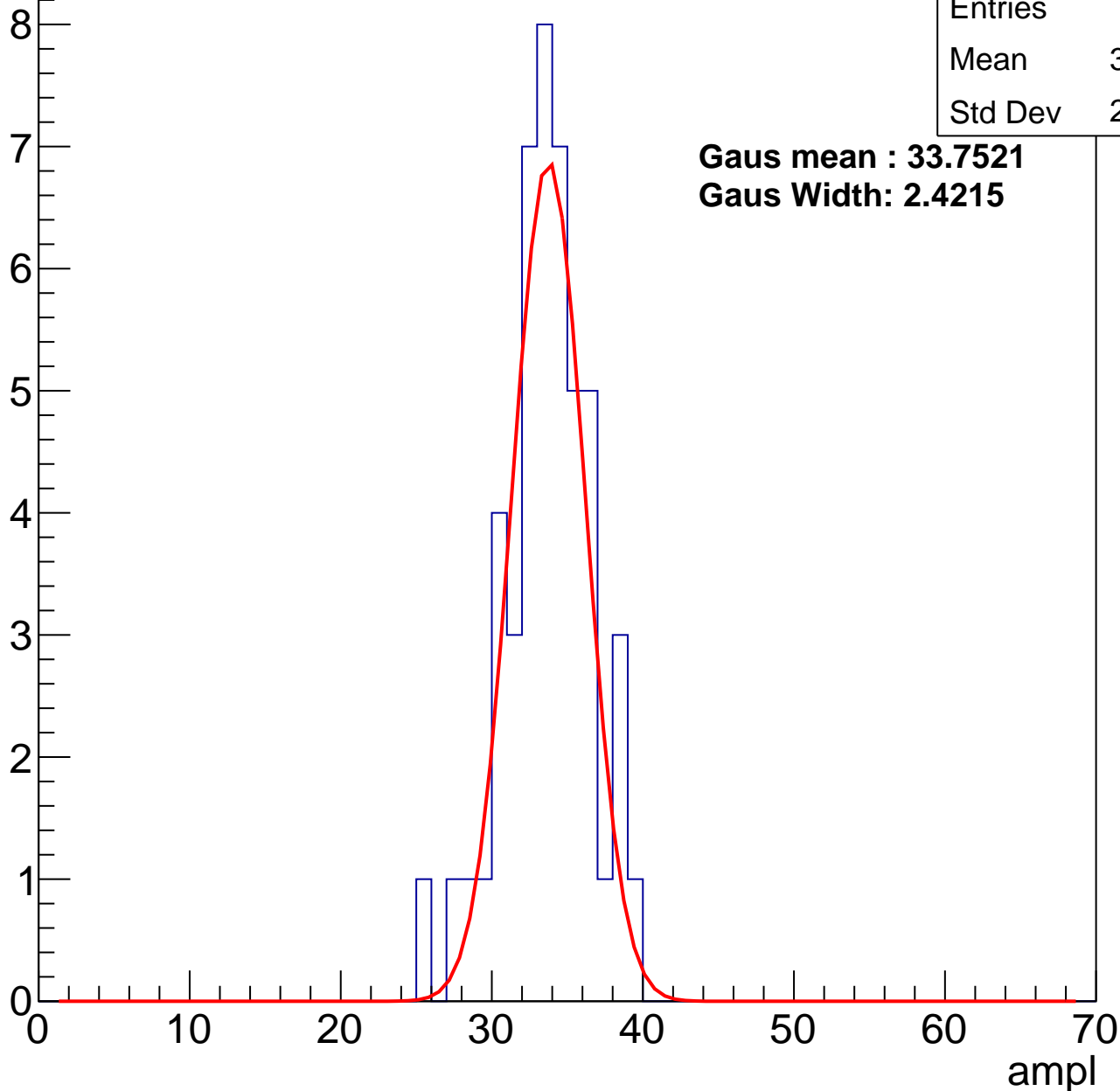
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	33.19
Std Dev	2.863

**Gaus mean : 33.7521**

**Gaus Width: 2.4215**



# B1L002S, U18-ch125, adc2

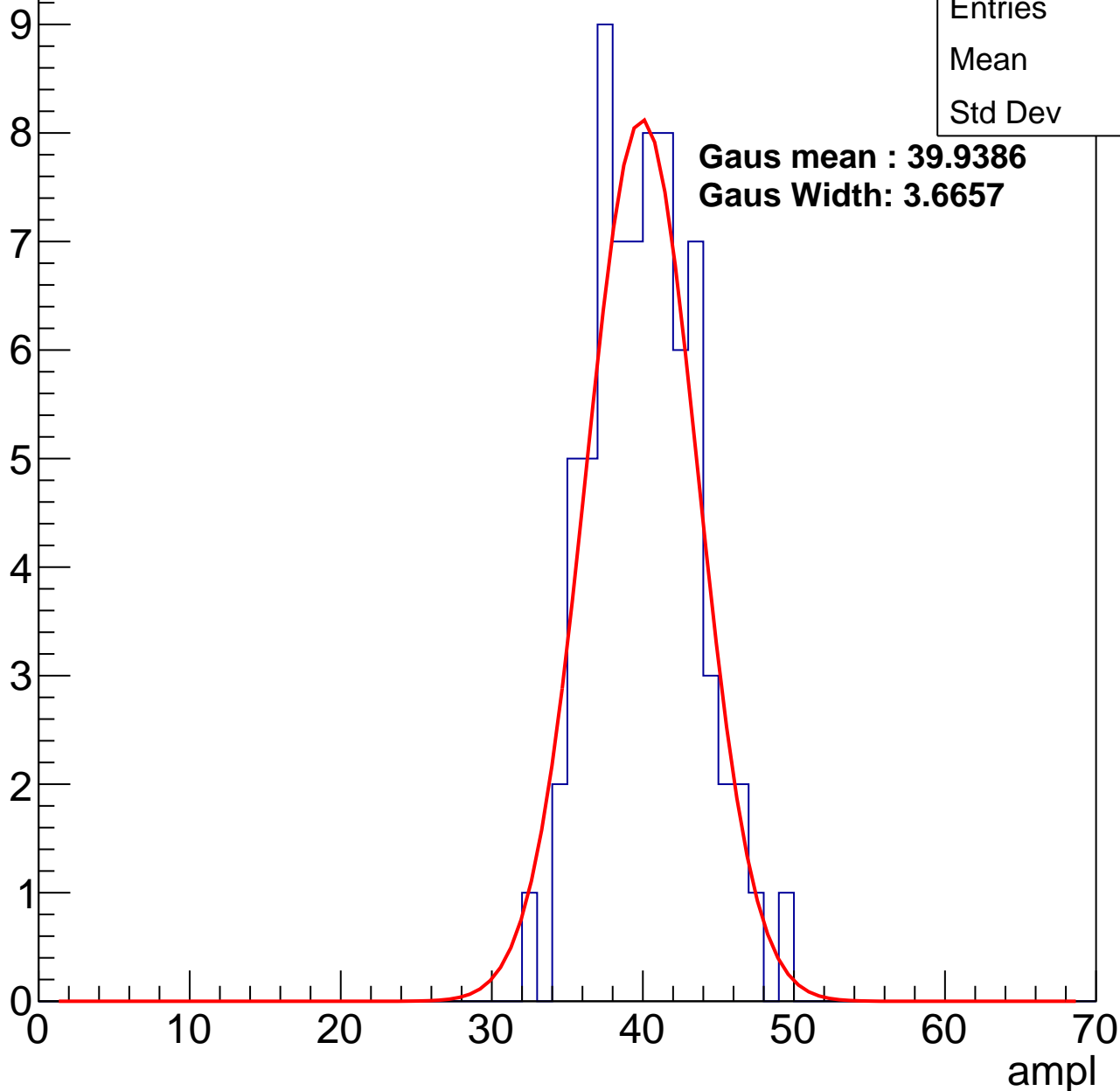
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	39.7
Std Dev	3.38

**Gaus mean : 39.9386**

**Gaus Width: 3.6657**

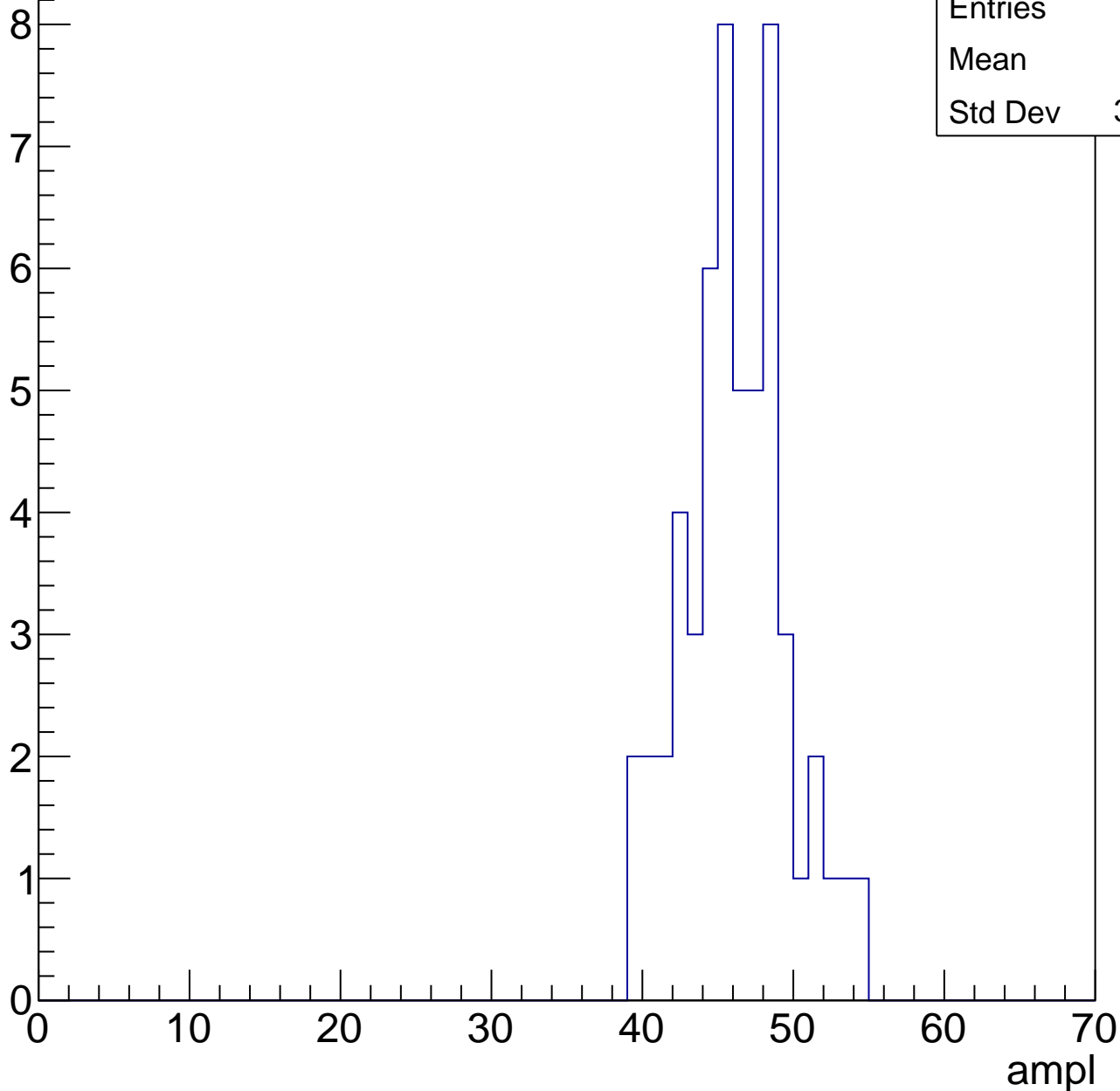


# B1L002S, U18-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	45.7
Std Dev	3.381

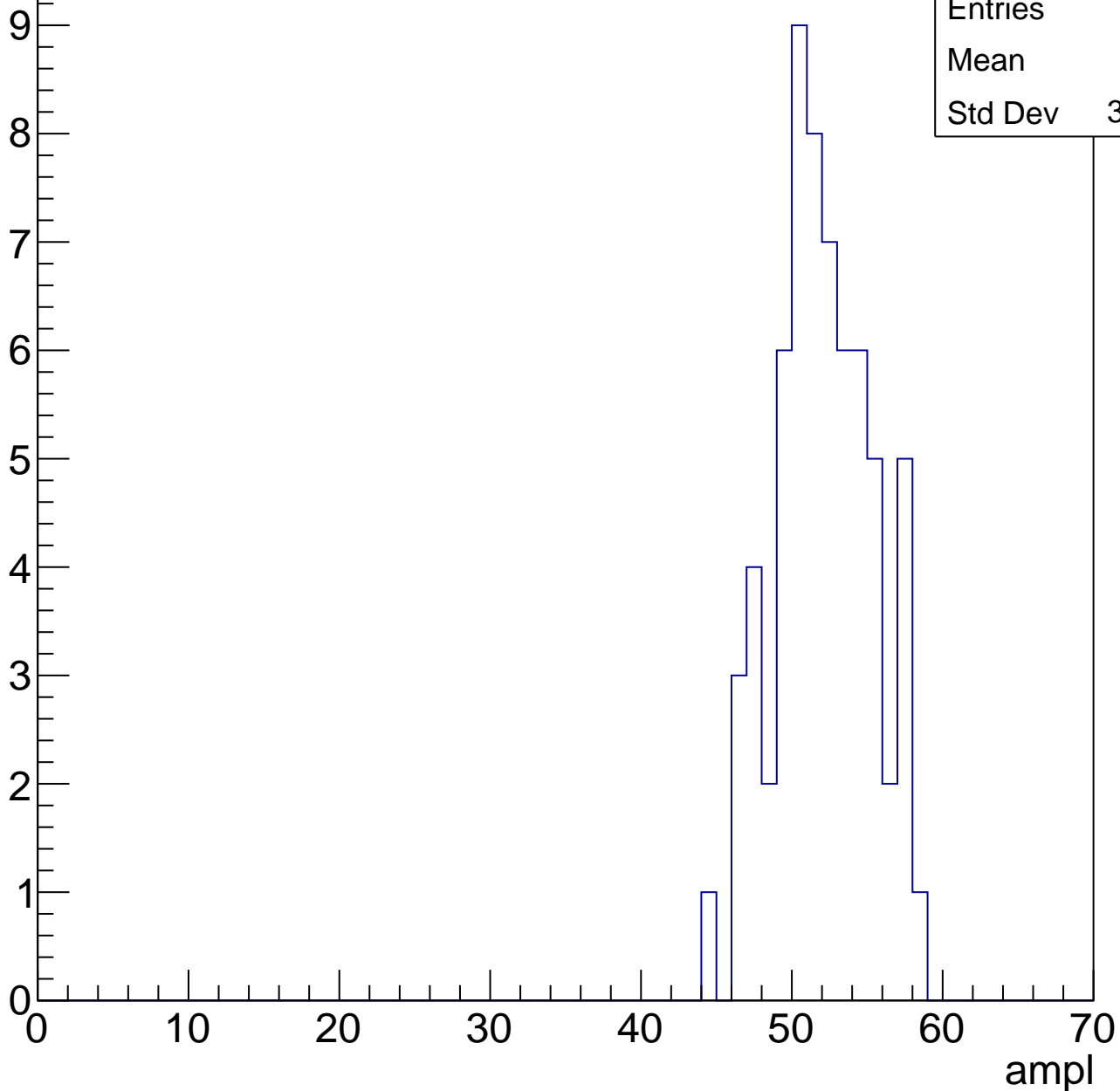


# B1L002S, U18-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	51.6
Std Dev	3.186

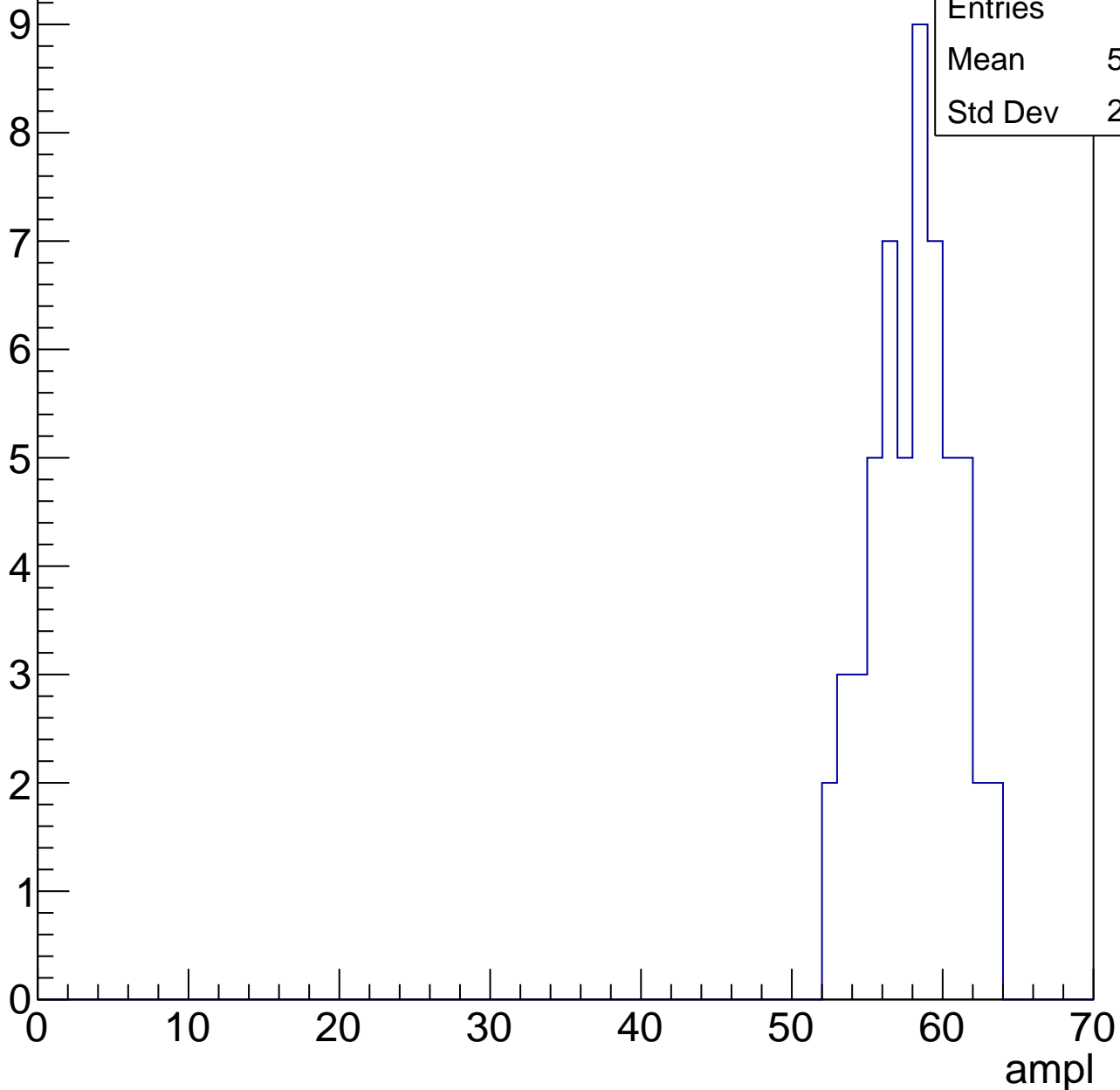


# B1L002S, U18-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.58
Std Dev	2.755

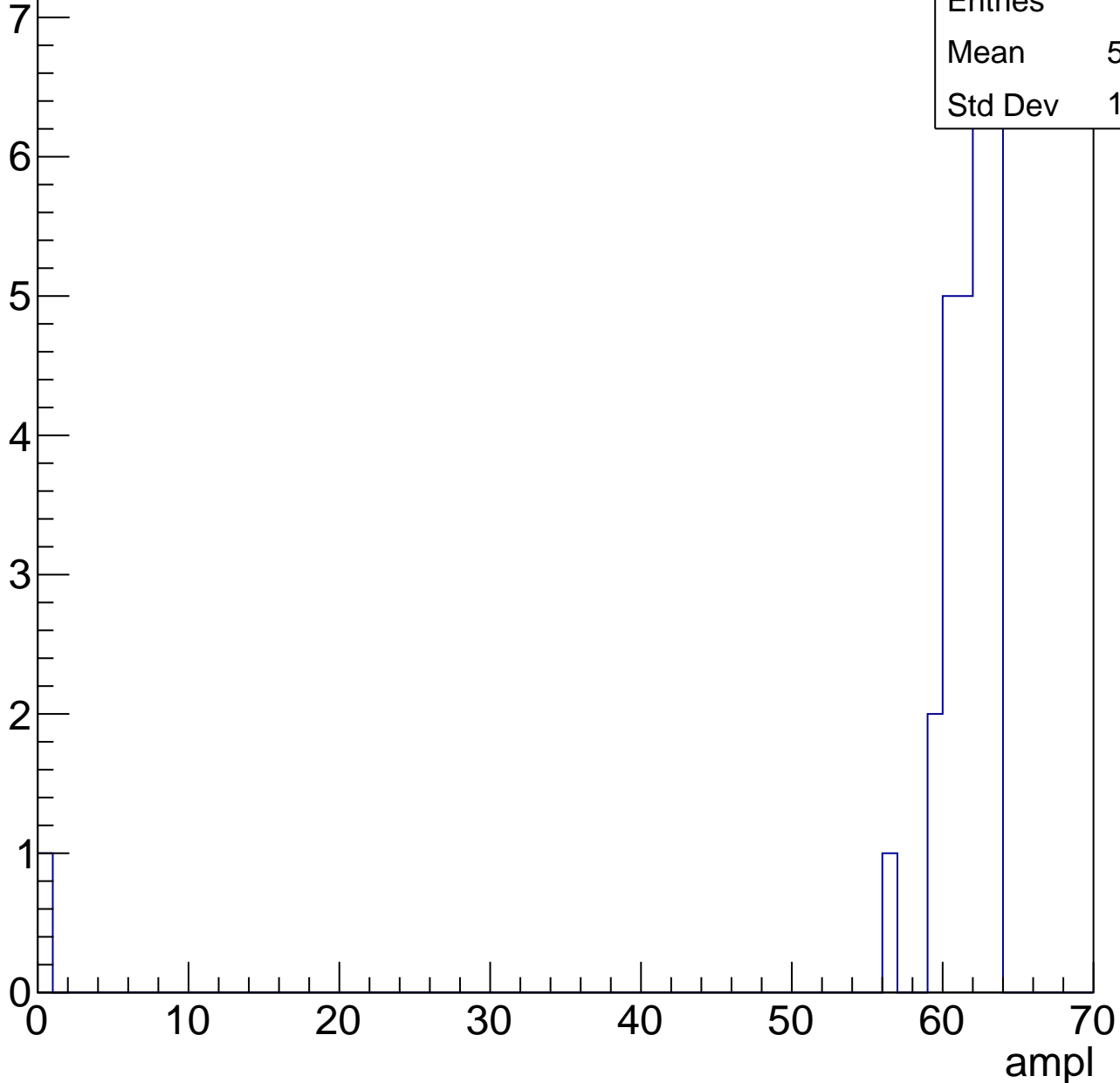


# B1L002S, U18-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59.07
Std Dev	11.48

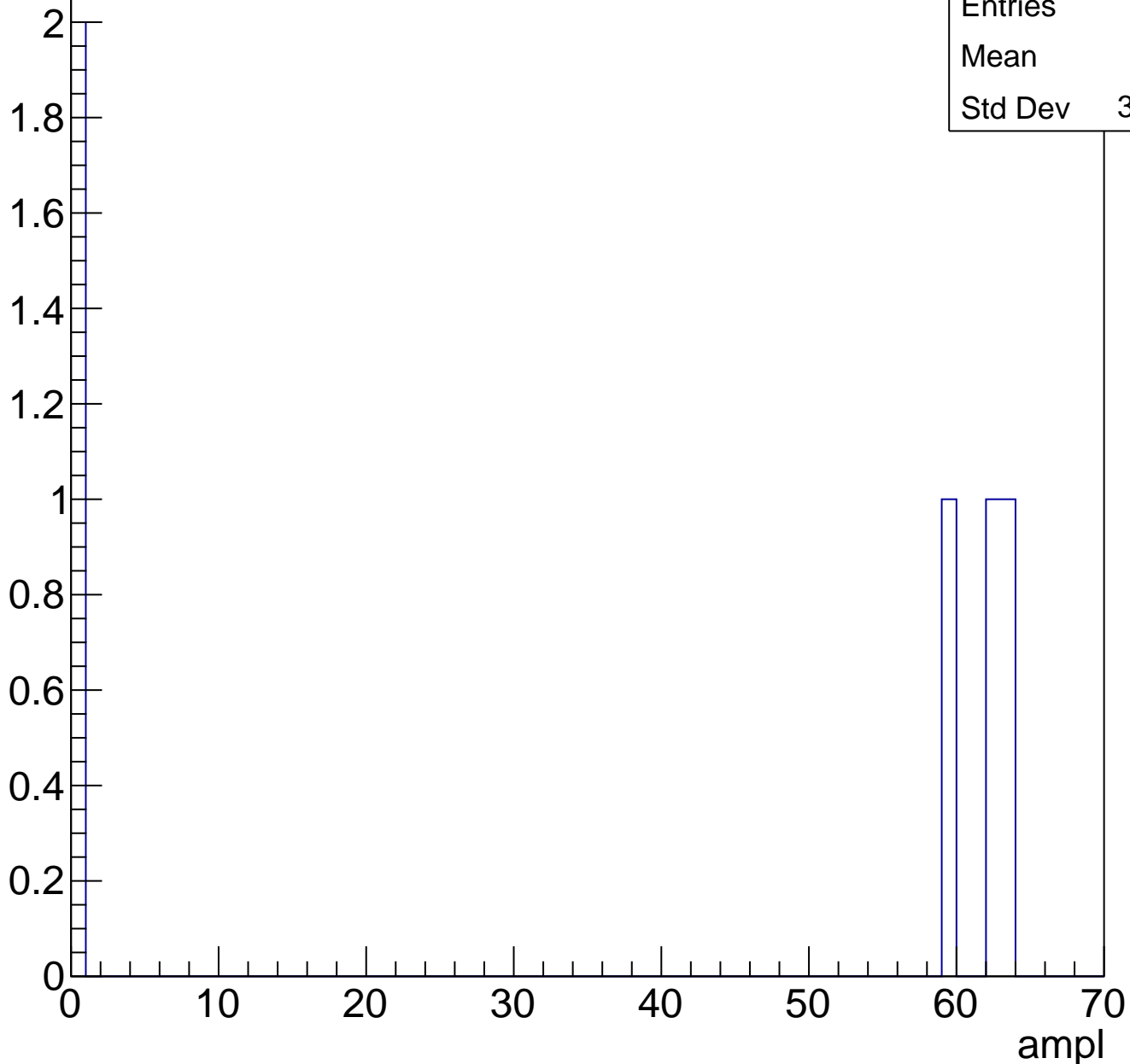




# B1L002S, U18-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch126, adc0

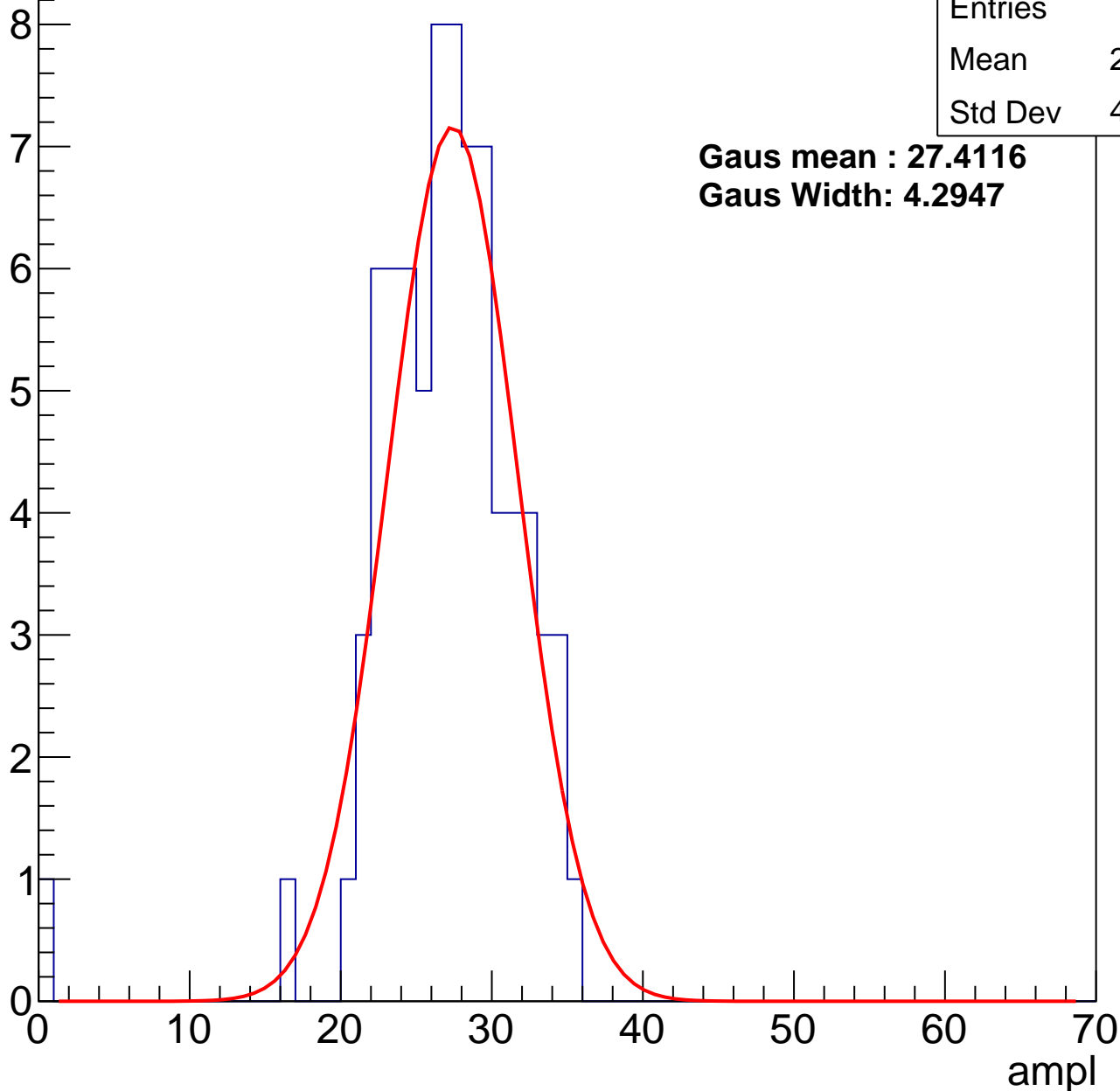
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	26.53
Std Dev	4.898

**Gaus mean : 27.4116**

**Gaus Width: 4.2947**



# B1L002S, U18-ch126, adc1

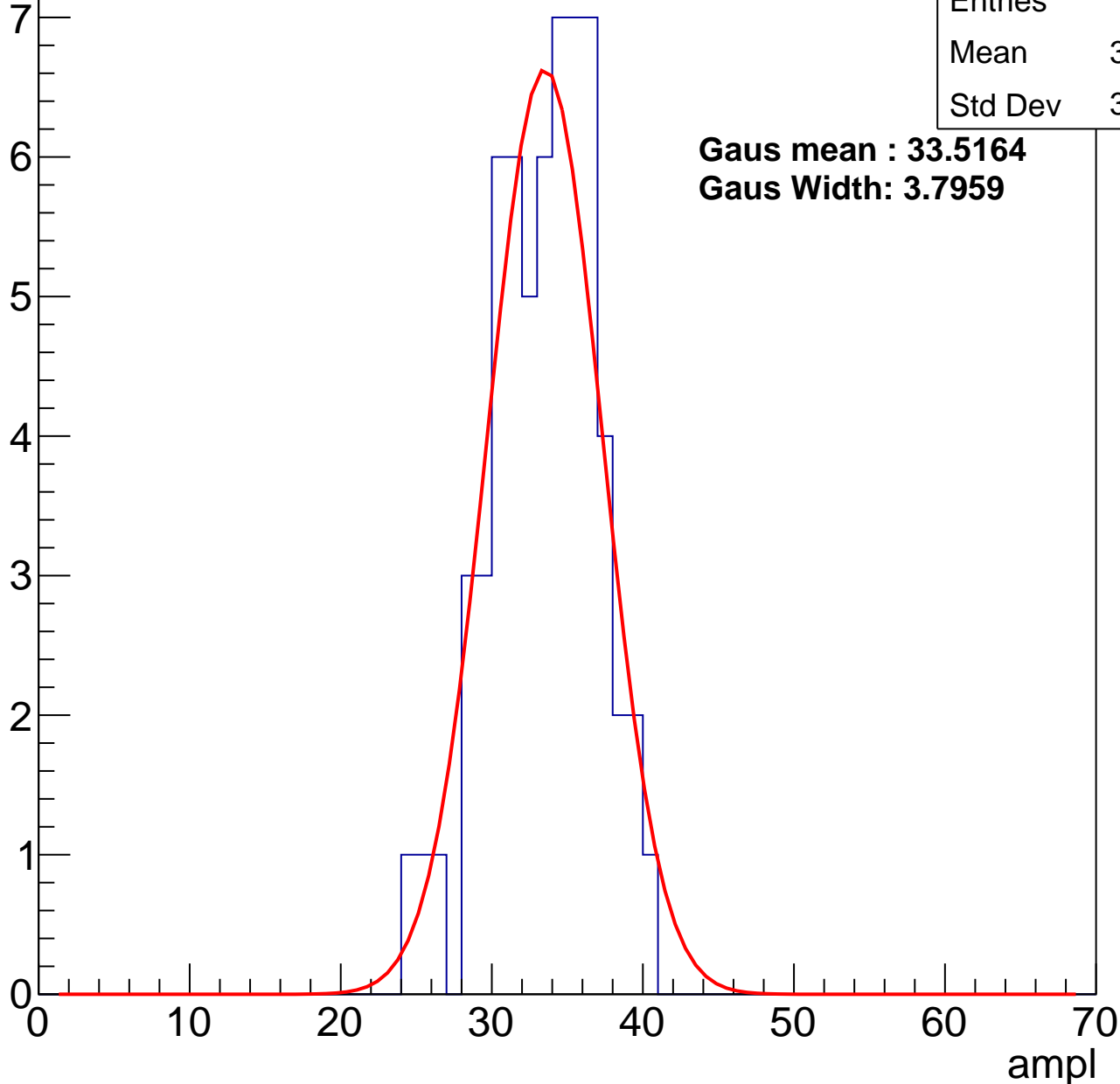
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.02
Std Dev	3.438

**Gaus mean : 33.5164**

**Gaus Width: 3.7959**



# B1L002S, U18-ch126, adc2

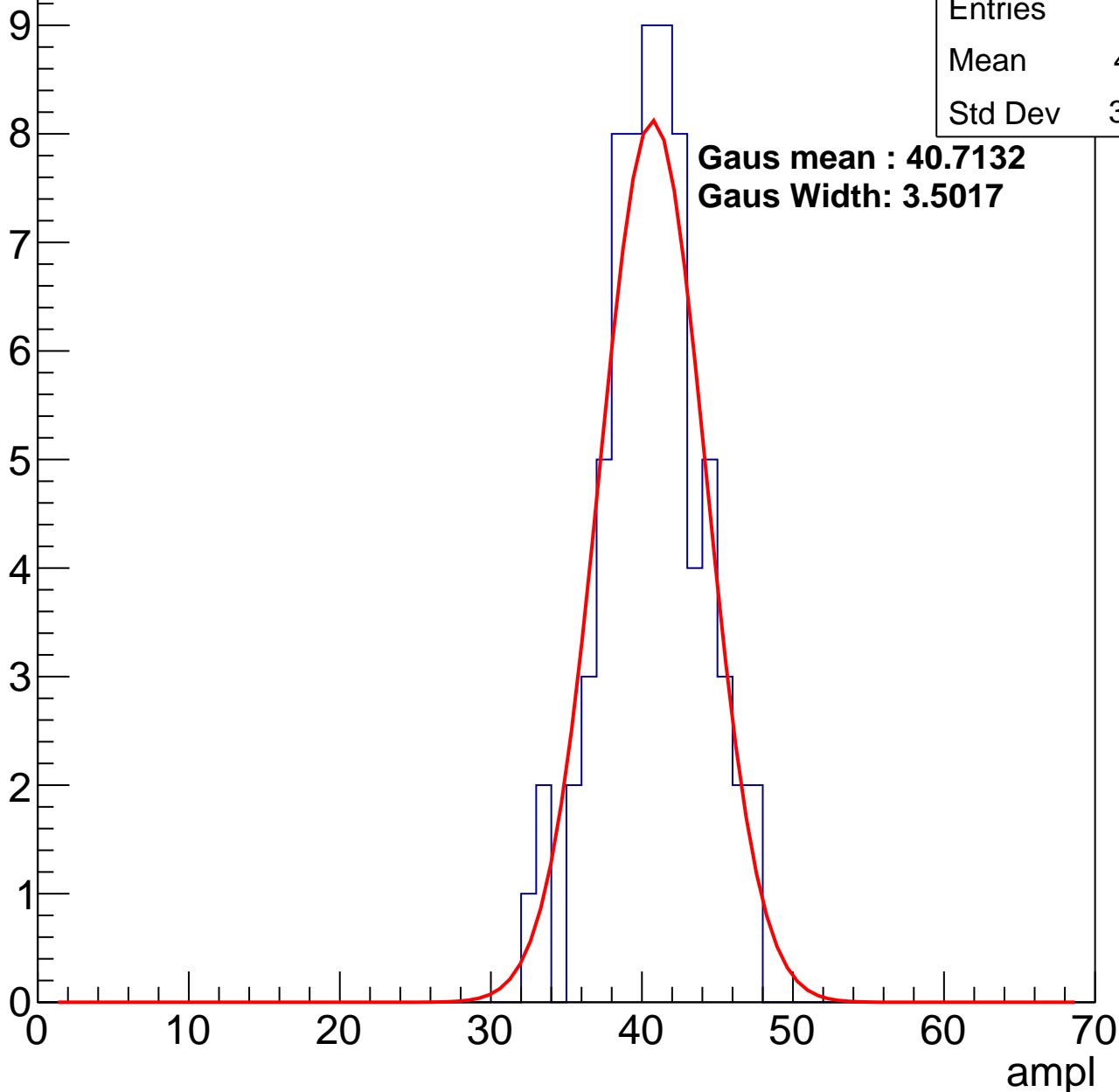
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.21
Std Dev	3.237

**Gaus mean : 40.7132**

**Gaus Width: 3.5017**

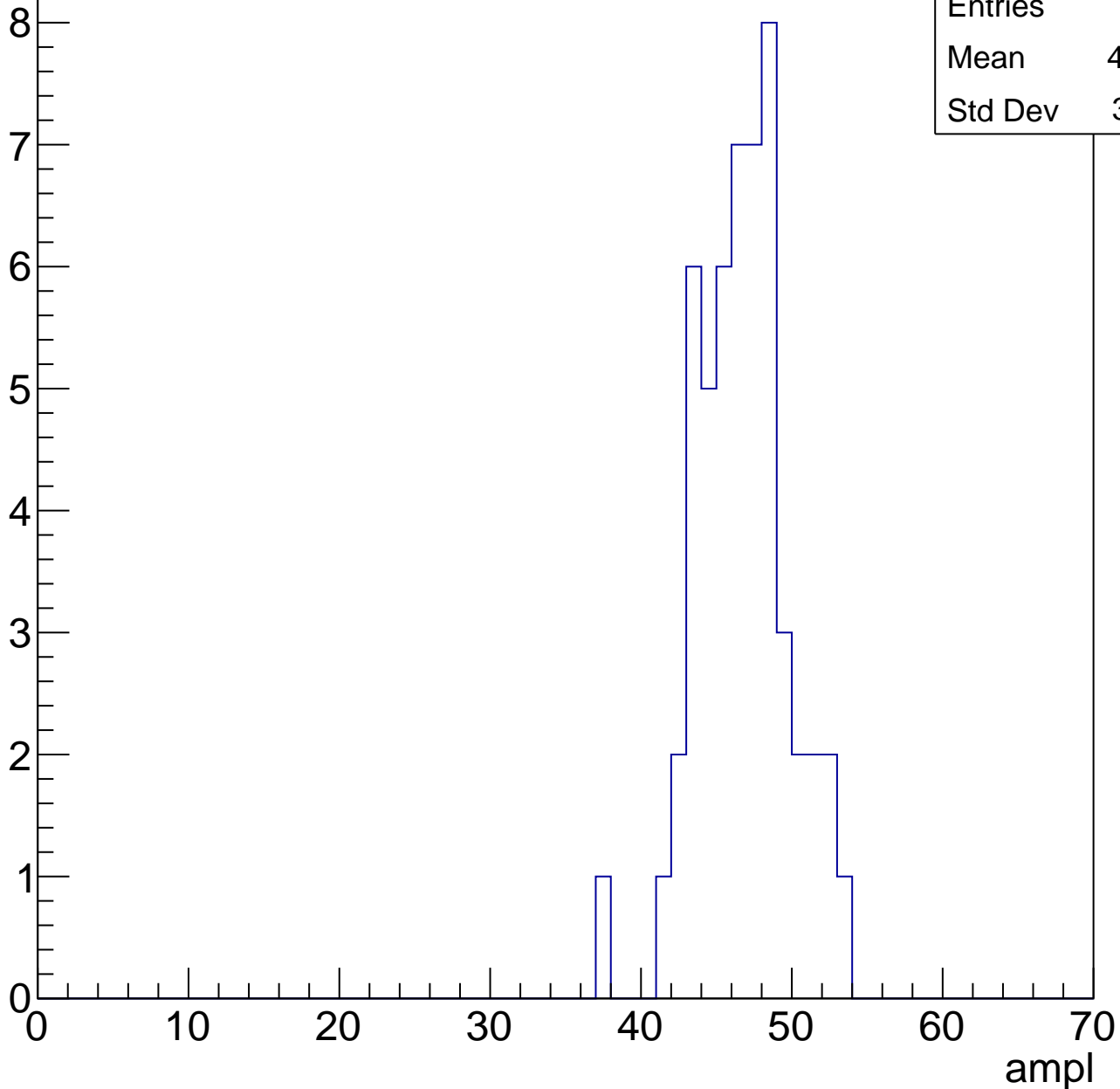


# B1L002S, U18-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.25
Std Dev	3.021

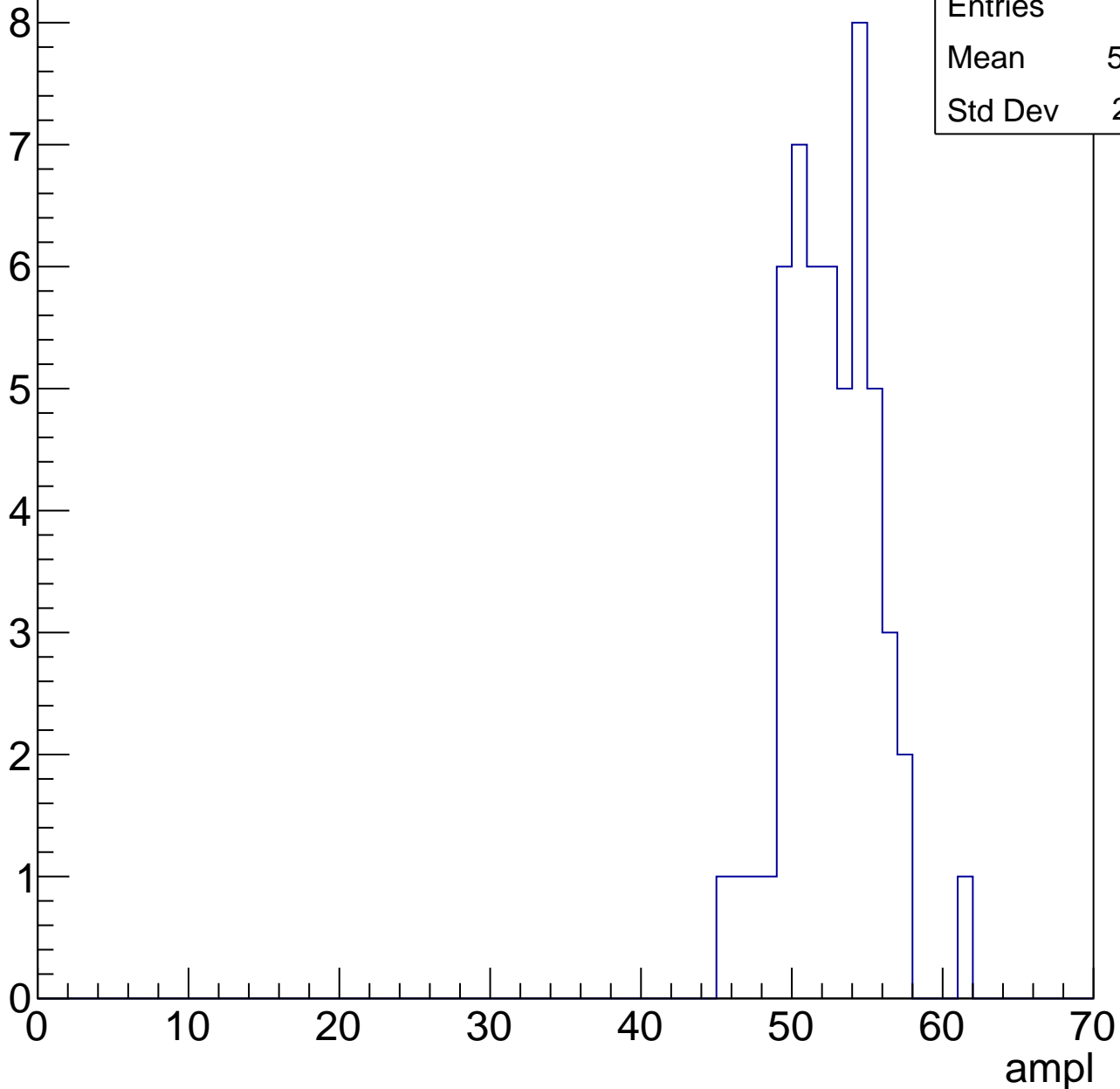


# B1L002S, U18-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.13
Std Dev	2.991

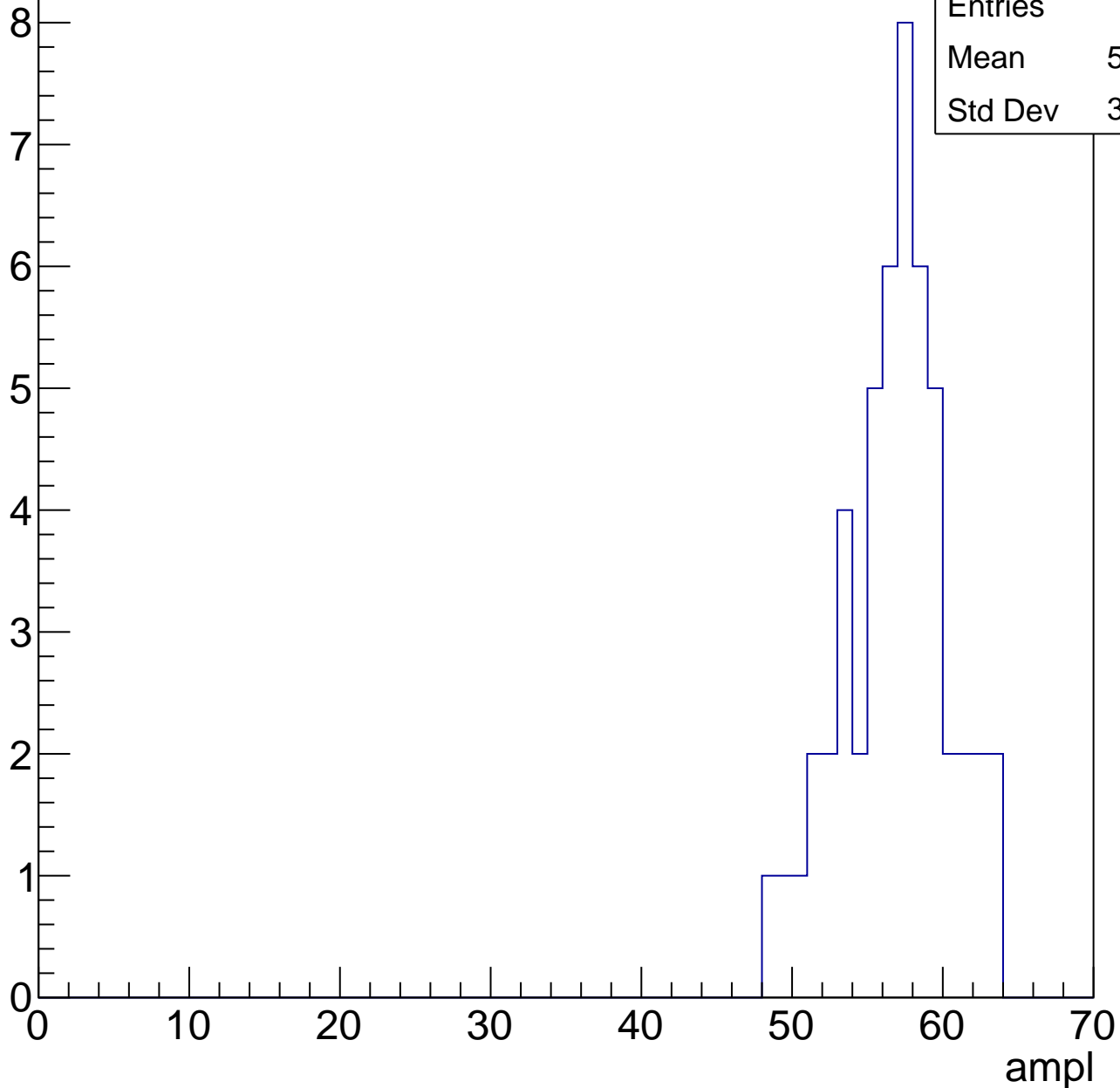


# B1L002S, U18-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	56.37
Std Dev	3.435



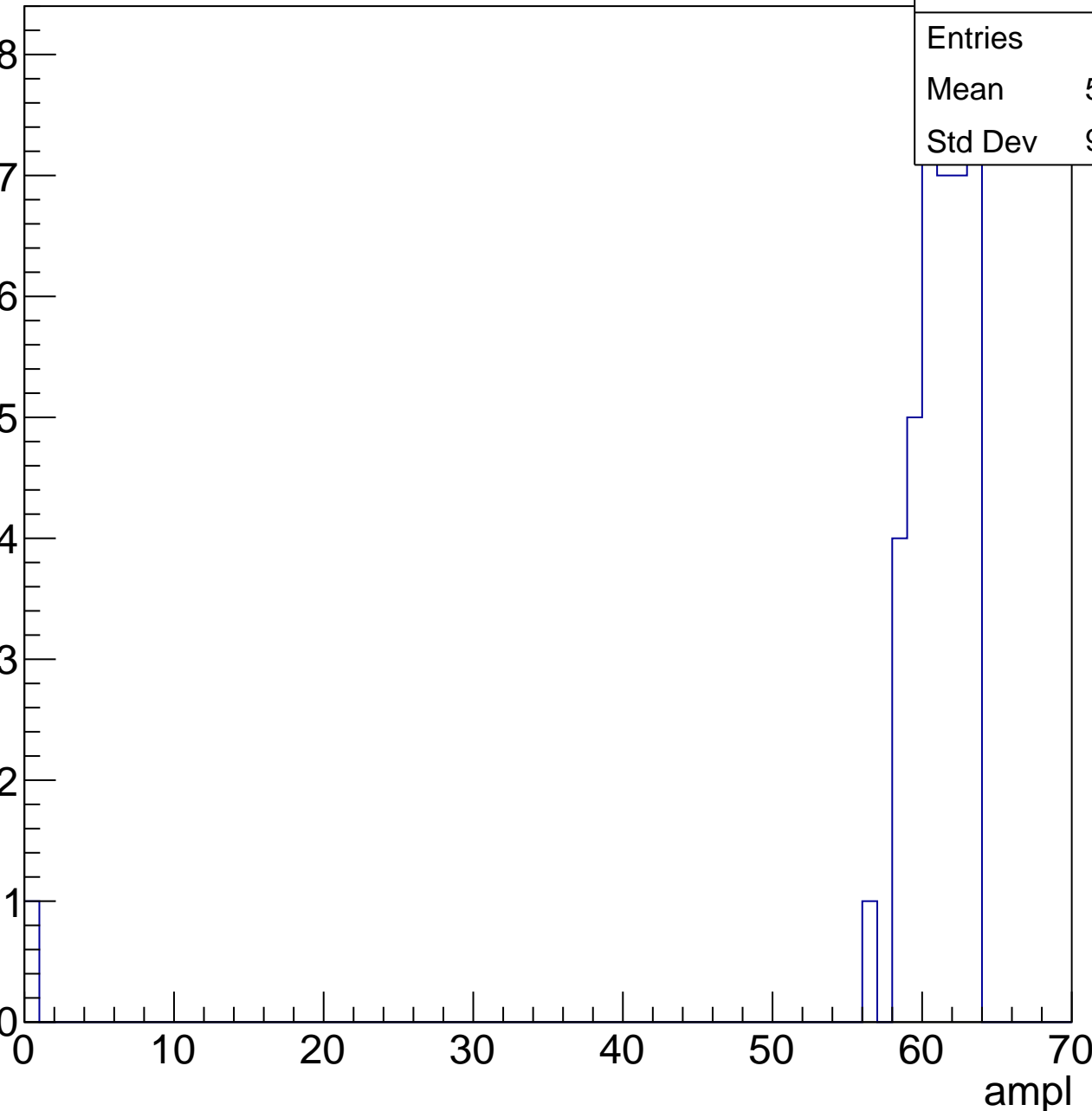
# B1L002S, U18-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	59.22
Std Dev	9.524

8  
7  
6  
5  
4  
3  
2  
1  
0





# B1L002S, U18-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch127, adc0

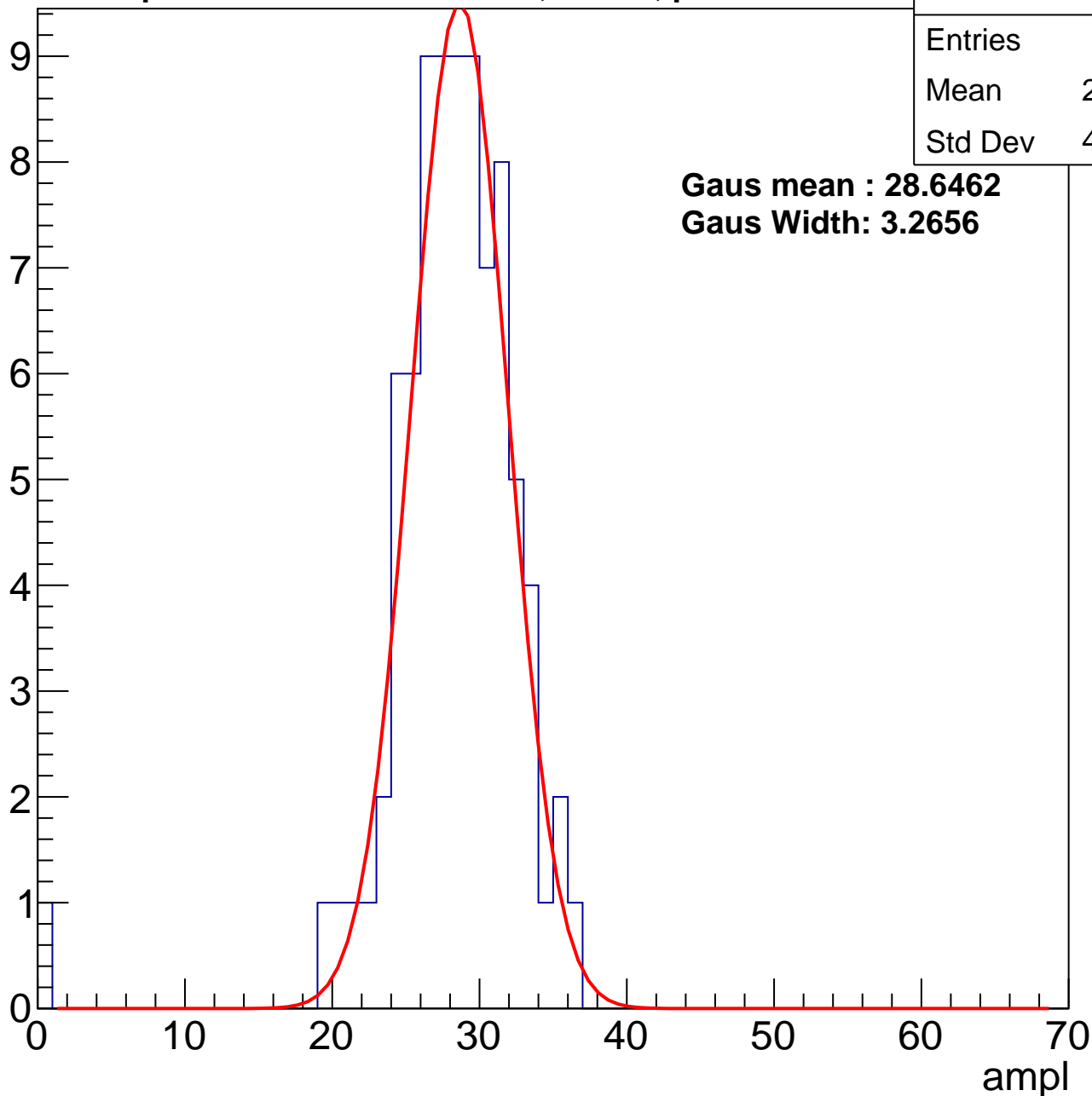
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	27.73
Std Dev	4.579

**Gaus mean : 28.6462**

**Gaus Width: 3.2656**



# B1L002S, U18-ch127, adc1

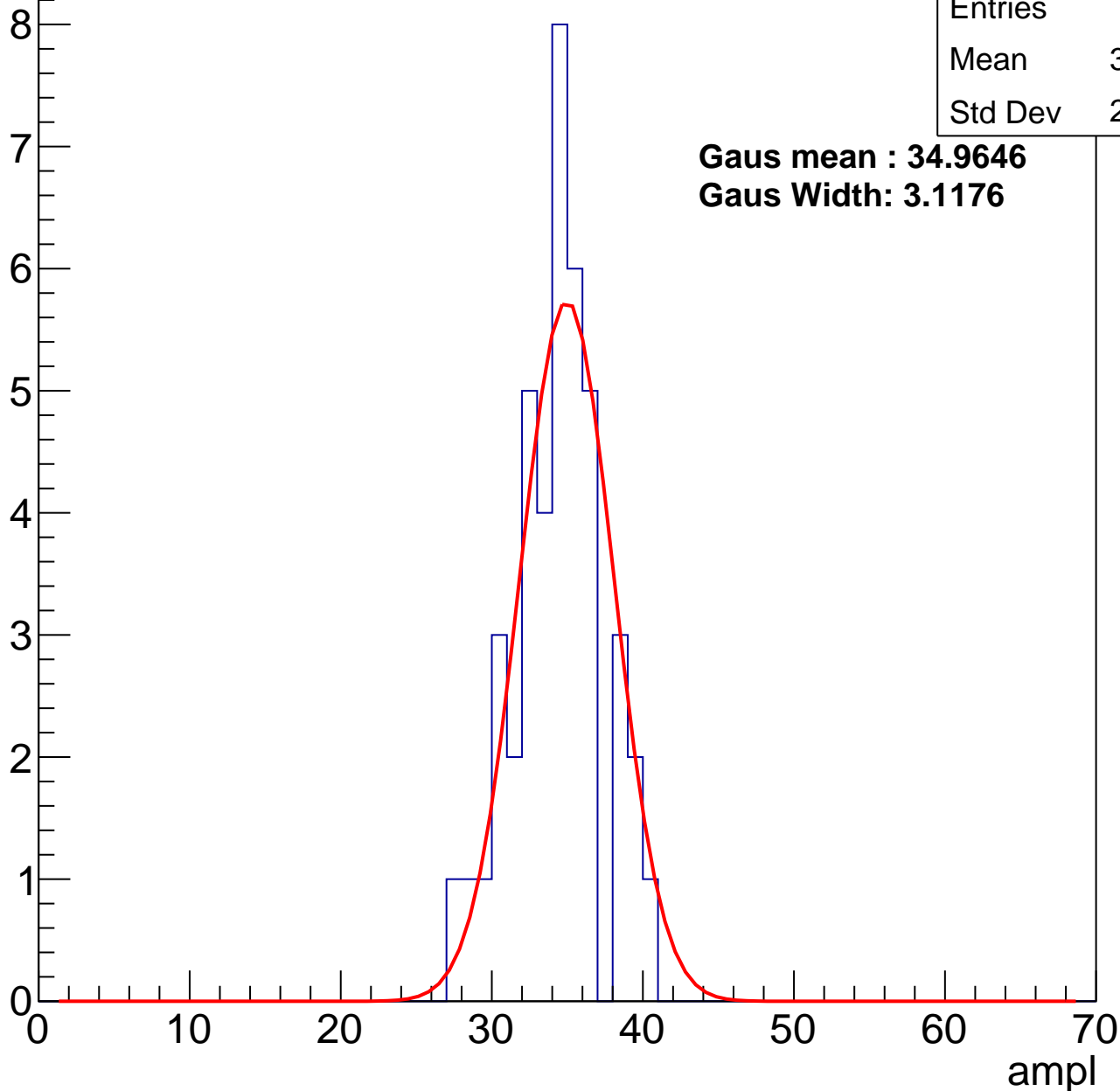
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	33.86
Std Dev	2.924

**Gaus mean : 34.9646**

**Gaus Width: 3.1176**



# B1L002S, U18-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	39.97
Std Dev	3.379

**Gaus mean : 40.4648**

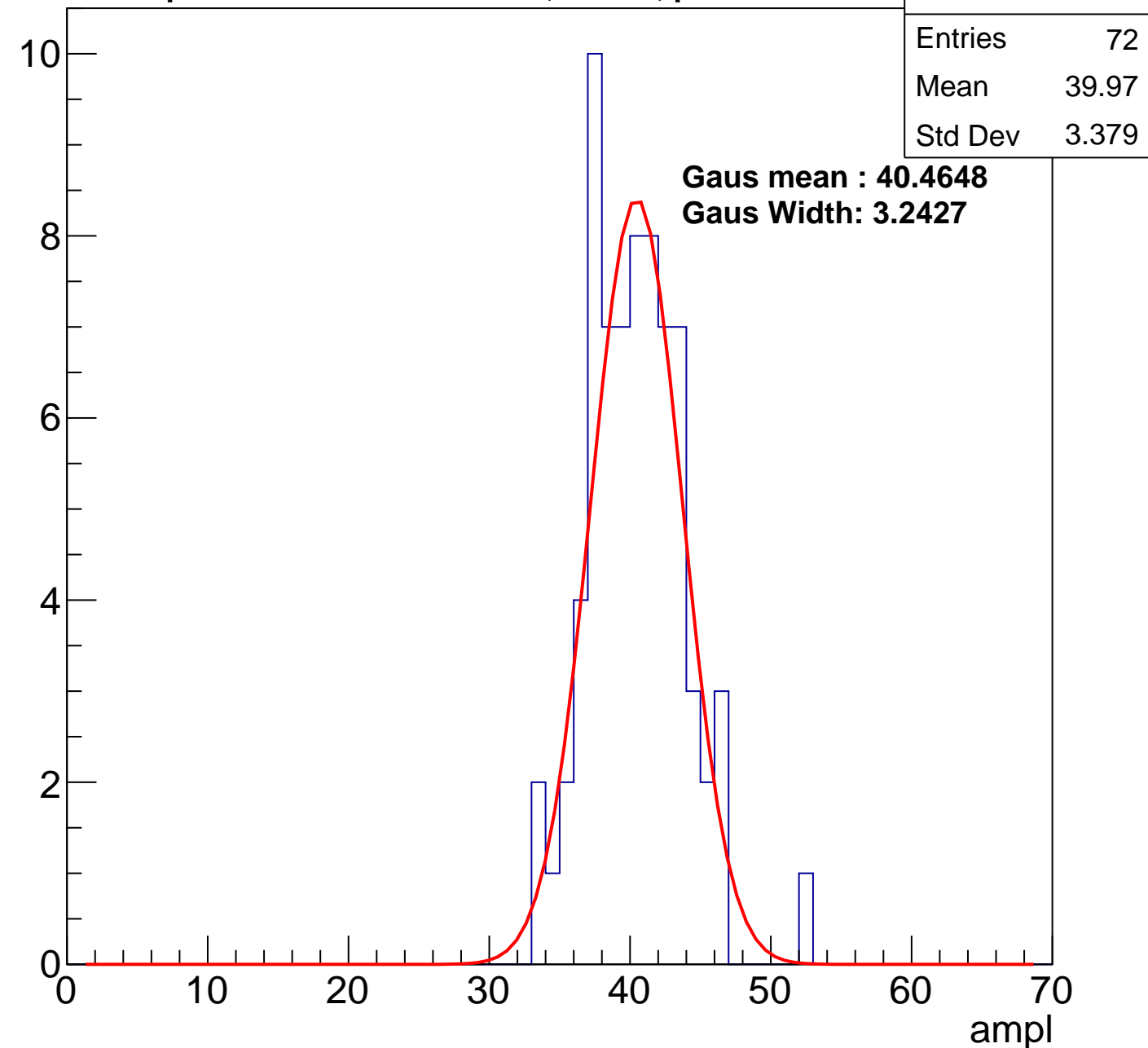
**Gaus Width: 3.2427**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

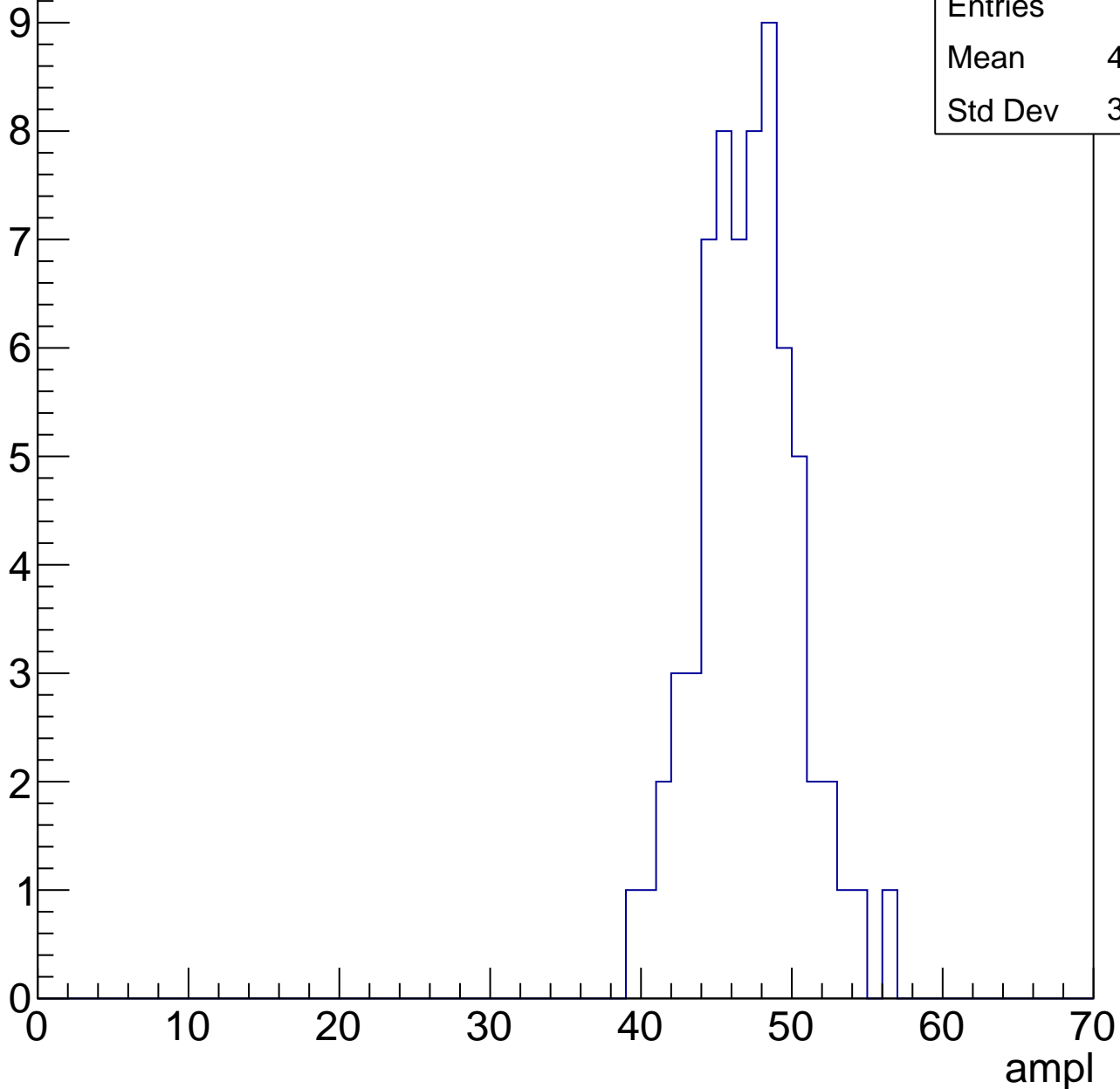


# B1L002S, U18-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	46.67
Std Dev	3.298

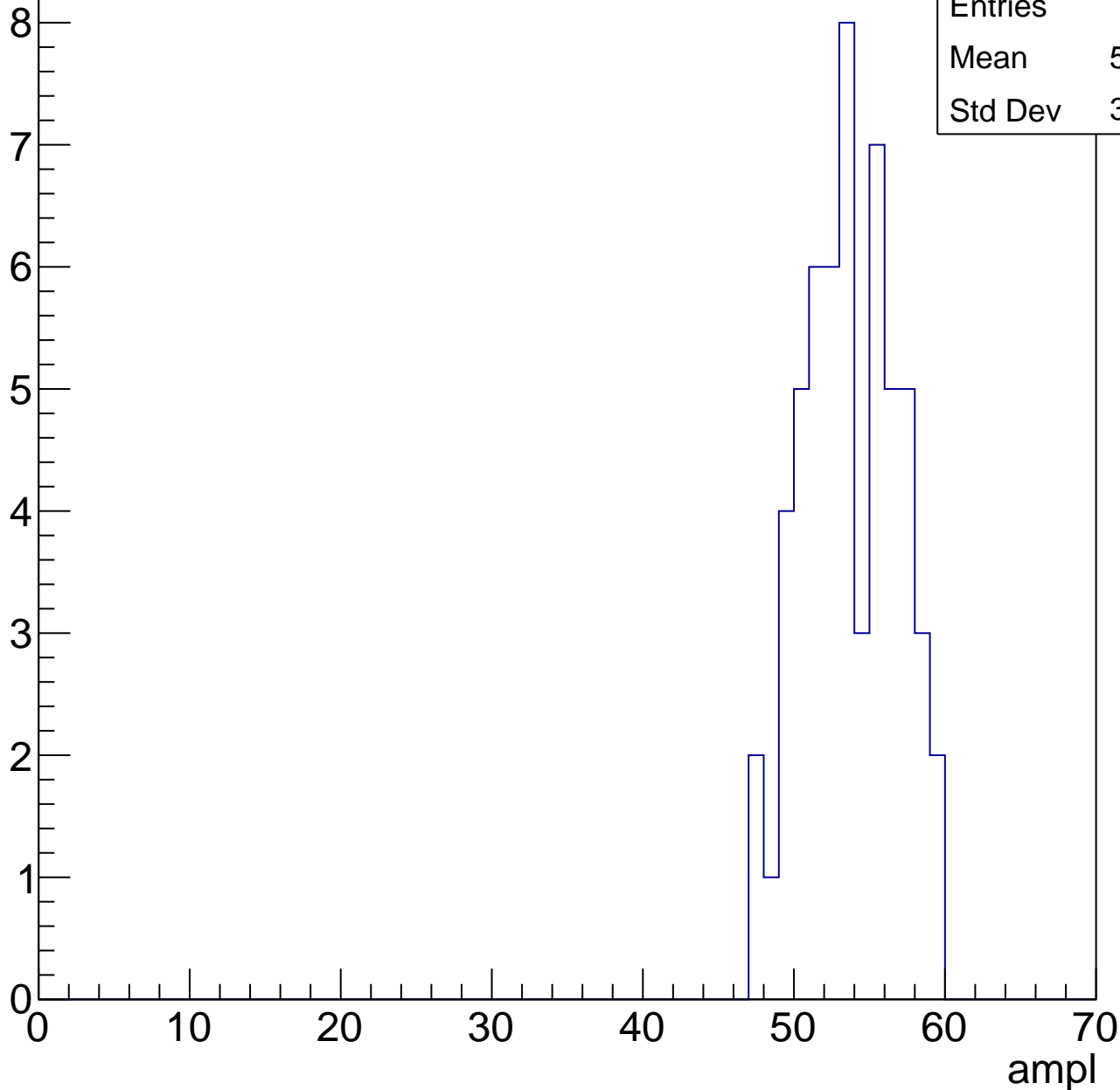


# B1L002S, U18-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.23
Std Dev	3.067

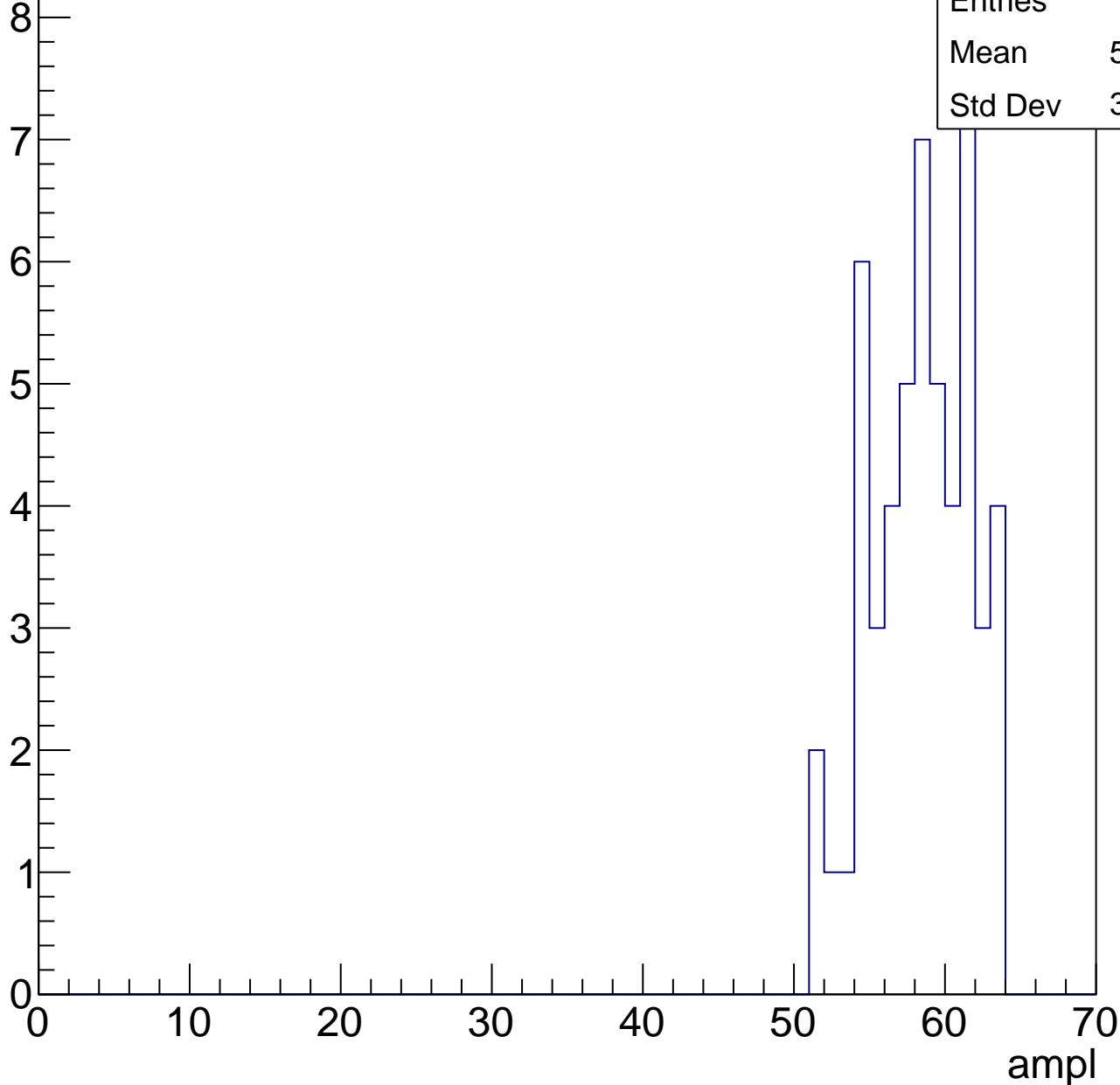


# B1L002S, U18-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.96
Std Dev	3.204

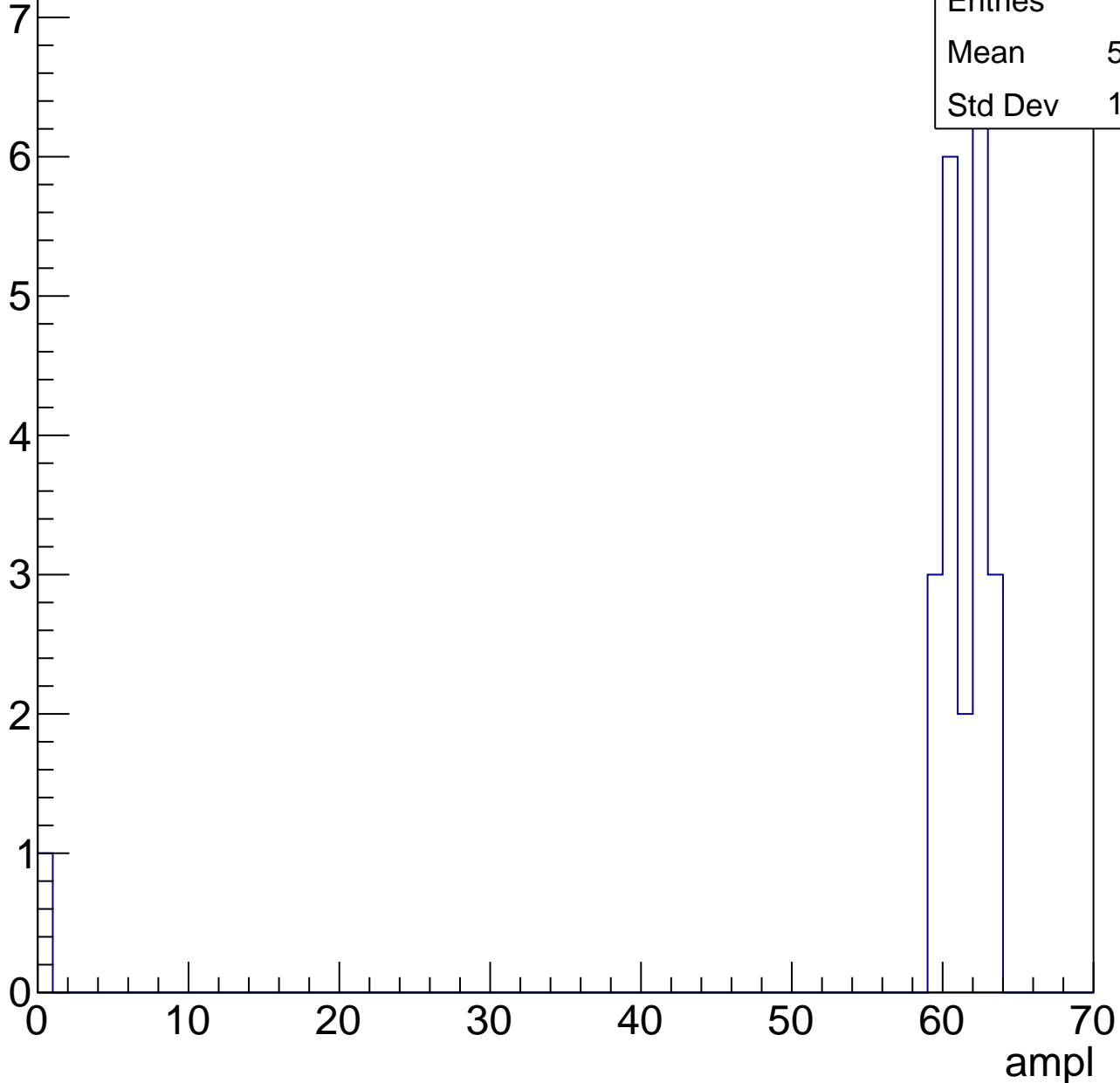


# B1L002S, U18-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.27
Std Dev	12.78

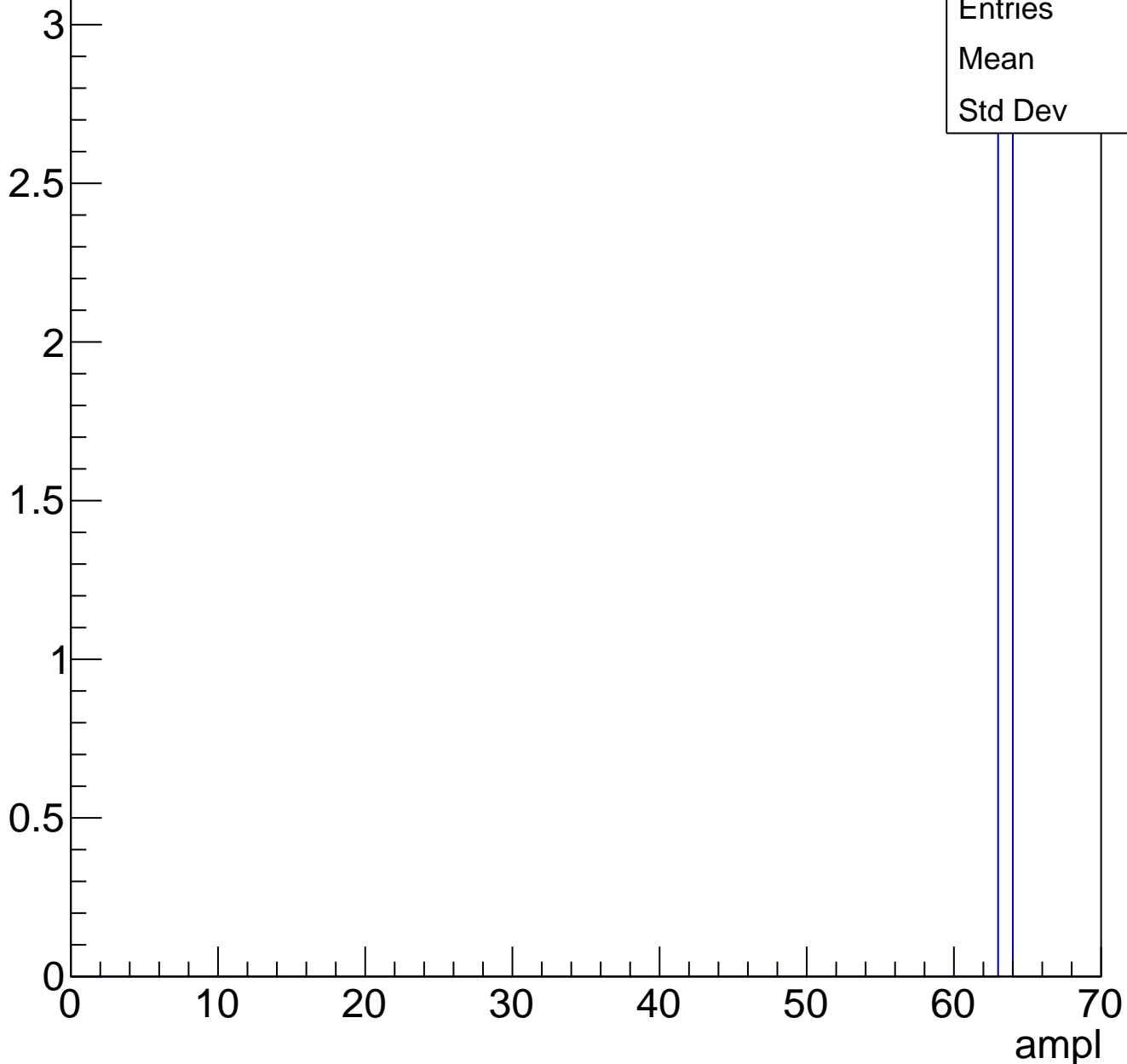




# B1L002S, U18-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U18-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

