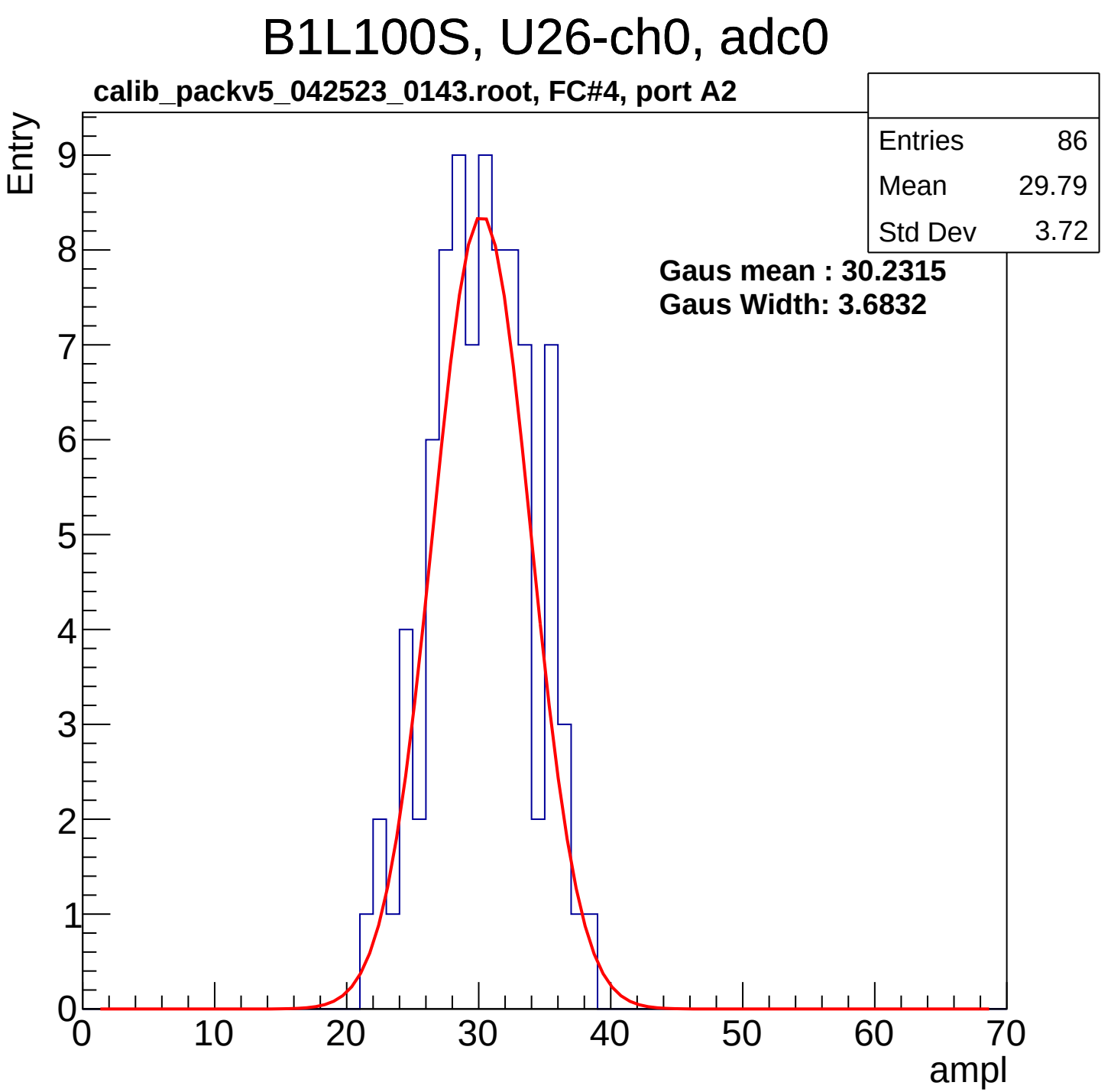
B1L100S, U26-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 29.79
Std Dev 3.72
Gaus mean : 30.2315
Gaus Width: 3.6832
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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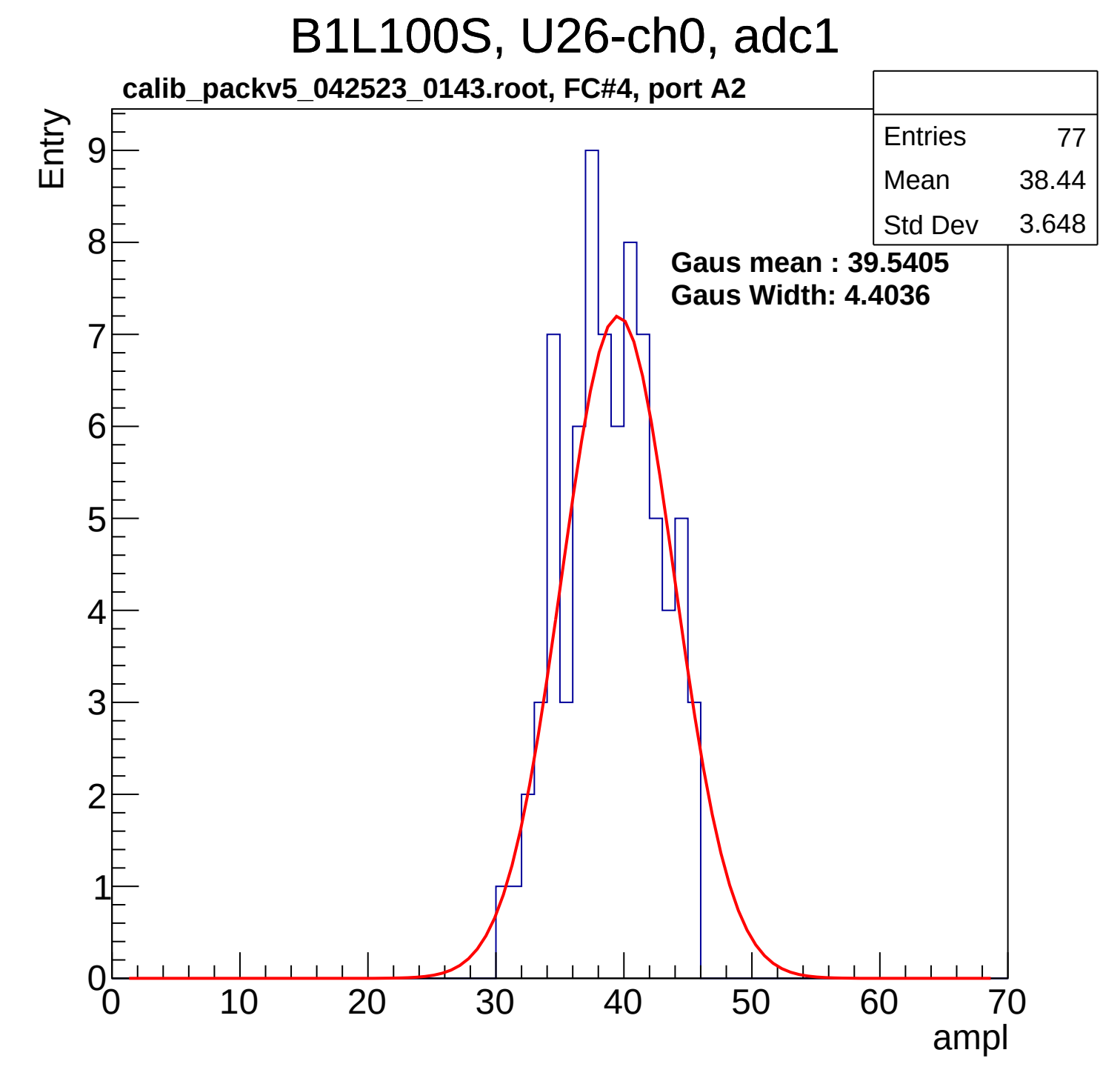
B1L100S, U26-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 38.44
Std Dev 3.648
Gaus mean : 39.5405
Gaus Width: 4.4036
Entry
ampl
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2
3
4
5
6
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8
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0
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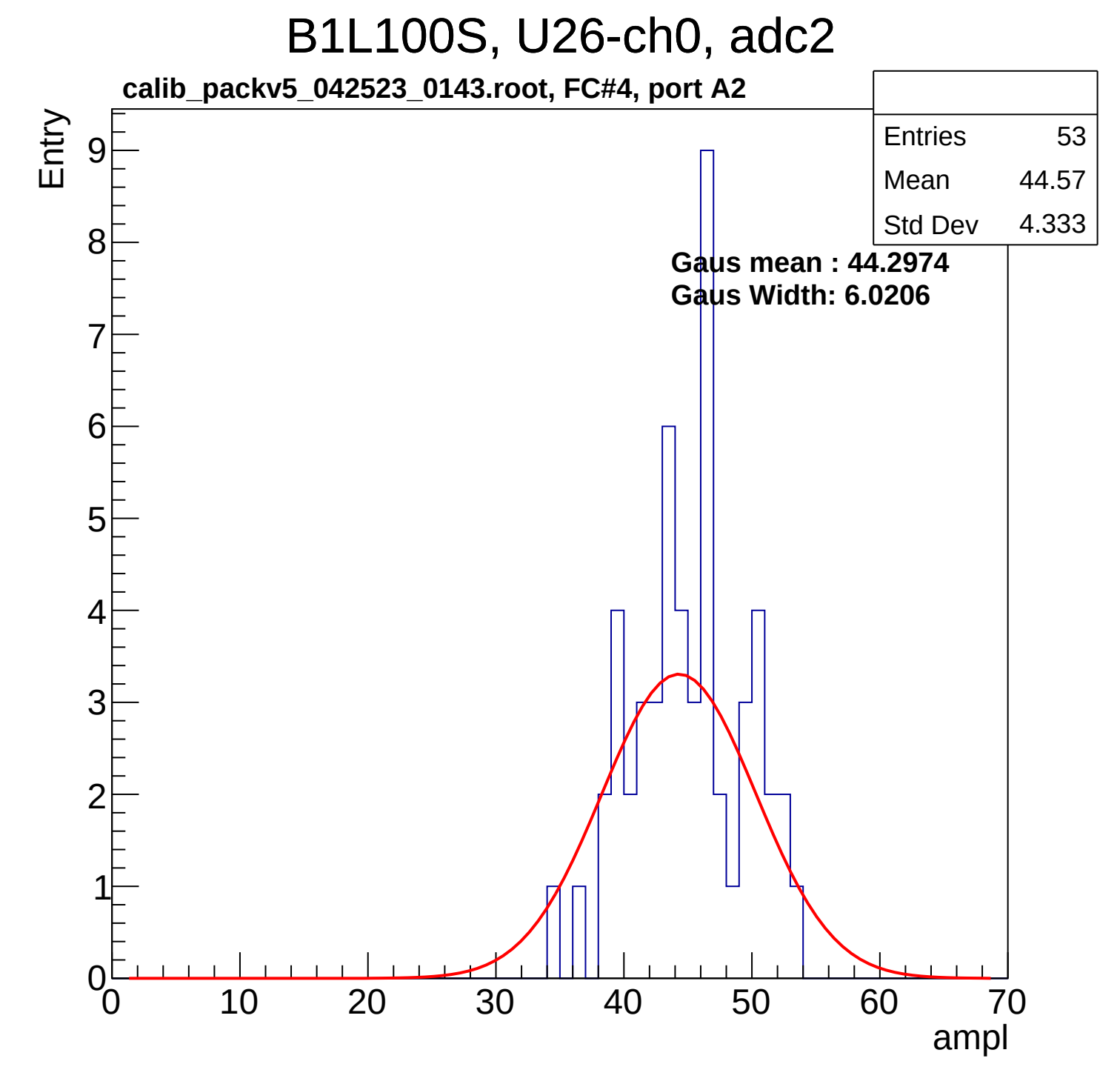
B1L100S, U26-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 44.57
Std Dev 4.333
Gaus mean : 44.2974
Gaus Width: 6.0206
Entry
ampl
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2
3
4
5
6
7
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0
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60
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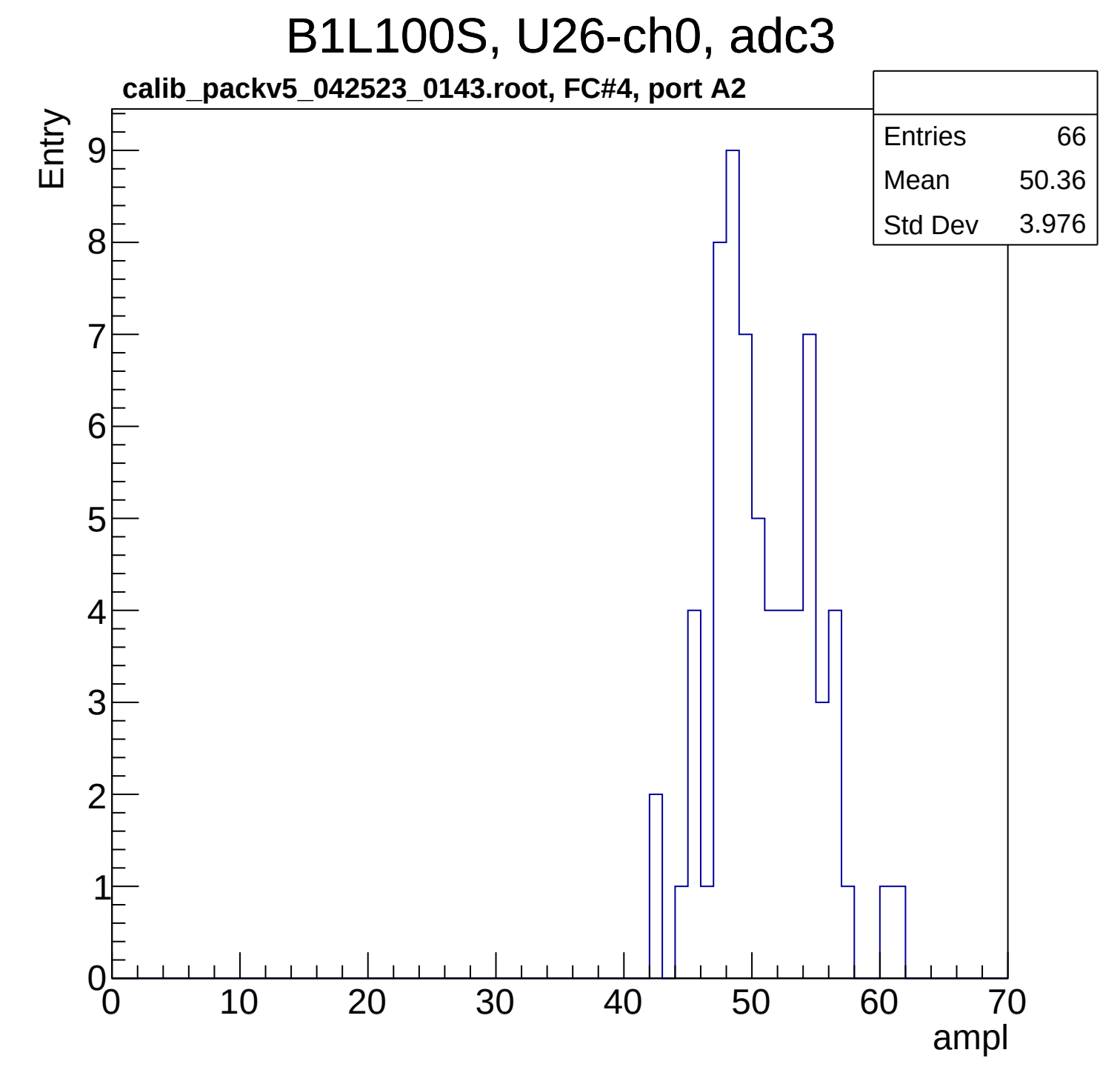
B1L100S, U26-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.36
Std Dev 3.976
Entry
ampl
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4
5
6
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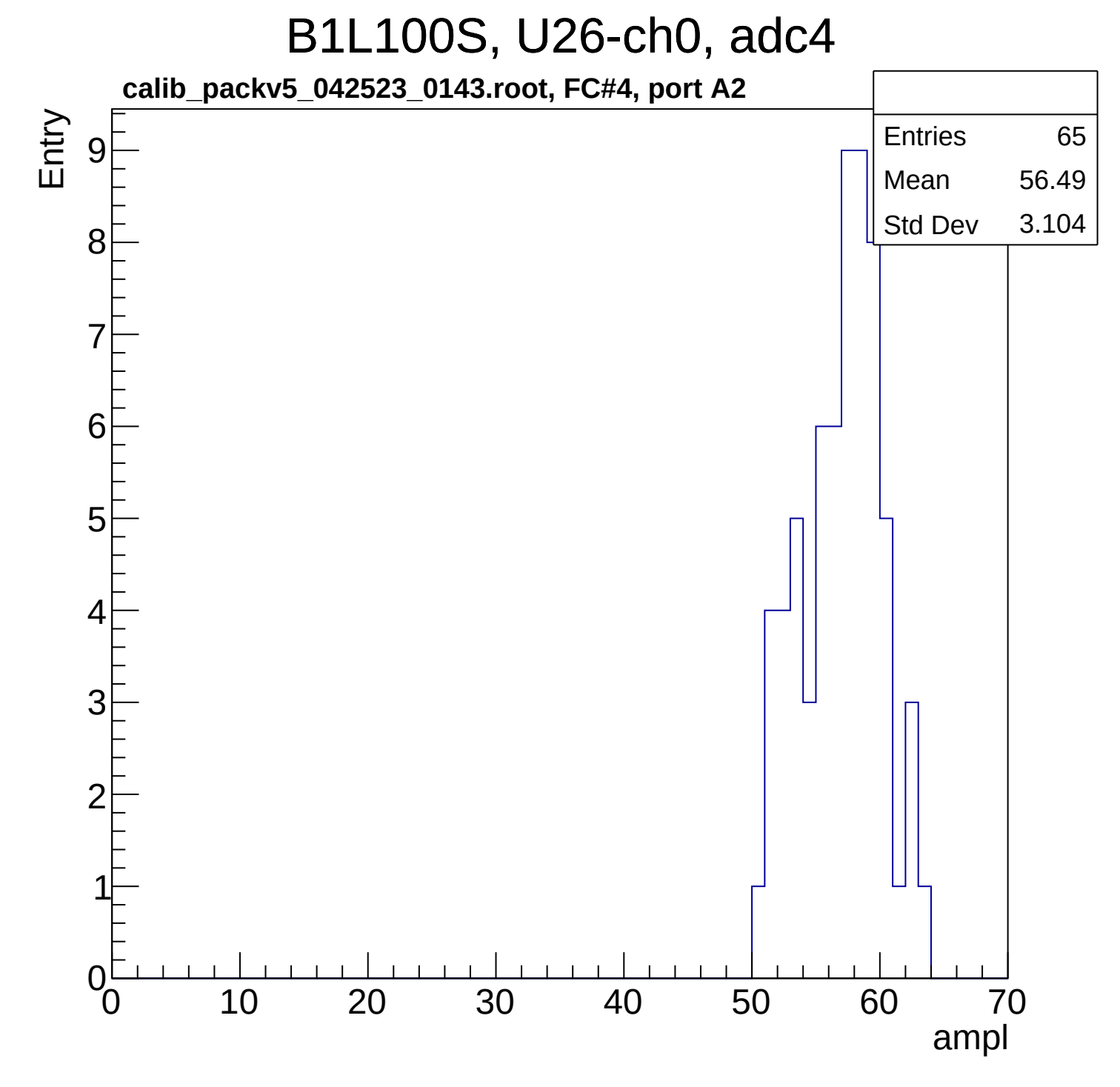
B1L100S, U26-ch0, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.49
Std Dev 3.104
Entry
ampl
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2
3
4
5
6
7
8
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0
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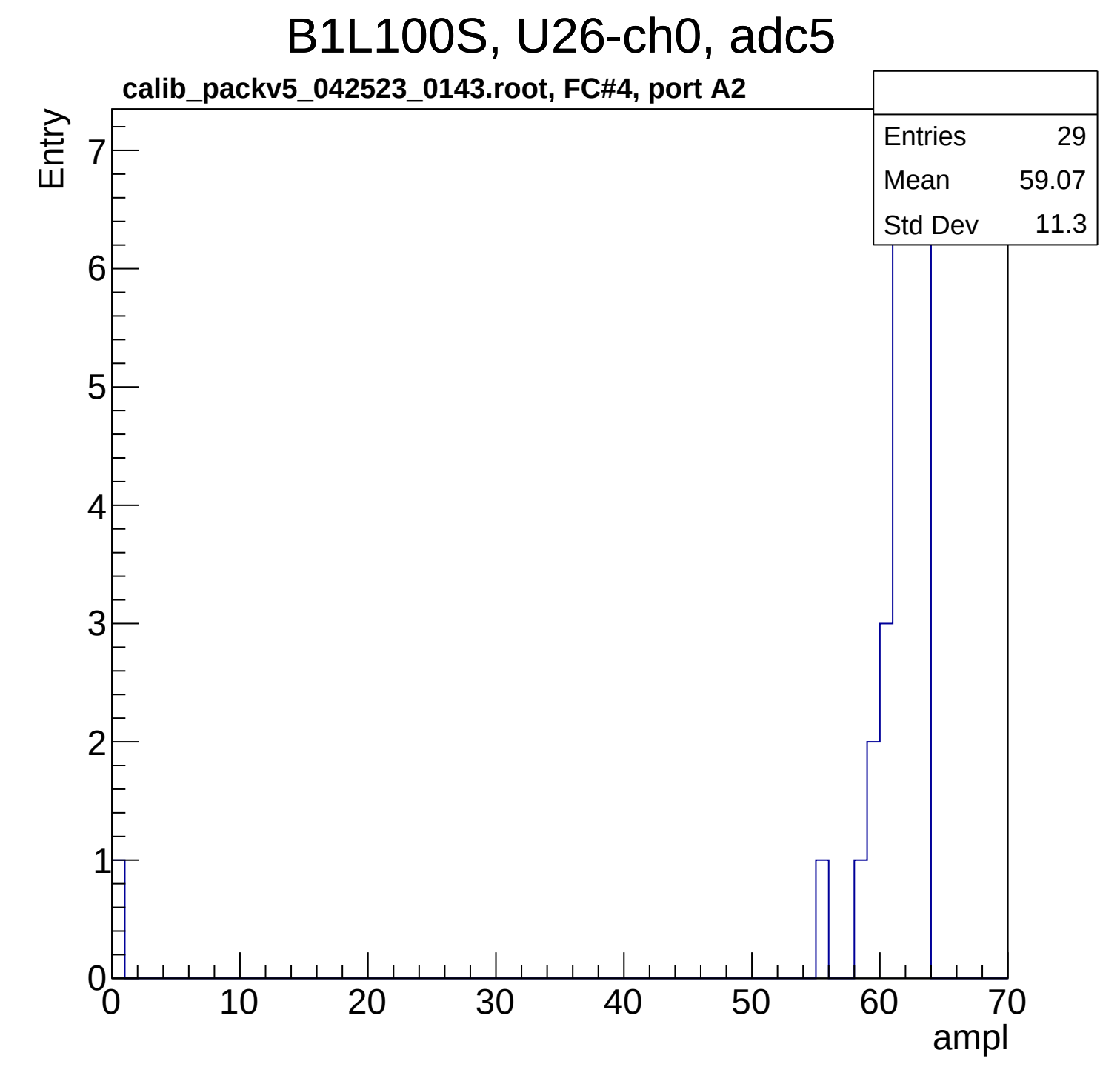
B1L100S, U26-ch0, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 59.07
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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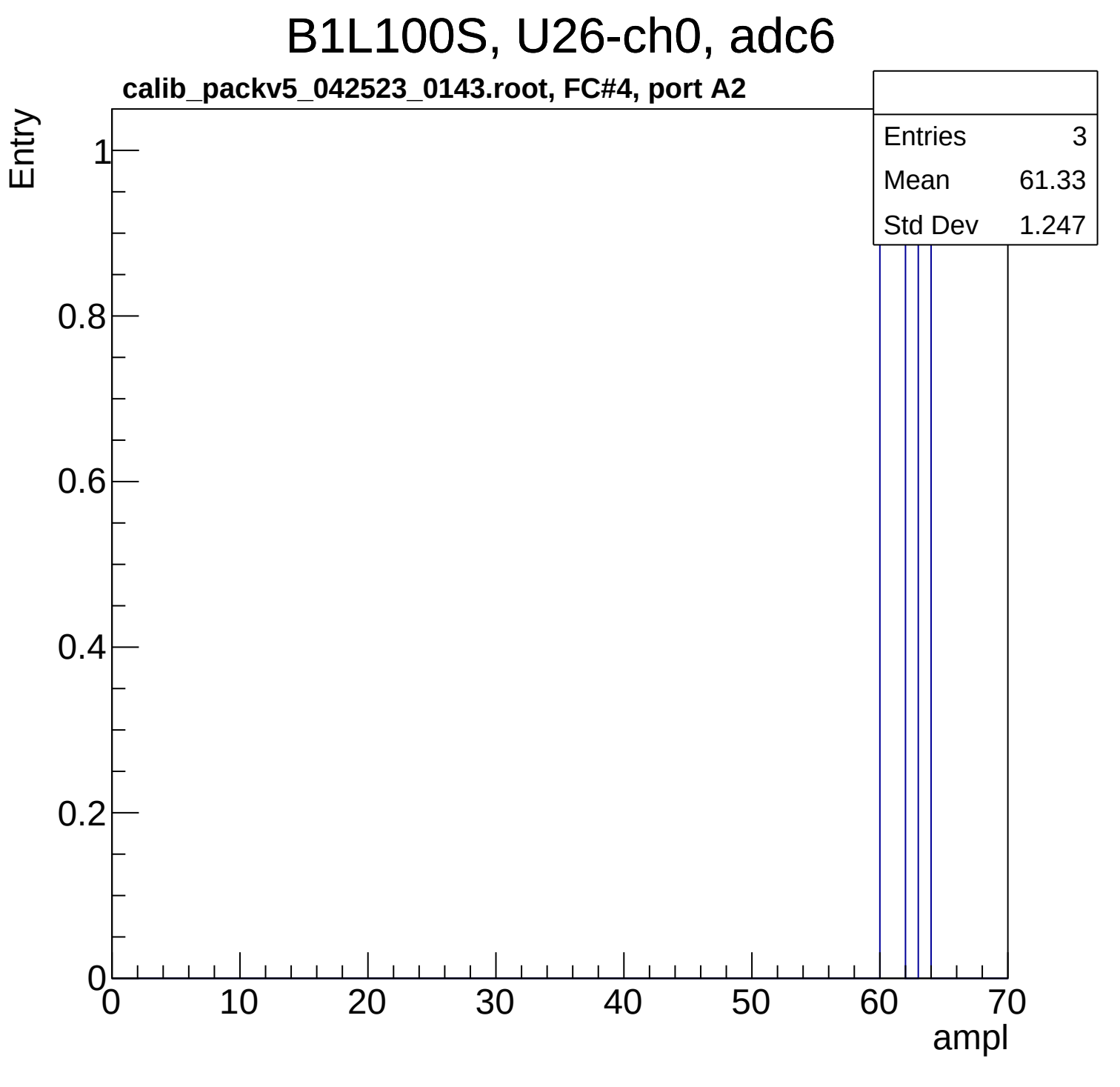
B1L100S, U26-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.247
Entry
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0.6
0.4
0.2
0
0
10
20
30
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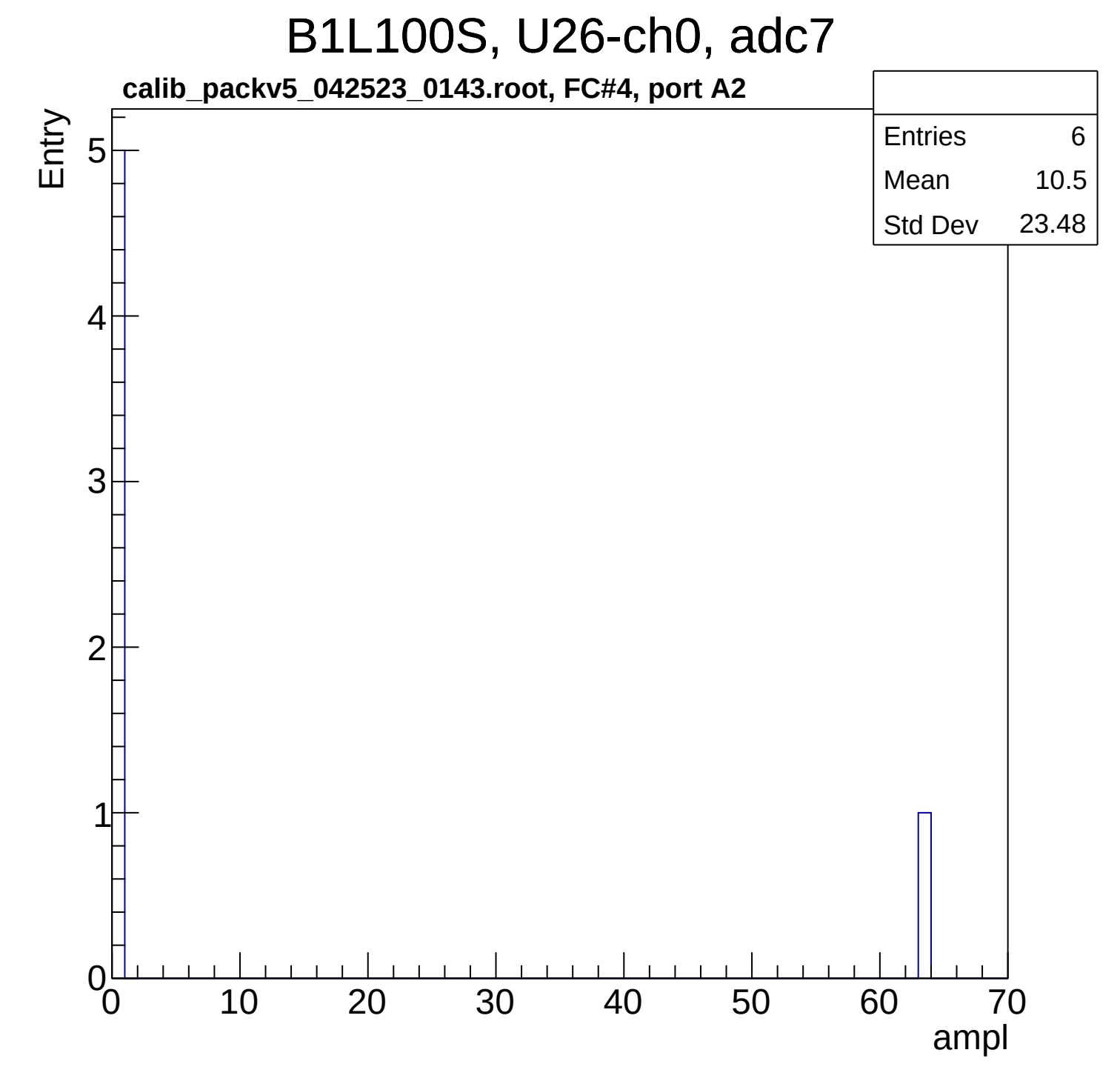
50
60
70
ampl


B1L100S, U26-ch0, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 10.5
Std Dev 23.48
Entry
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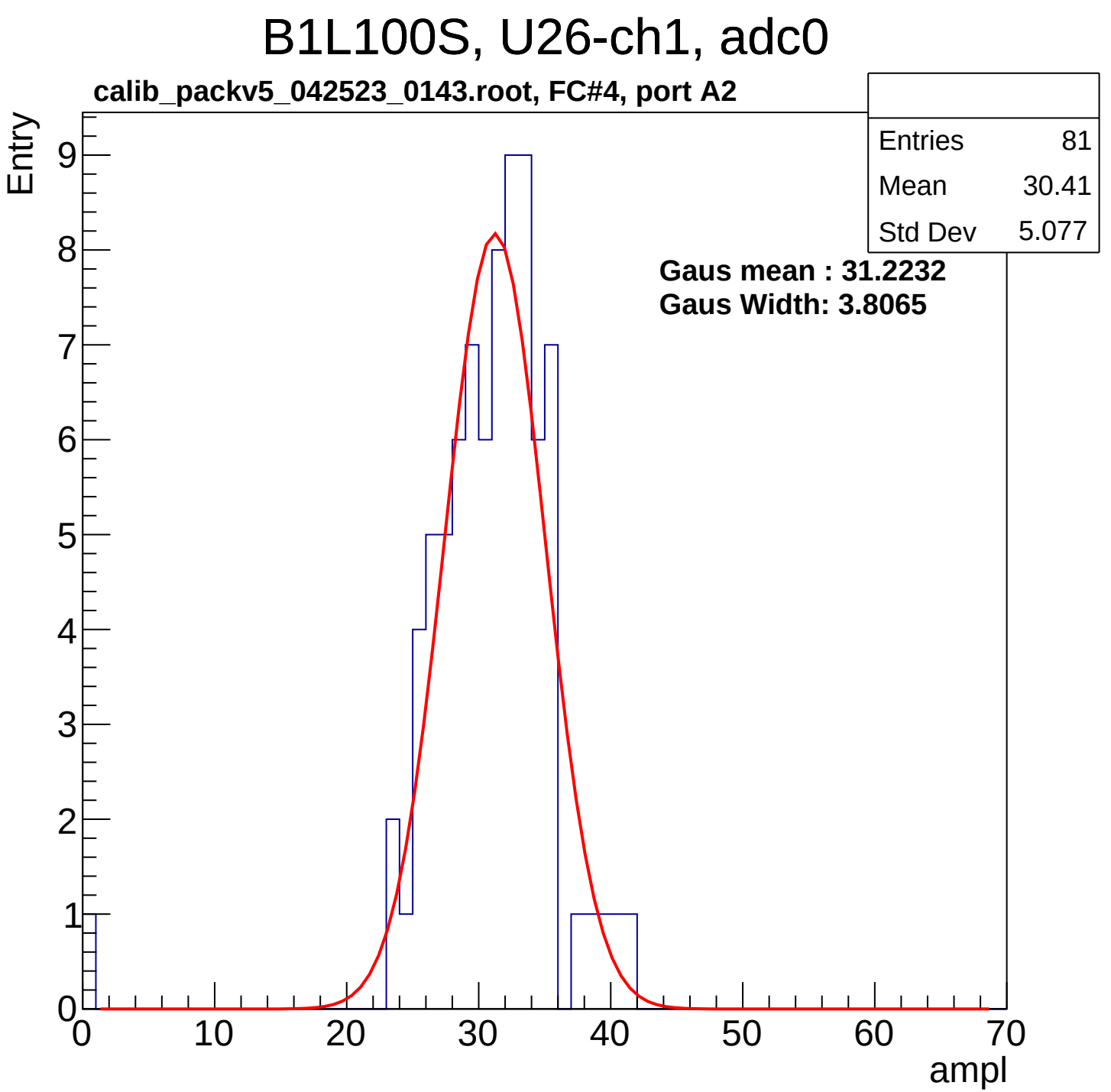
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.41
Std Dev 5.077
Gaus mean : 31.2232
Gaus Width: 3.8065
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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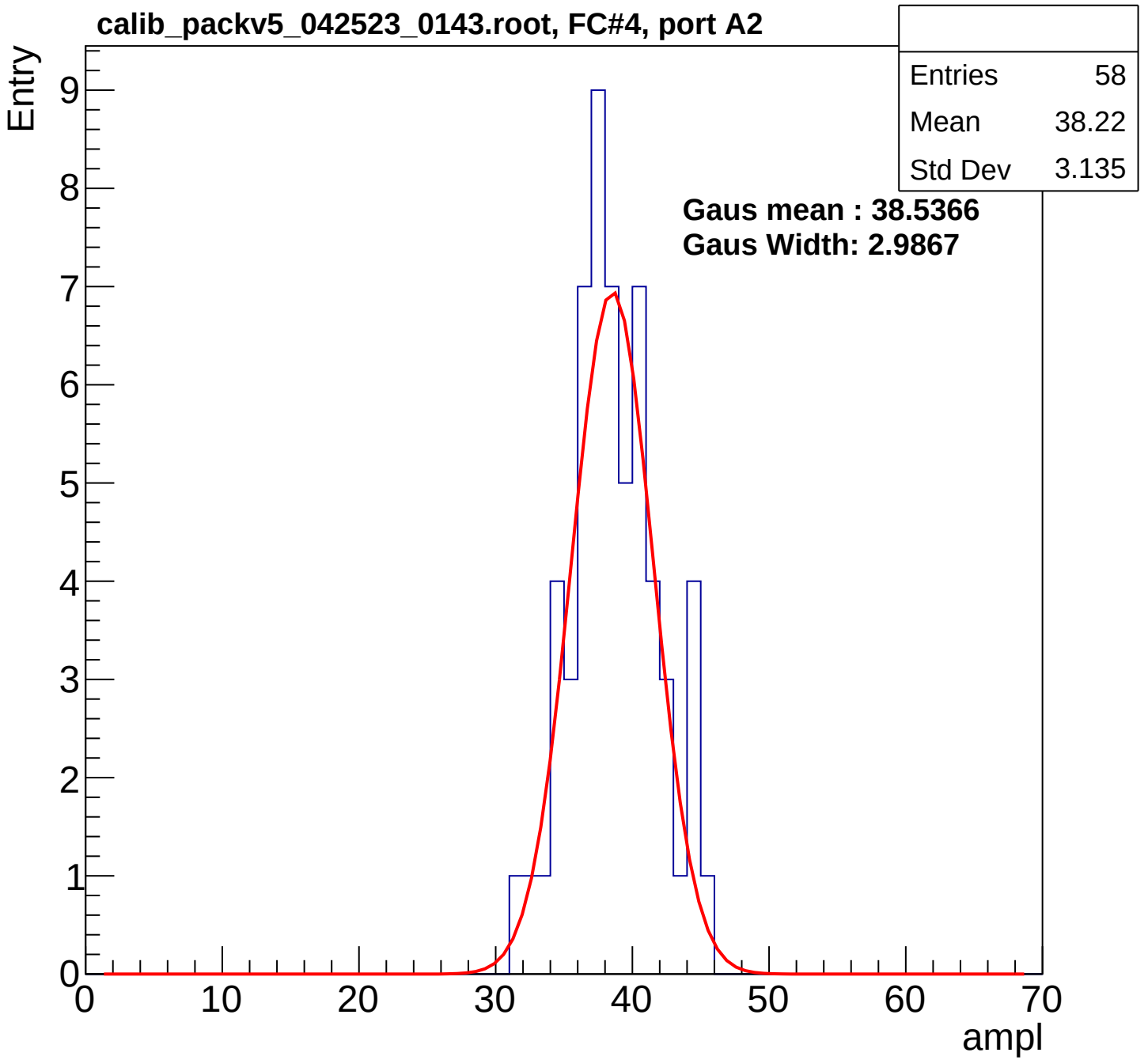
B1L100S, U26-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 38.22
Std Dev 3.135
Gaus mean : 38.5366
Gaus Width: 2.9867
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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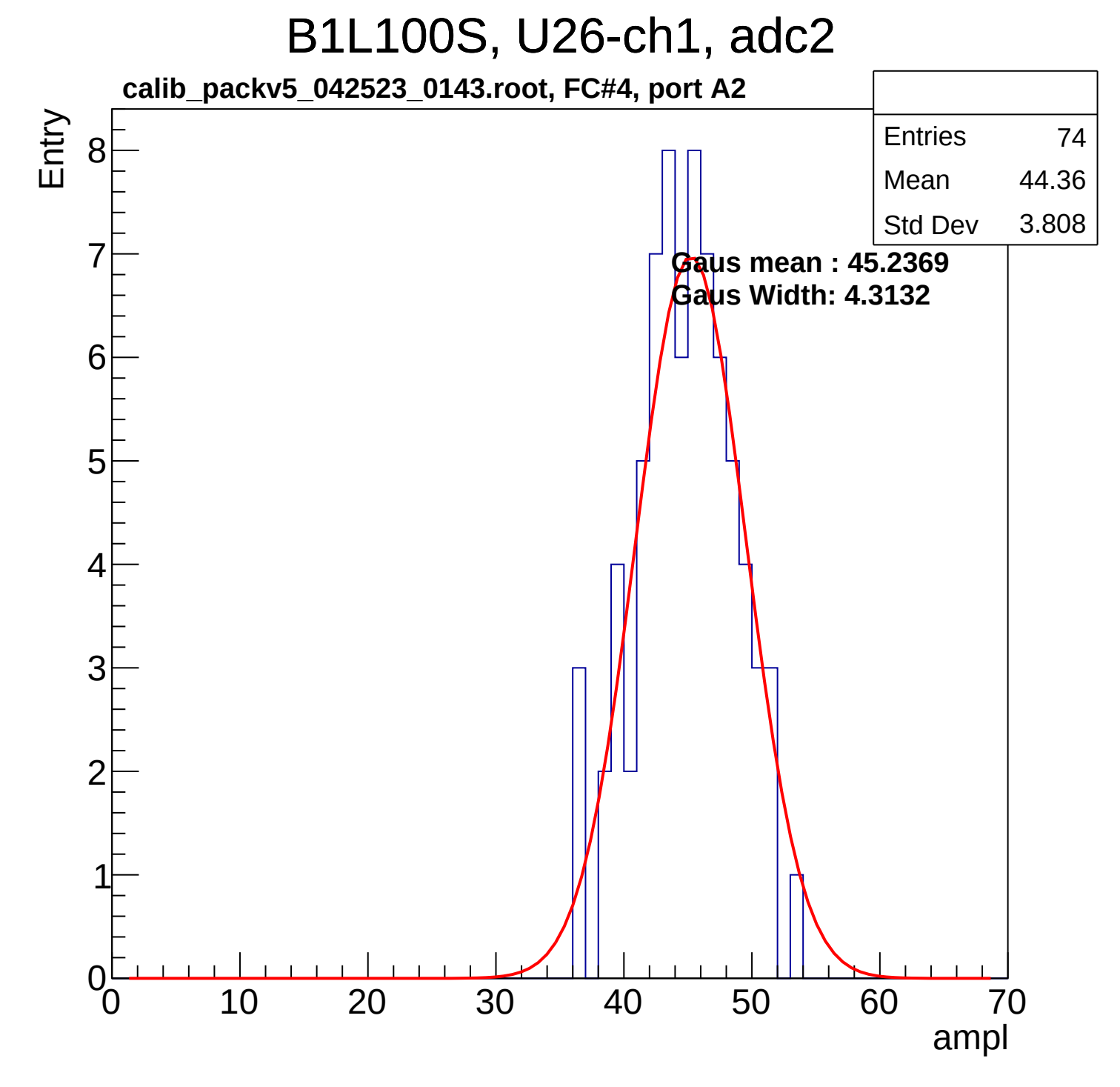
B1L100S, U26-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 44.36
Std Dev 3.808
Gaus mean : 45.2369
Gaus Width: 4.3132
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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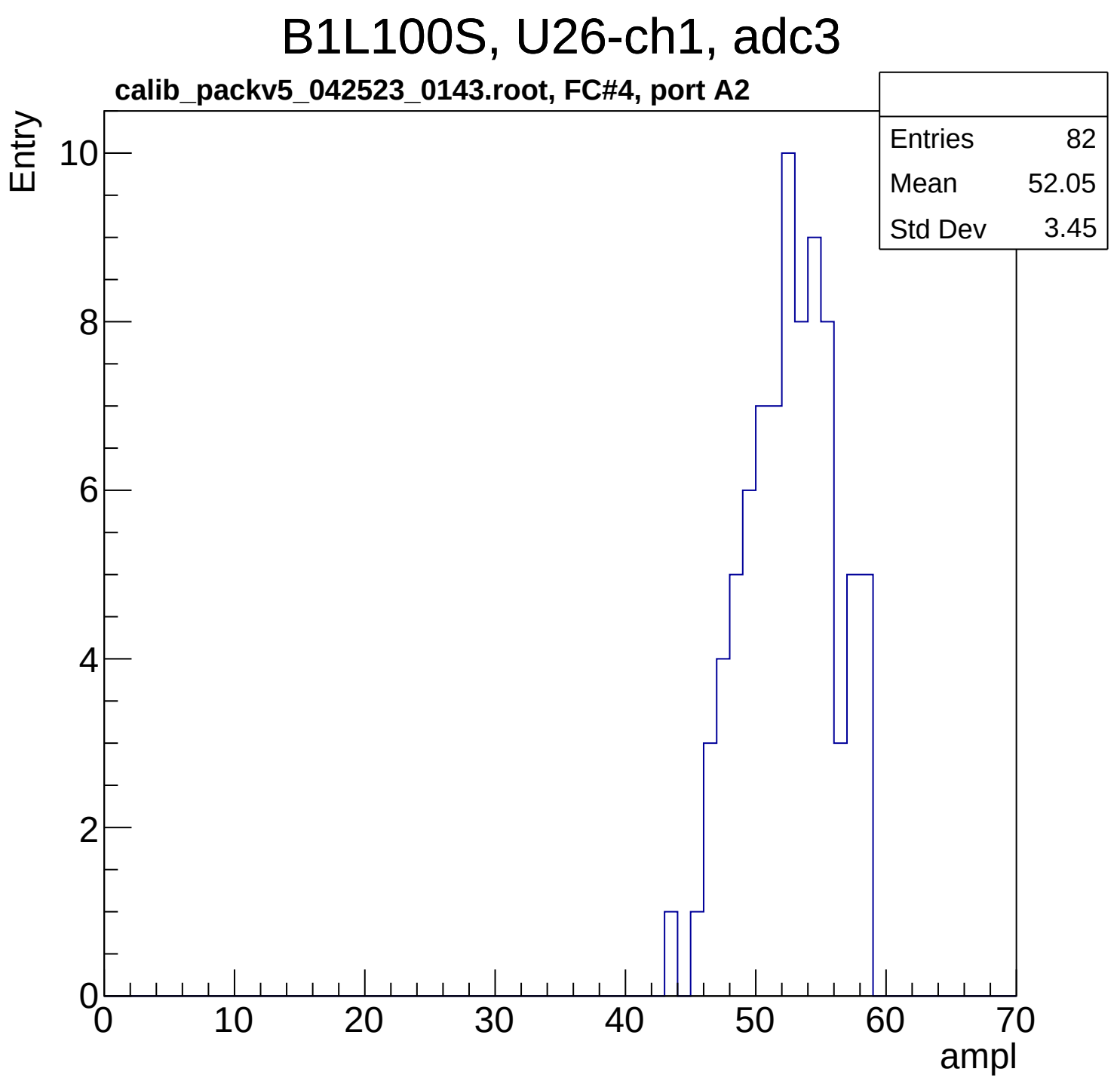
B1L100S, U26-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 52.05
Std Dev 3.45
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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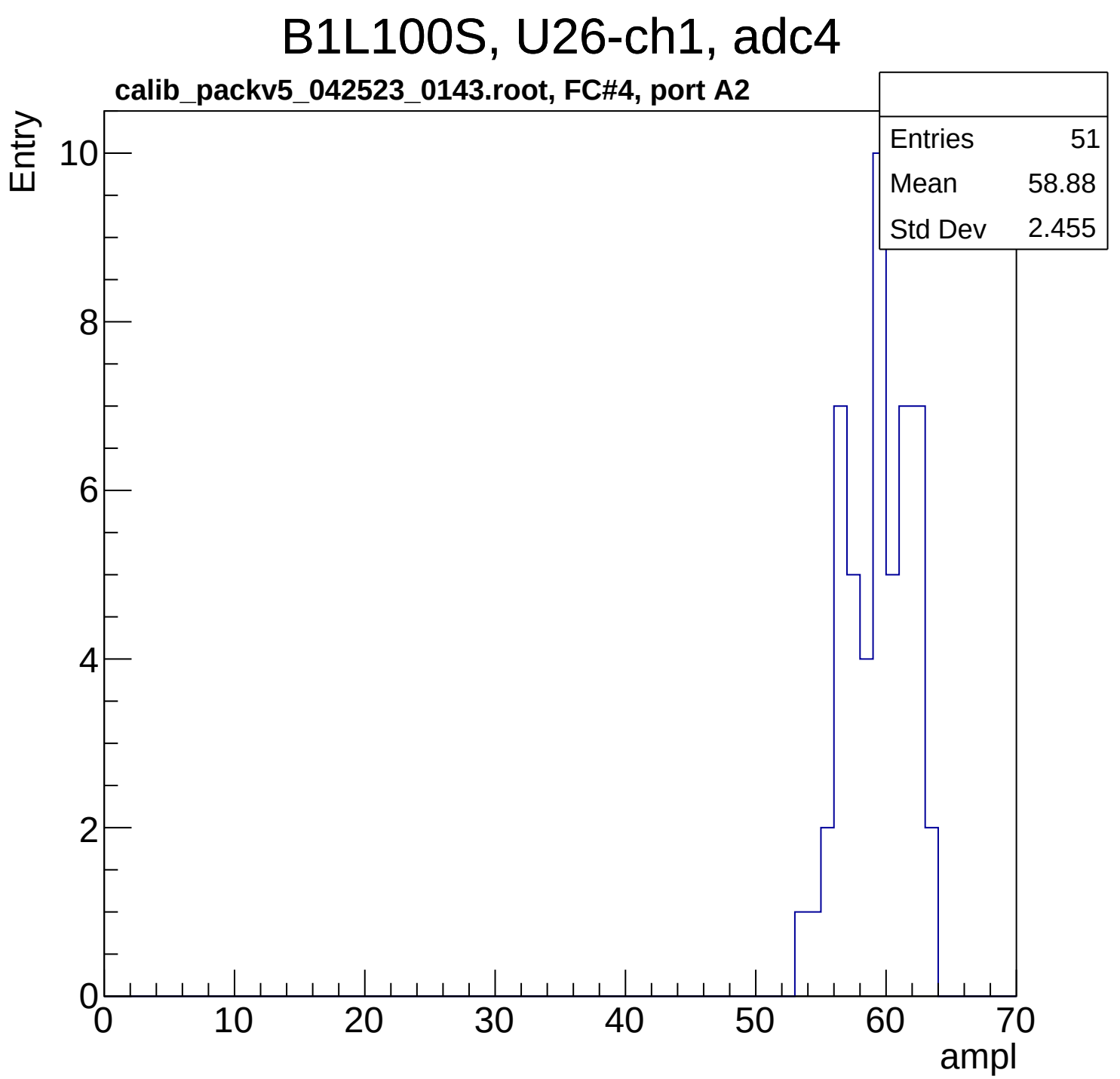
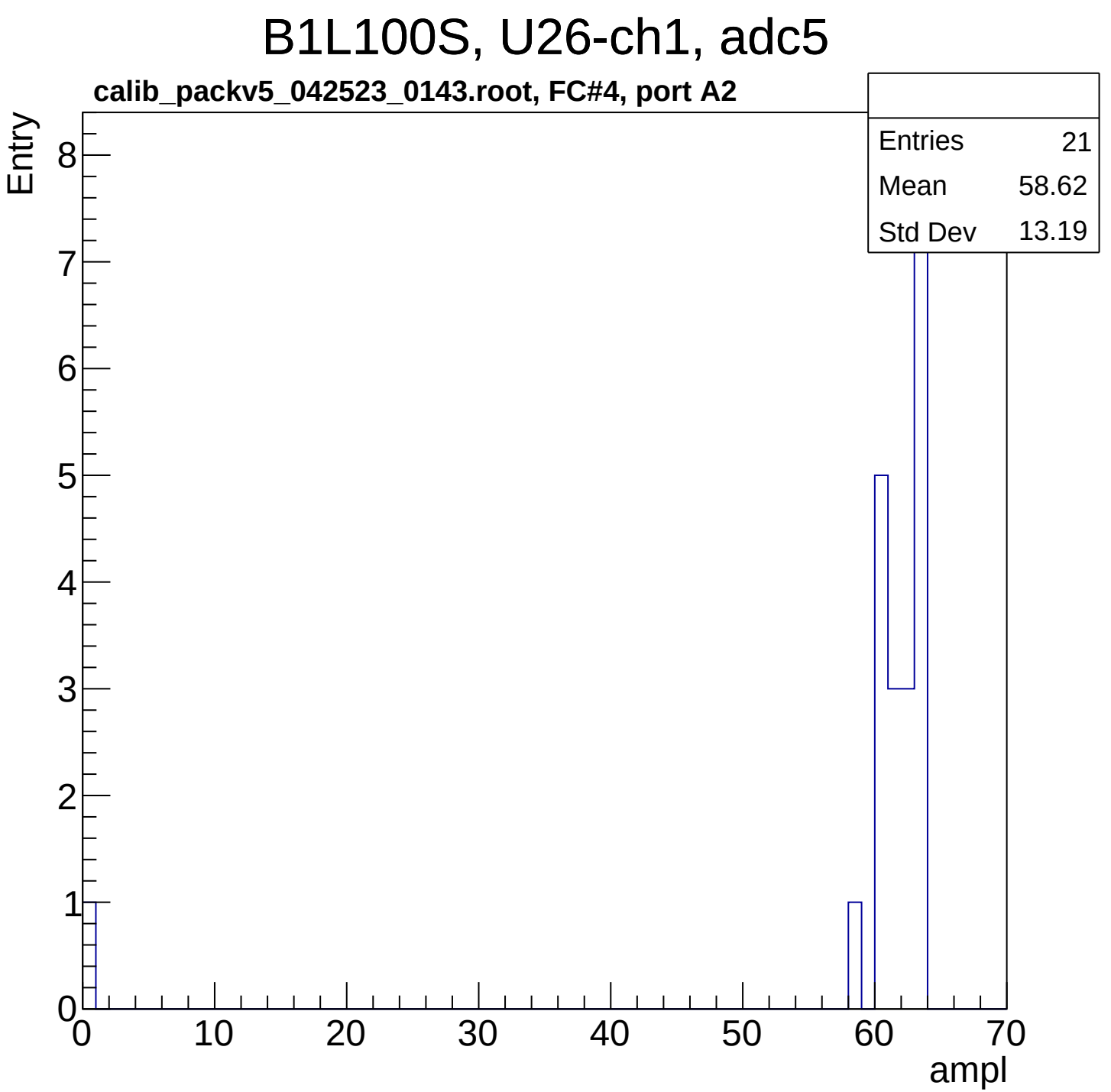

B1L100S, U26-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.88
Std Dev 2.455
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 21
Mean 58.62
Std Dev 13.19
Entry
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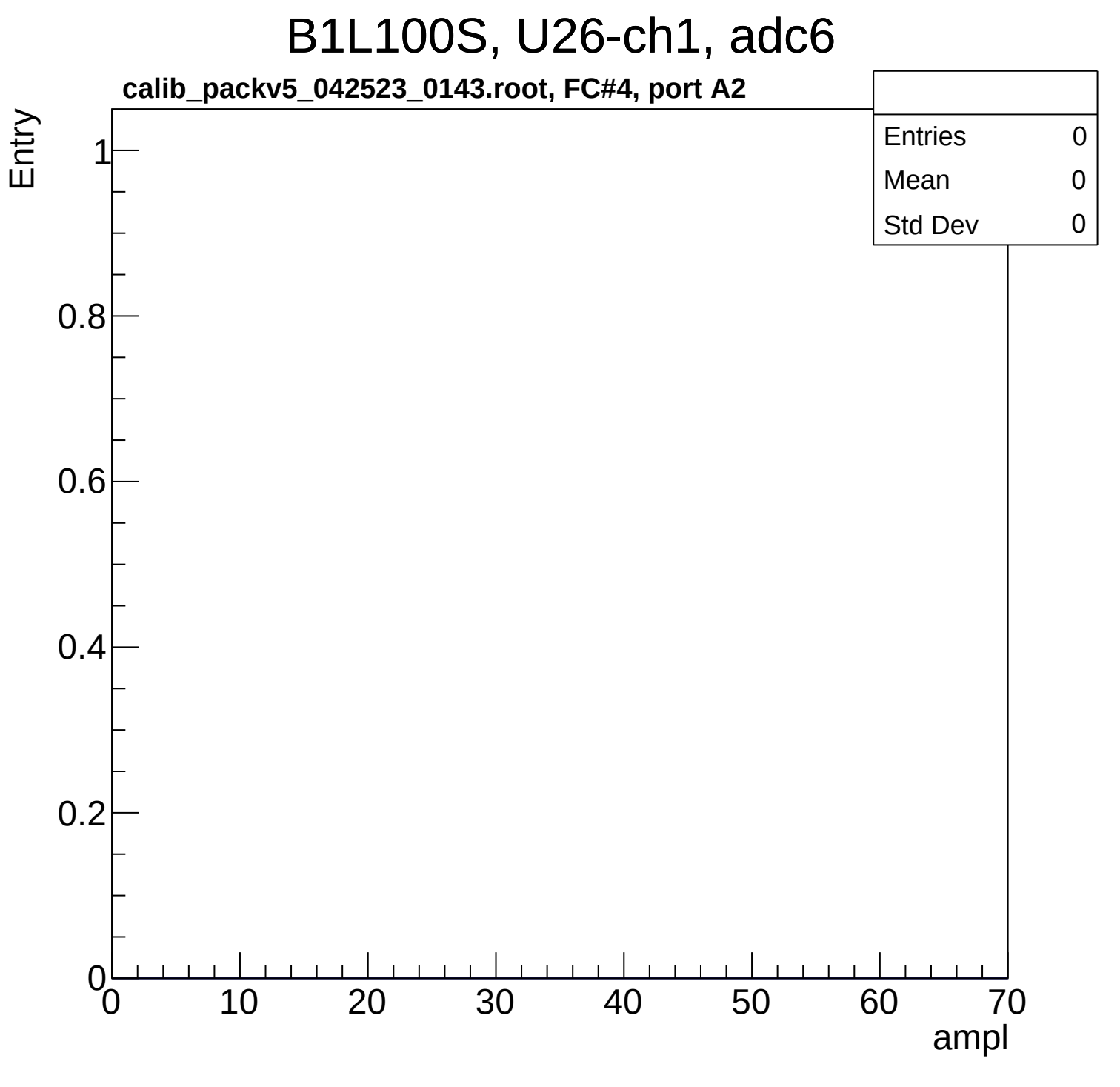
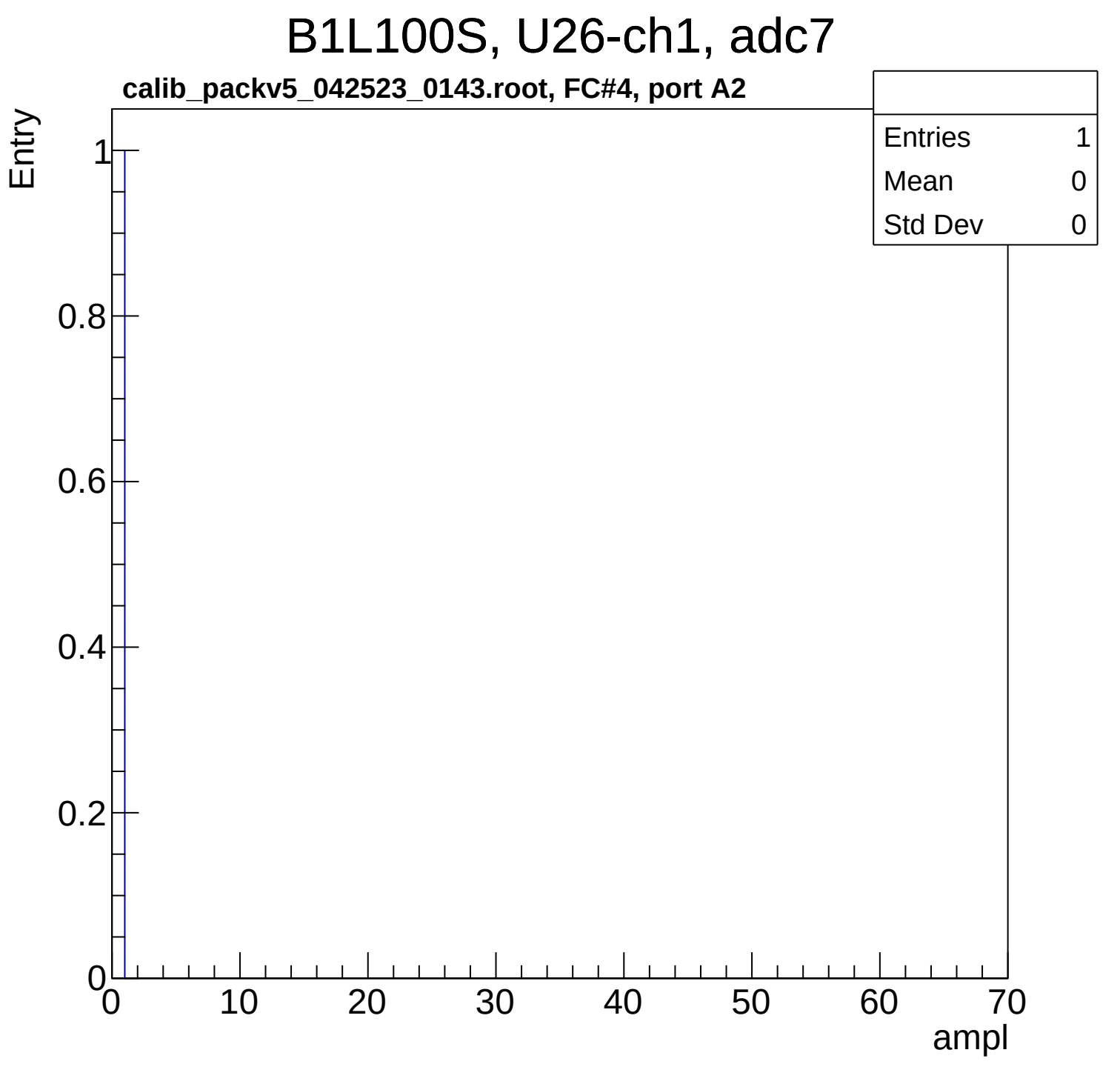
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch1, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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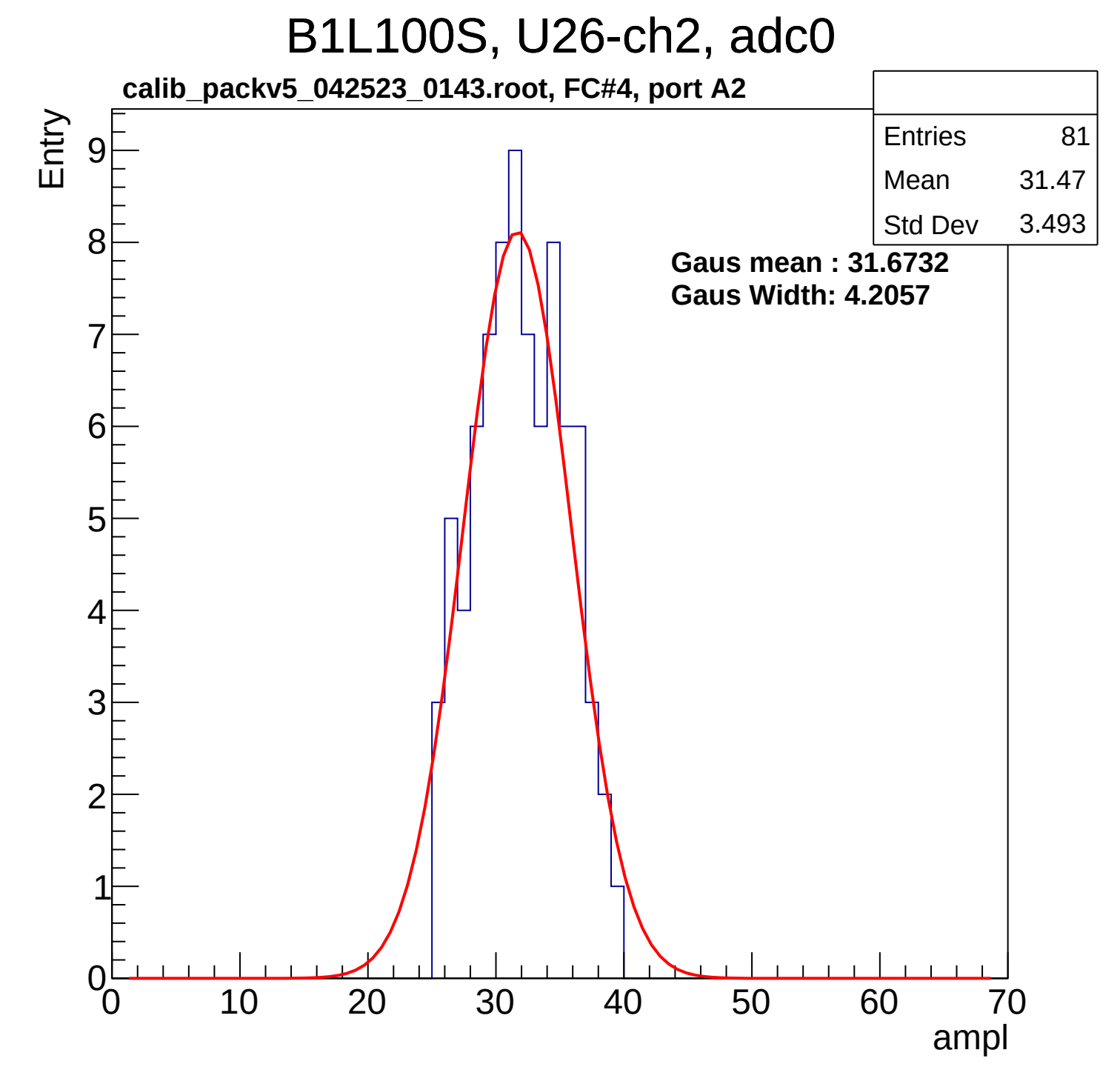
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 31.47
Std Dev 3.493
Gaus mean : 31.6732
Gaus Width: 4.2057
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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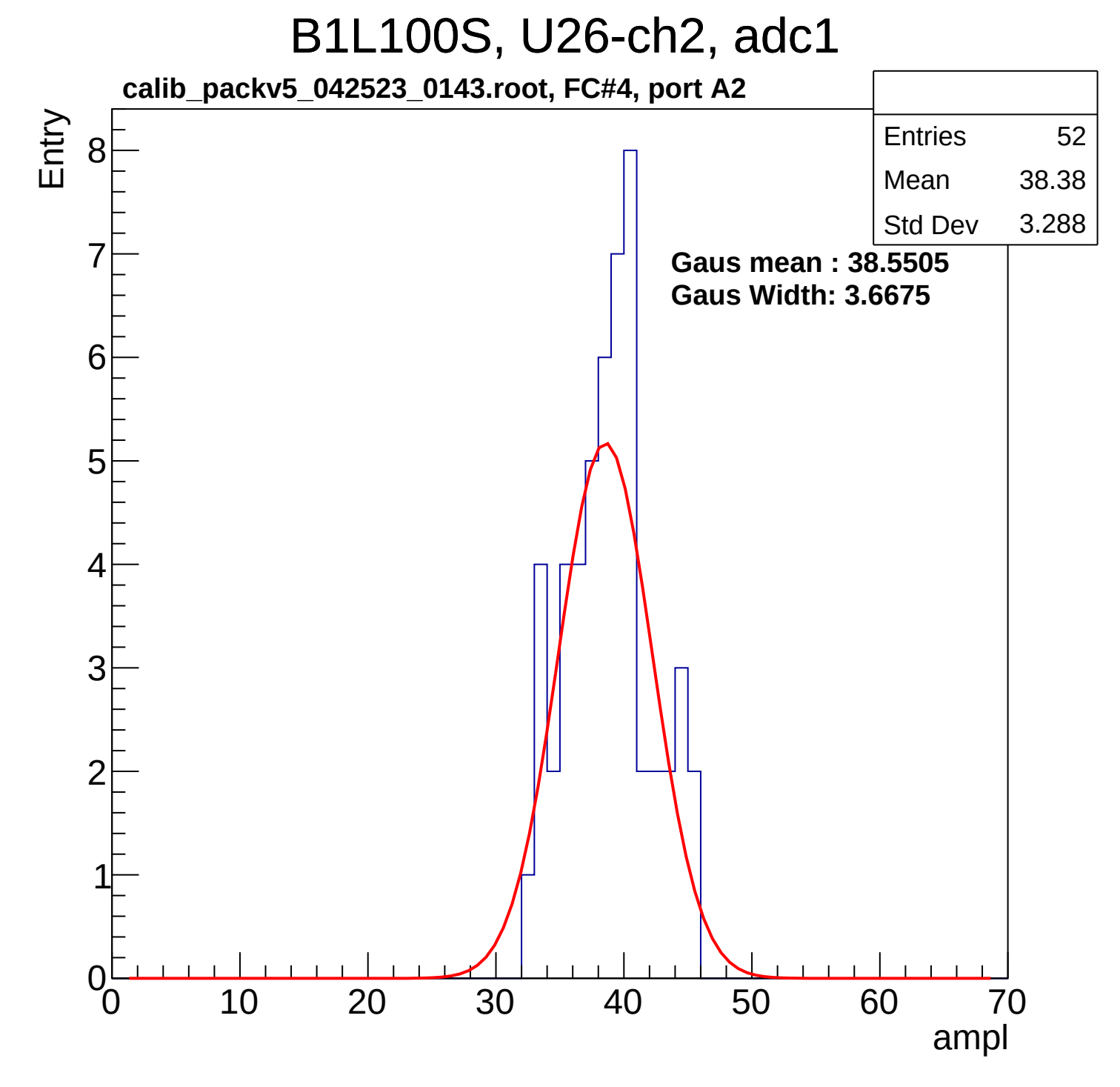
B1L100S, U26-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 38.38
Std Dev 3.288
Gaus mean : 38.5505
Gaus Width: 3.6675
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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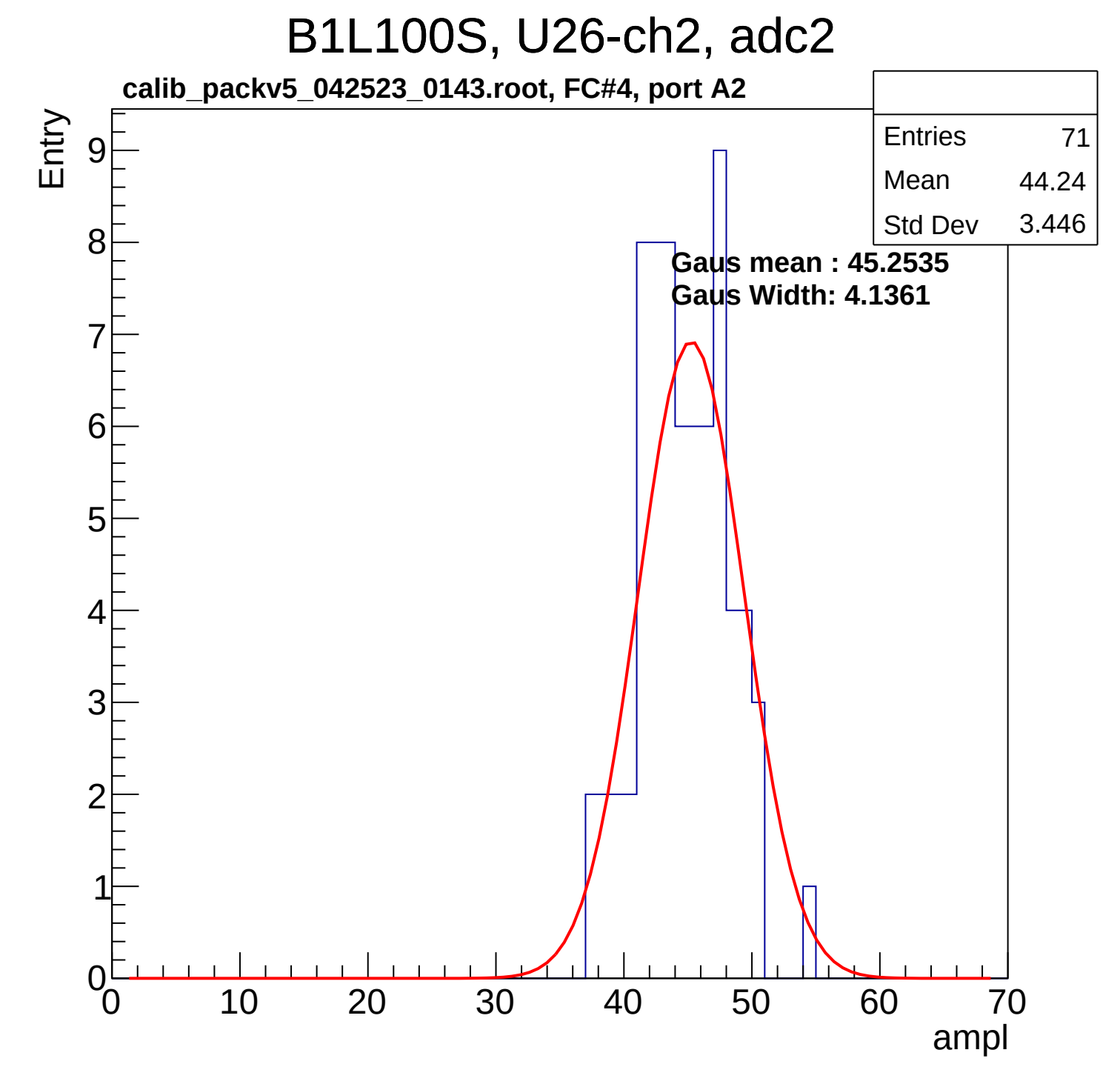
40
50
60
70


B1L100S, U26-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 44.24
Std Dev 3.446
Gaus mean : 45.2535
Gaus Width: 4.1361
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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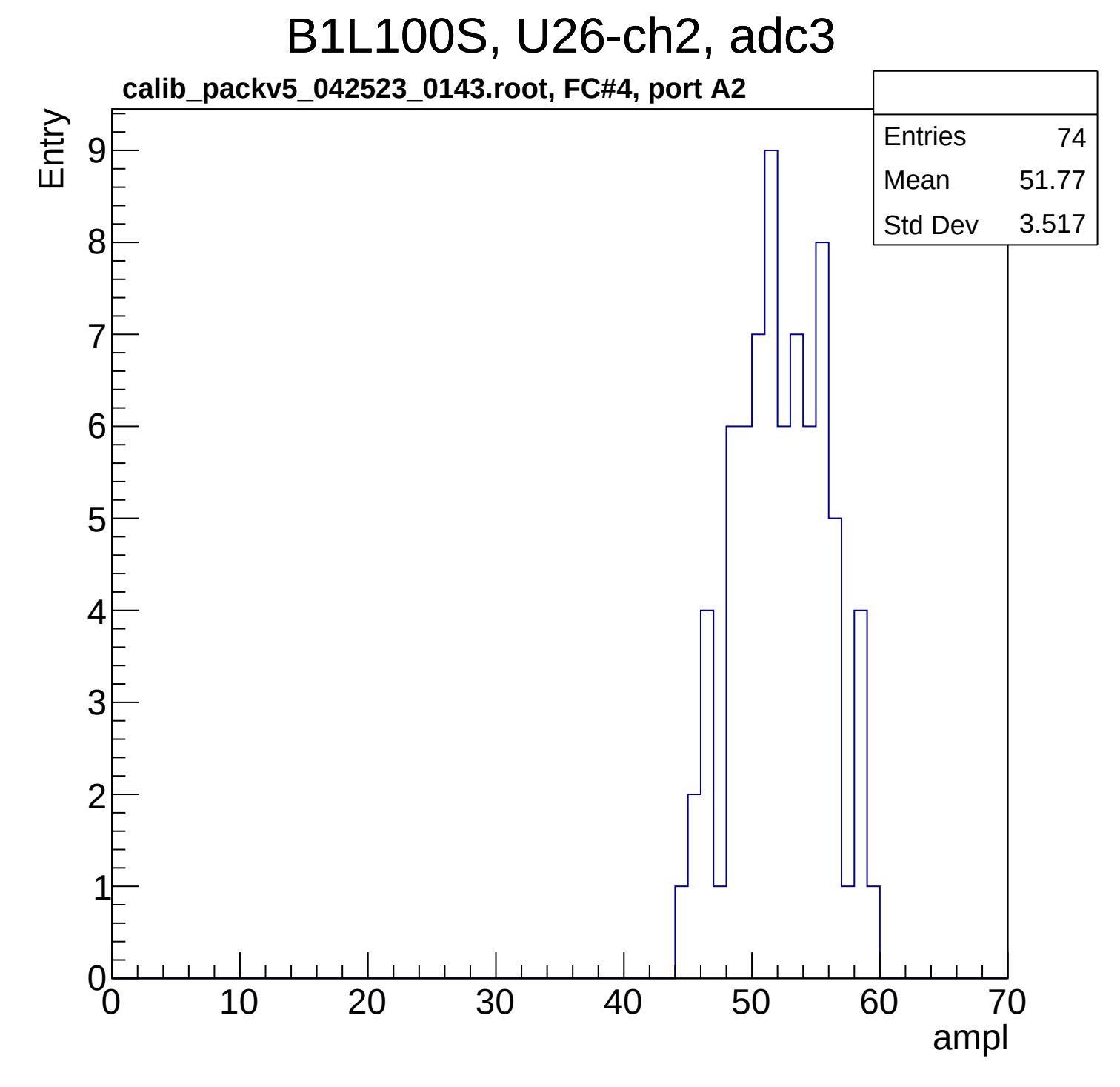
B1L100S, U26-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.77
Std Dev 3.517
Entry
ampl
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2
3
4
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6
7
8
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0
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60
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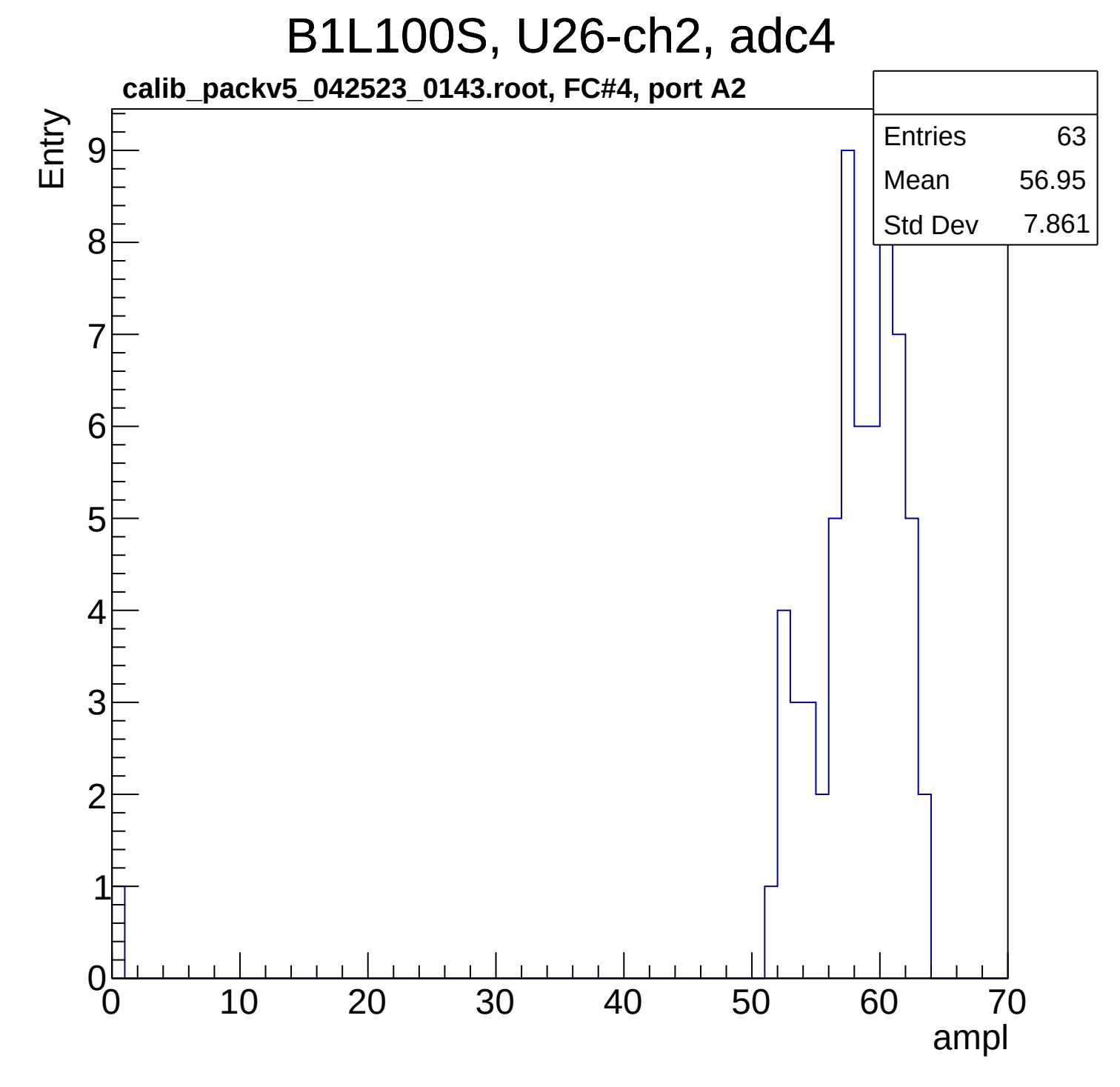
B1L100S, U26-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.95
Std Dev 7.861
Entry
ampl
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6
7
8
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0
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60
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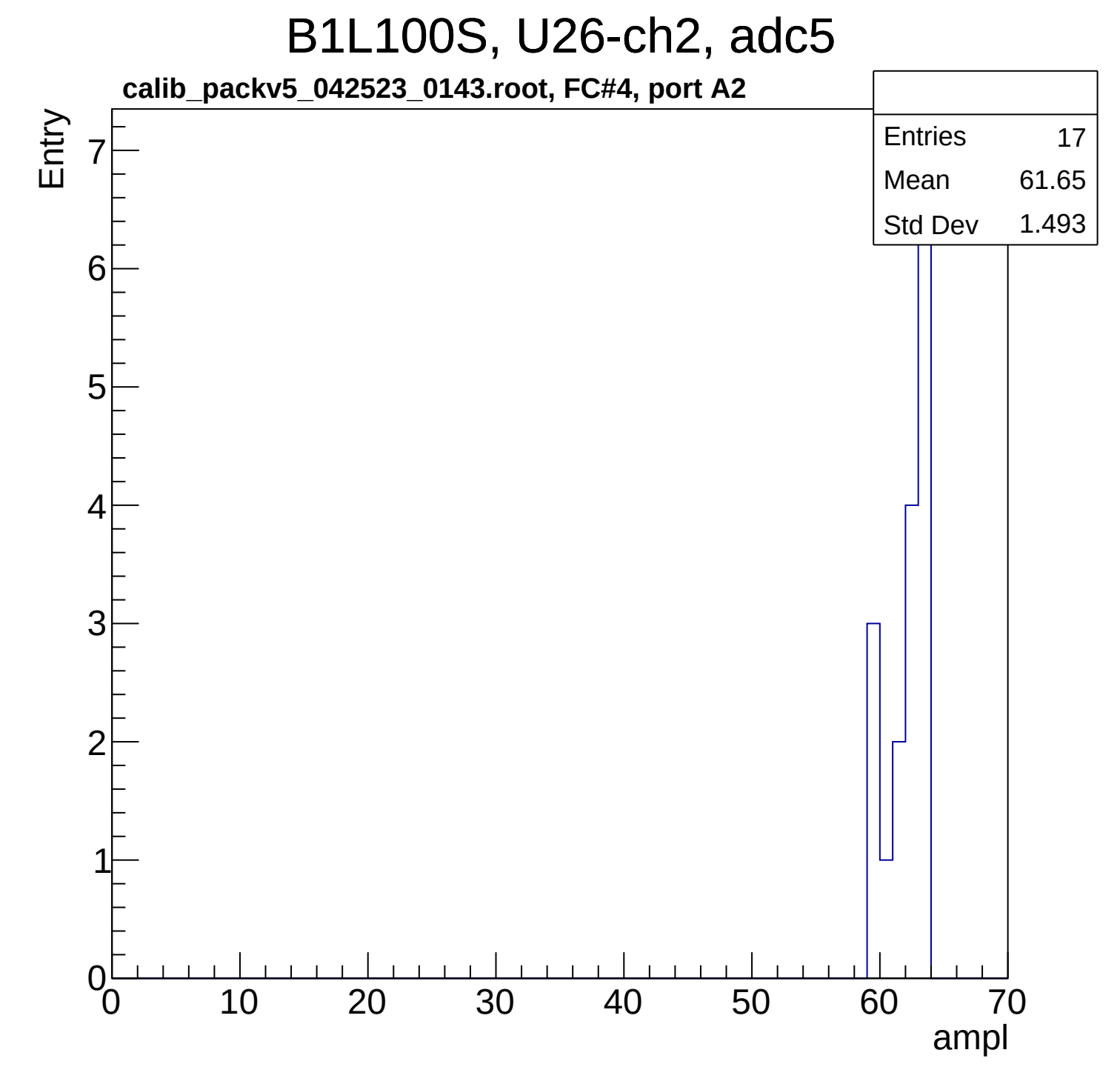
B1L100S, U26-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 17
Mean 61.65
Std Dev 1.493
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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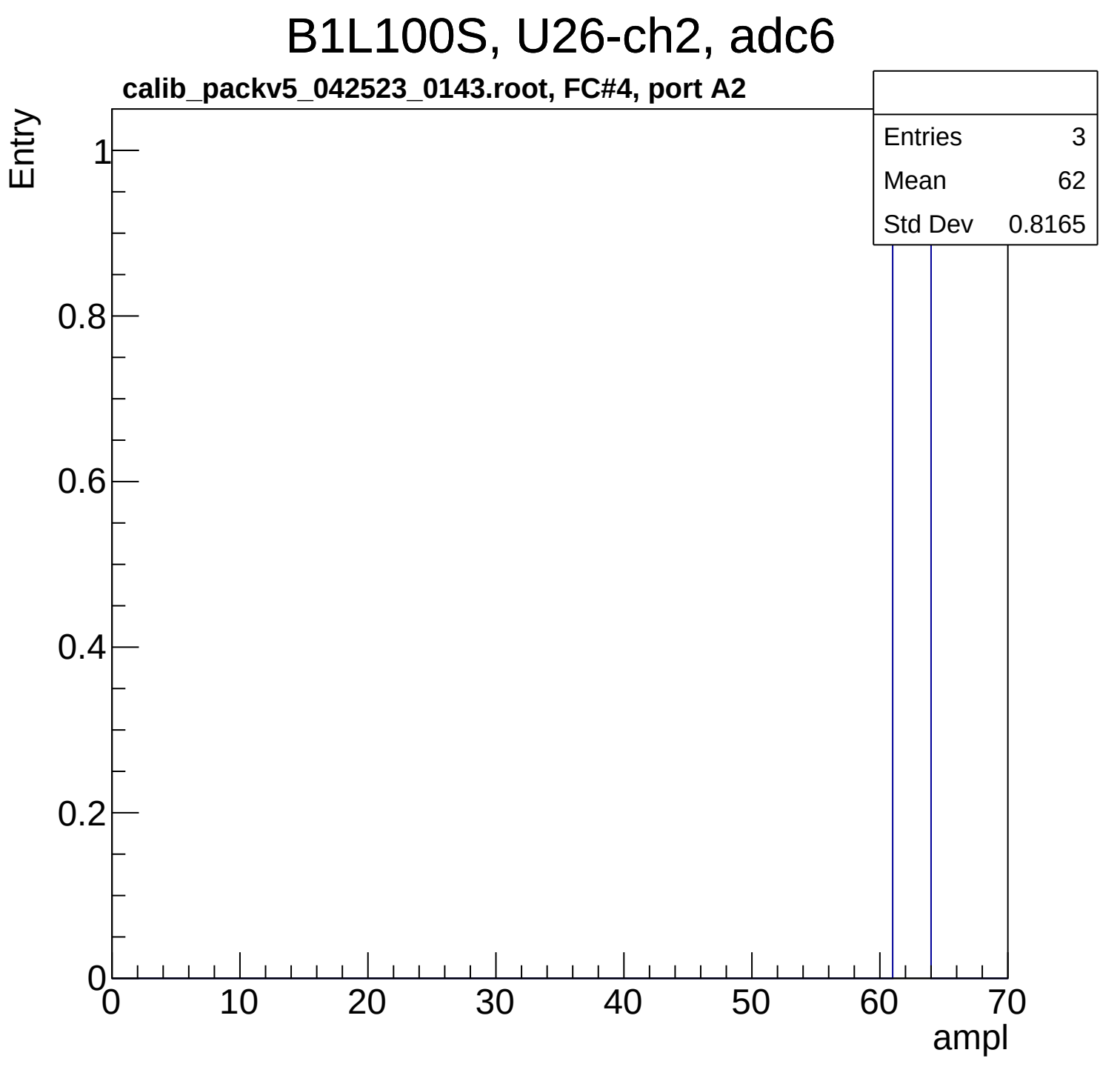
B1L100S, U26-ch2, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
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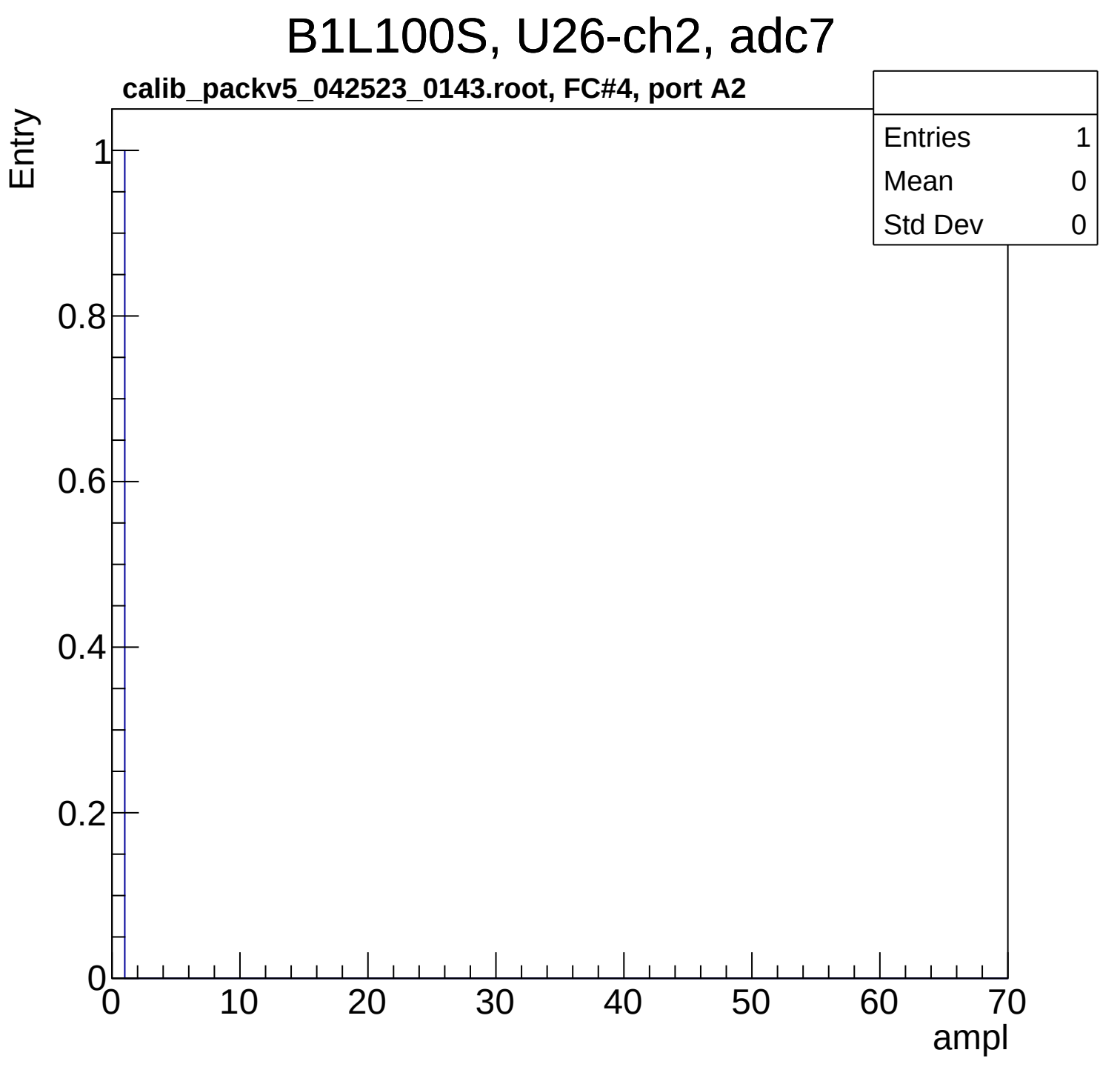
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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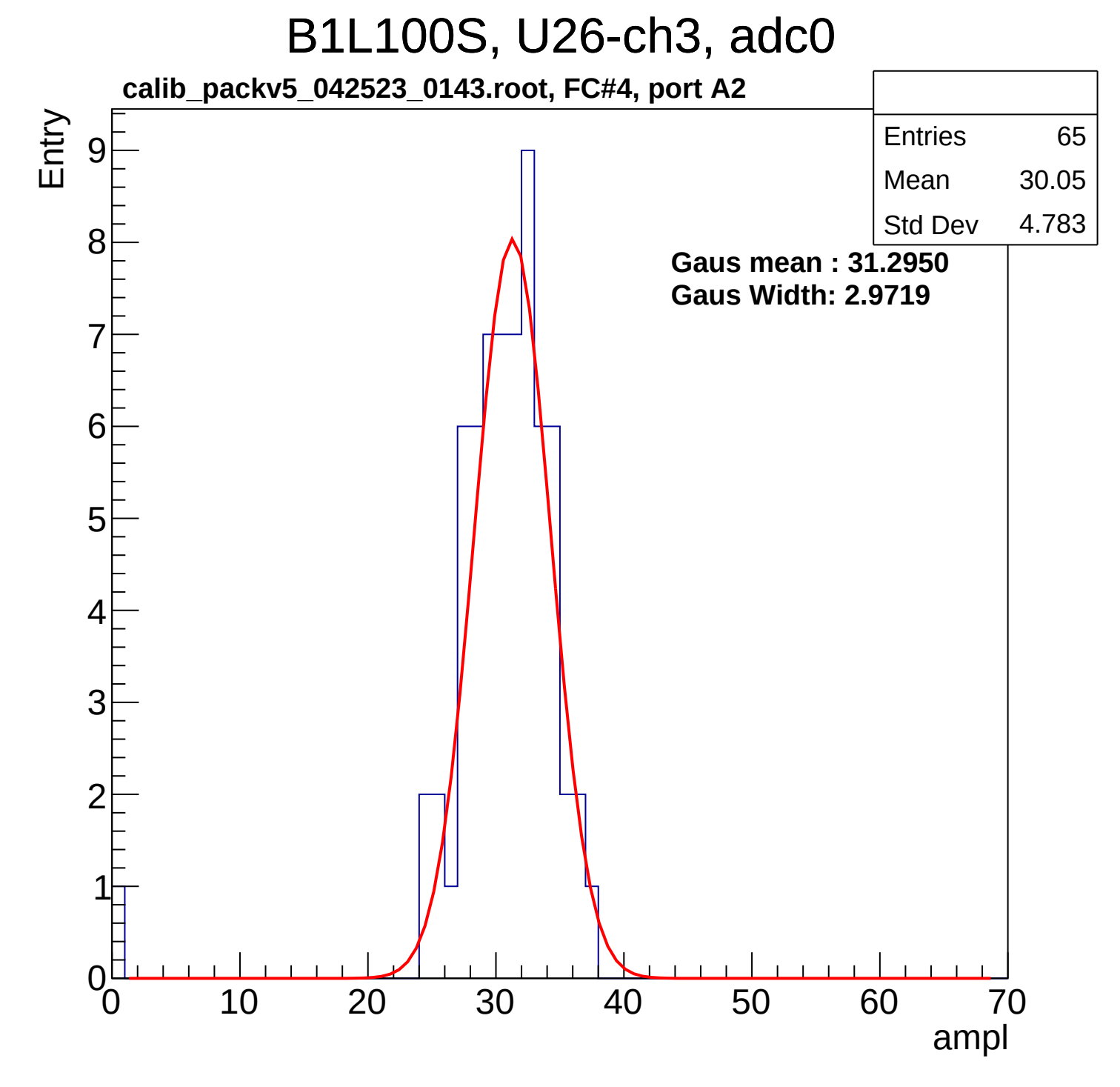
50
60
70
ampl


B1L100S, U26-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.05
Std Dev 4.783
Gaus mean : 31.2950
Gaus Width: 2.9719
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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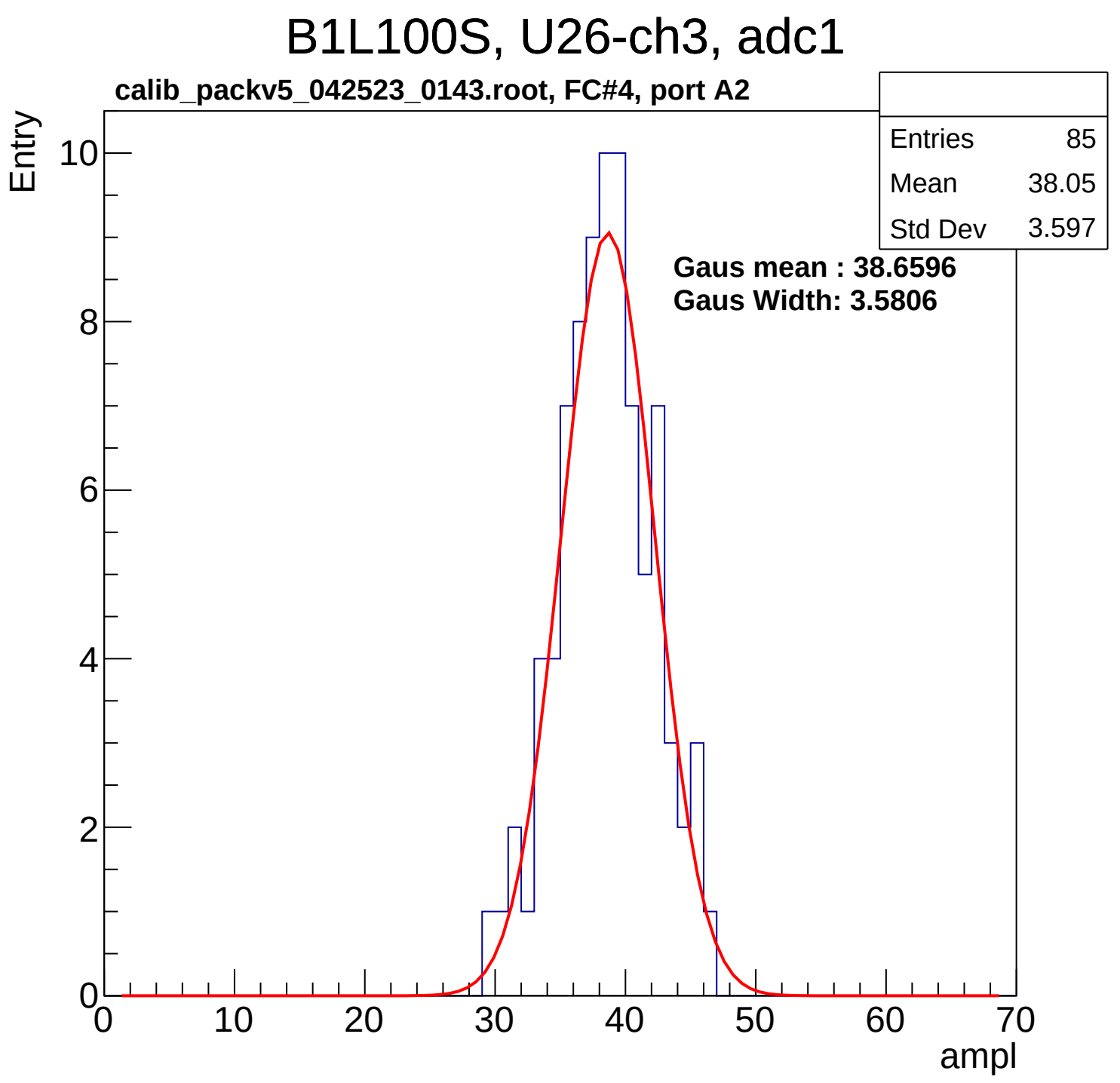
B1L100S, U26-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 38.05
Std Dev 3.597
Gaus mean : 38.6596
Gaus Width: 3.5806
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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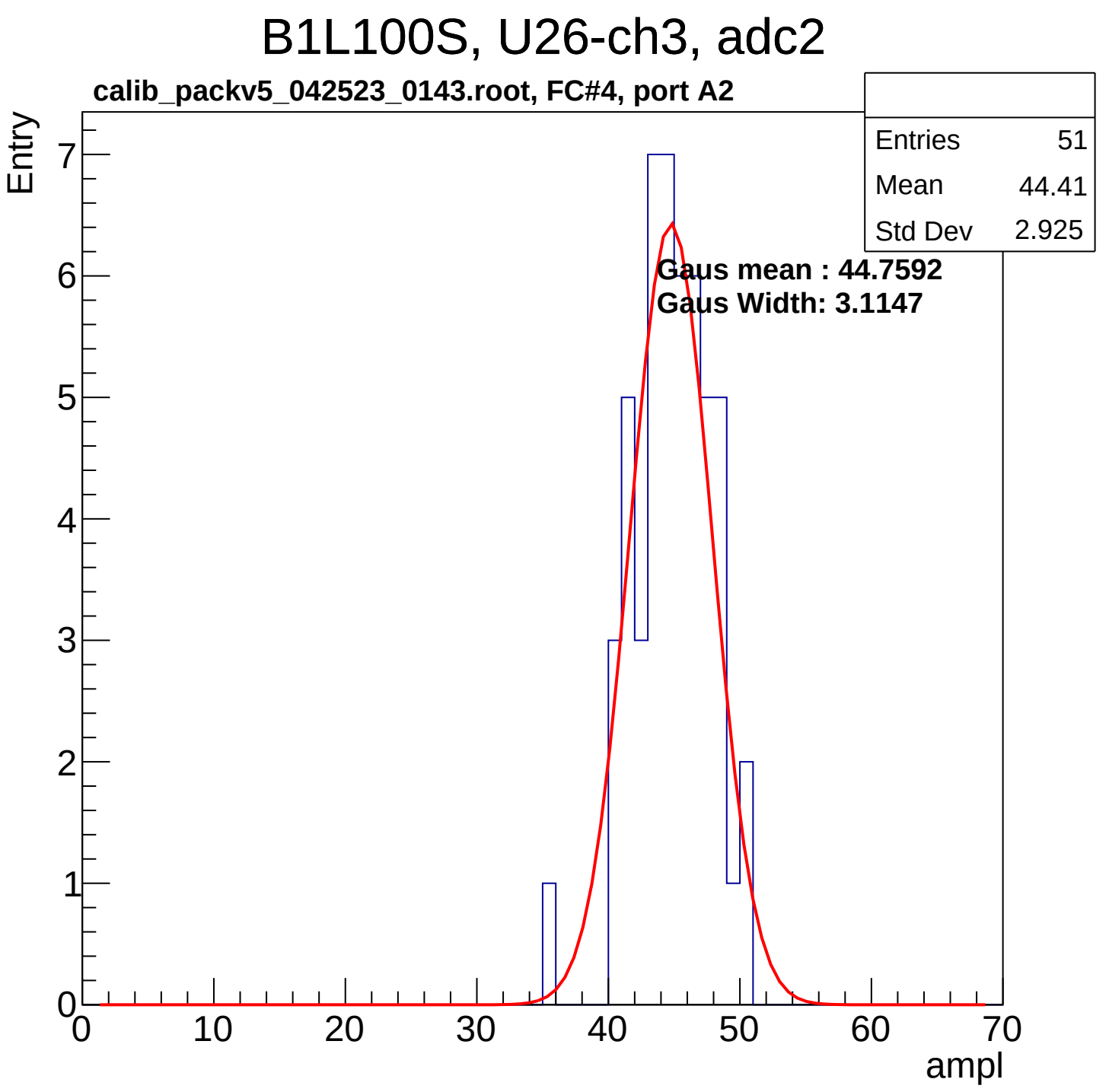
B1L100S, U26-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 44.41
Std Dev 2.925
Gaus mean : 44.7592
Gaus Width: 3.1147
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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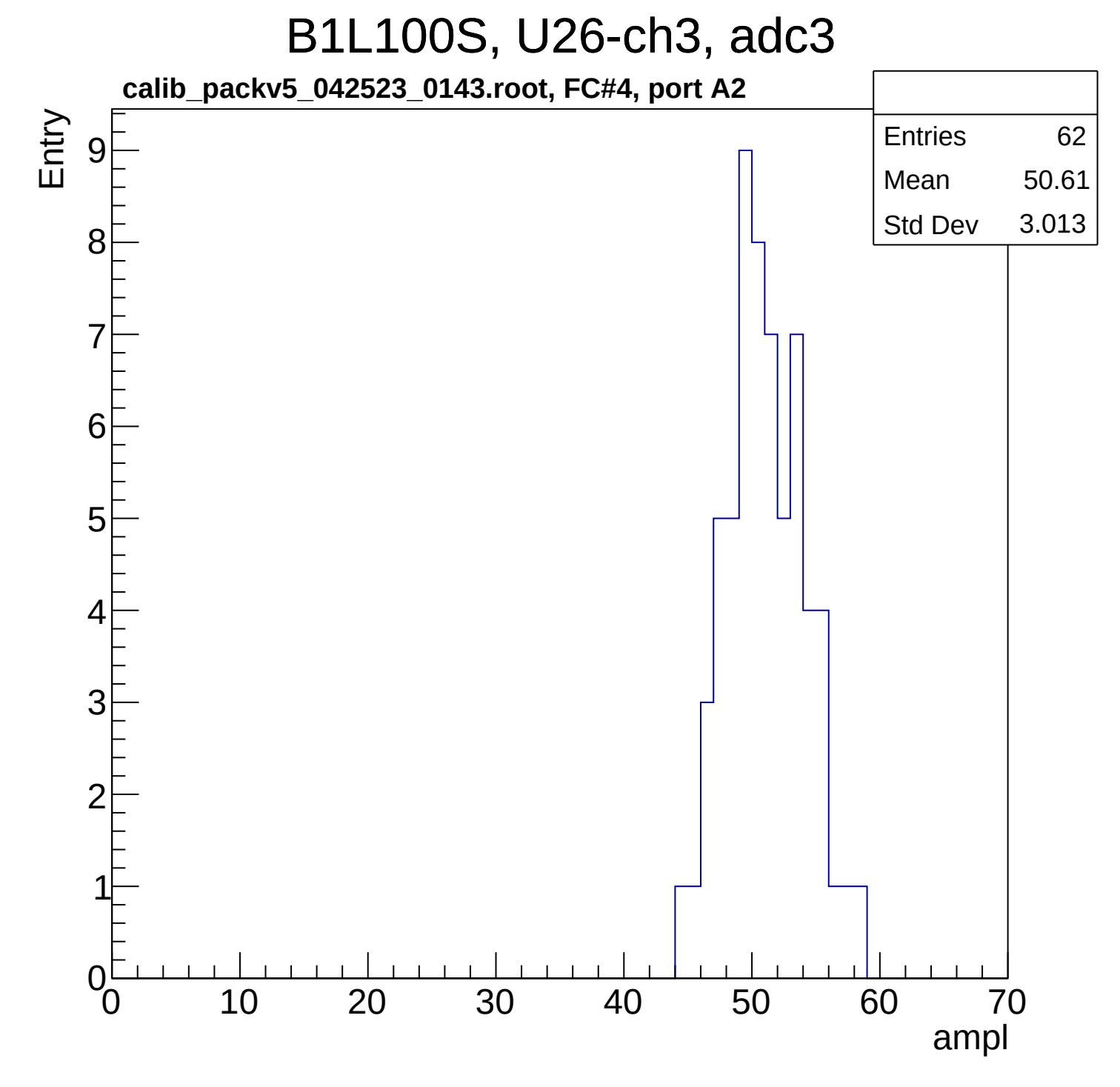
B1L100S, U26-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.61
Std Dev 3.013
Entry
ampl
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4
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7
8
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0
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60
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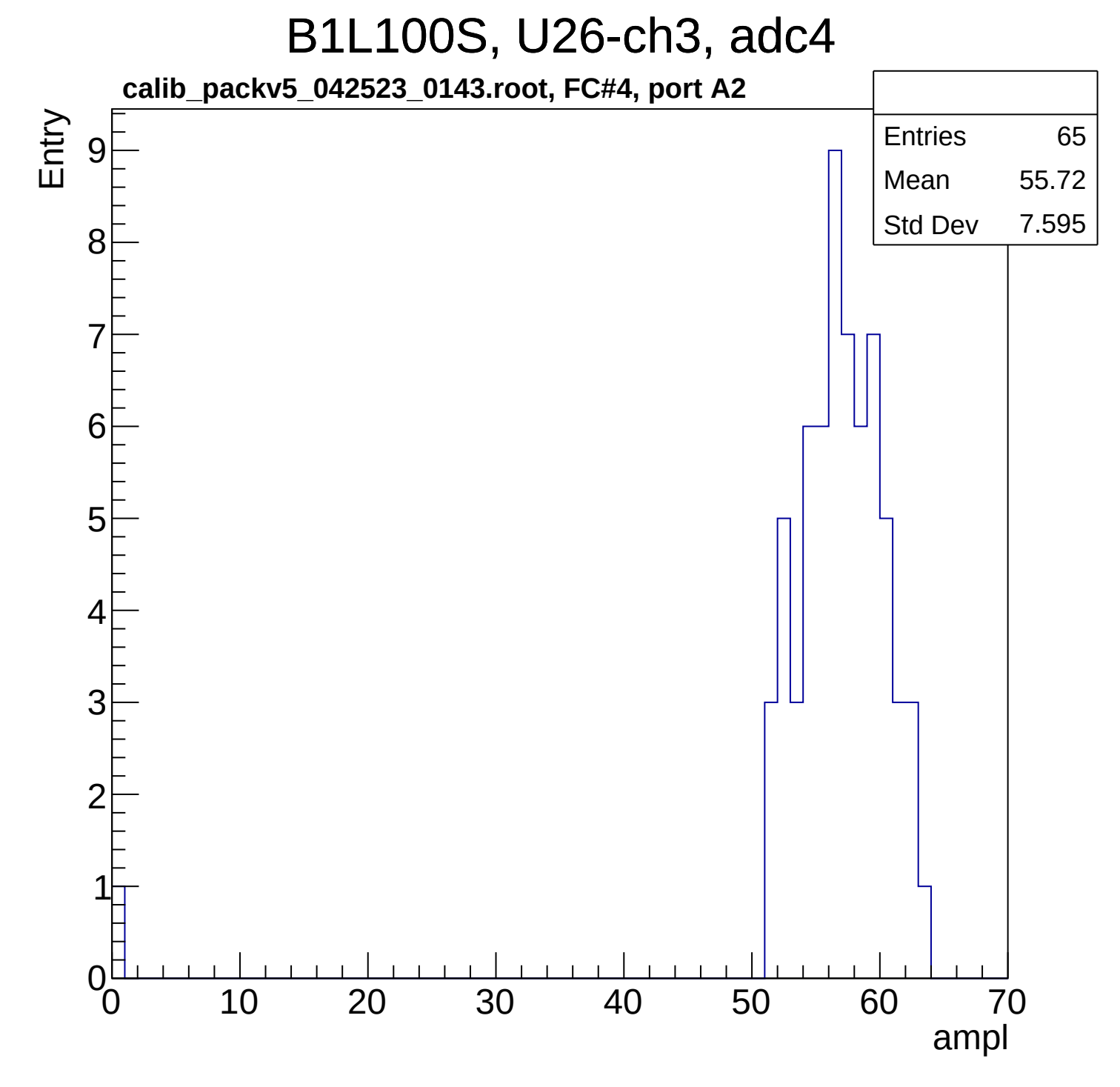
B1L100S, U26-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.72
Std Dev 7.595
Entry
ampl
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3
4
5
6
7
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0
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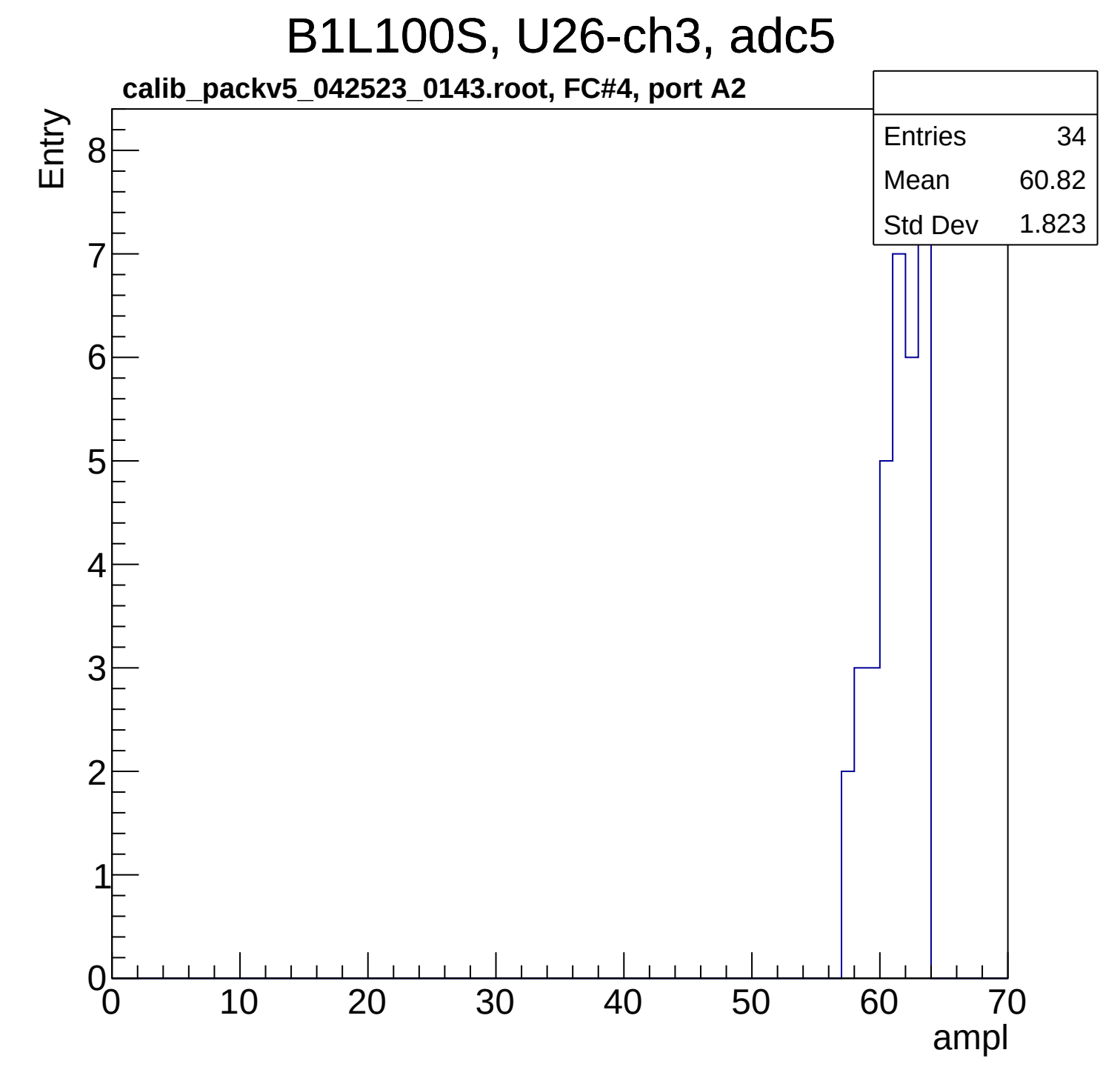
B1L100S, U26-ch3, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 60.82
Std Dev 1.823
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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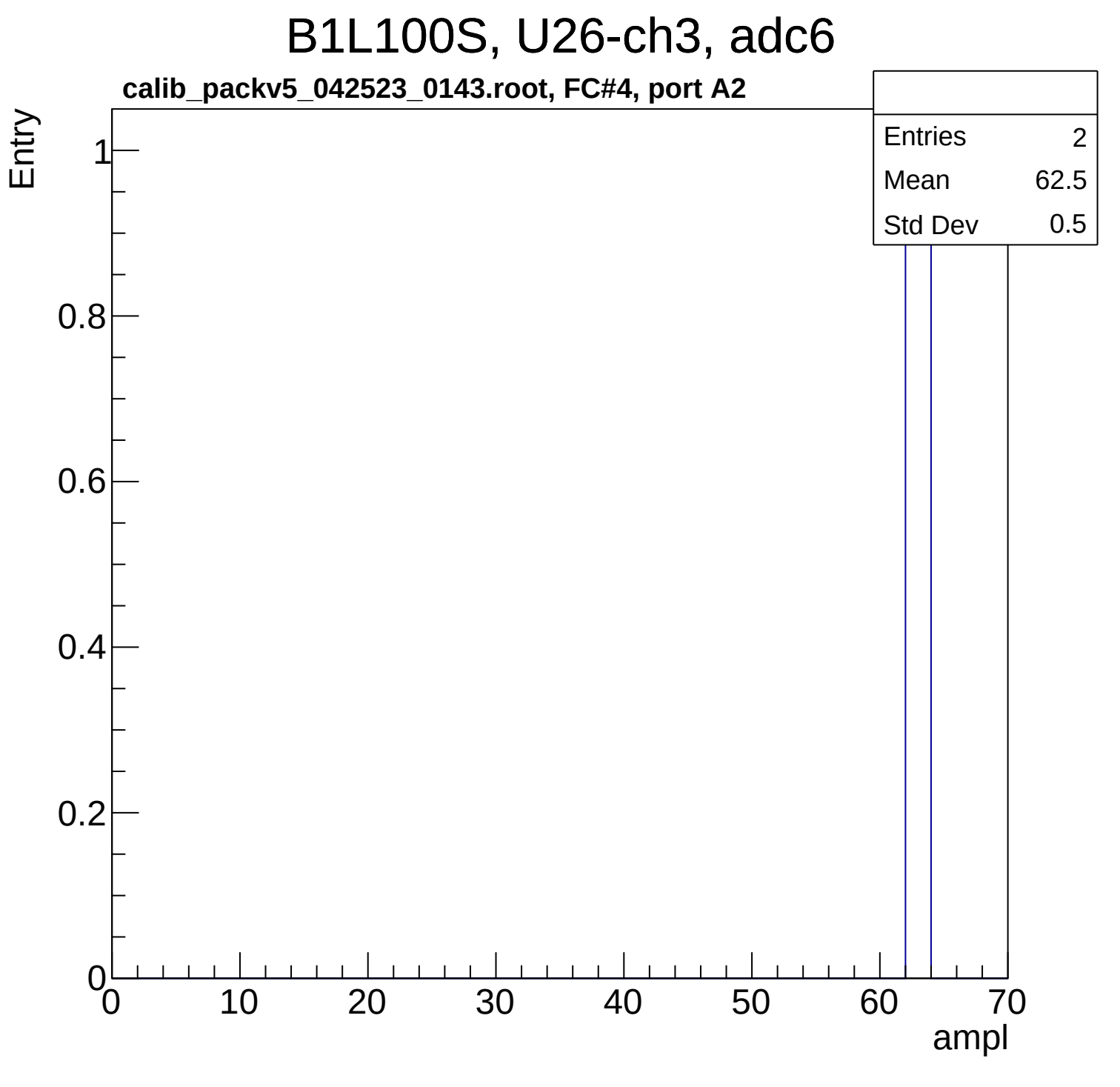
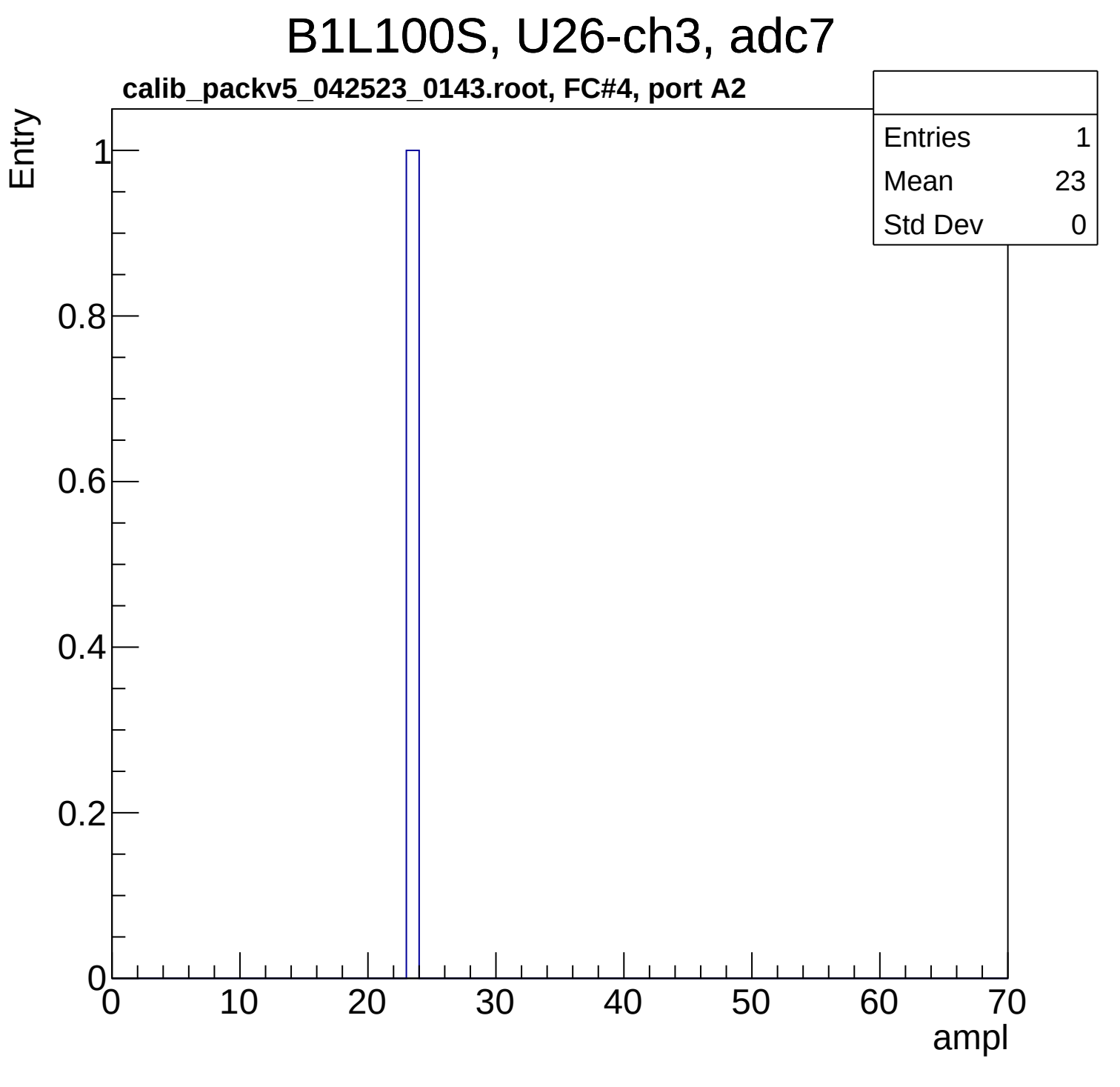

B1L100S, U26-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch3, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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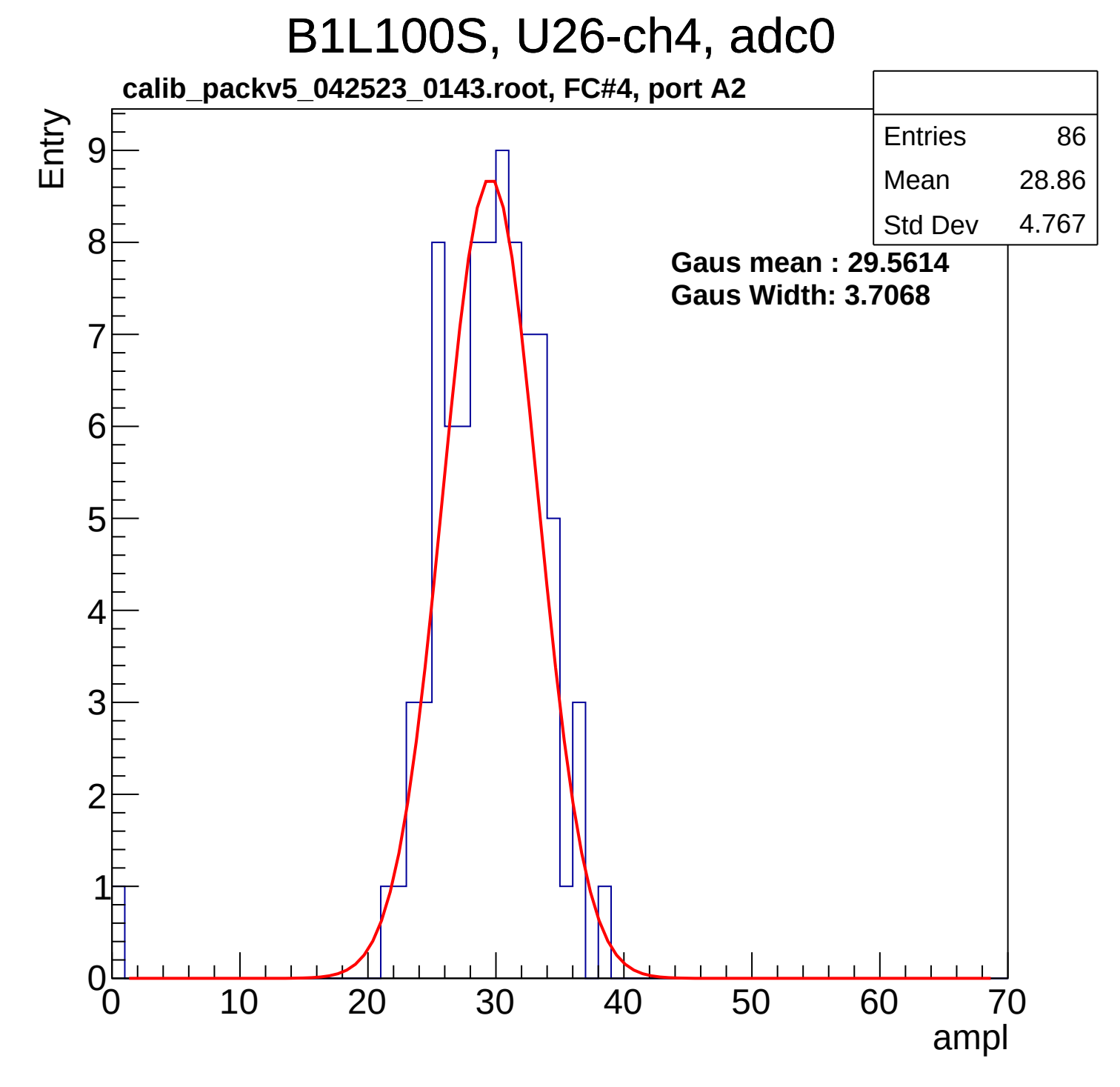
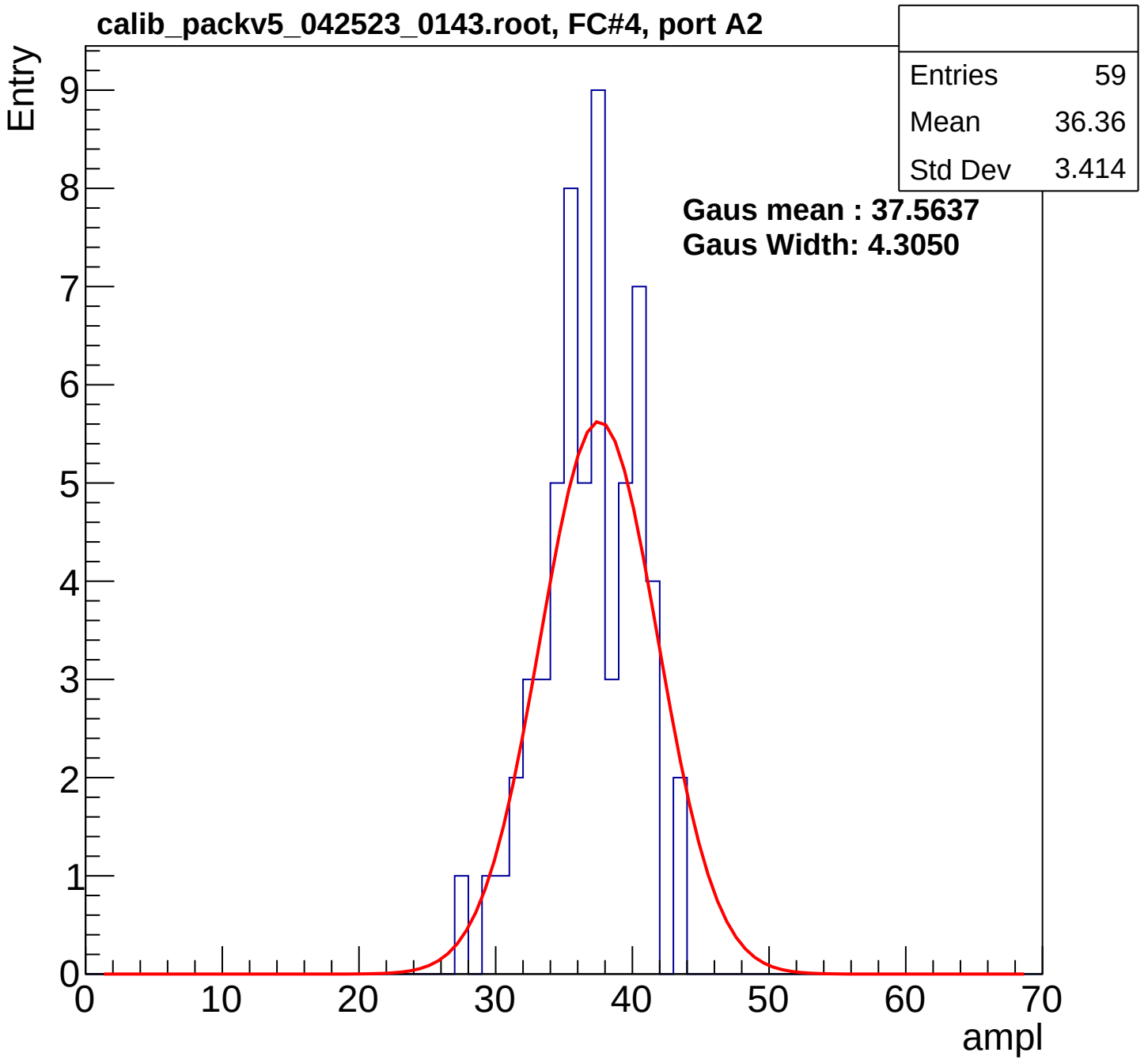
40
50
60
70
ampl


B1L100S, U26-ch4, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 28.86
Std Dev 4.767
Gaus mean : 29.5614
Gaus Width: 3.7068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.36
Std Dev 3.414
Gaus mean : 37.5637
Gaus Width: 4.3050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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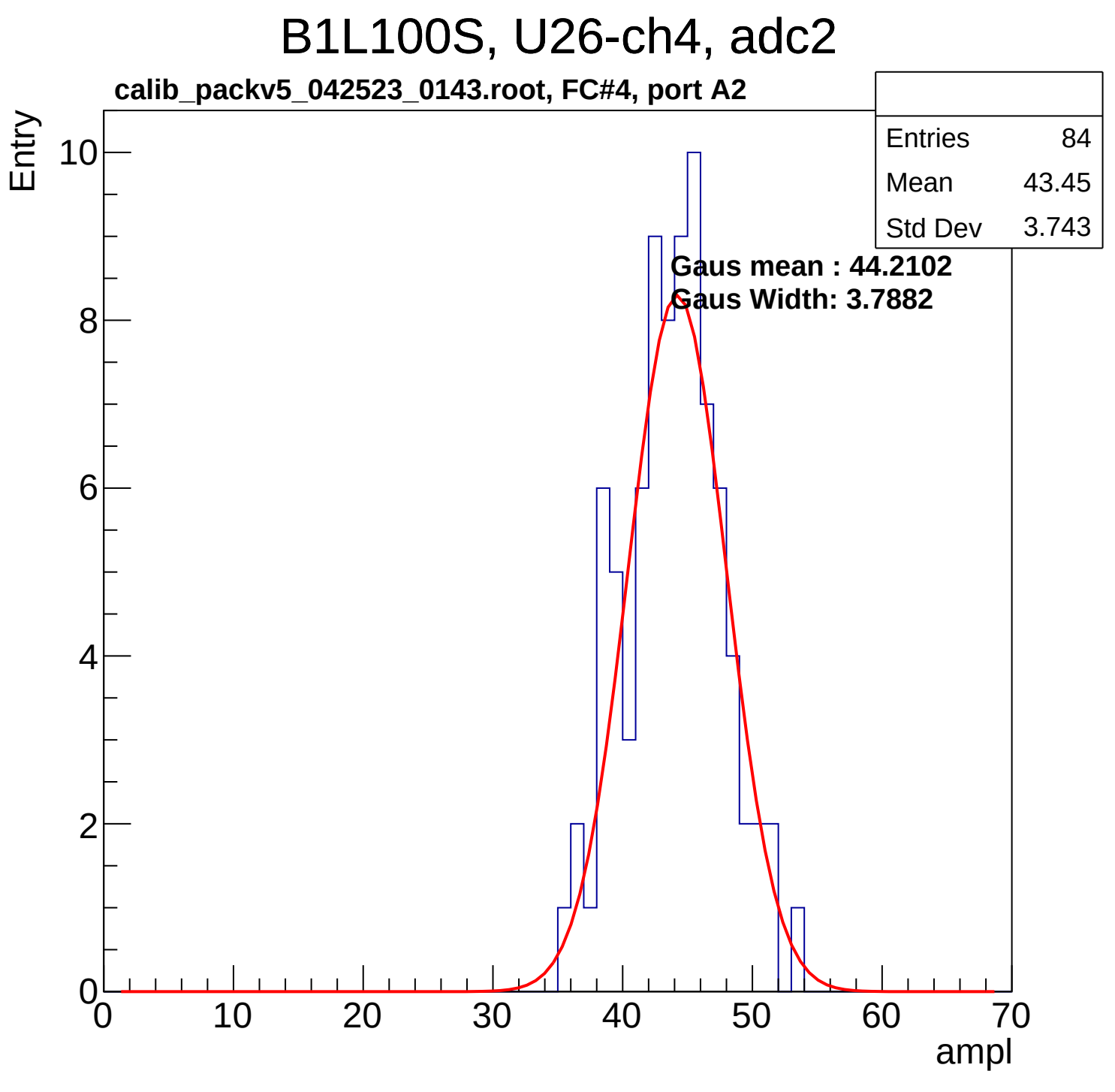
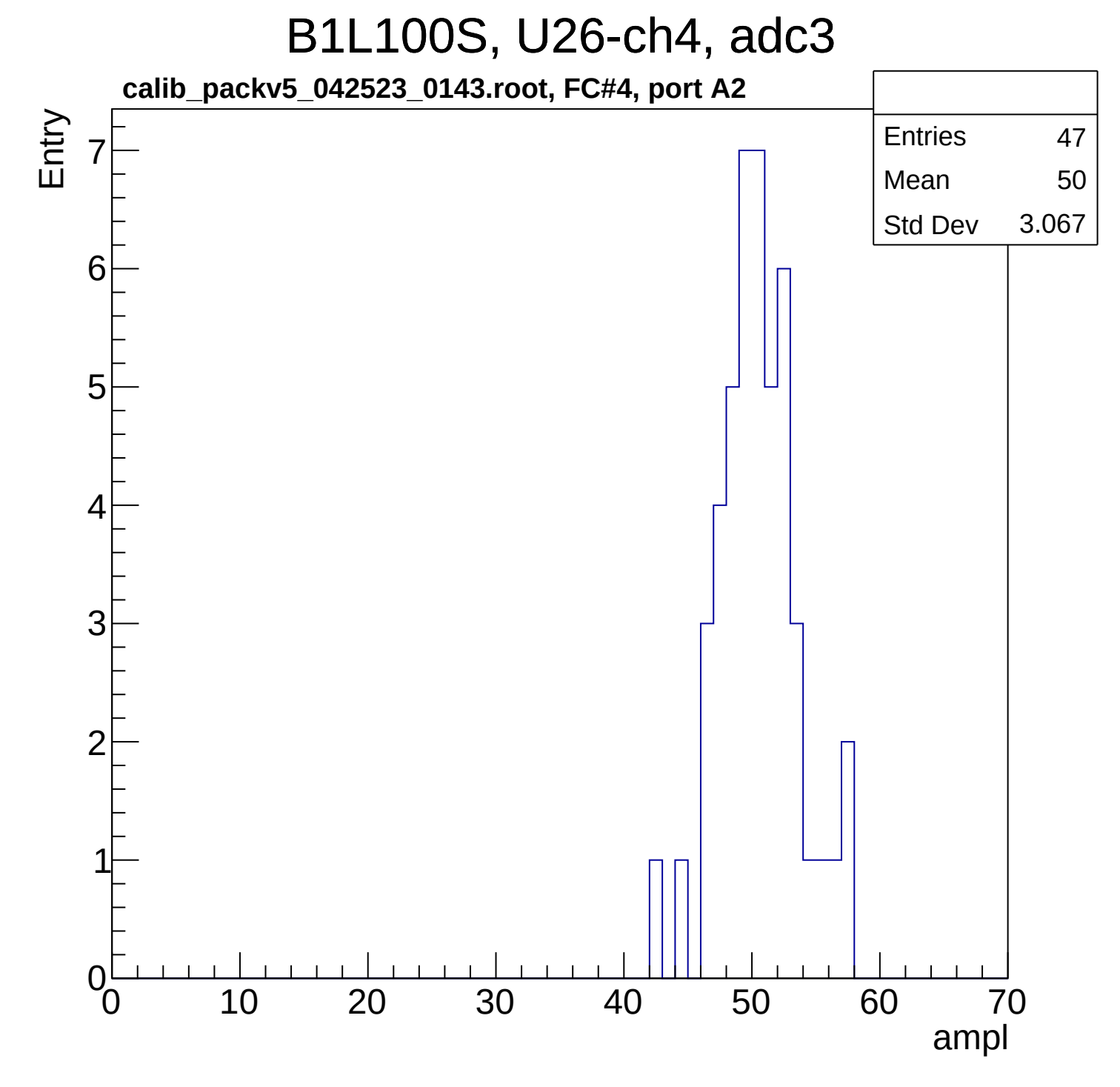
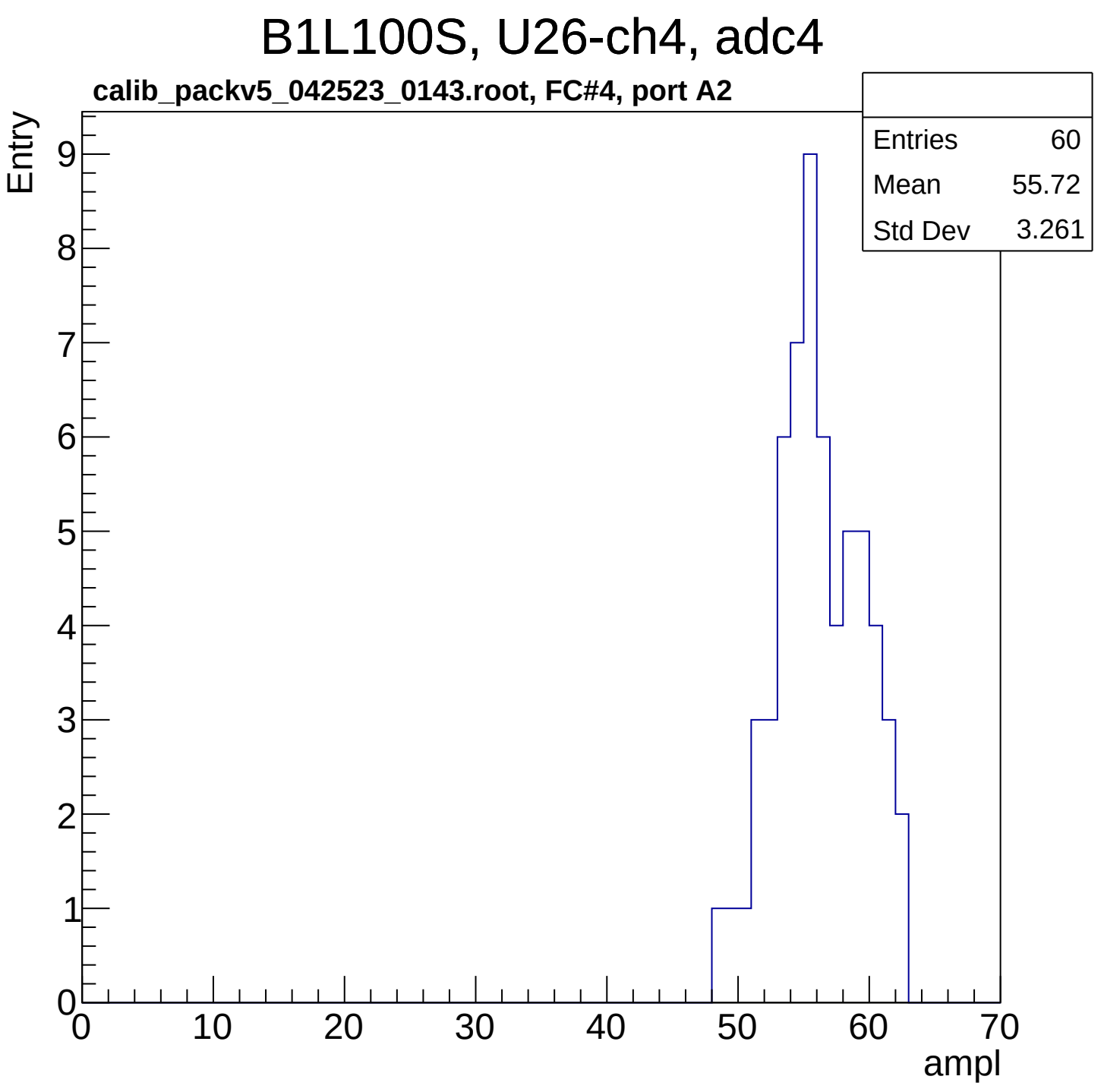

B1L100S, U26-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 43.45
Std Dev 3.743
Gaus mean : 44.2102
Gaus Width: 3.7882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch4, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 50
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.72
Std Dev 3.261
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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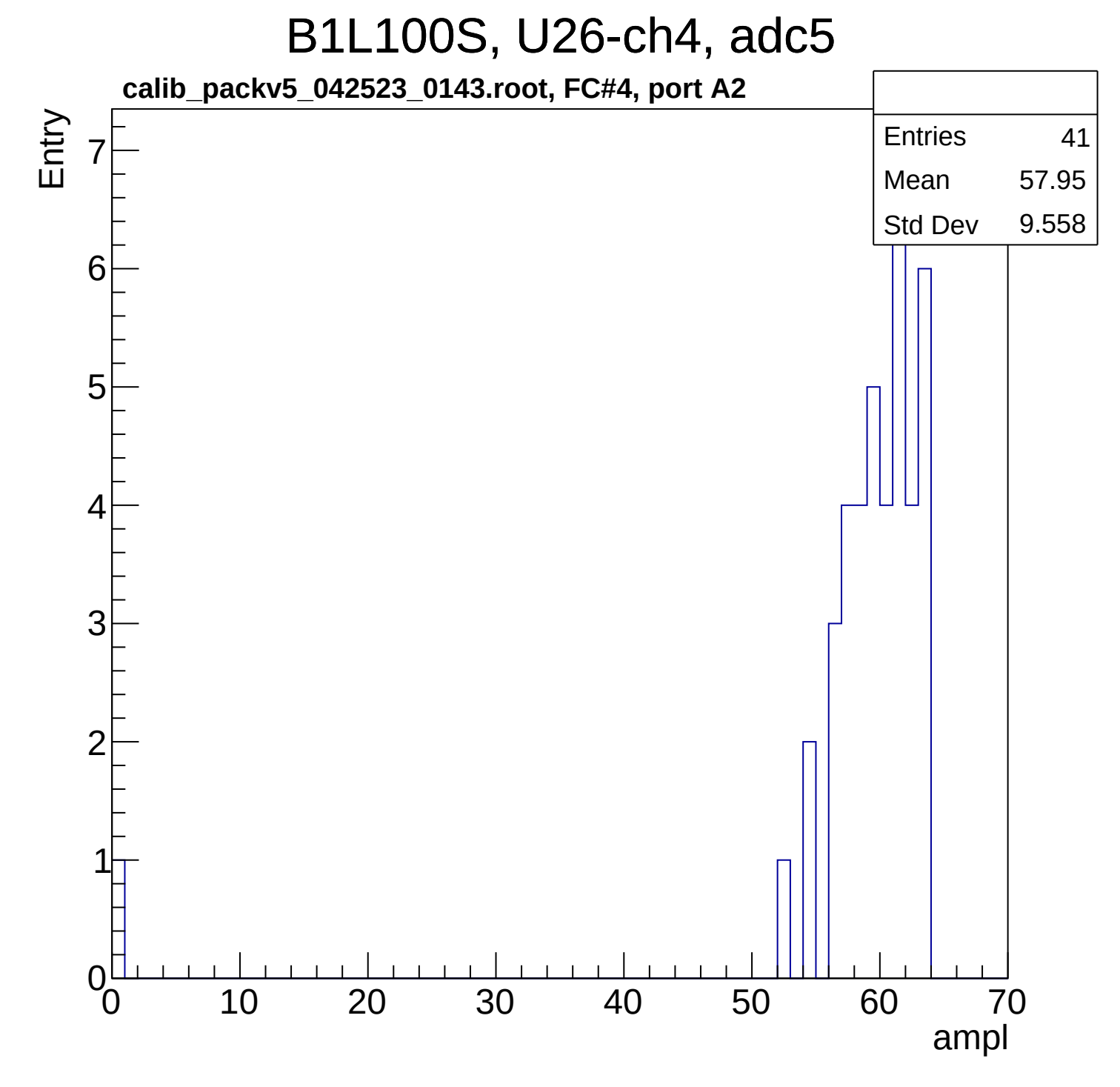
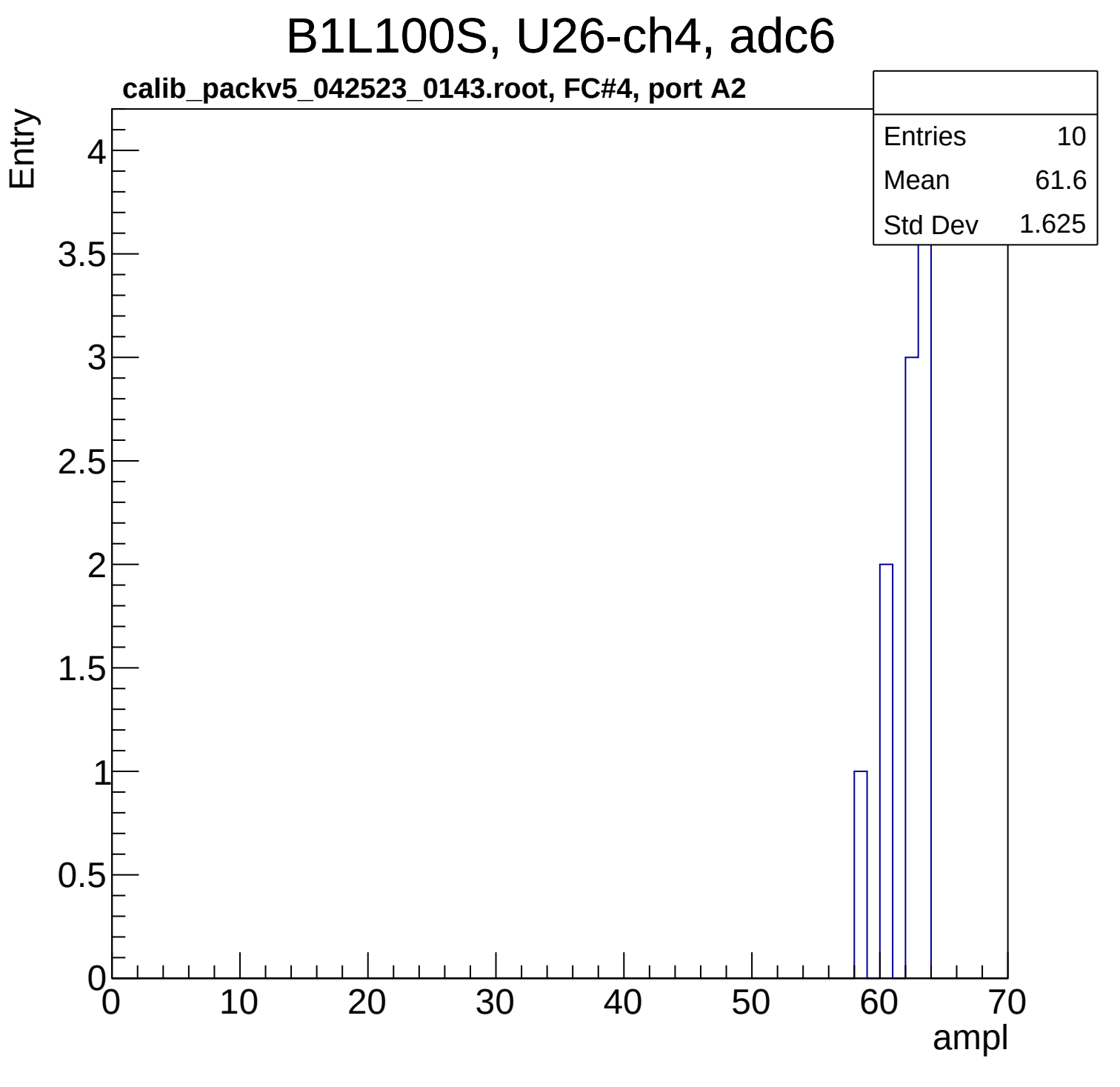

B1L100S, U26-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 57.95
Std Dev 9.558
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch4, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.6
Std Dev 1.625
Entry
ampl
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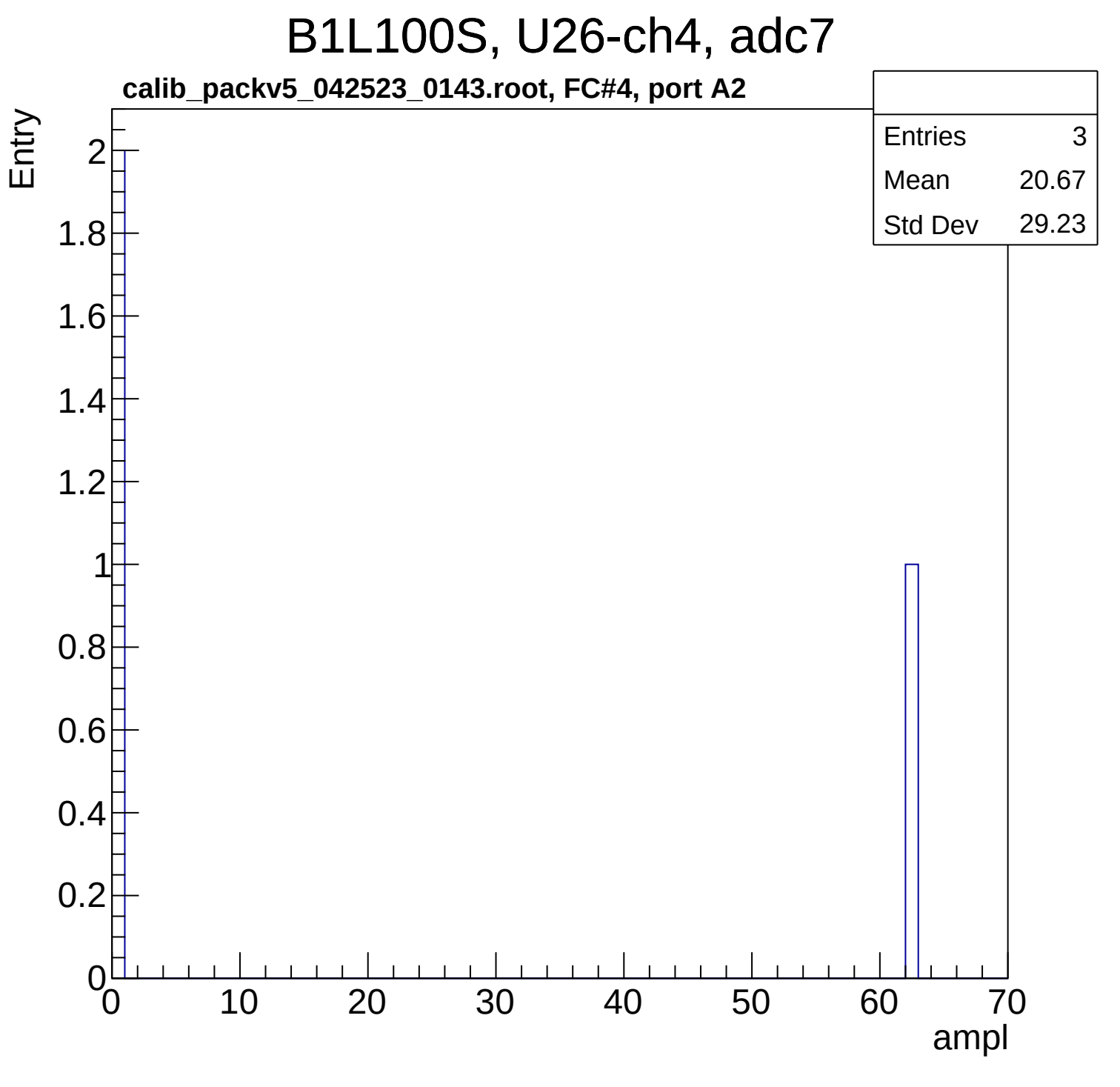
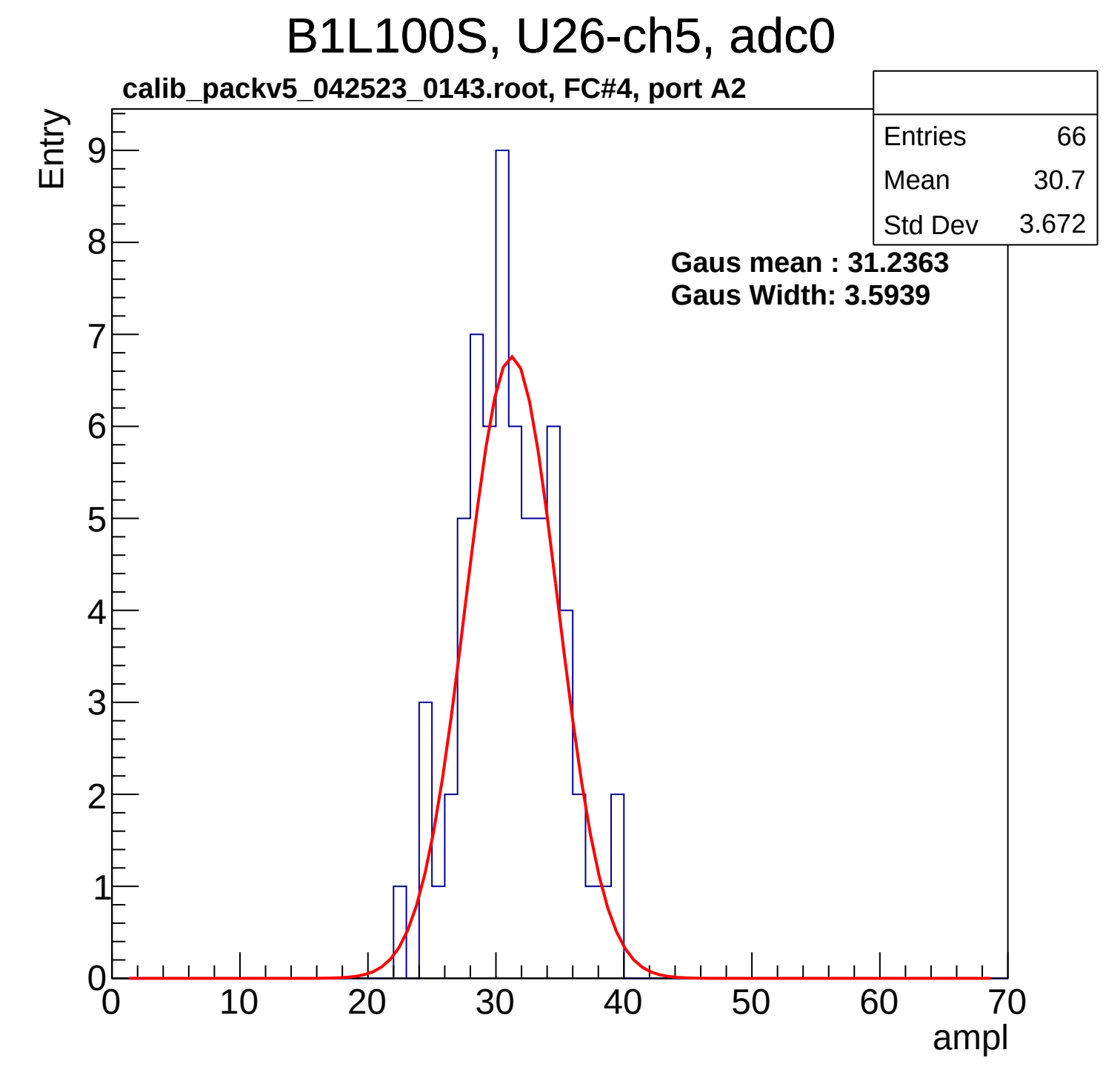
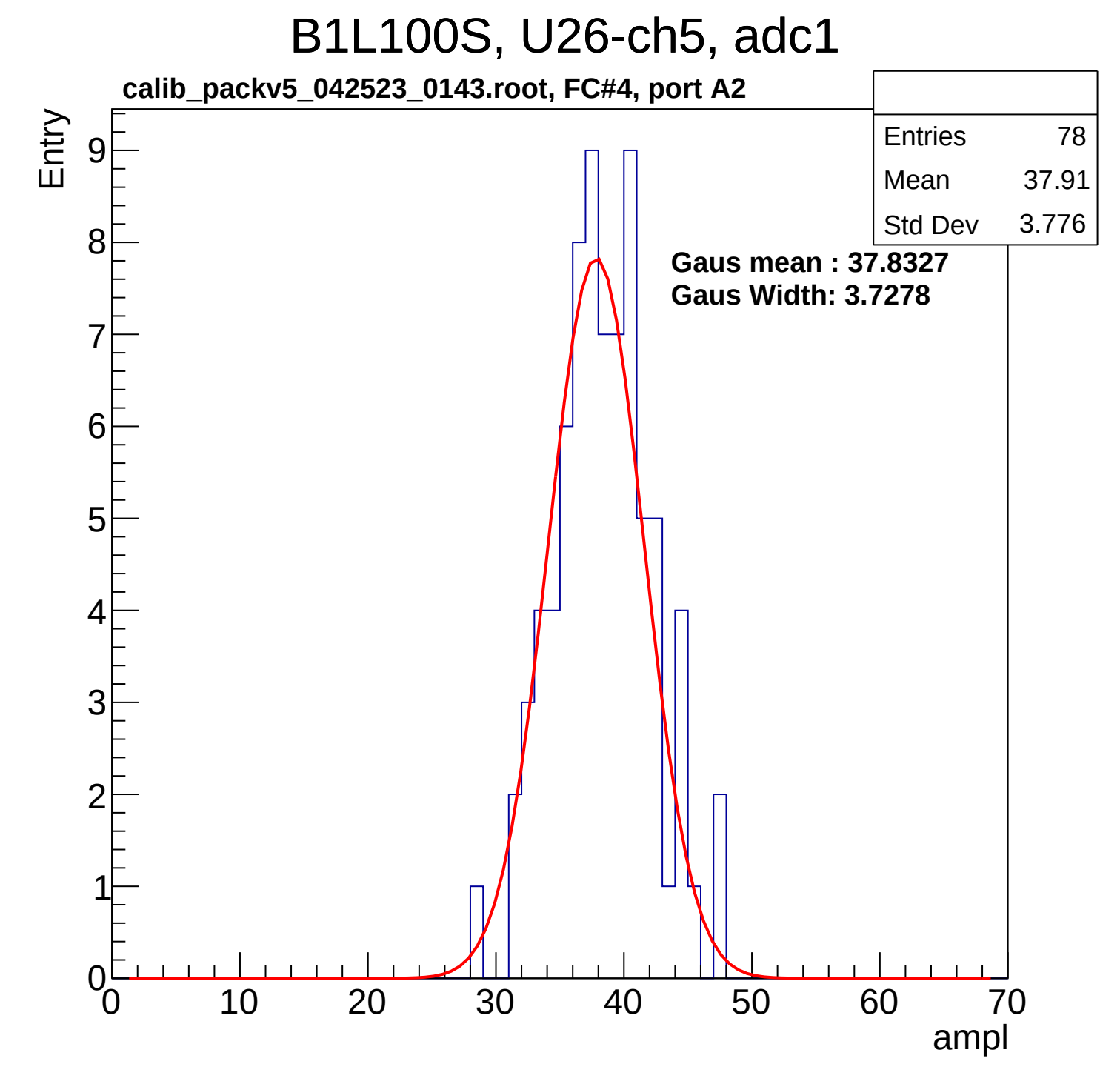
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U26-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.7
Std Dev 3.672
Gaus mean : 31.2363
Gaus Width: 3.5939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.91
Std Dev 3.776
Gaus mean : 37.8327
Gaus Width: 3.7278
Entry
ampl
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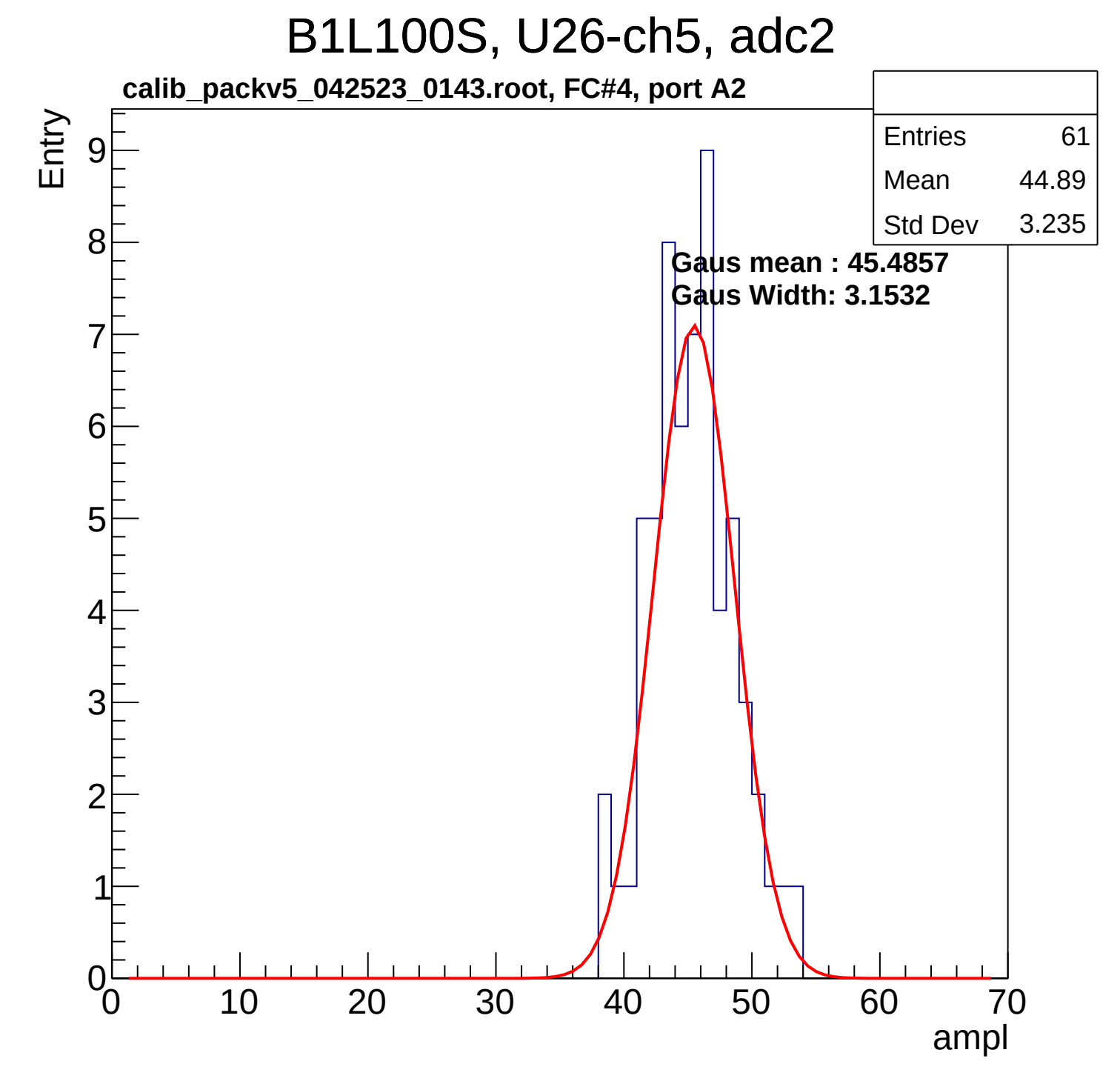
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U26-ch5, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 44.89
Std Dev 3.235
Gaus mean : 45.4857
Gaus Width: 3.1532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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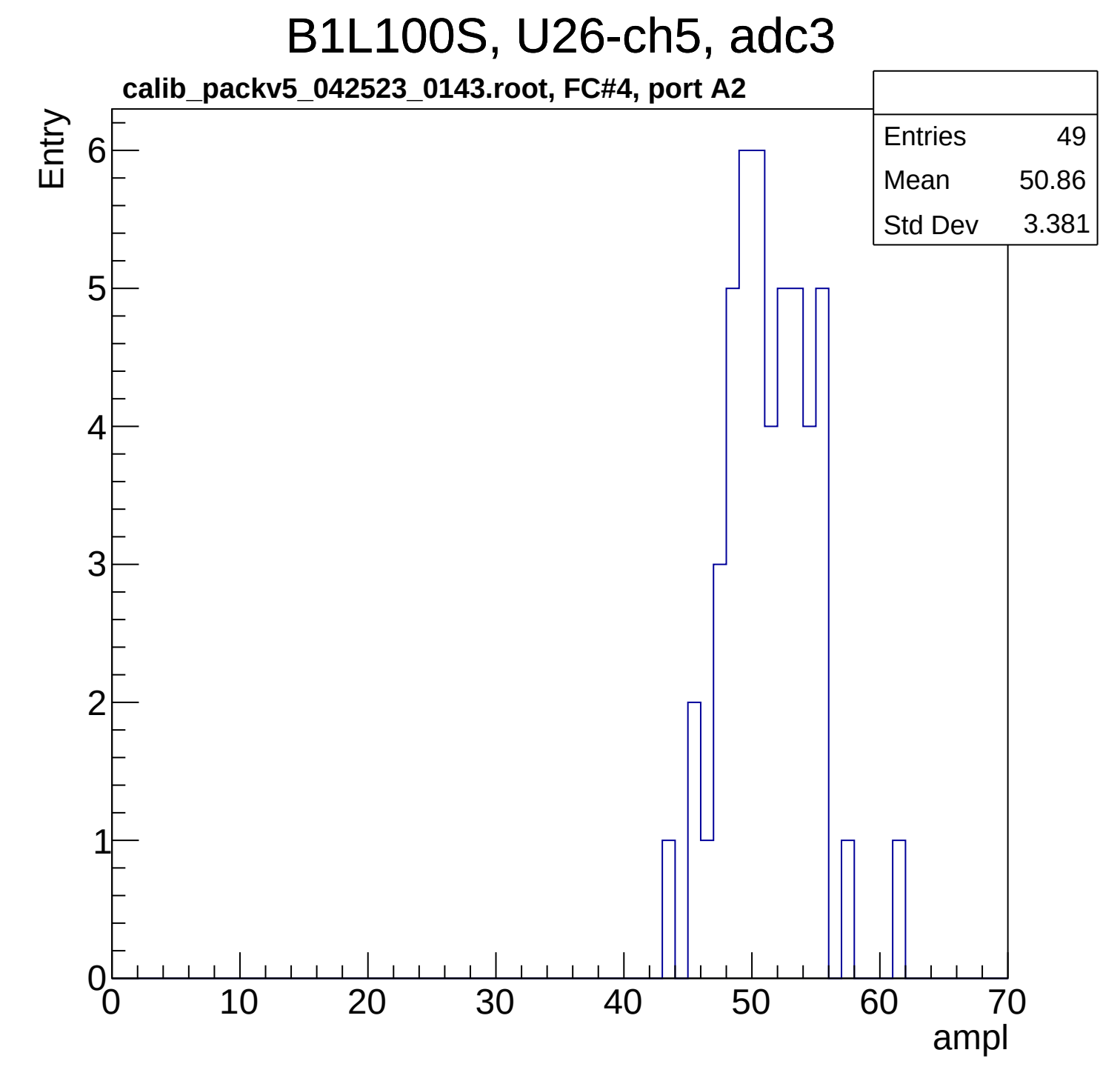
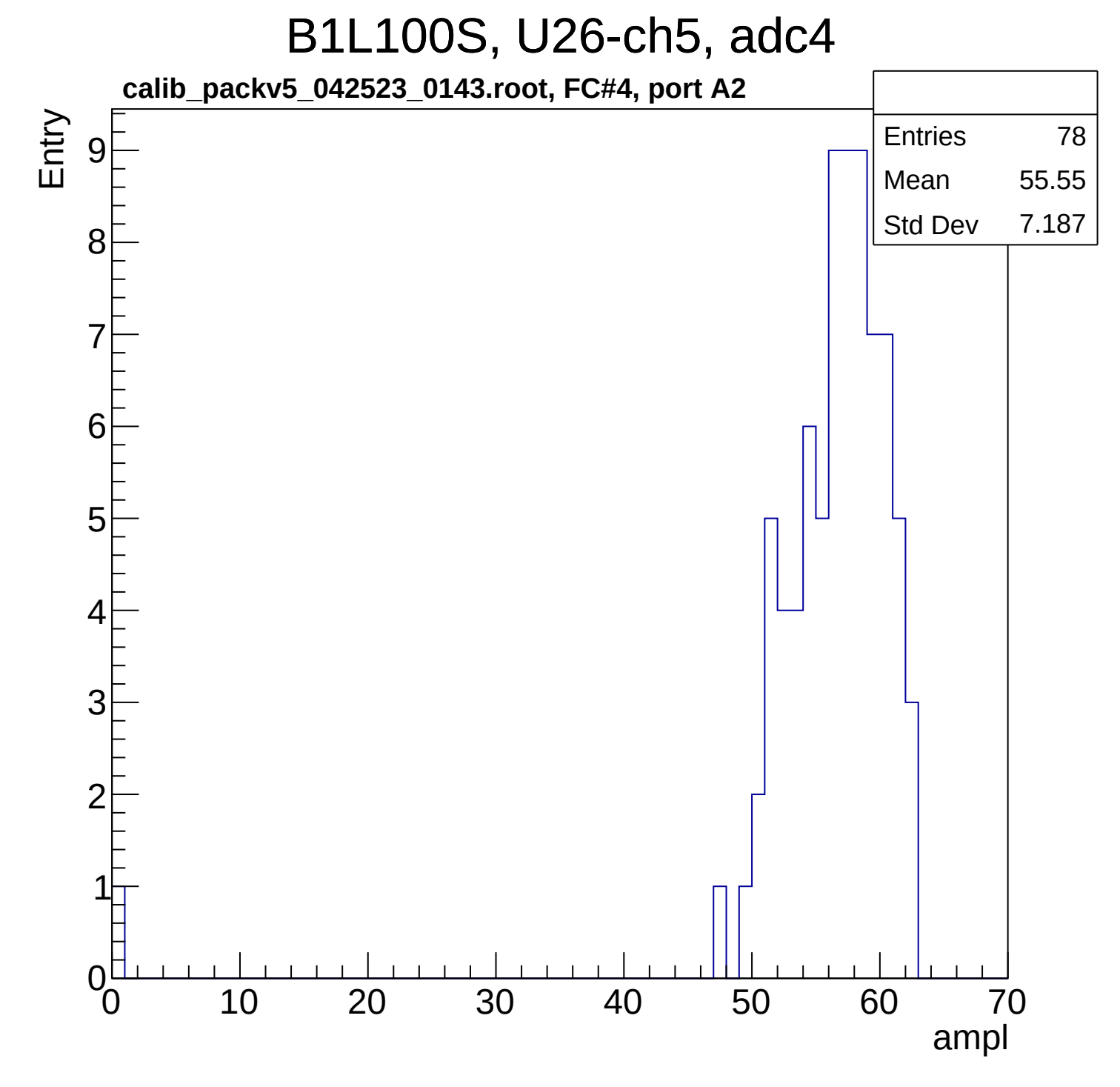

B1L100S, U26-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 50.86
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U26-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 55.55
Std Dev 7.187
Entry
ampl
0
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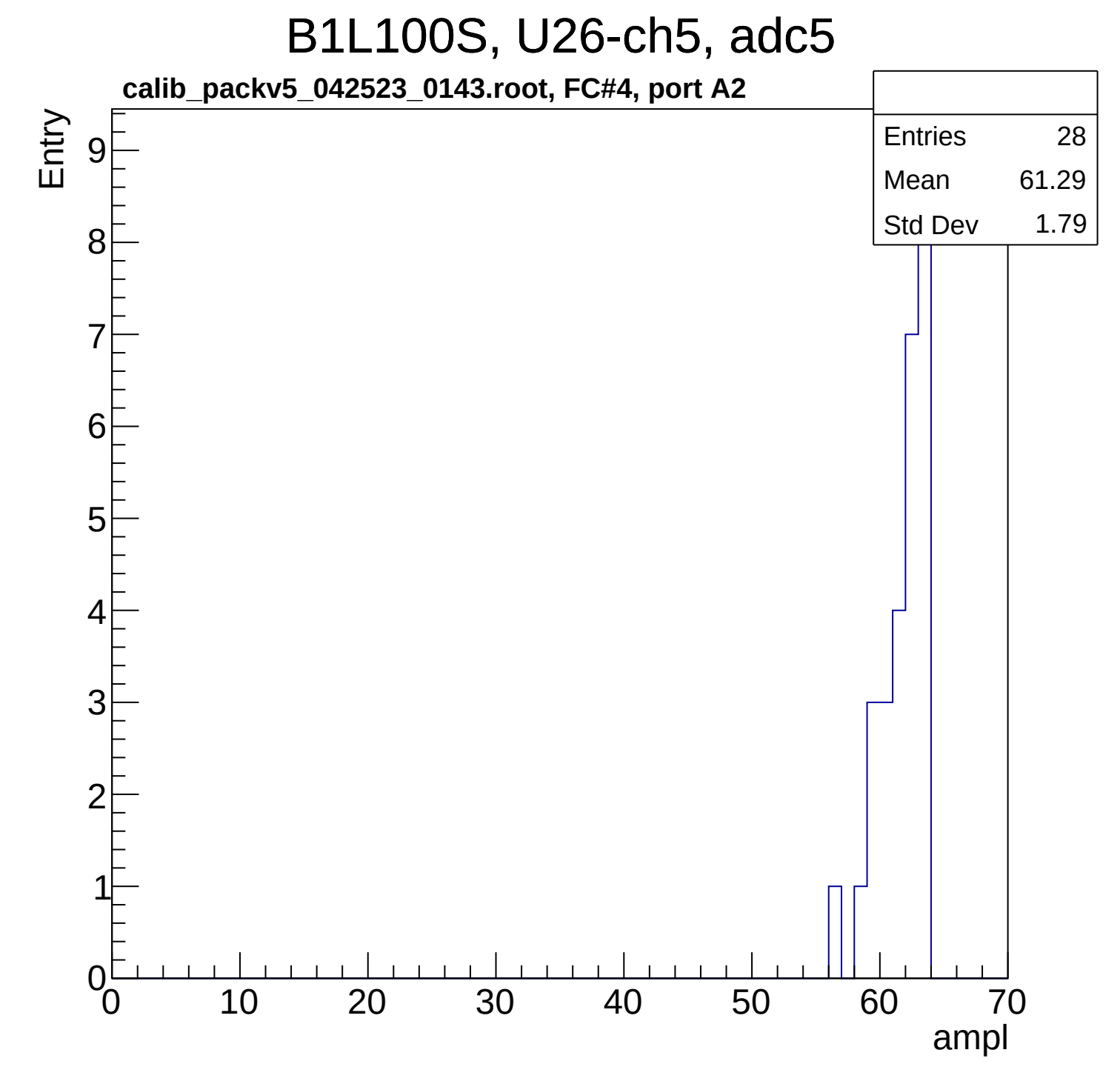
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch5, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 61.29
Std Dev 1.79
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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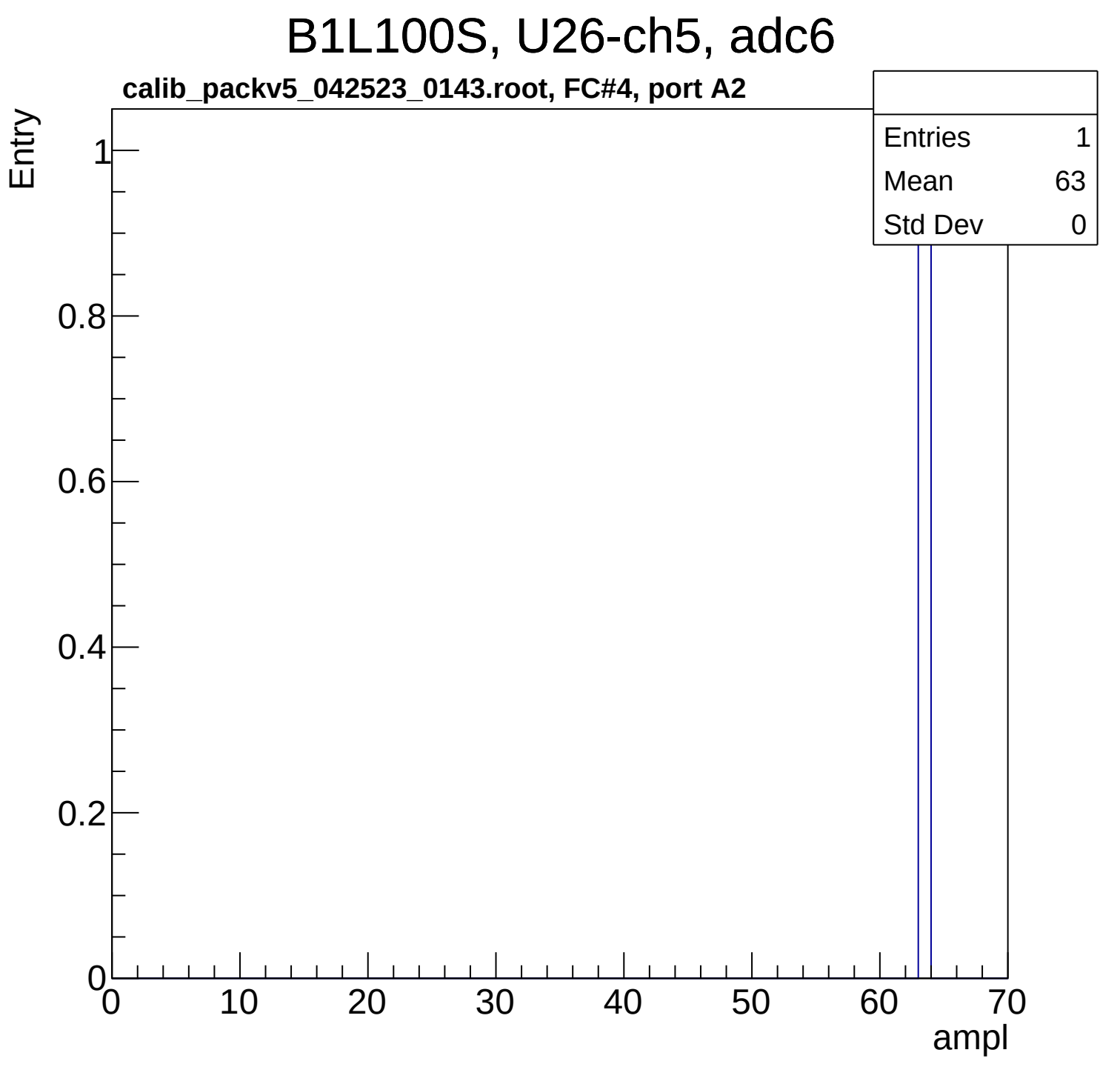
B1L100S, U26-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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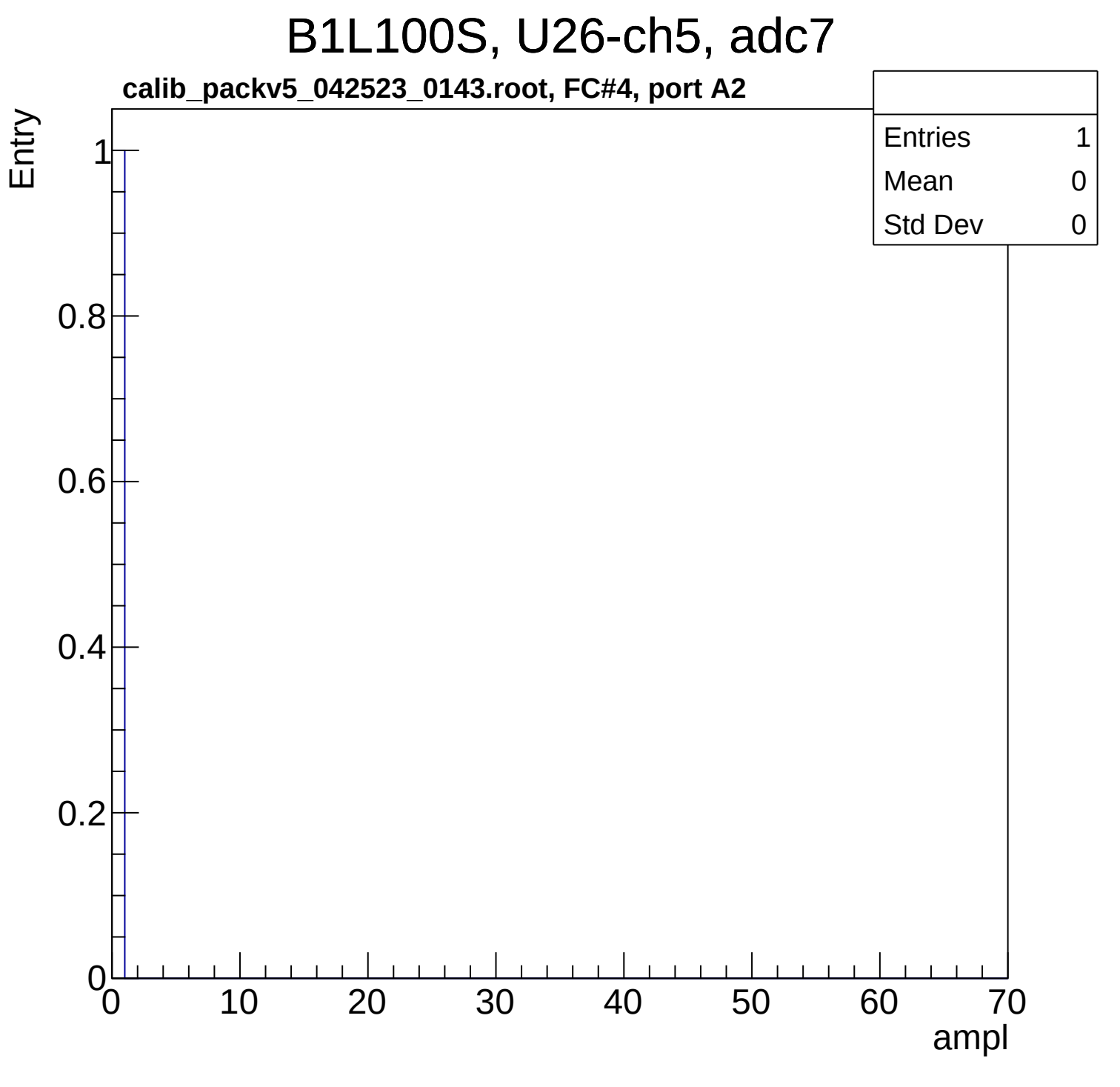
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch5, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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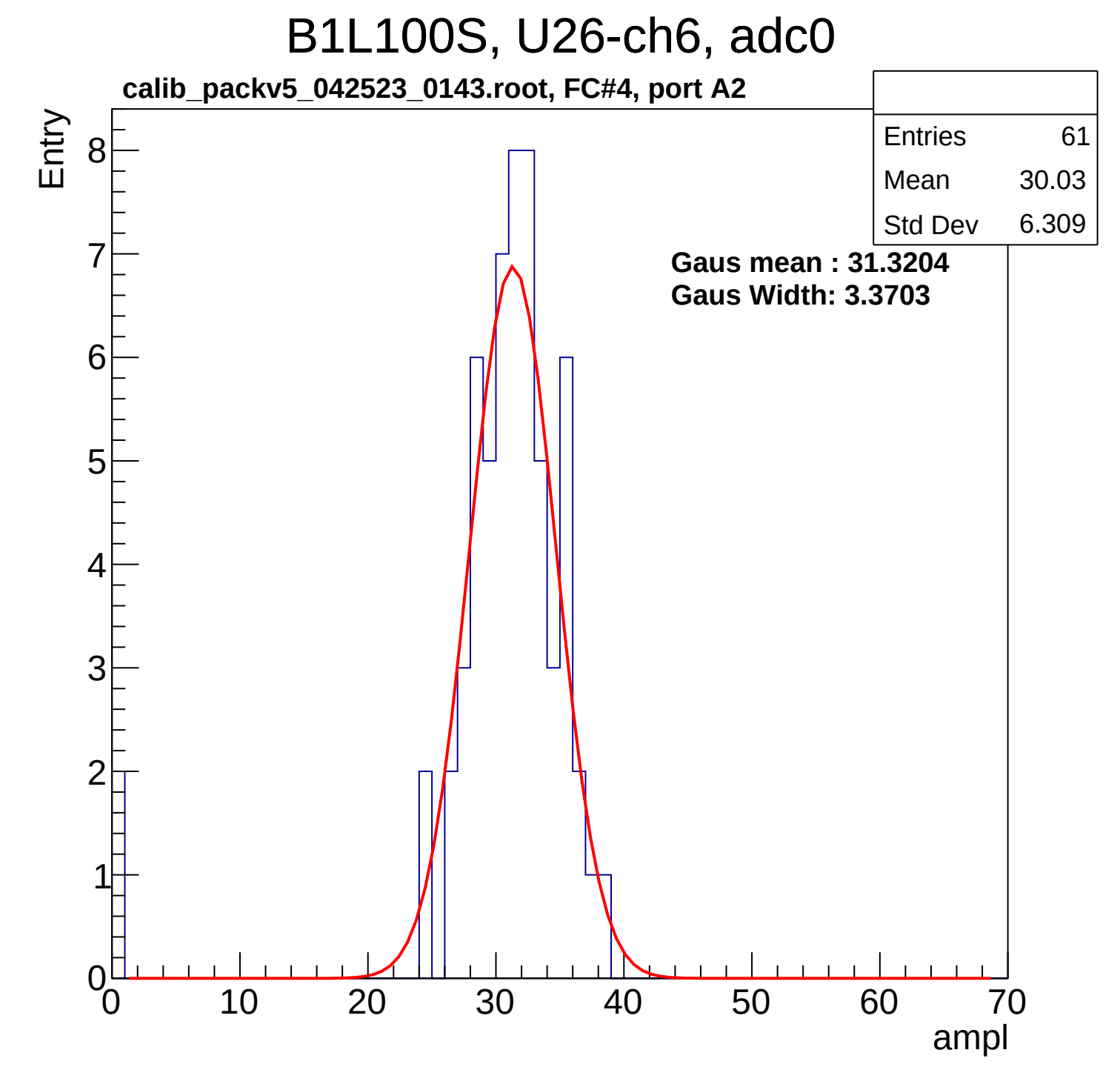
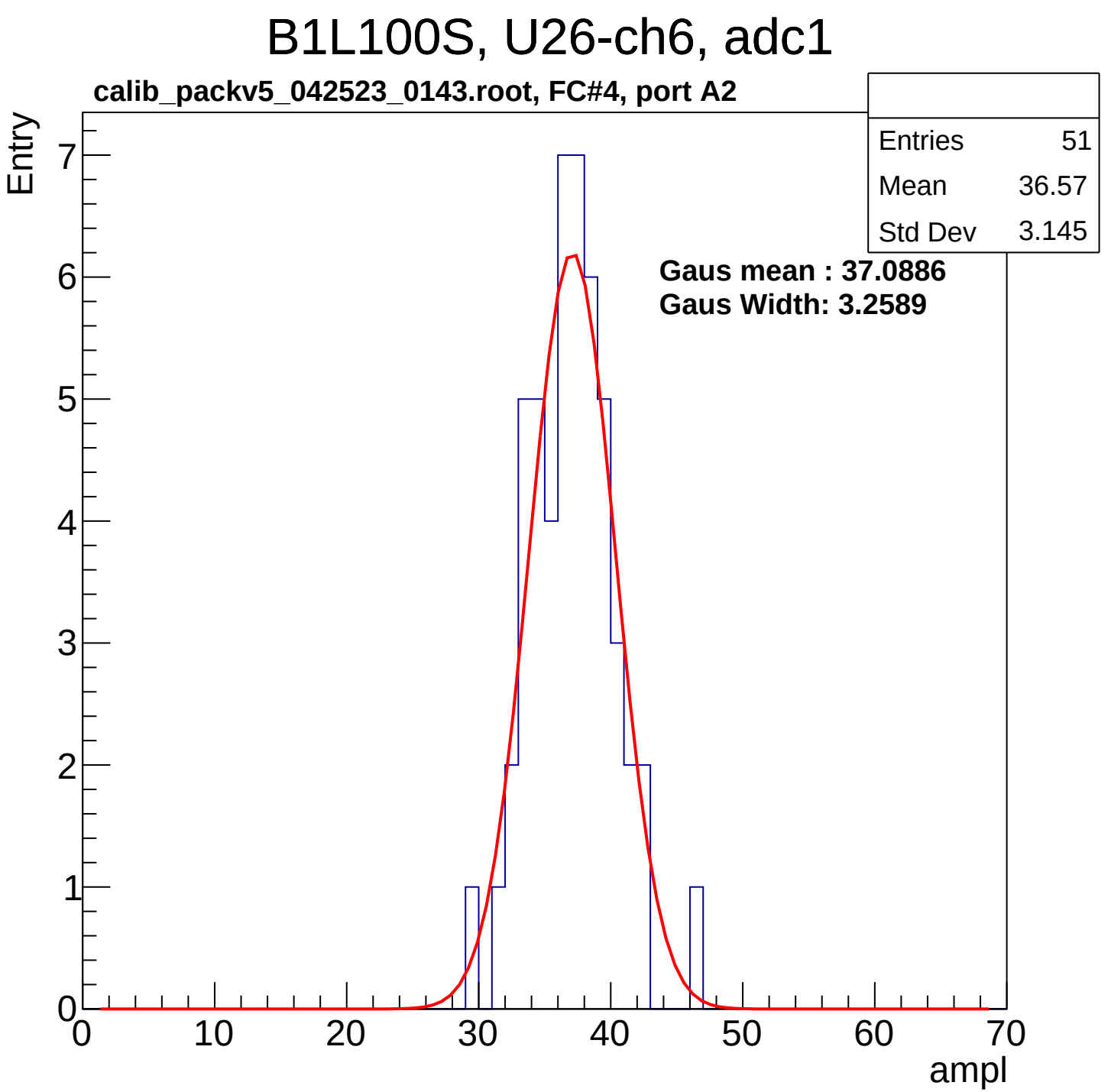
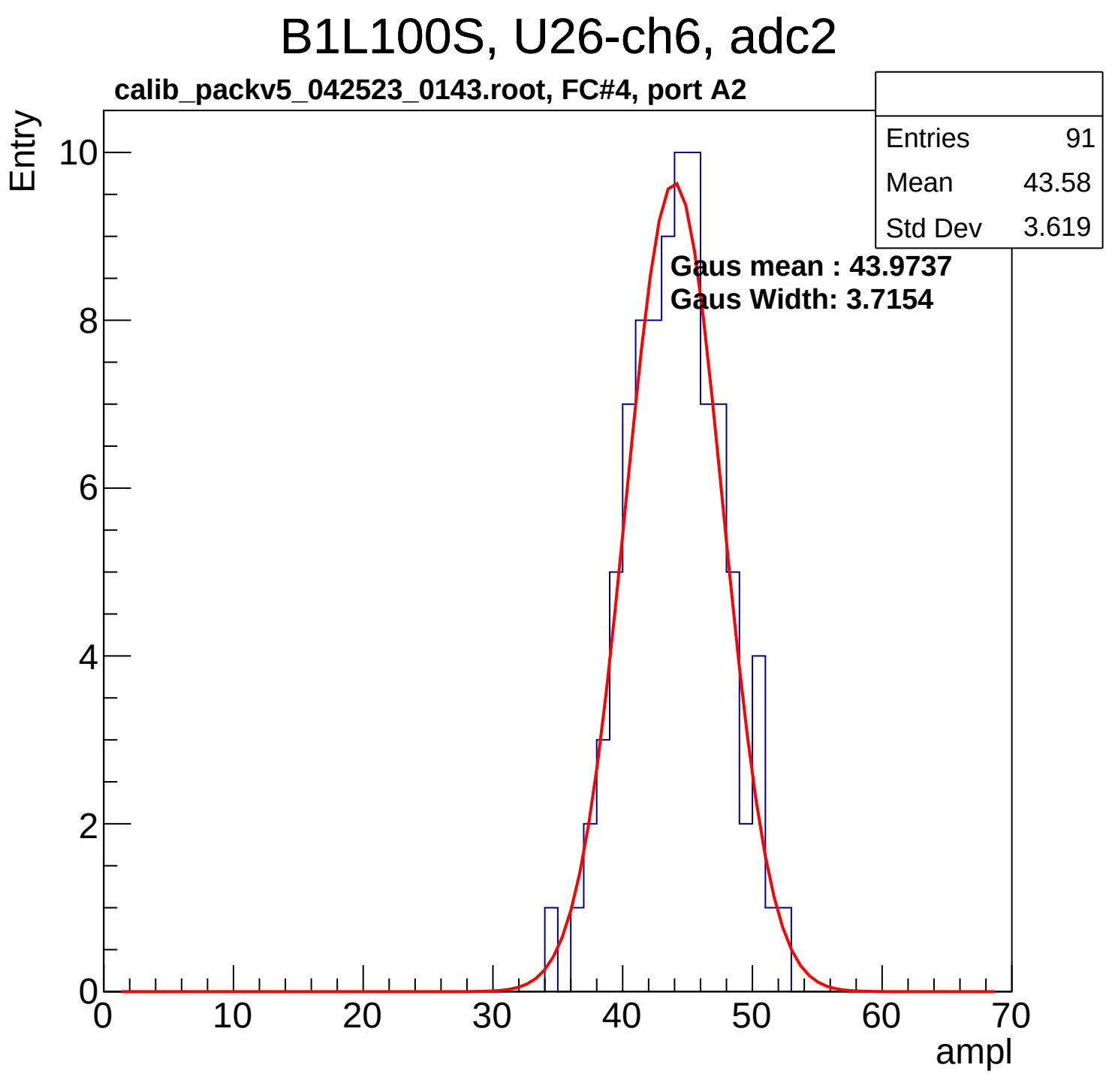
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.03
Std Dev 6.309
Gaus mean : 31.3204
Gaus Width: 3.3703
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 36.57
Std Dev 3.145
Gaus mean : 37.0886
Gaus Width: 3.2589
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch6, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 43.58
Std Dev 3.619
Gaus mean : 43.9737
Gaus Width: 3.7154
Entry
ampl
0
2
4
6
8
10
0
10
20
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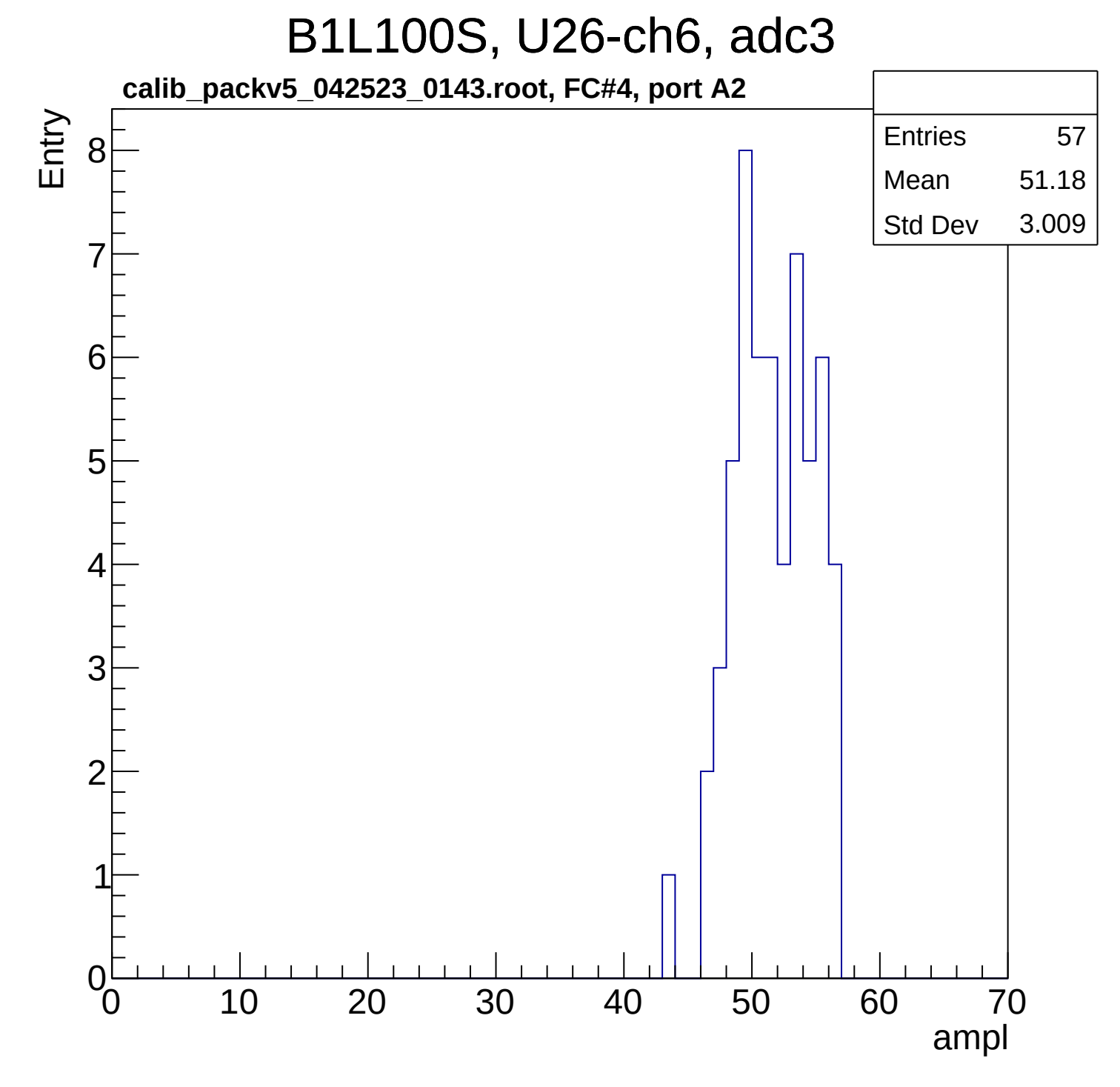
40
50
60
70


B1L100S, U26-ch6, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 51.18
Std Dev 3.009
Entry
ampl
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4
5
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7
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0
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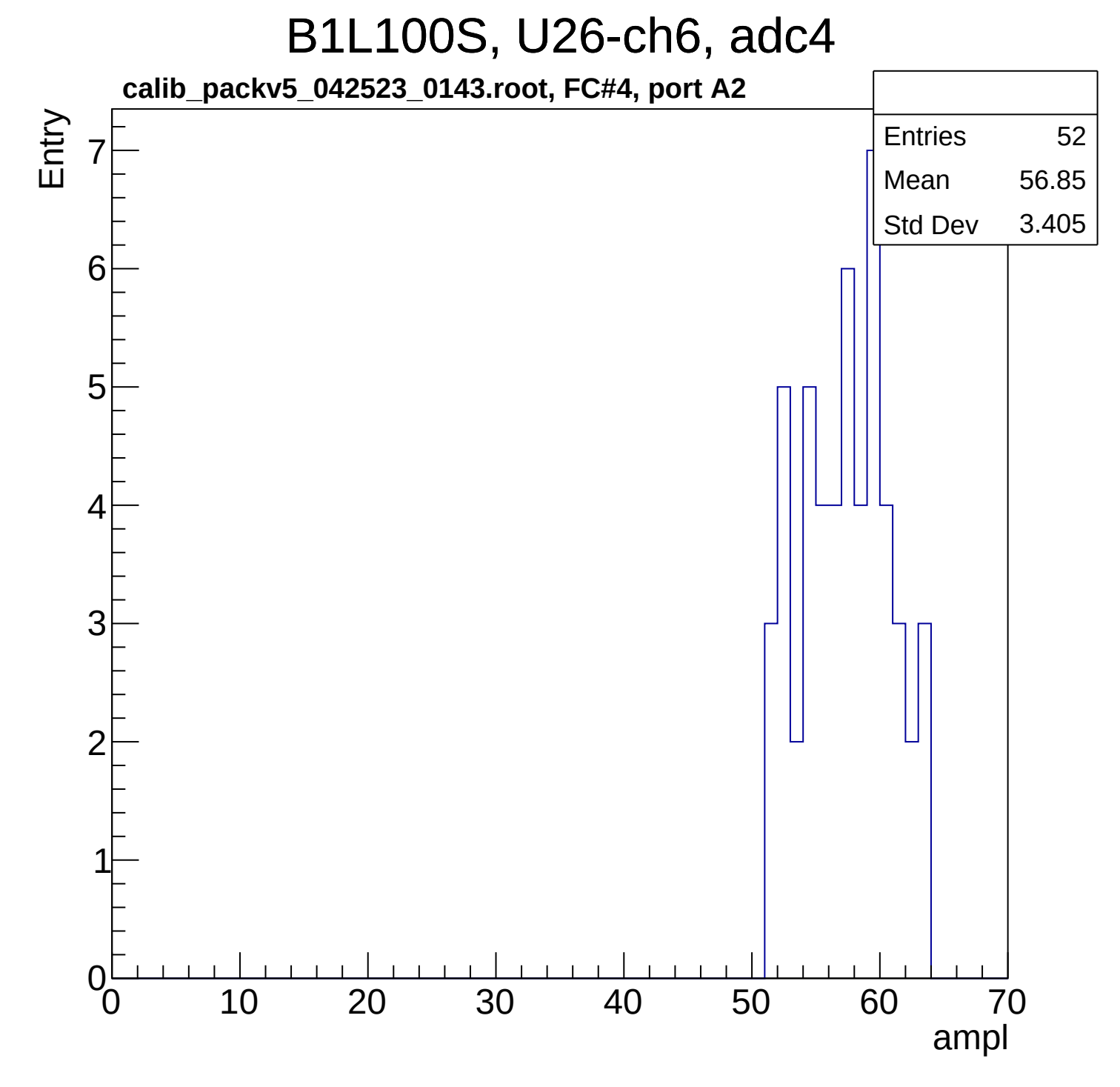
60
70


B1L100S, U26-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.85
Std Dev 3.405
Entry
ampl
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3
4
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6
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0
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20
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40
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60
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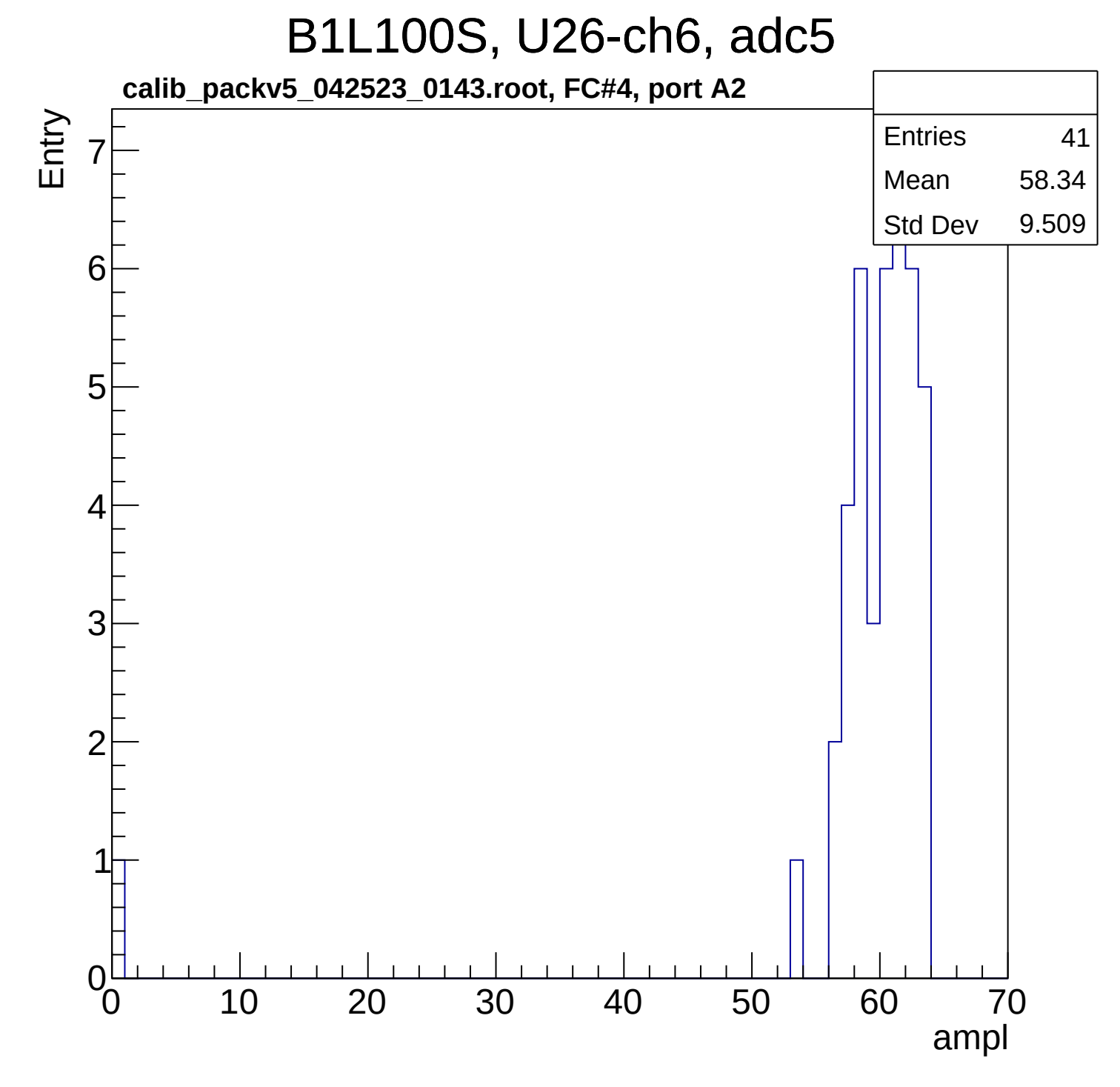
B1L100S, U26-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.34
Std Dev 9.509
Entry
ampl
0
1
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3
4
5
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0
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20
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40
50
60
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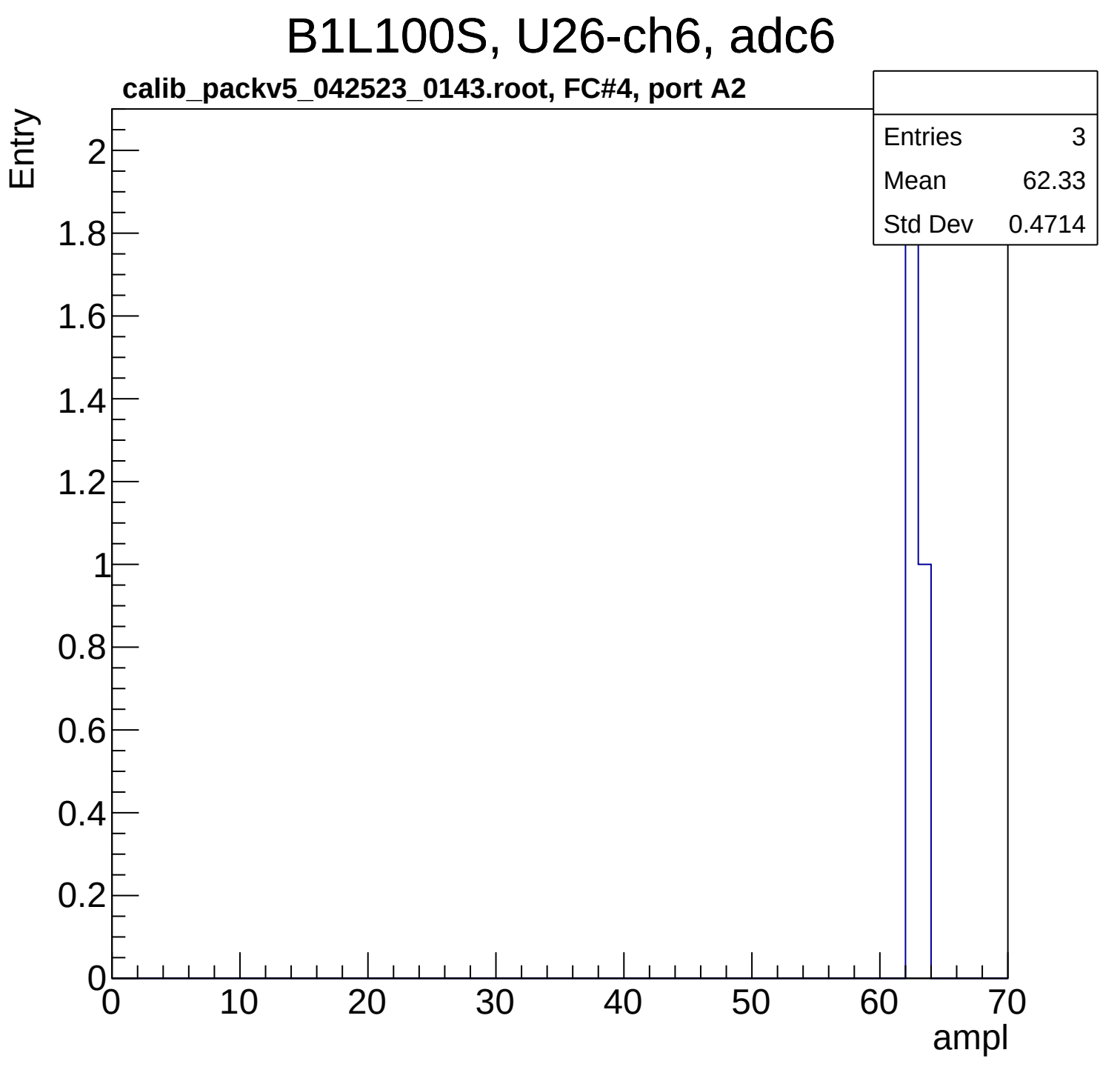
B1L100S, U26-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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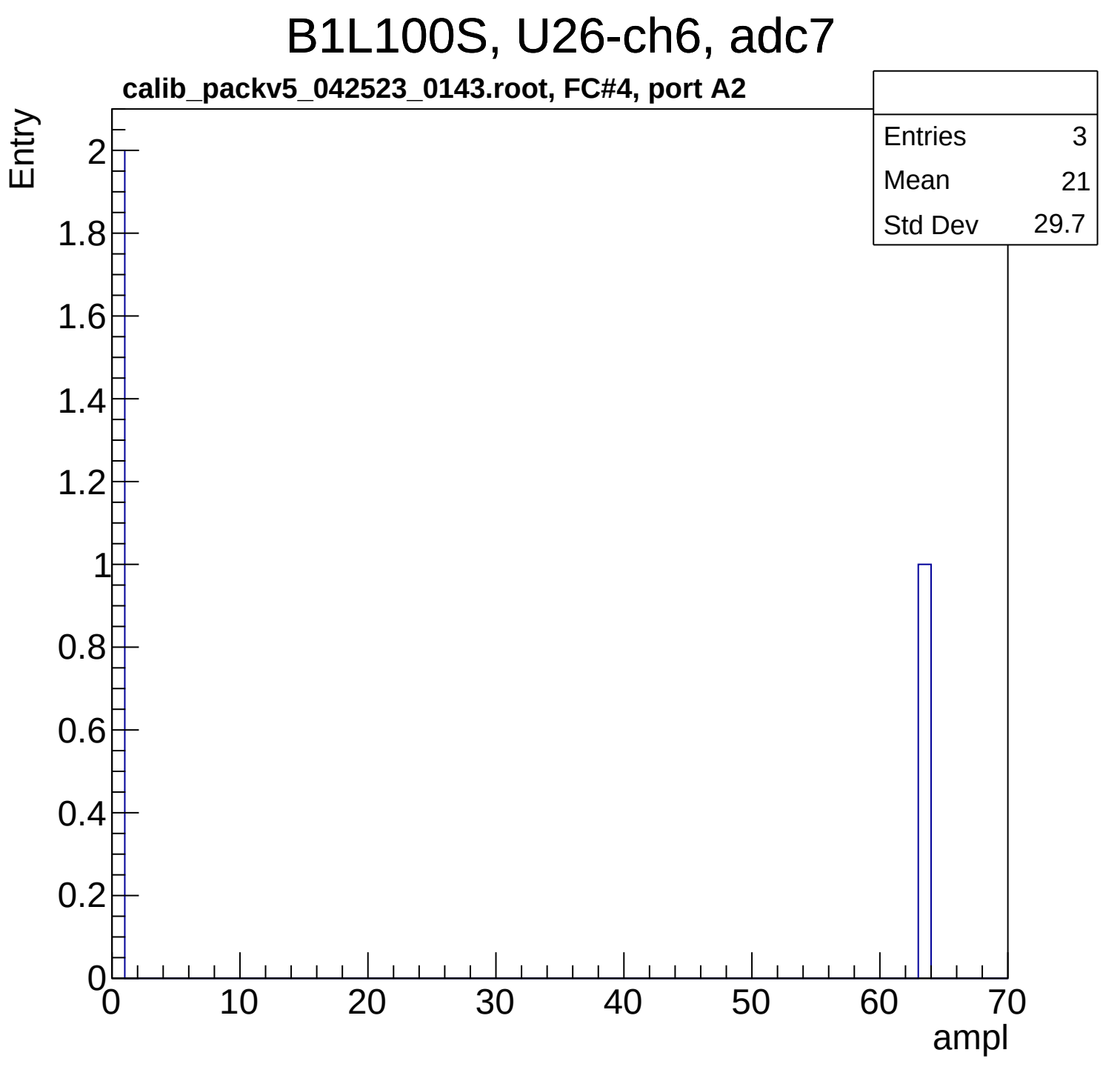
60
70


B1L100S, U26-ch6, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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60
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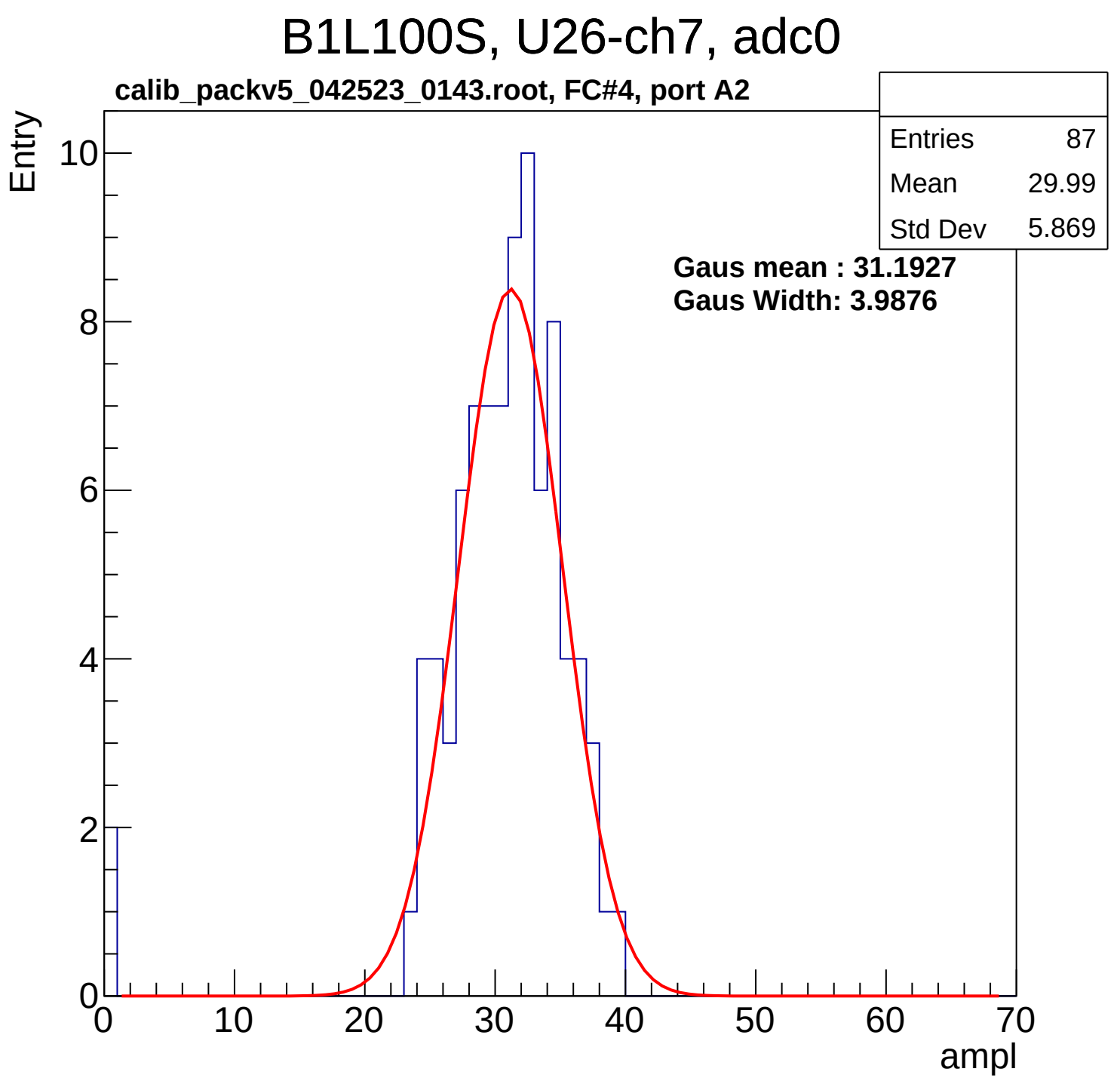
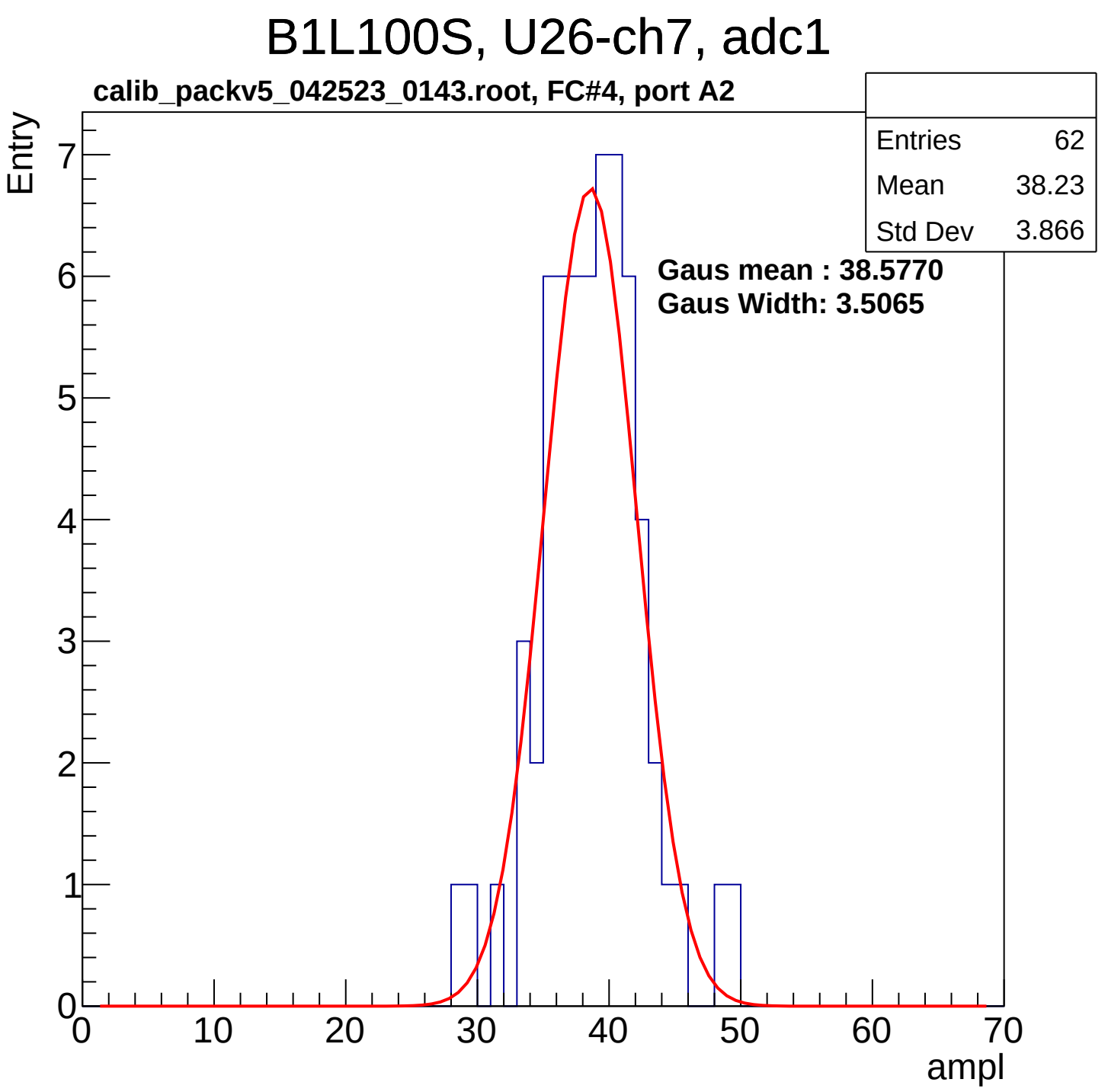
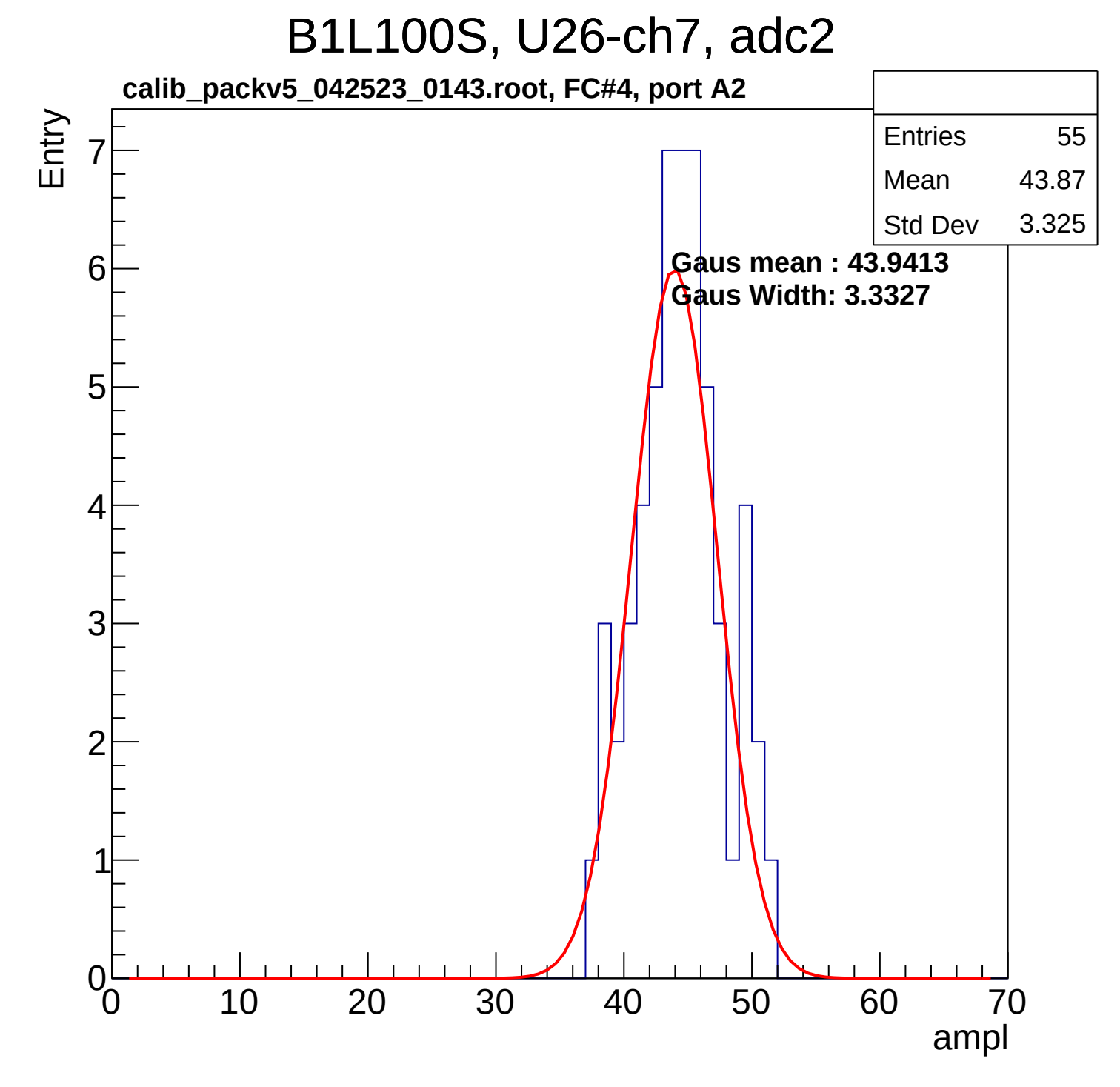

B1L100S, U26-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 29.99
Std Dev 5.869
Gaus mean : 31.1927
Gaus Width: 3.9876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch7, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 38.23
Std Dev 3.866
Gaus mean : 38.5770
Gaus Width: 3.5065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 43.87
Std Dev 3.325
Gaus mean : 43.9413
Gaus Width: 3.3327
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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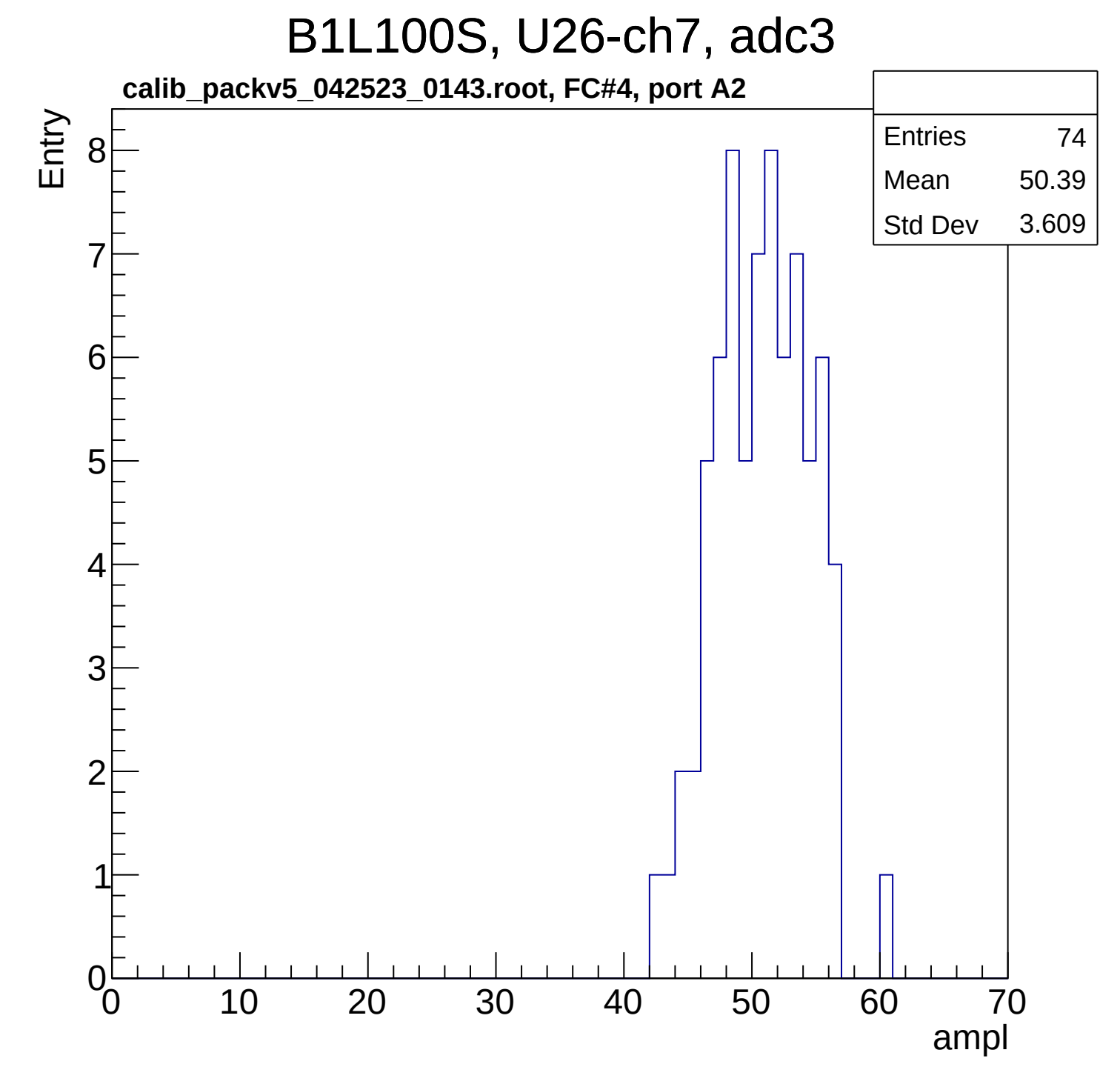
B1L100S, U26-ch7, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.39
Std Dev 3.609
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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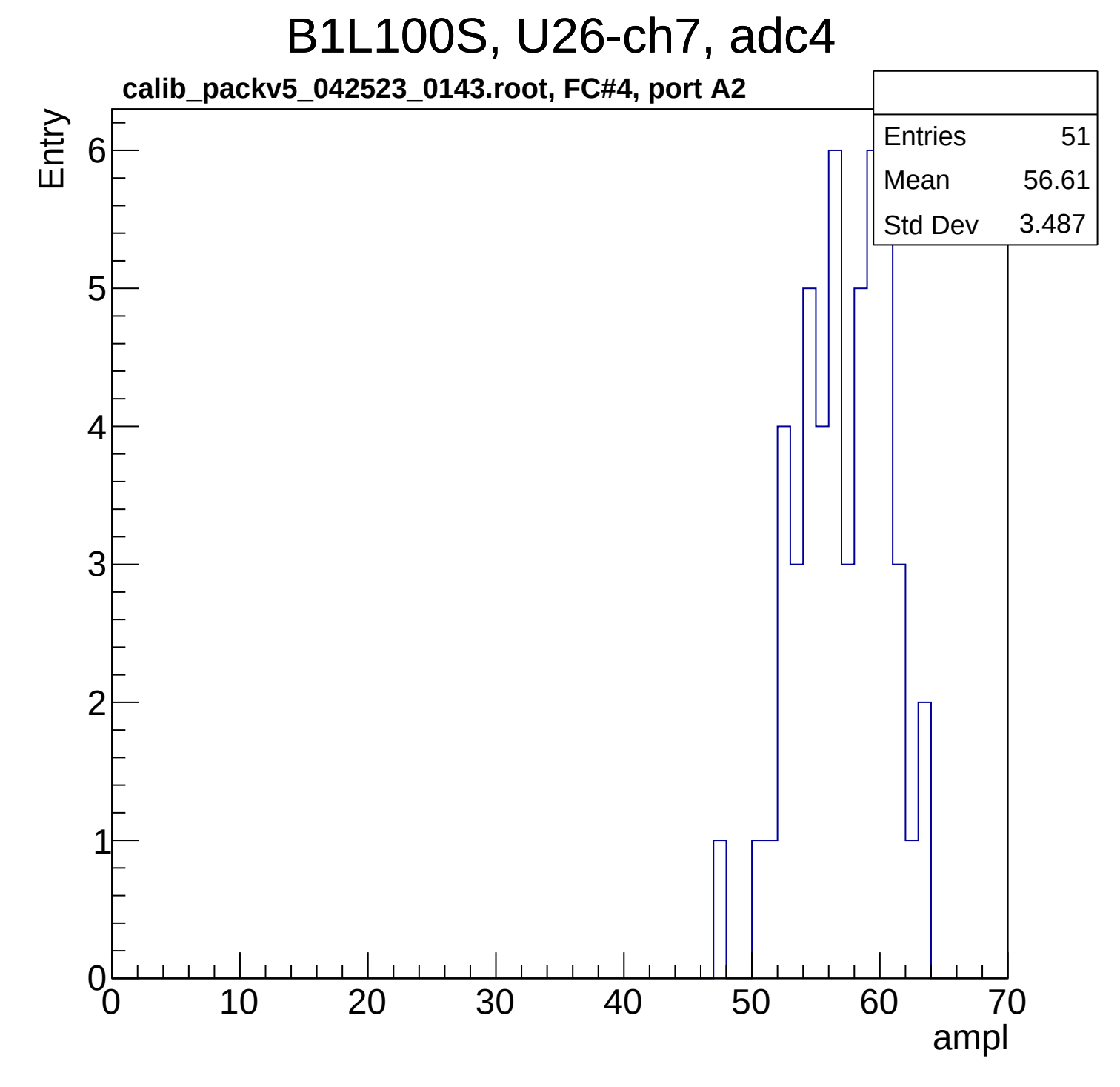
60
70


B1L100S, U26-ch7, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.61
Std Dev 3.487
Entry
ampl
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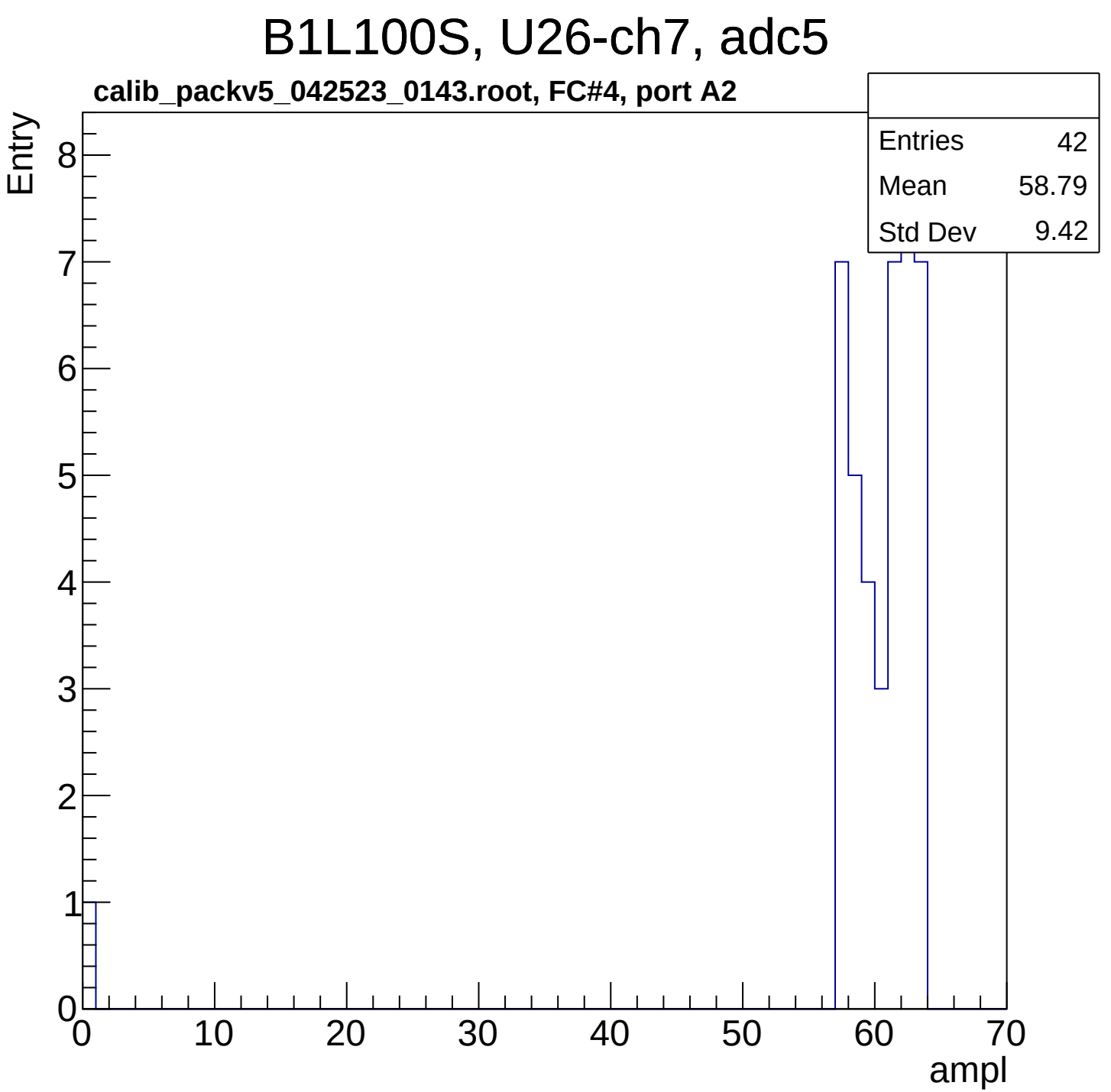
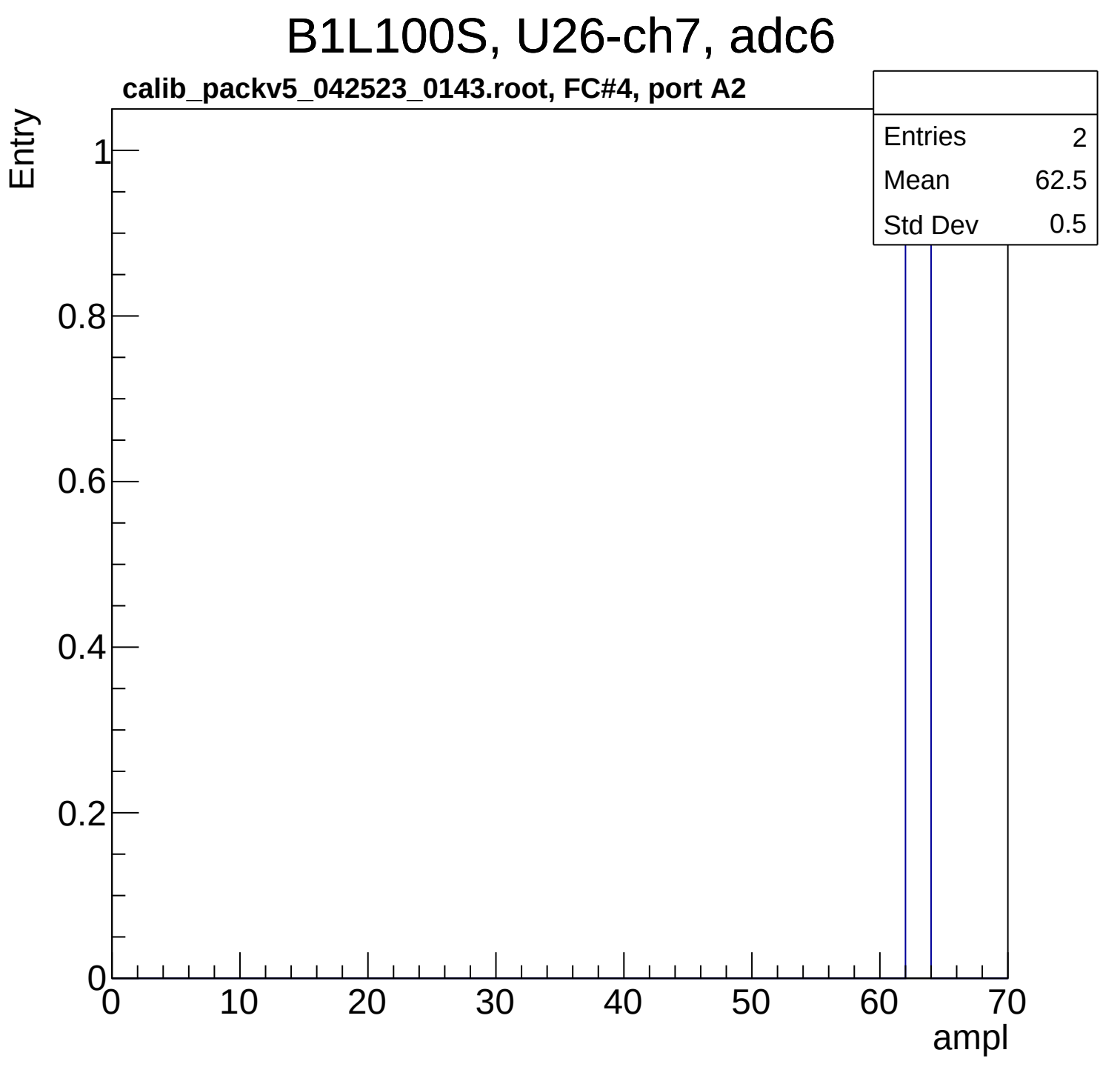

B1L100S, U26-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.79
Std Dev 9.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
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10
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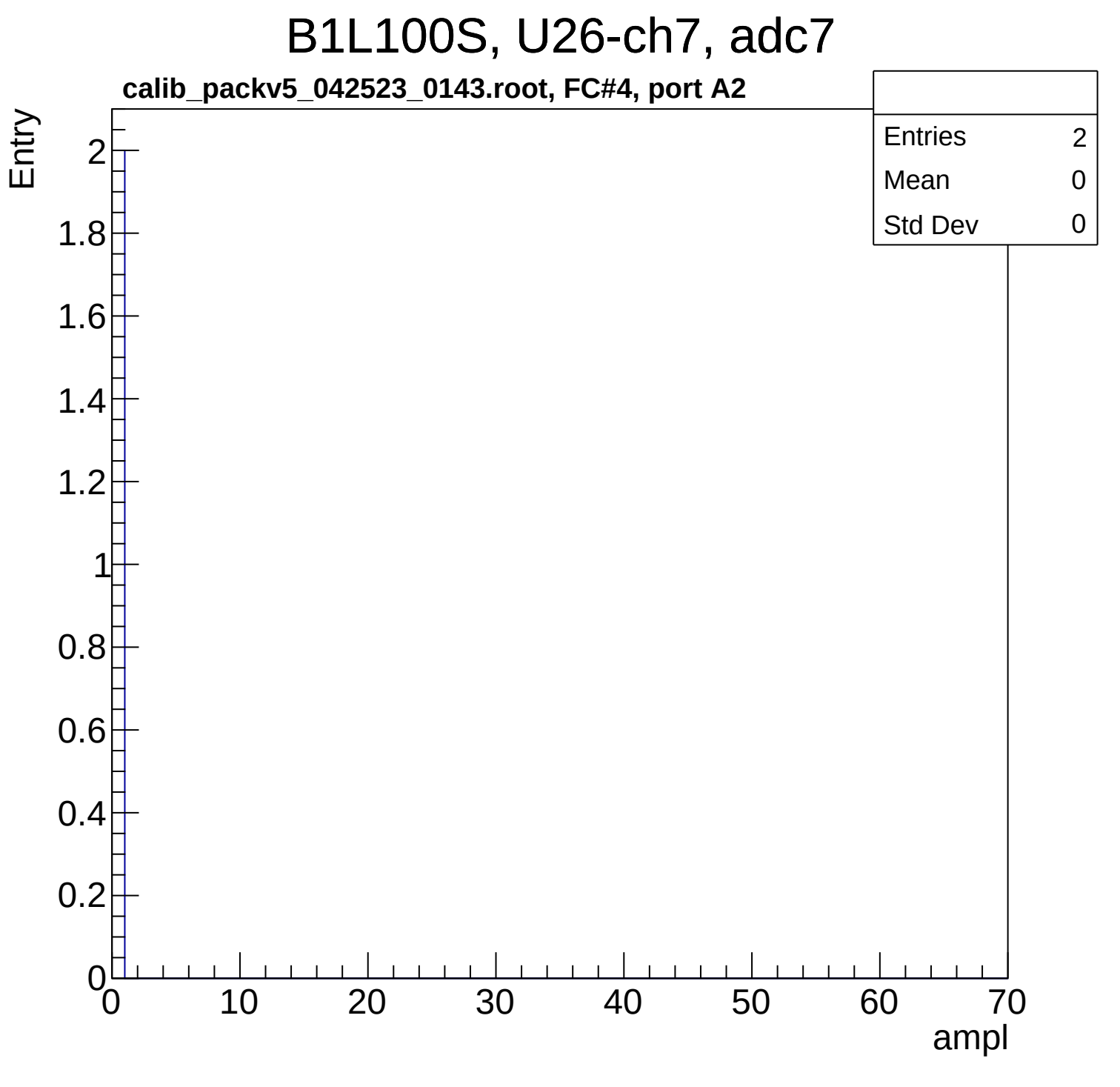
50
60
70
ampl


B1L100S, U26-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
0
10
20
30
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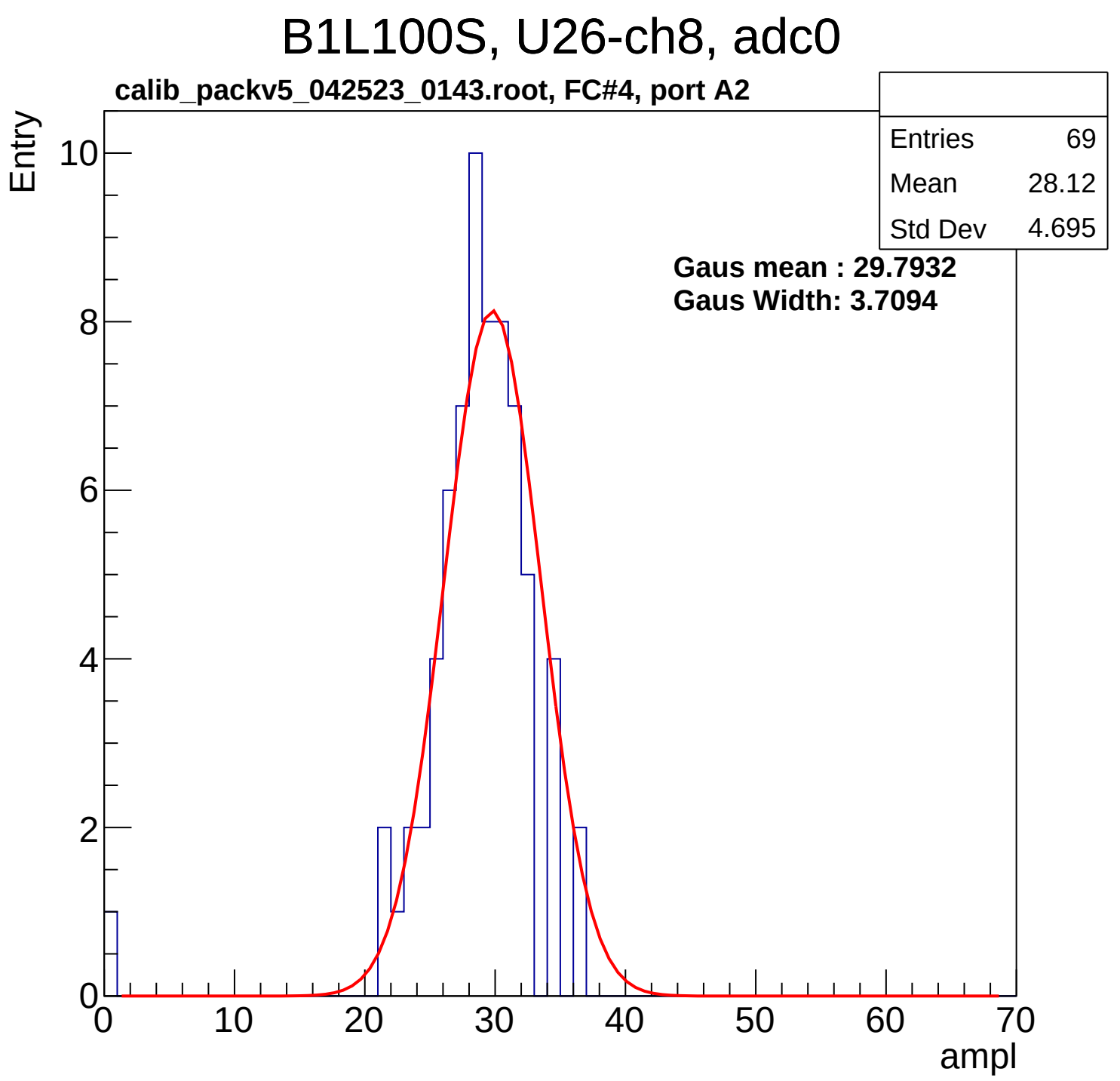
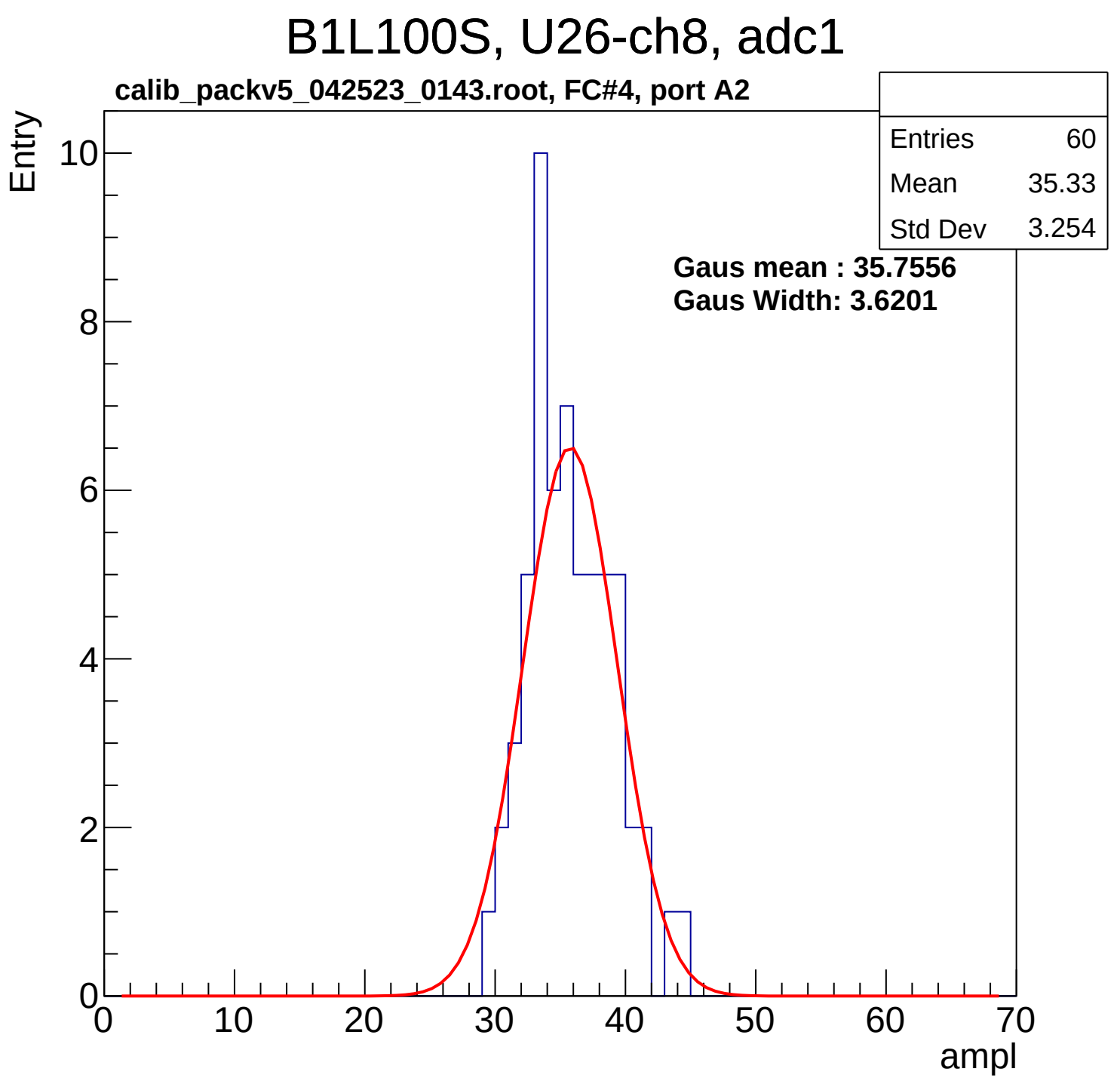
50
60
70
ampl


B1L100S, U26-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.12
Std Dev 4.695
Gaus mean : 29.7932
Gaus Width: 3.7094
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.33
Std Dev 3.254
Gaus mean : 35.7556
Gaus Width: 3.6201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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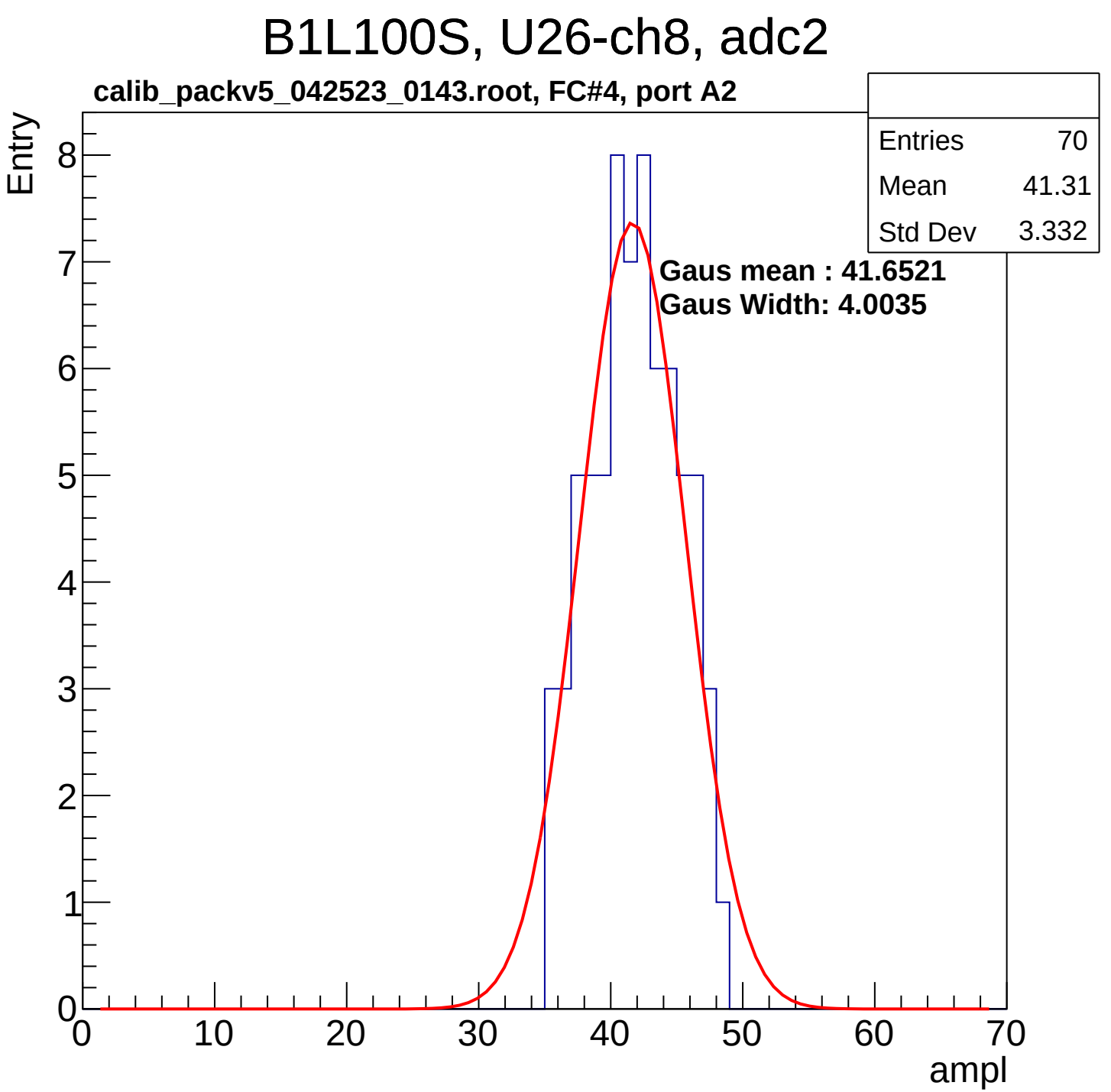
B1L100S, U26-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 41.31
Std Dev 3.332
Gaus mean : 41.6521
Gaus Width: 4.0035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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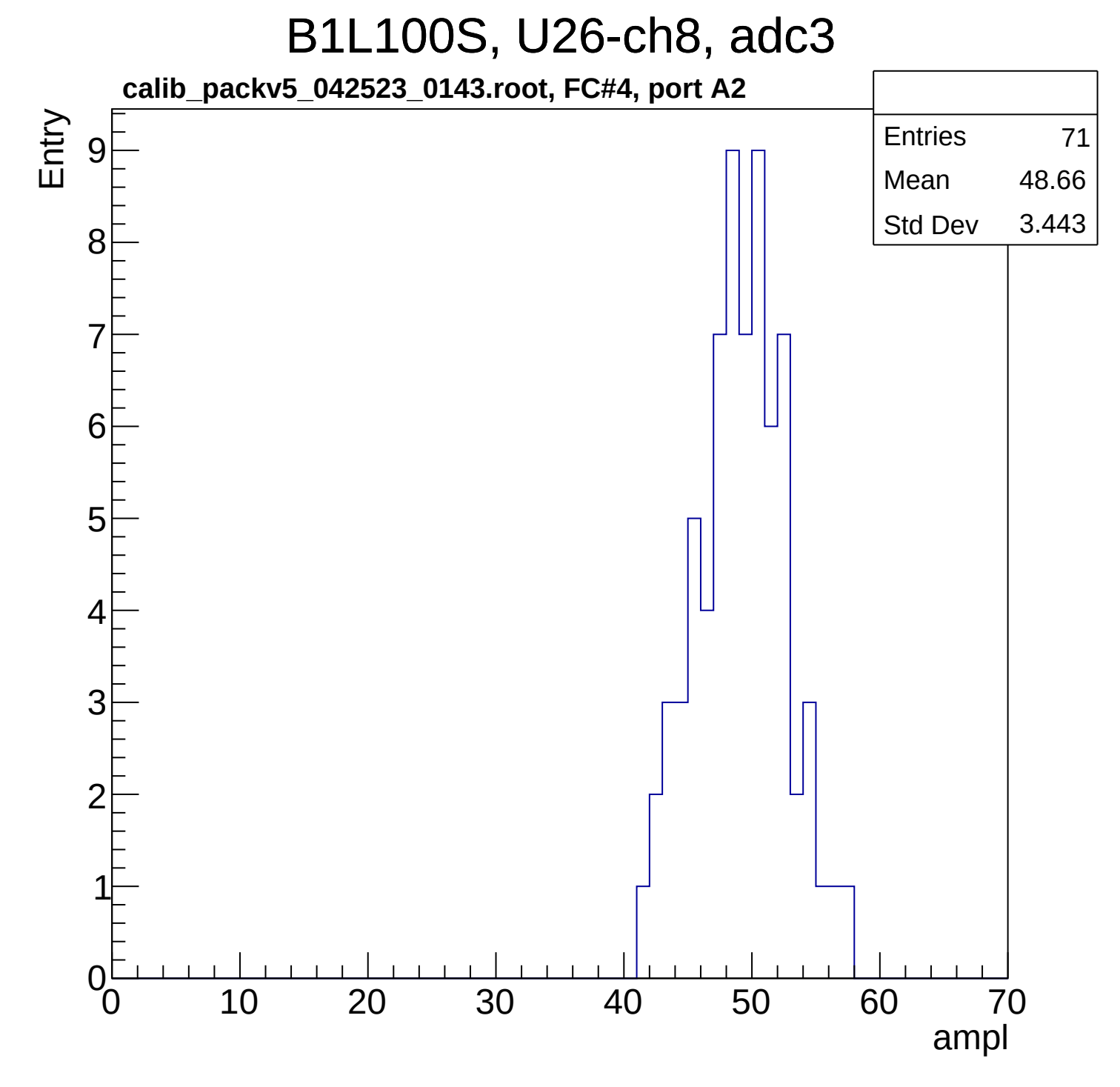
B1L100S, U26-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.66
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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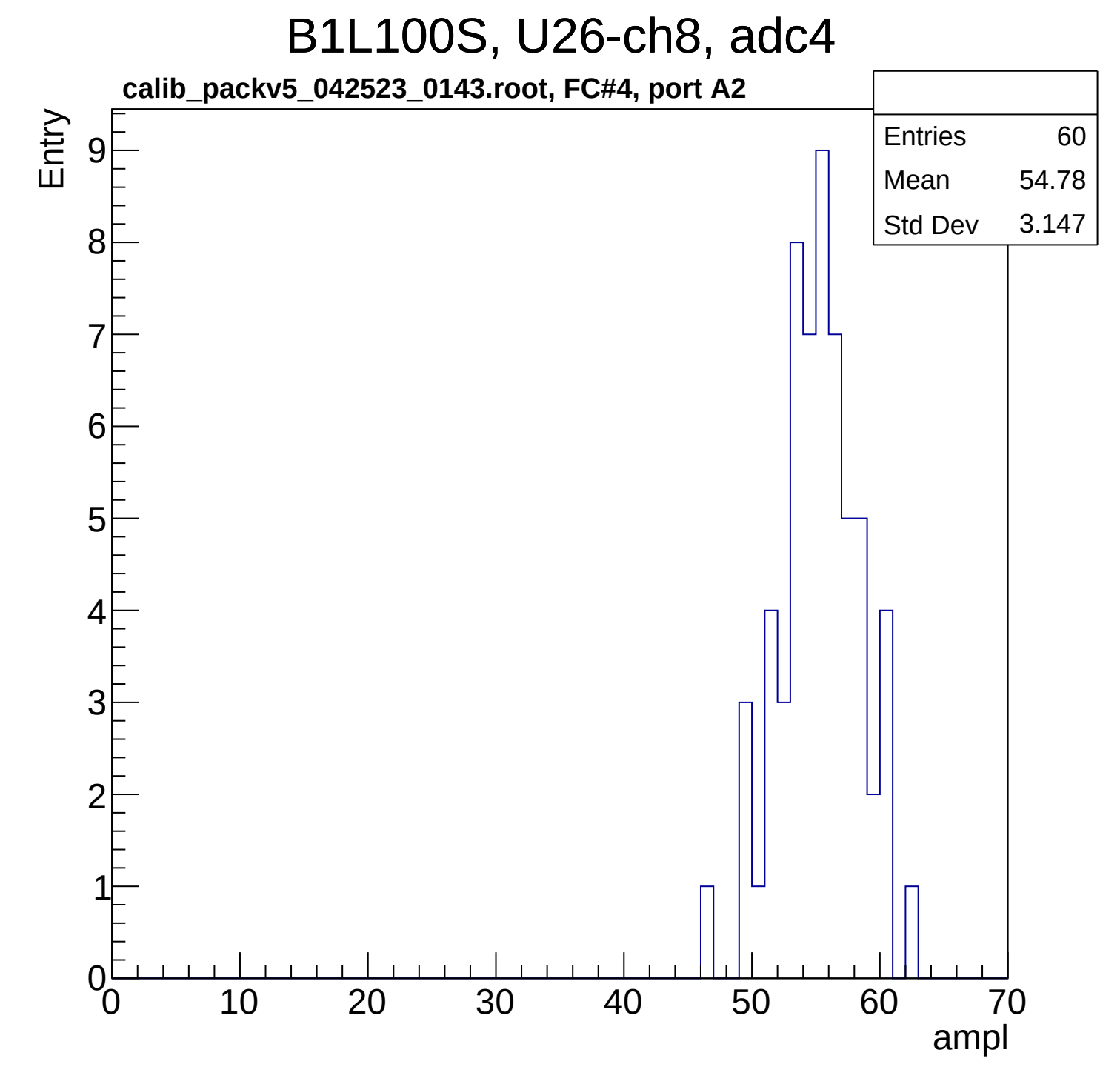
B1L100S, U26-ch8, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 54.78
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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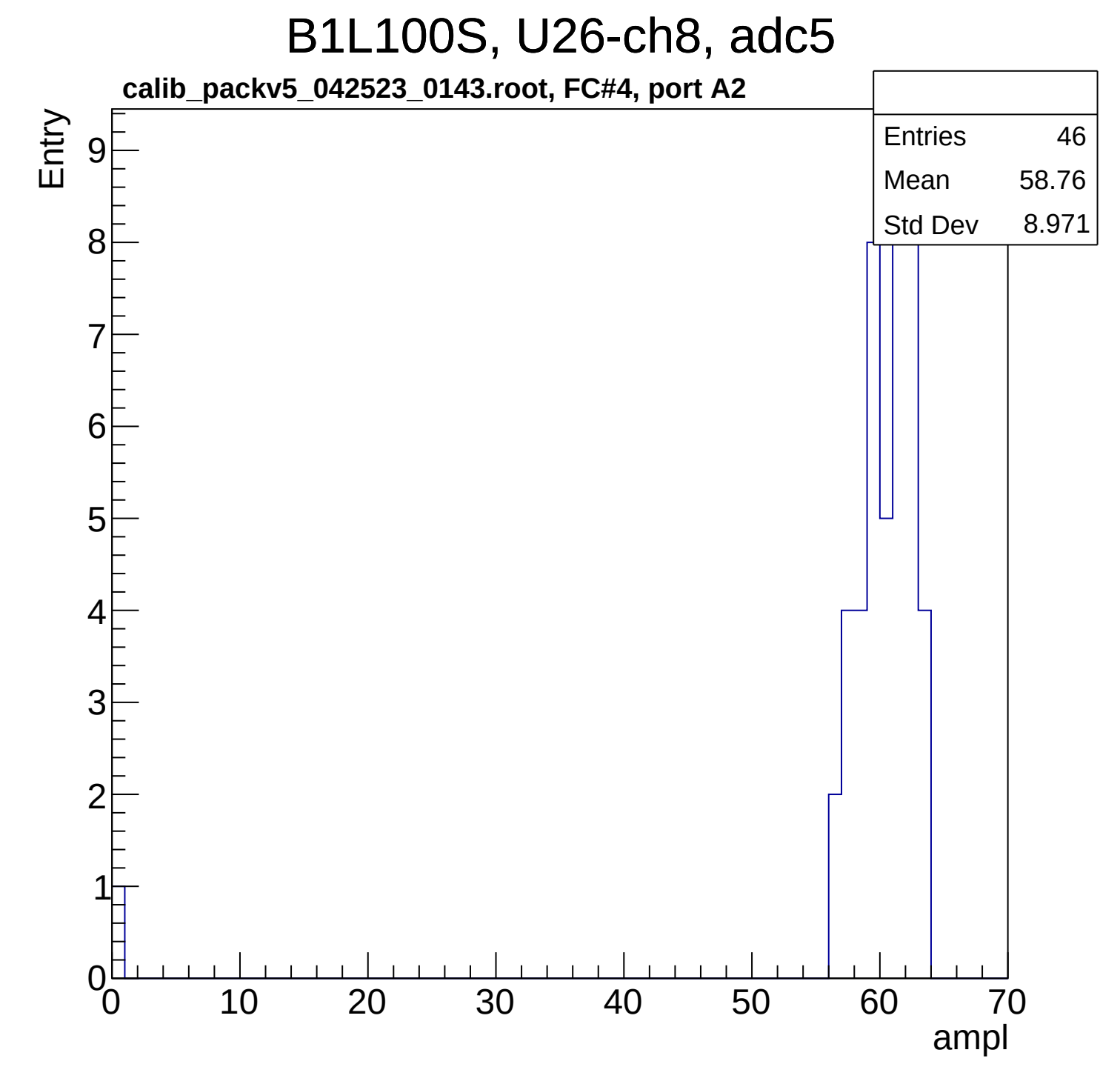
B1L100S, U26-ch8, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.76
Std Dev 8.971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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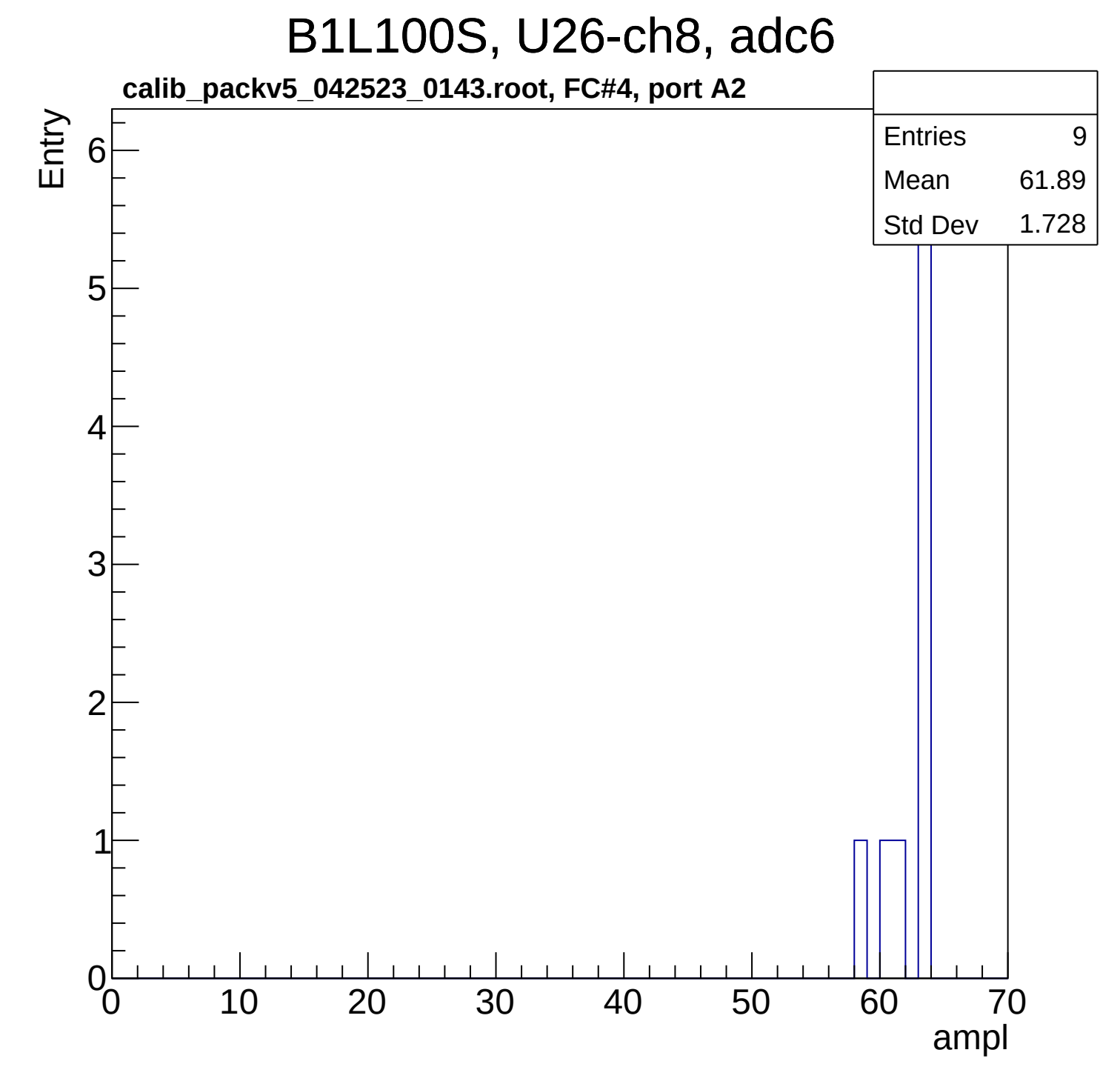
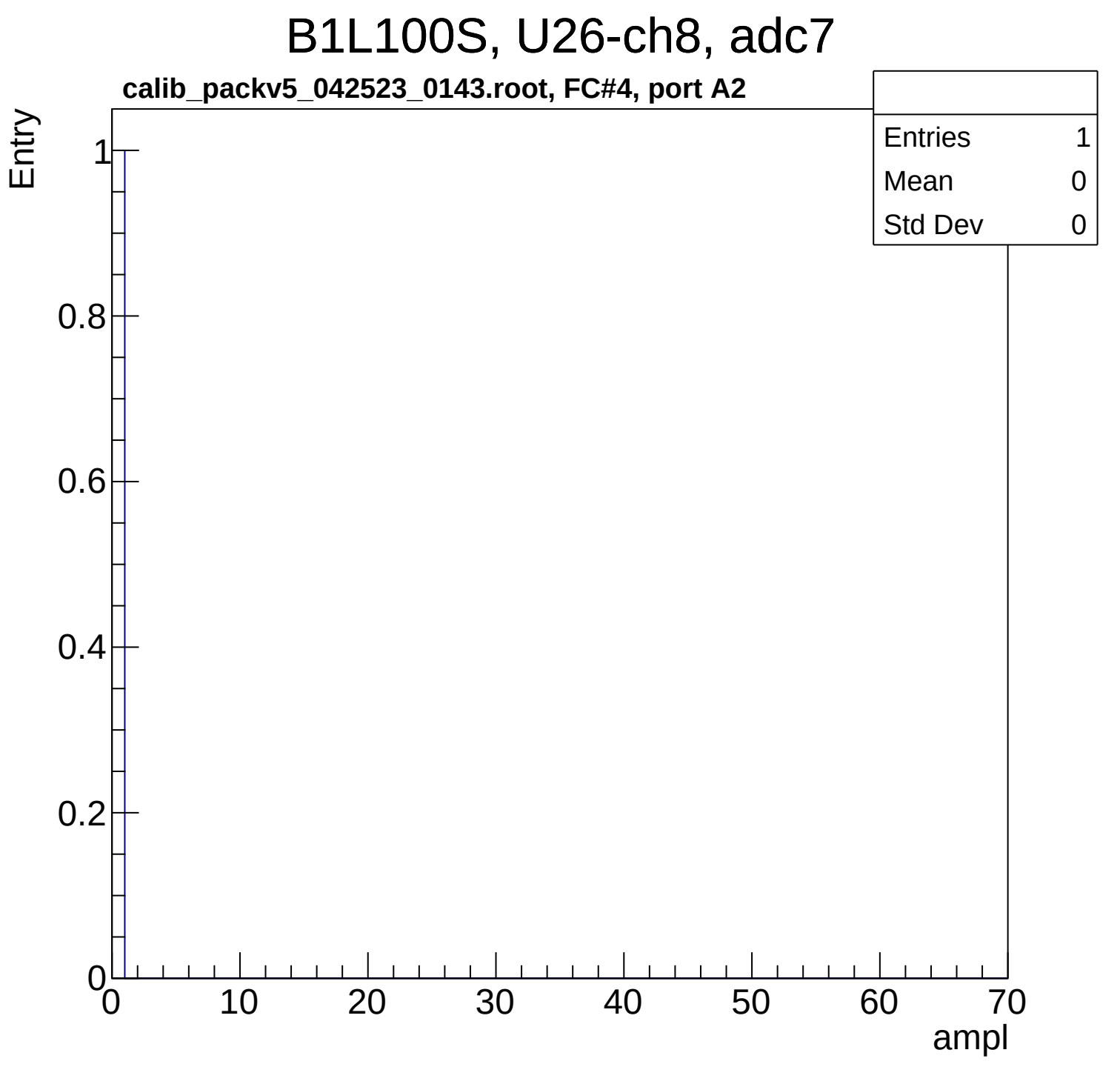
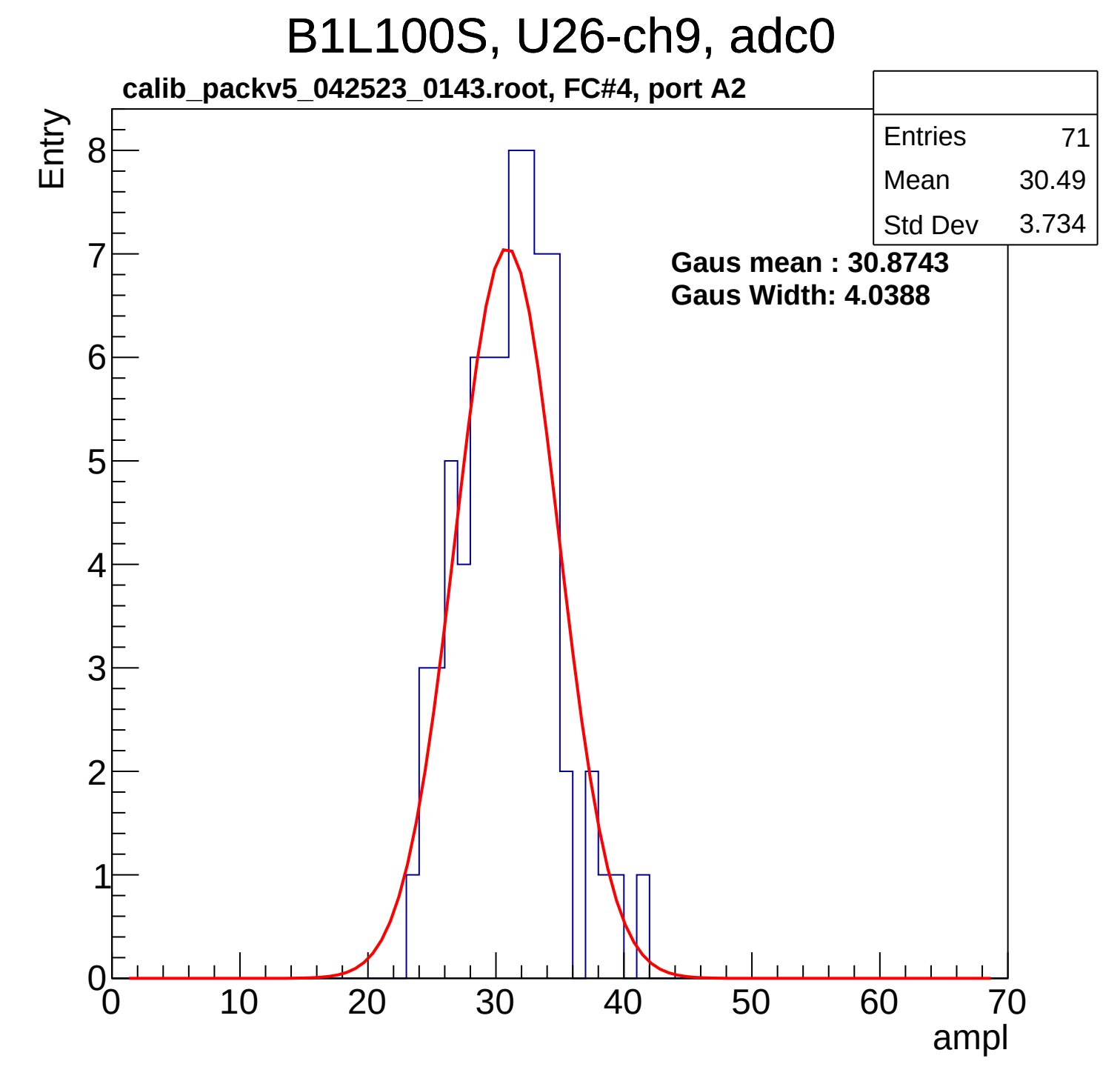

B1L100S, U26-ch8, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.89
Std Dev 1.728
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U26-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.49
Std Dev 3.734
Gaus mean : 30.8743
Gaus Width: 4.0388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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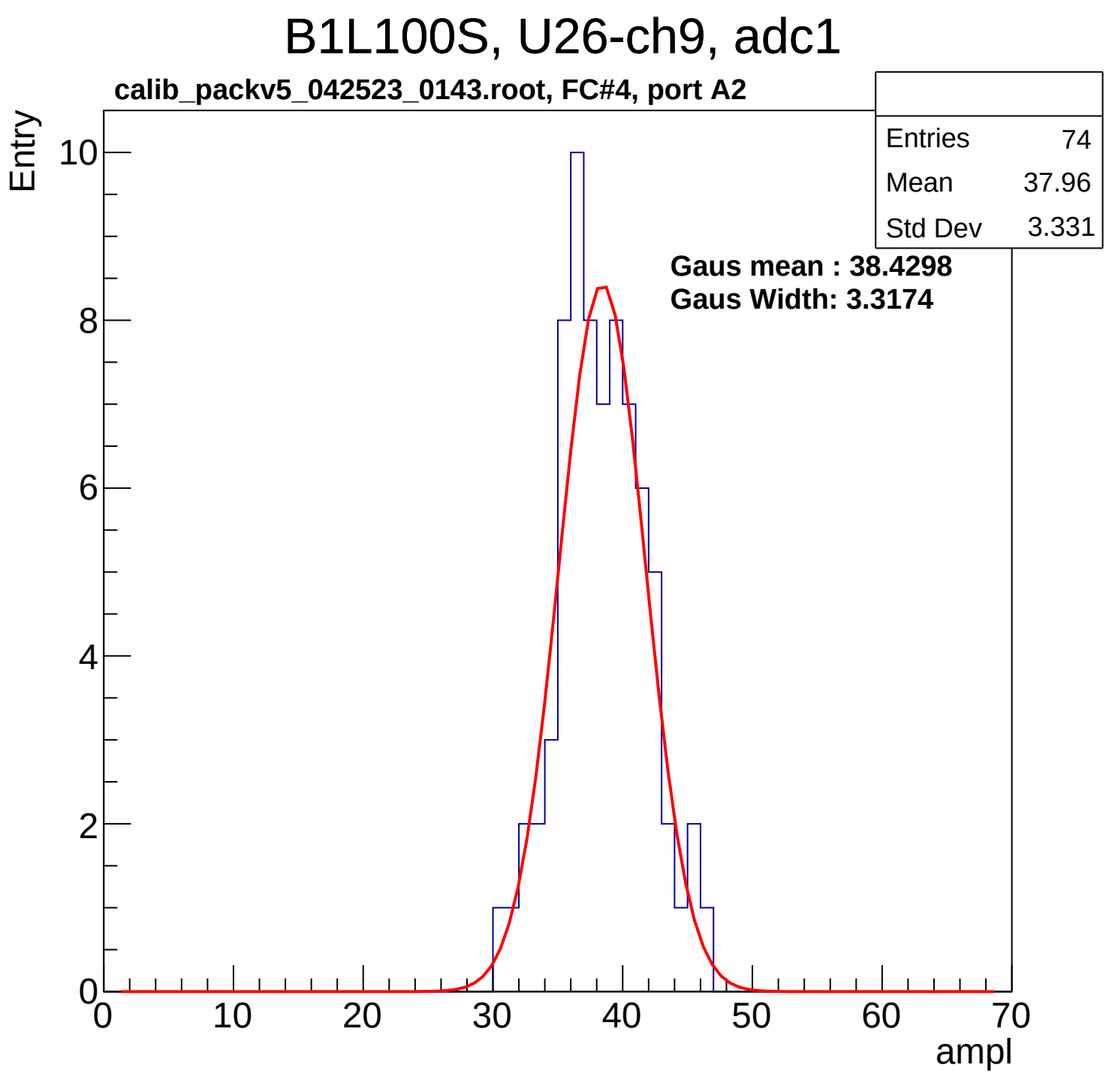
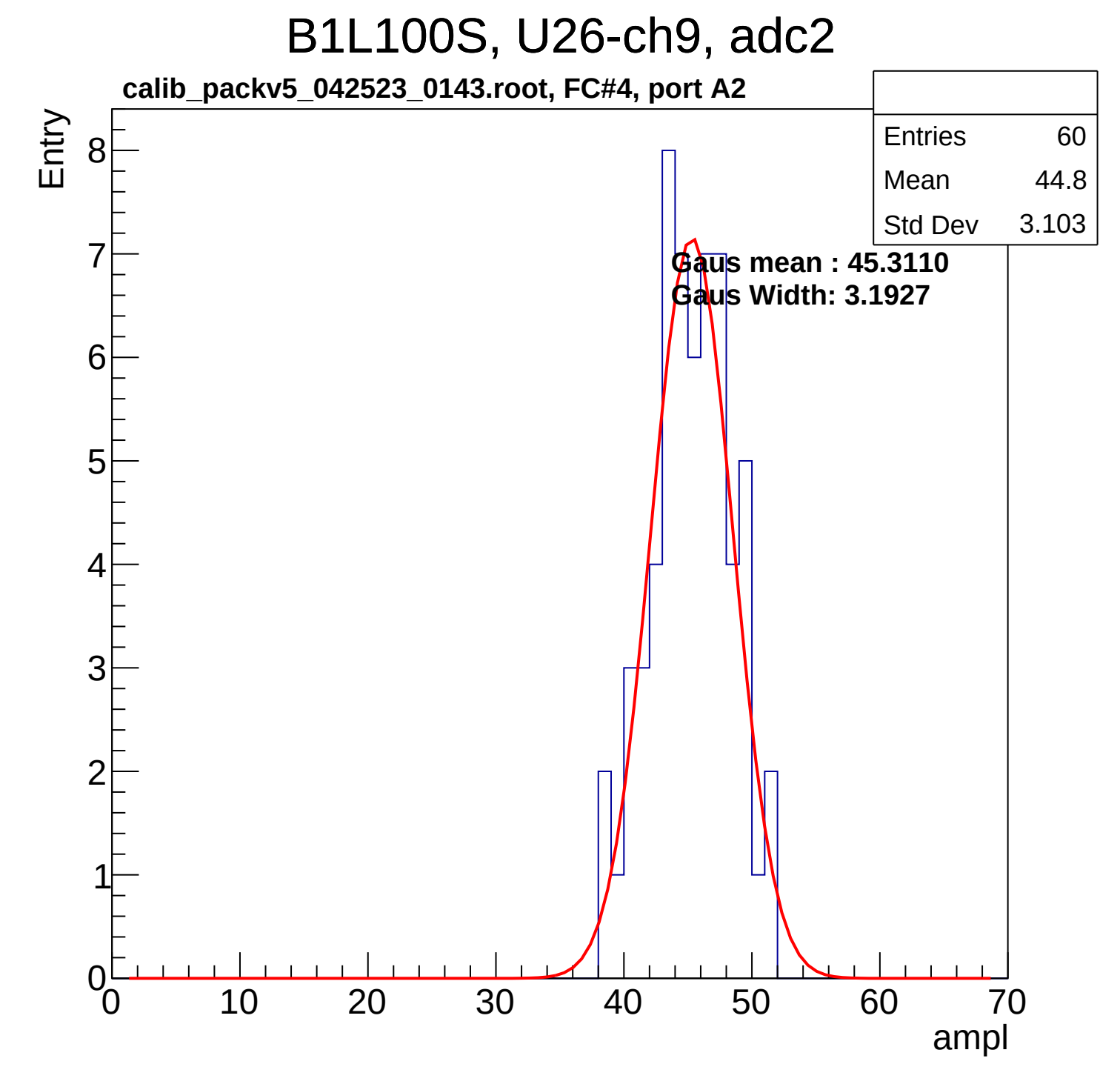
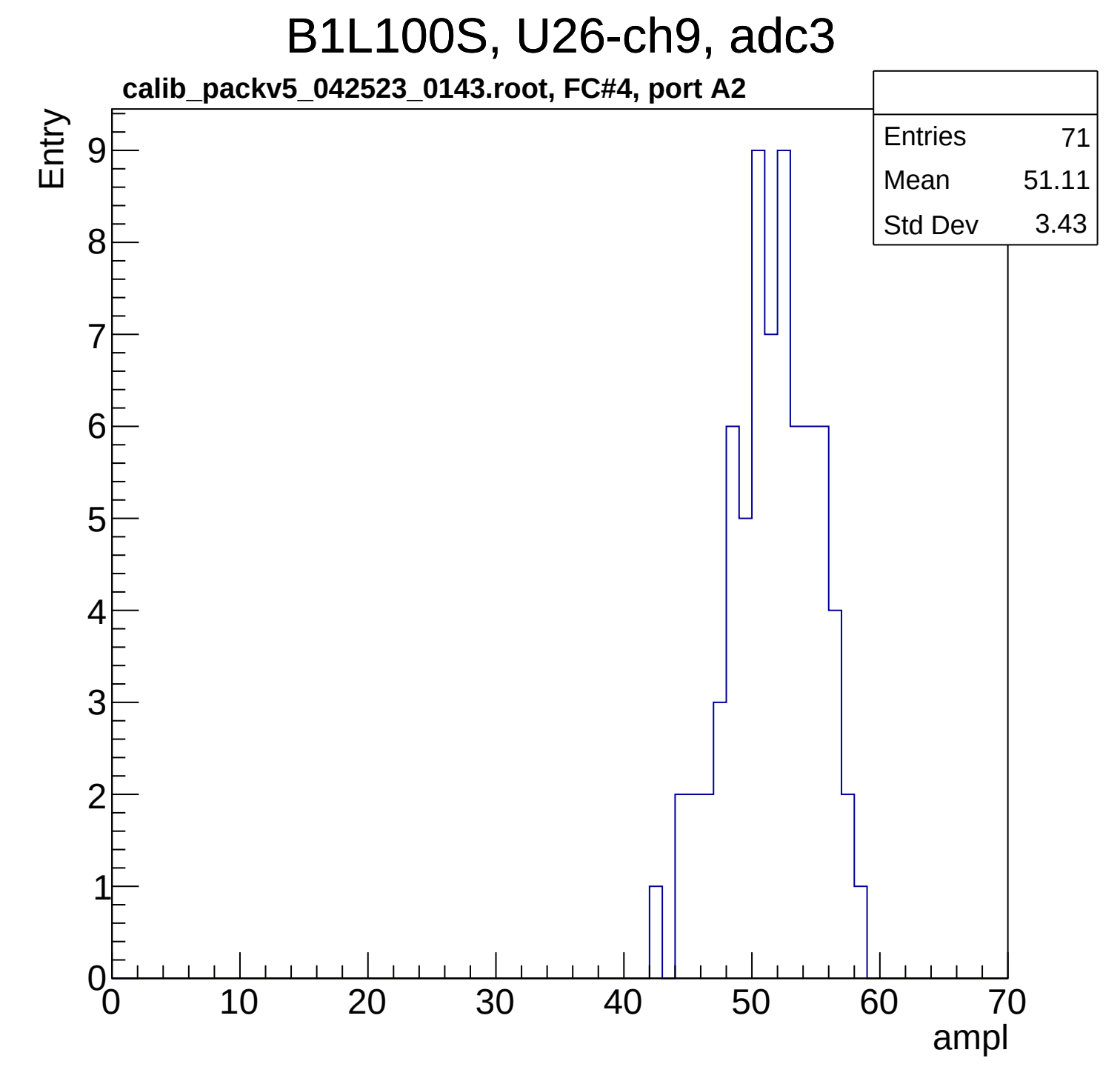
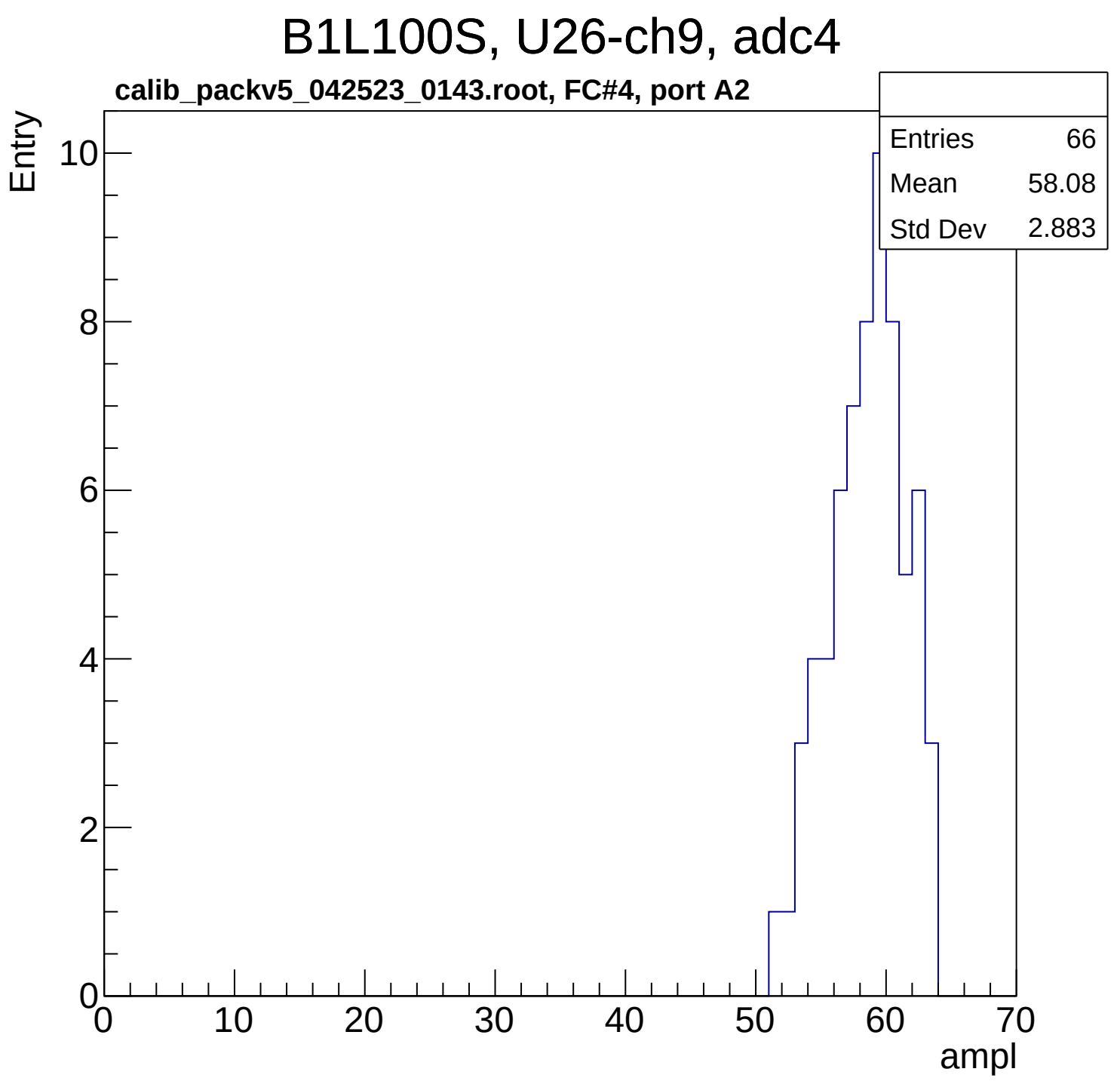
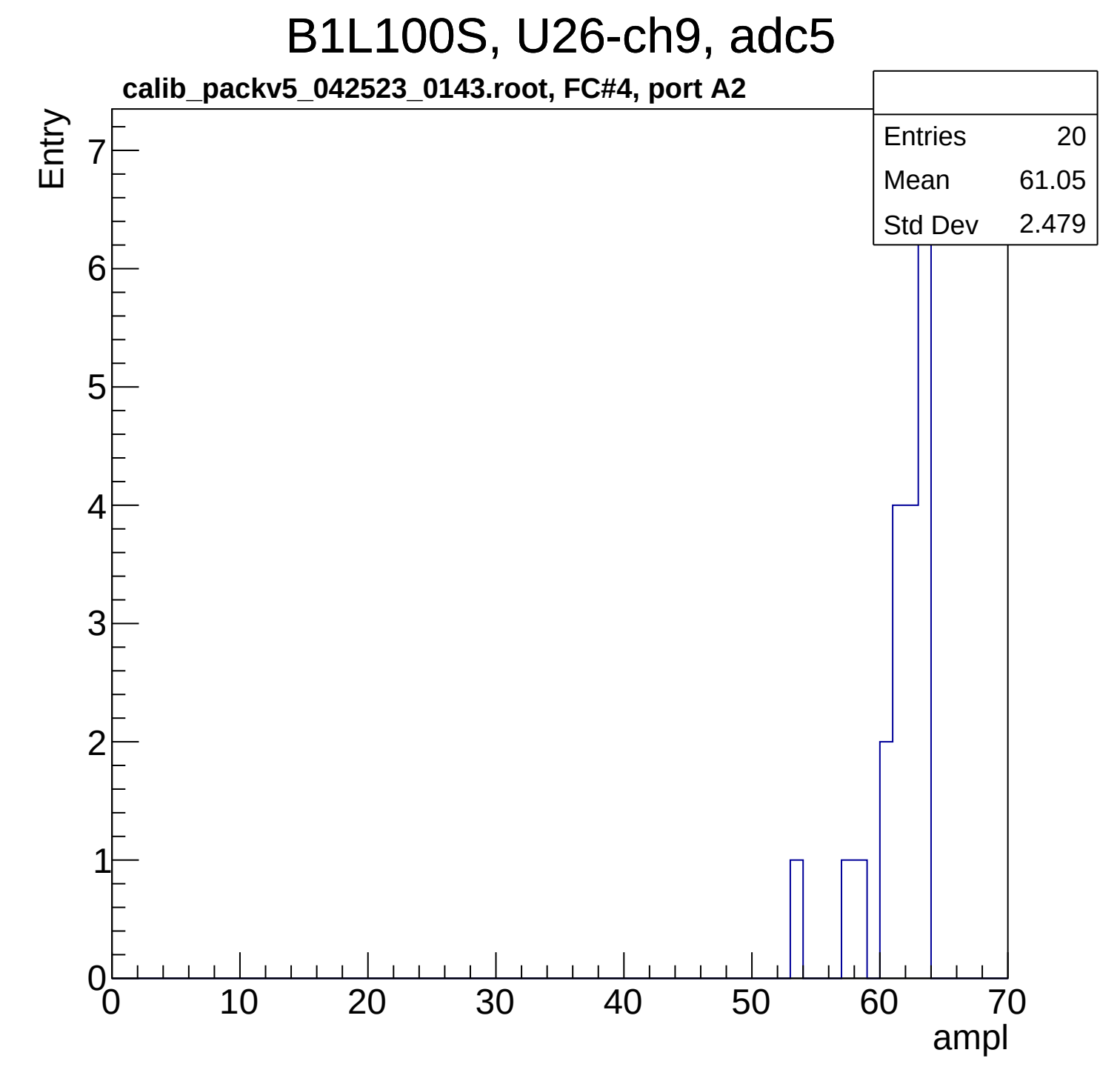
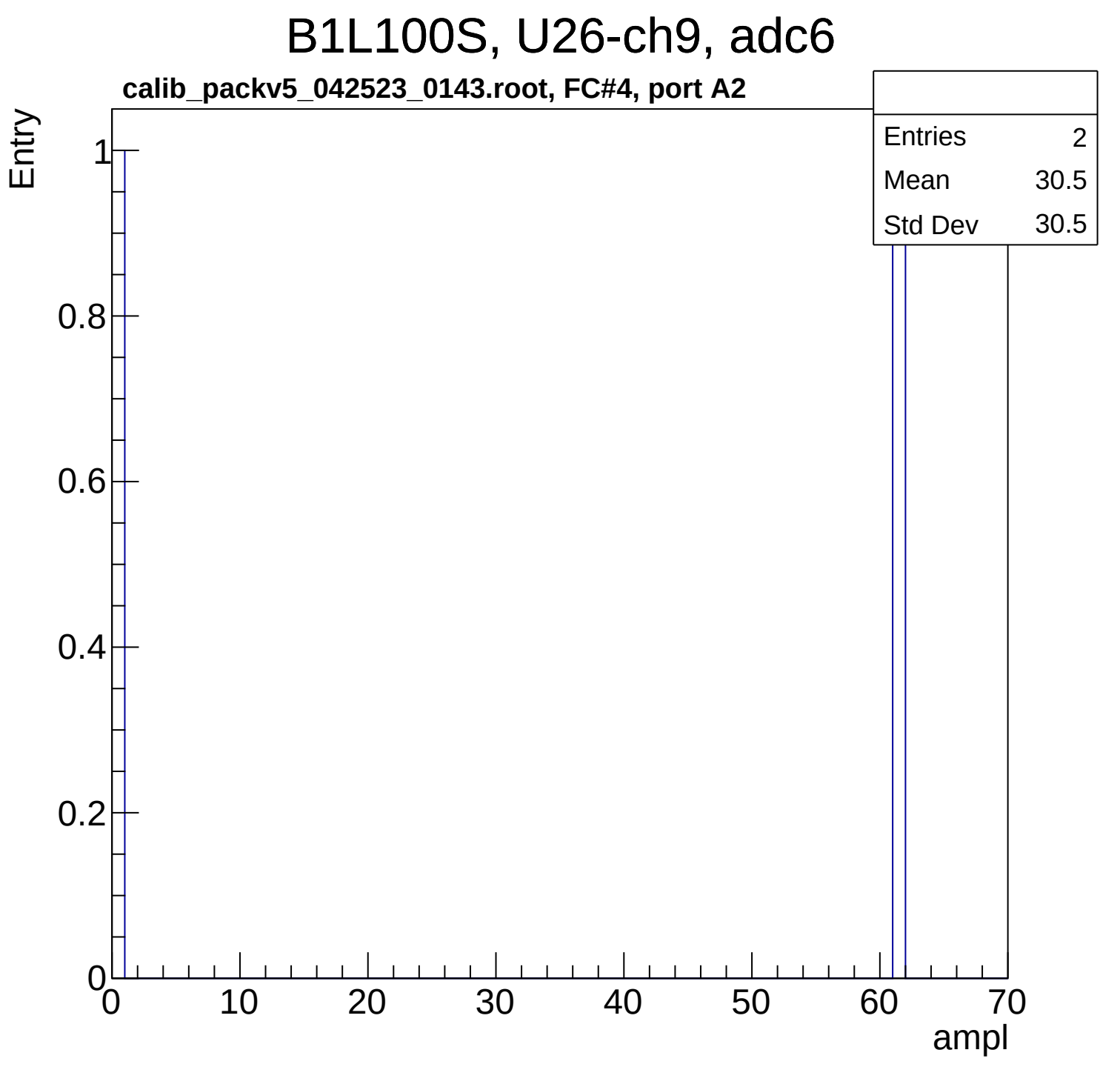

B1L100S, U26-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.96
Std Dev 3.331
Gaus mean : 38.4298
Gaus Width: 3.3174
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

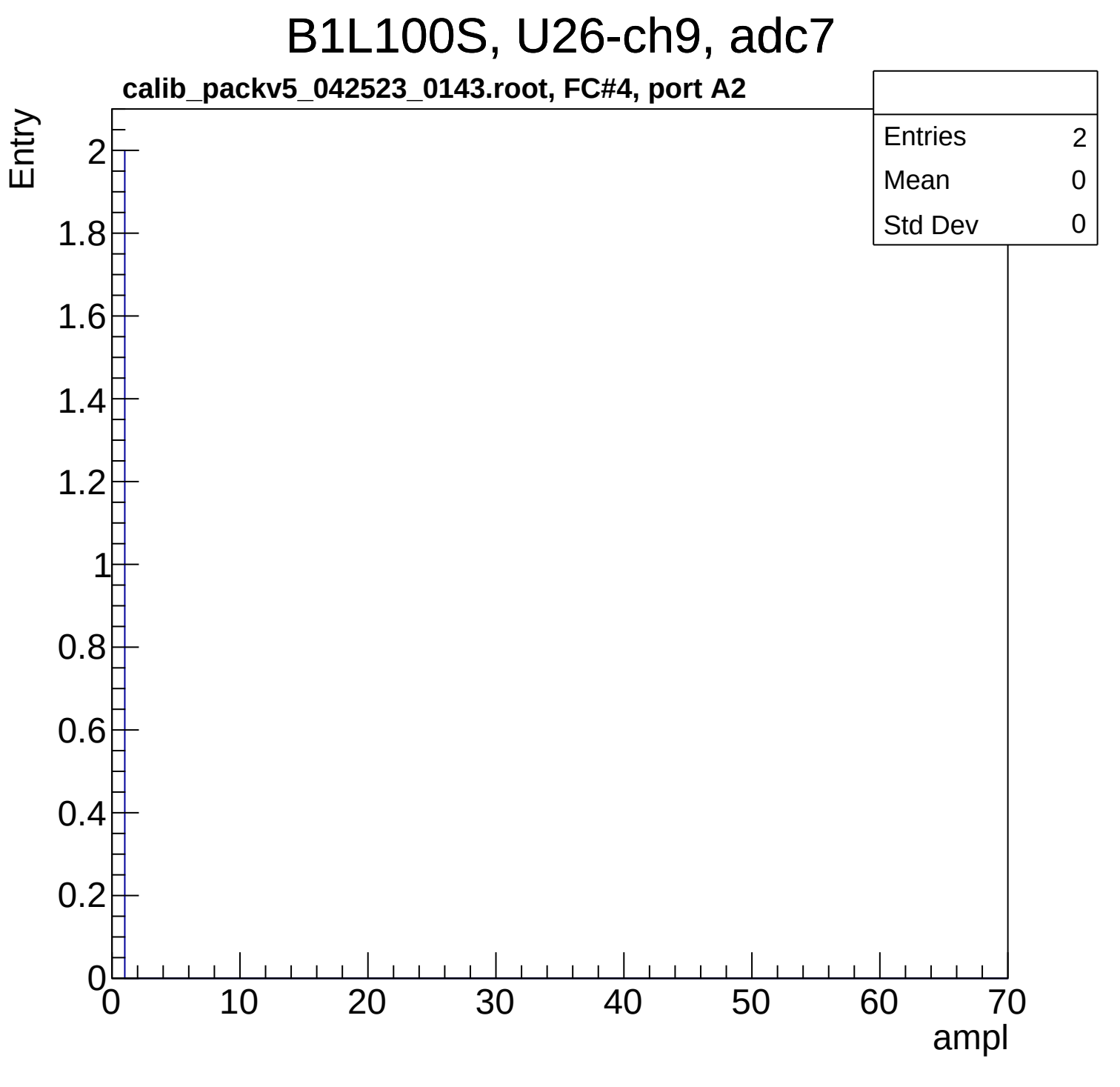

B1L100S, U26-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.8
Std Dev 3.103
Gaus mean : 45.3110
Gaus Width: 3.1927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

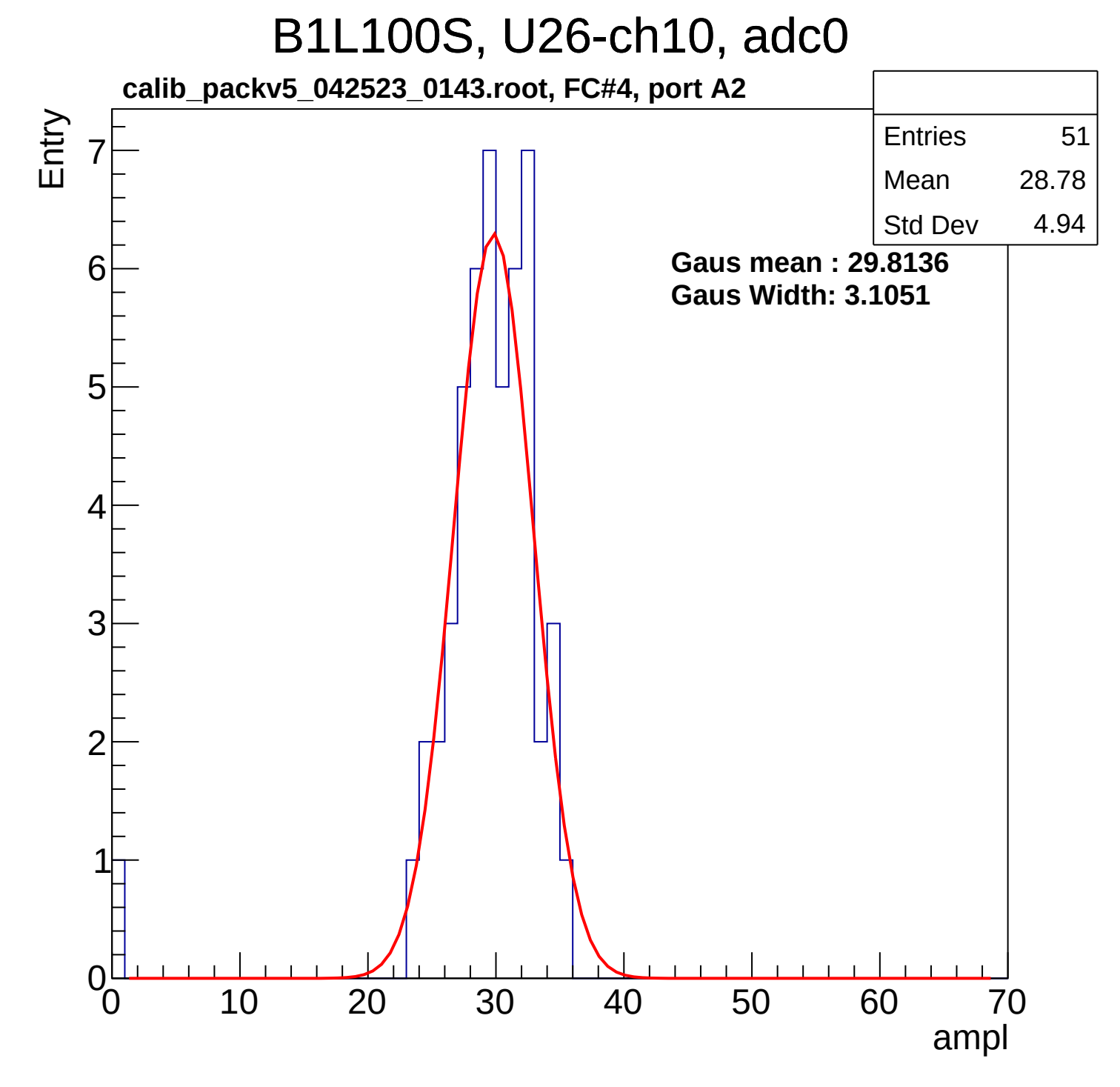

B1L100S, U26-ch9, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 51.11
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

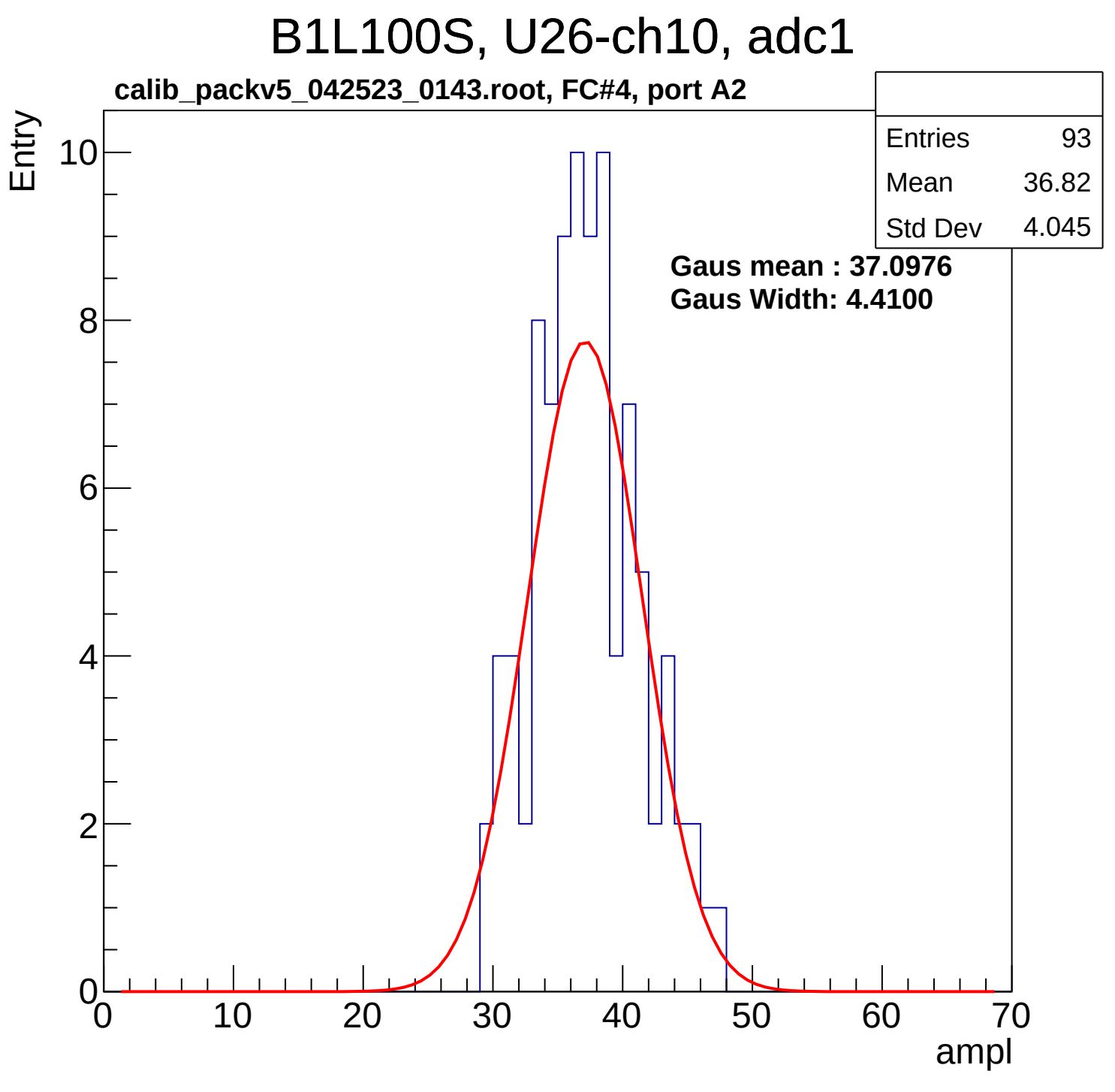

B1L100S, U26-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 58.08
Std Dev 2.883
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

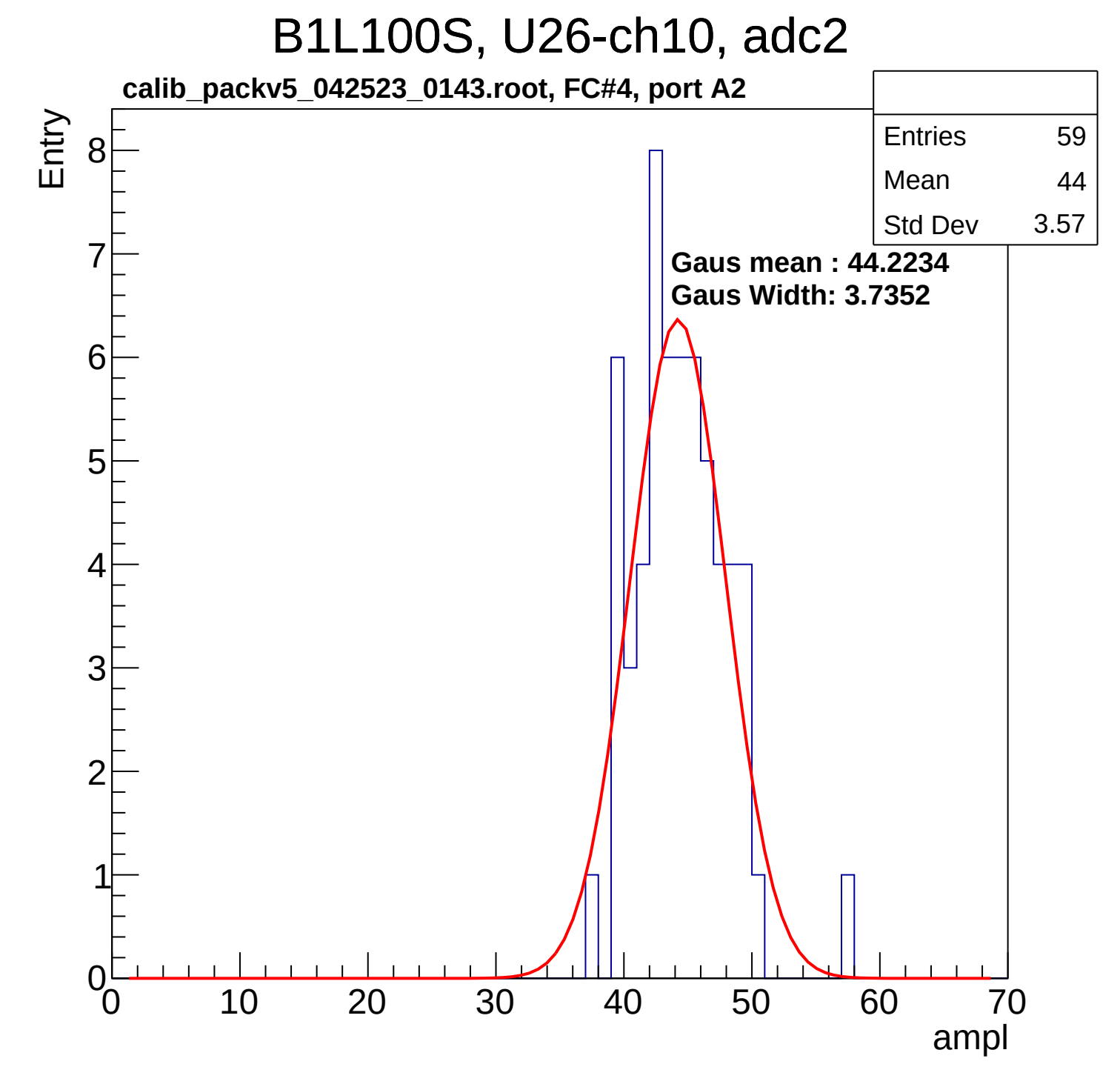

B1L100S, U26-ch9, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 20
Mean 61.05
Std Dev 2.479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

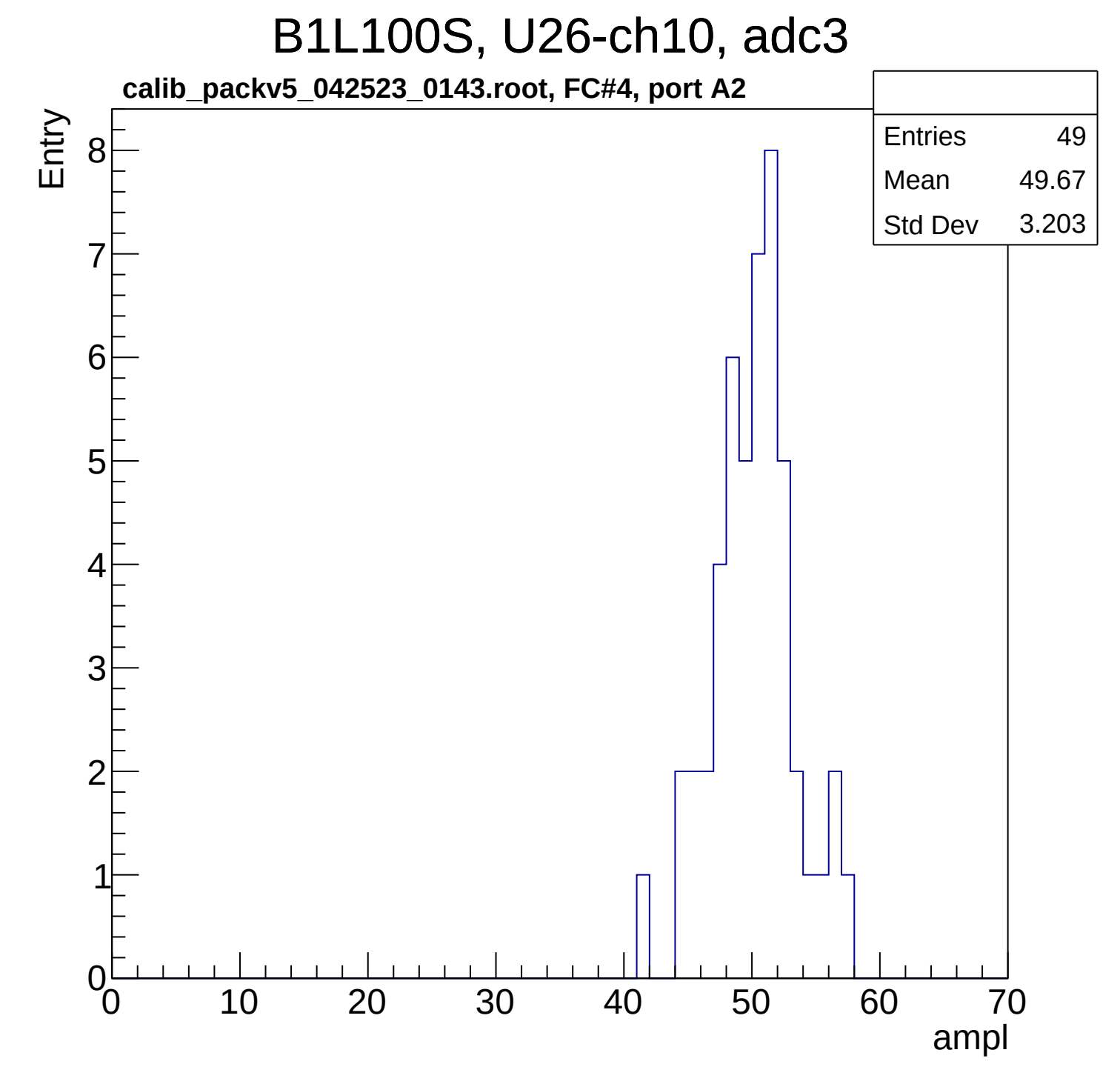

B1L100S, U26-ch9, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 28.78
Std Dev 4.94
Gaus mean : 29.8136
Gaus Width: 3.1051
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 93
Mean 36.82
Std Dev 4.045
Gaus mean : 37.0976
Gaus Width: 4.4100
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44
Std Dev 3.57
Gaus mean : 44.2234
Gaus Width: 3.7352
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 49.67
Std Dev 3.203
Entry
ampl
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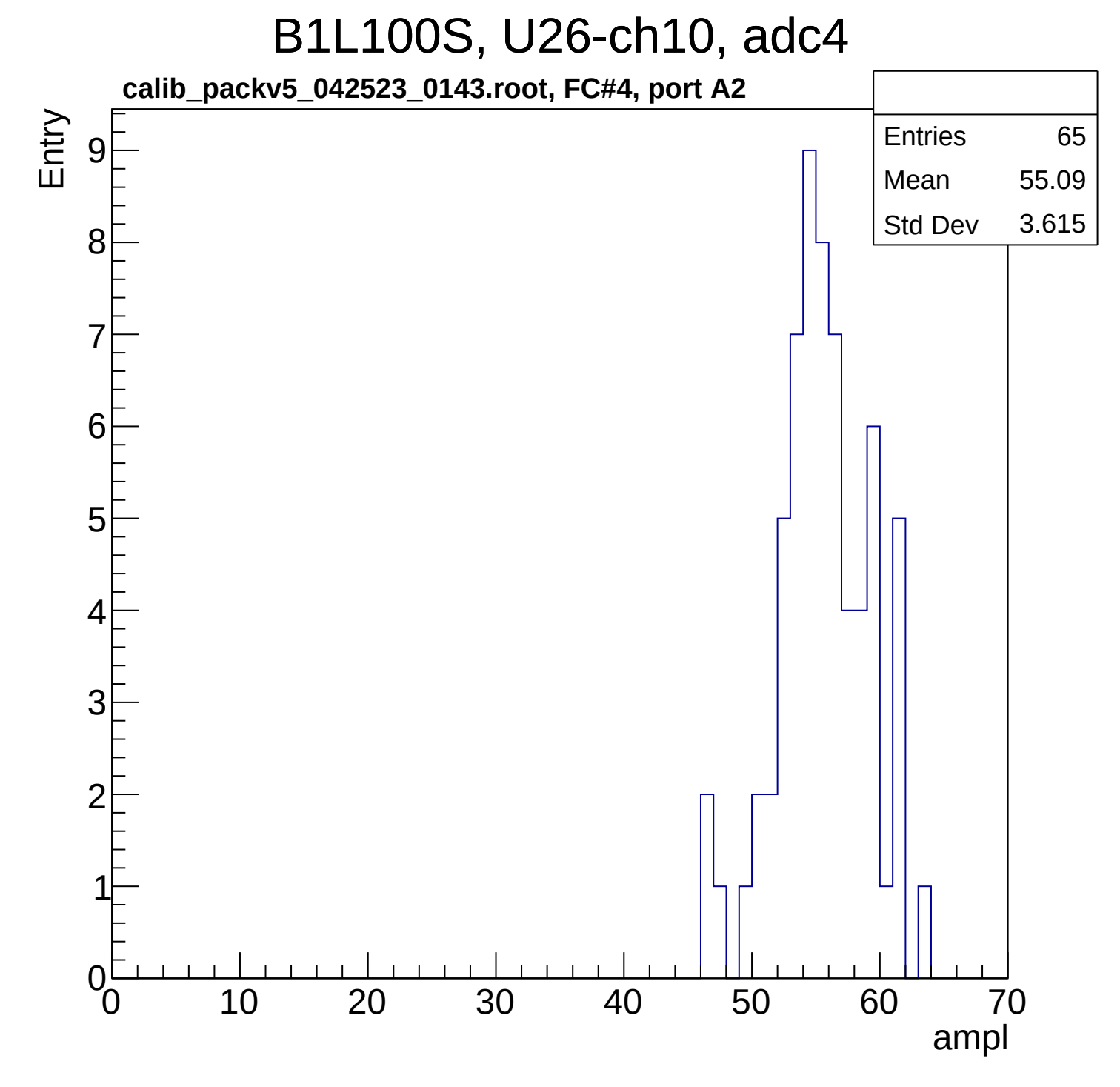
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.09
Std Dev 3.615
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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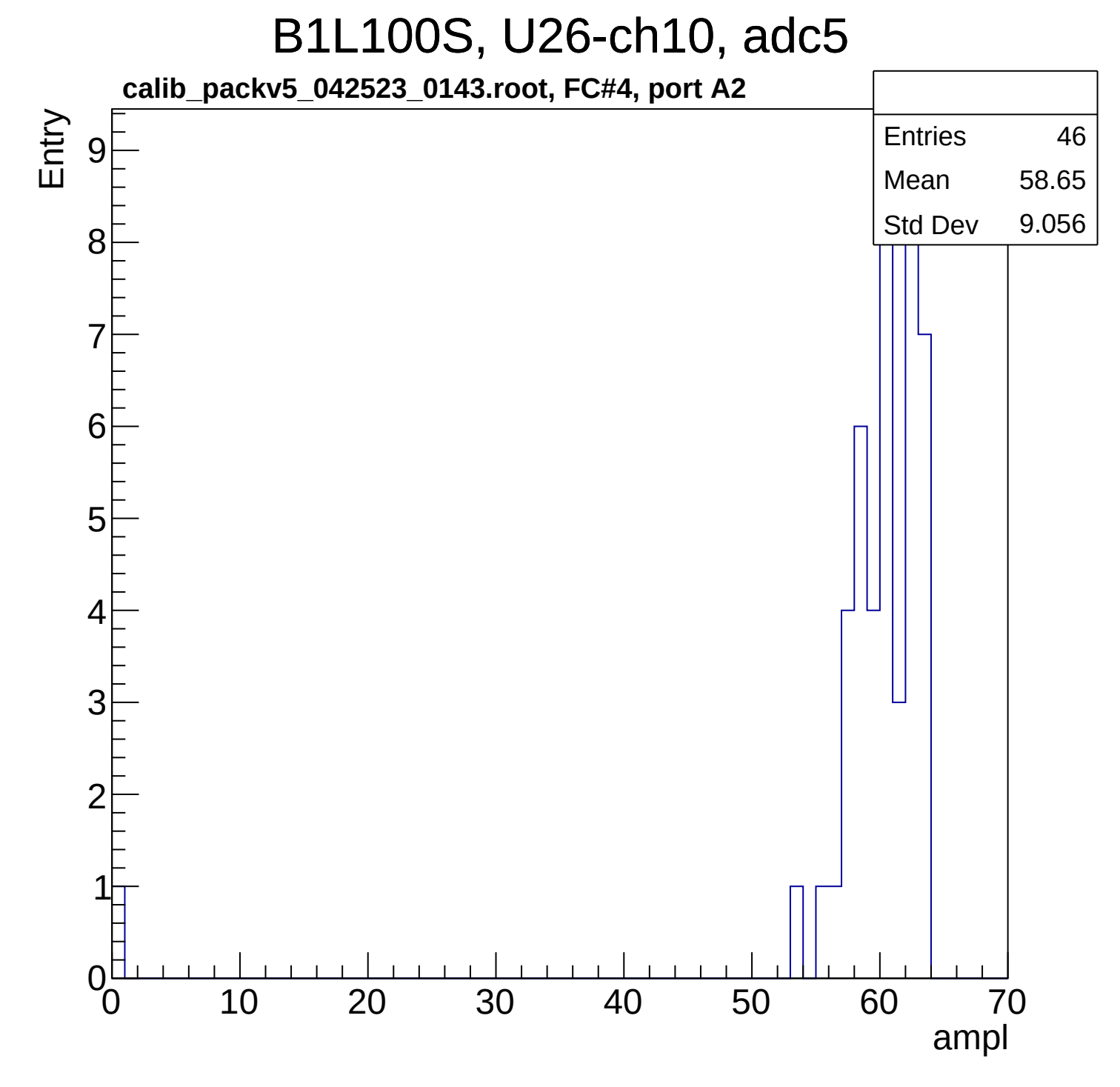
40
50
60
70


B1L100S, U26-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.65
Std Dev 9.056
Entry
ampl
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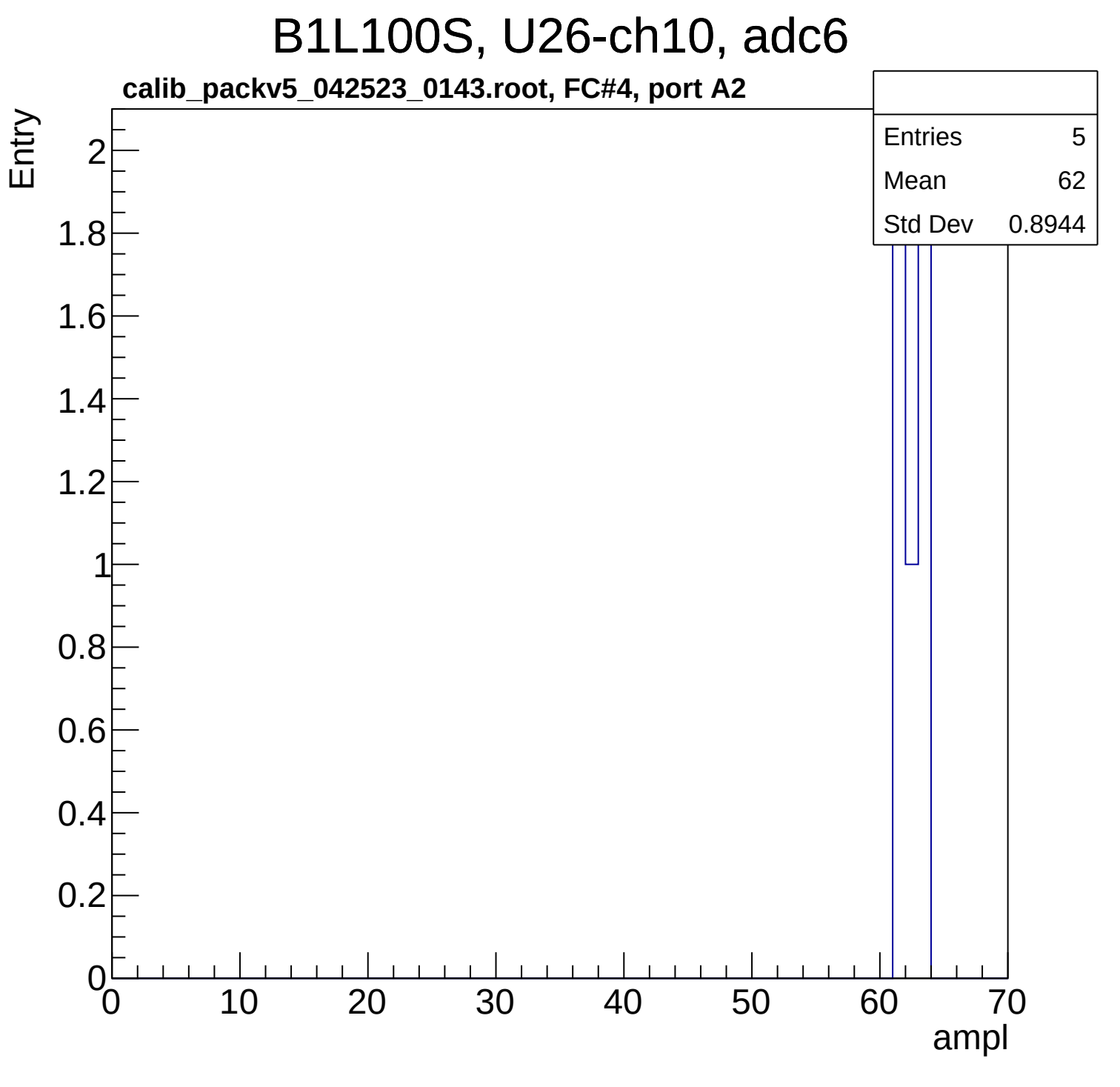
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch10, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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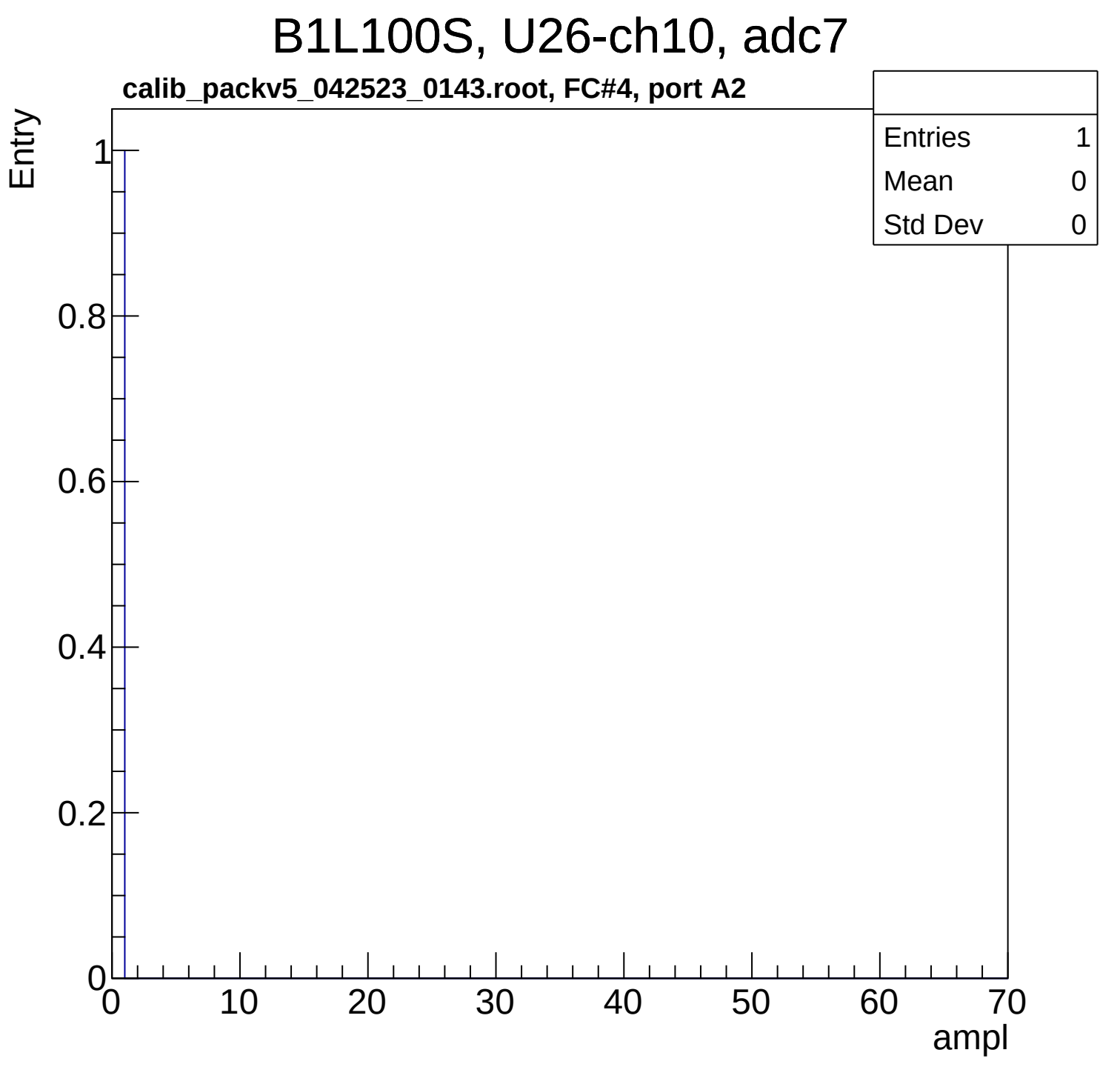
B1L100S, U26-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
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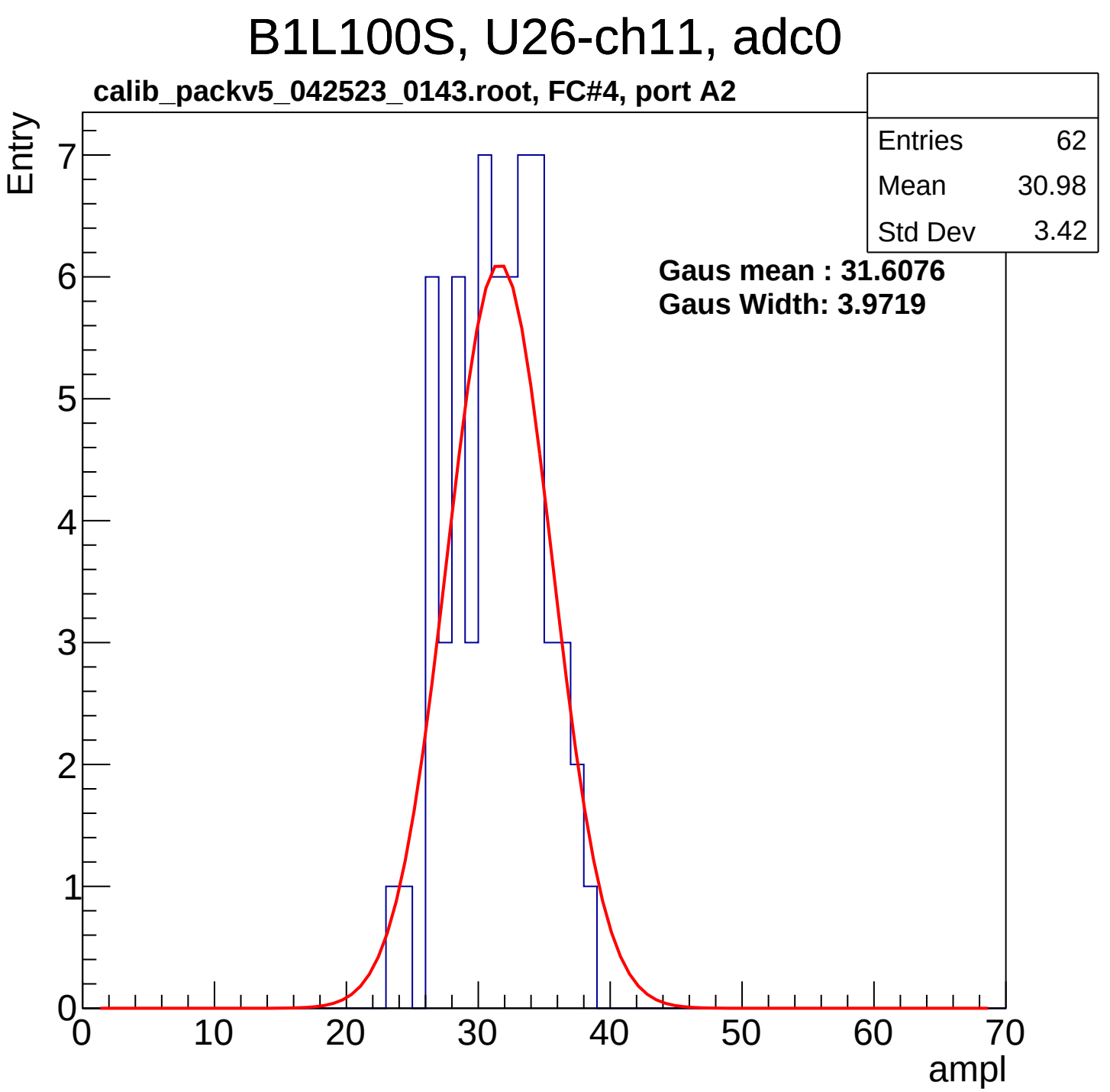
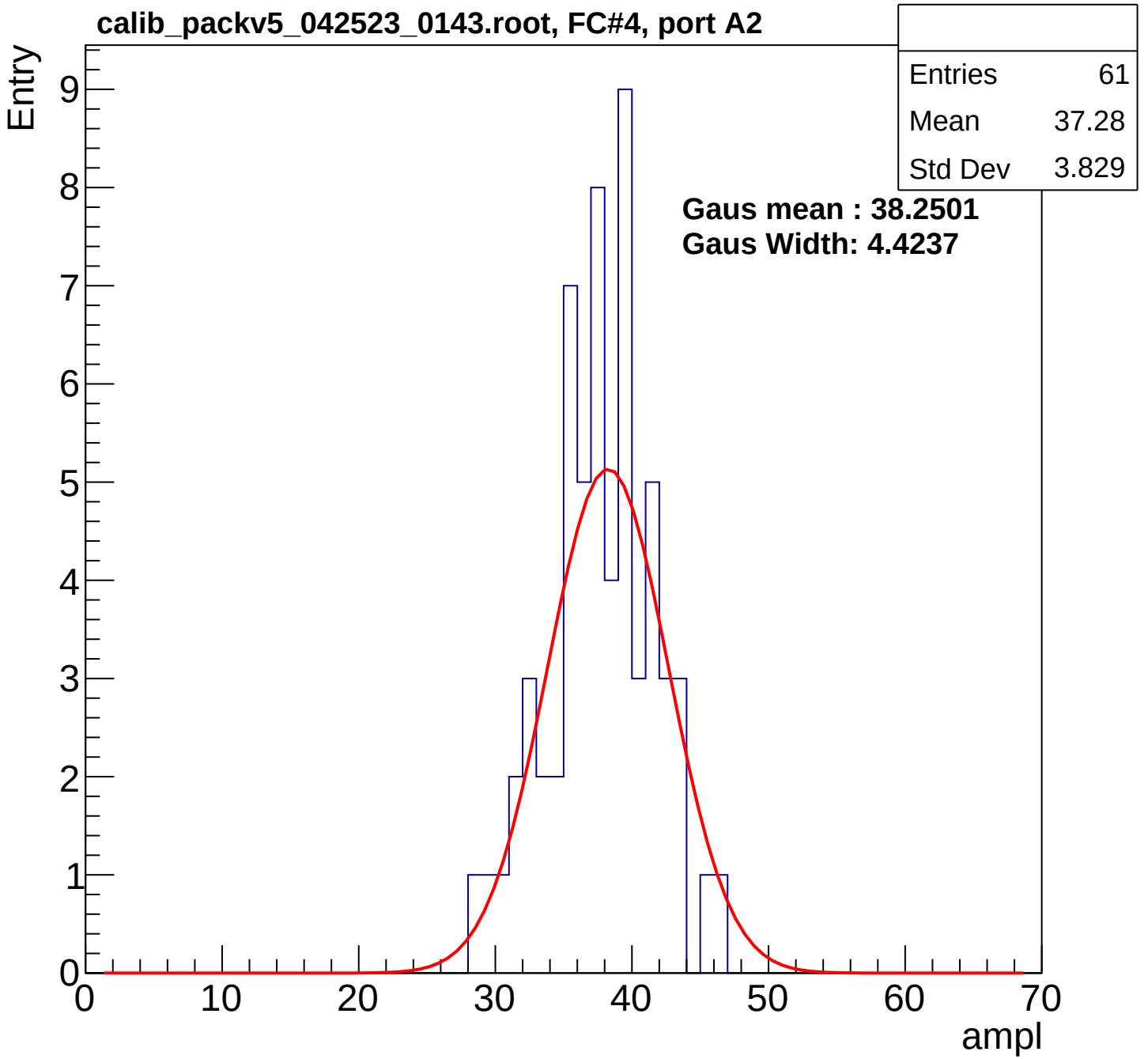
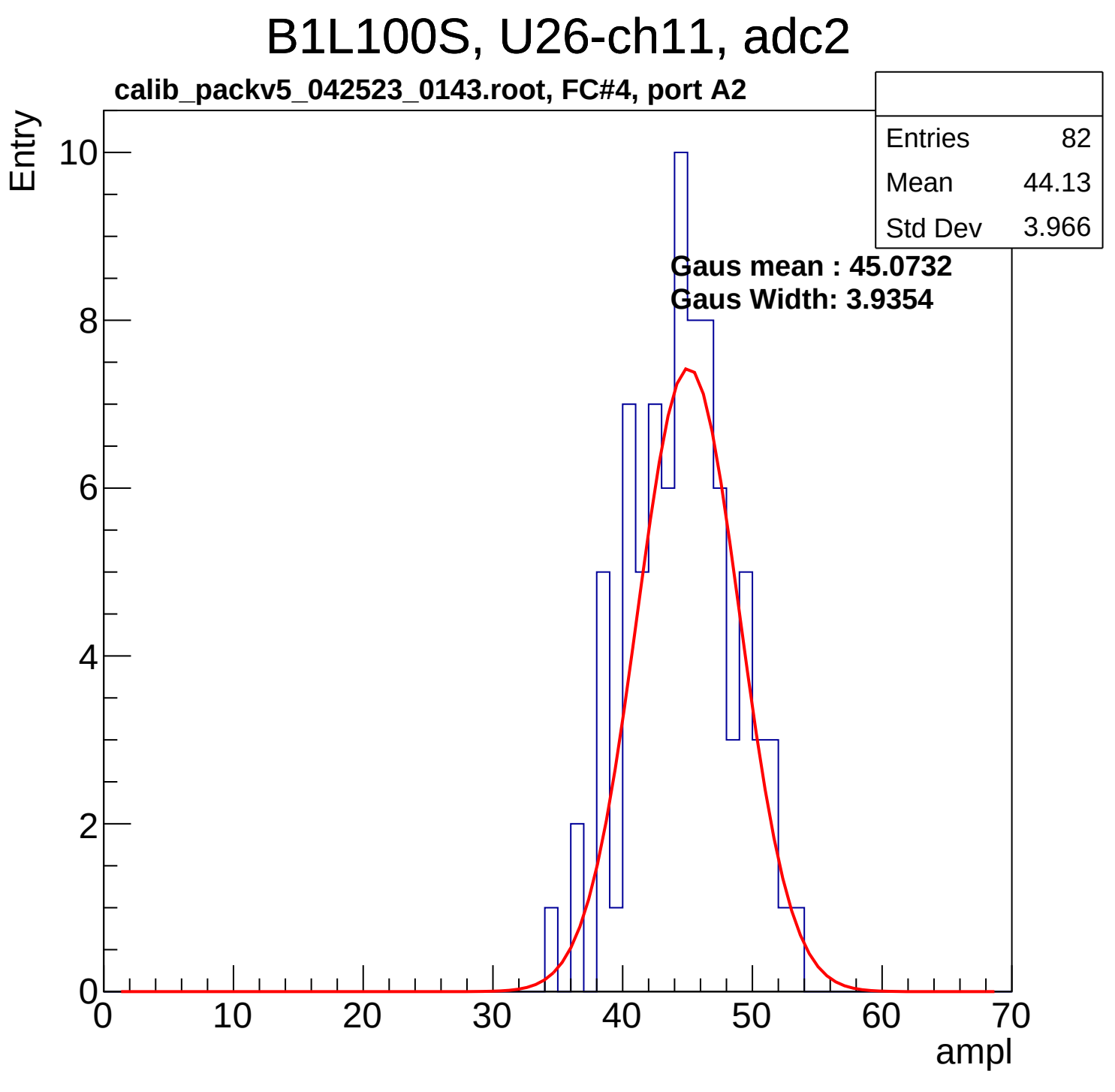
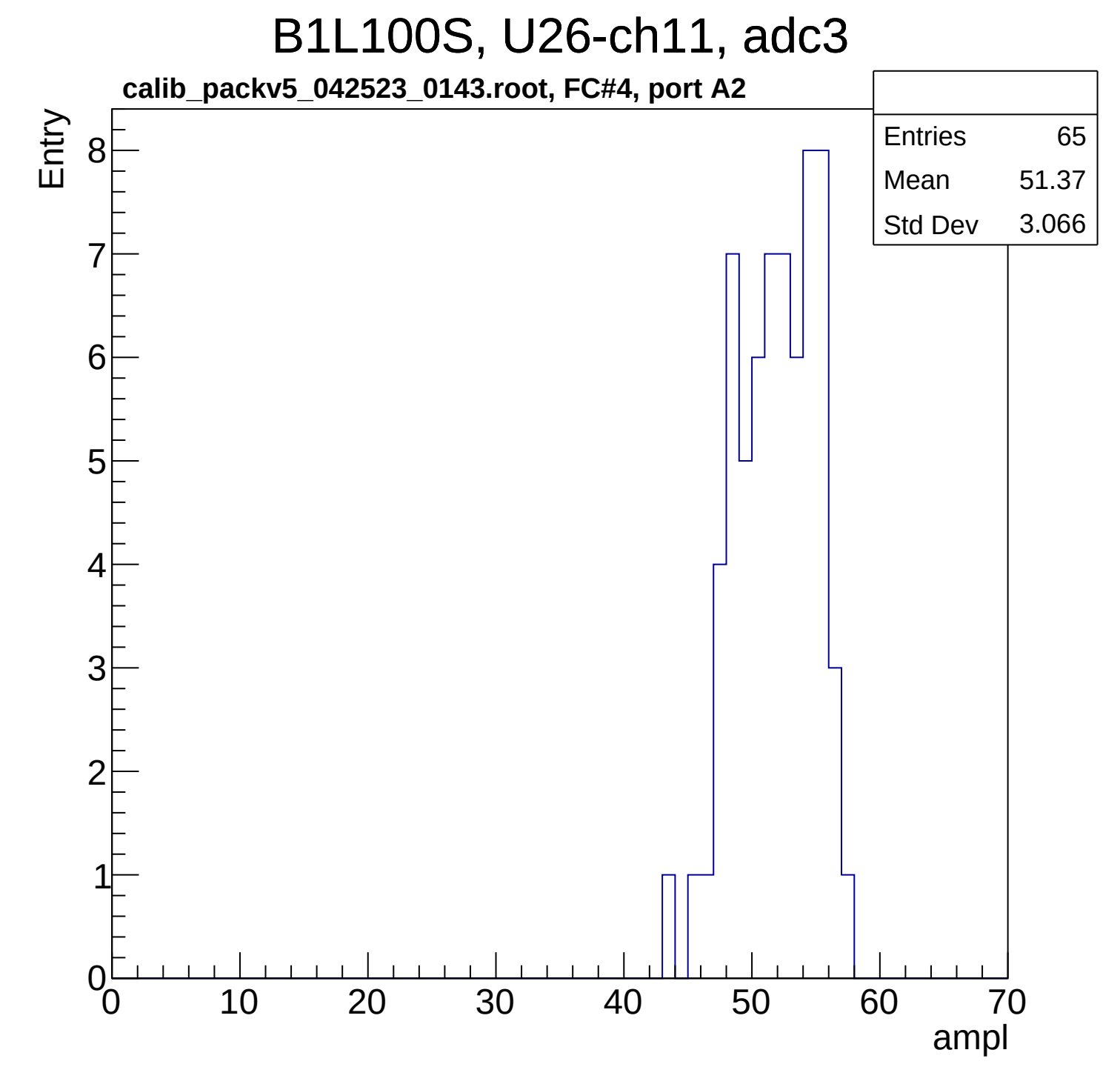
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30.98
Std Dev 3.42
Gaus mean : 31.6076
Gaus Width: 3.9719
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.28
Std Dev 3.829
Gaus mean : 38.2501
Gaus Width: 4.4237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 44.13
Std Dev 3.966
Gaus mean : 45.0732
Gaus Width: 3.9354
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch11, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 51.37
Std Dev 3.066
Entry
ampl
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1
2
3
4
5
6
7
8
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10
20
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40
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60
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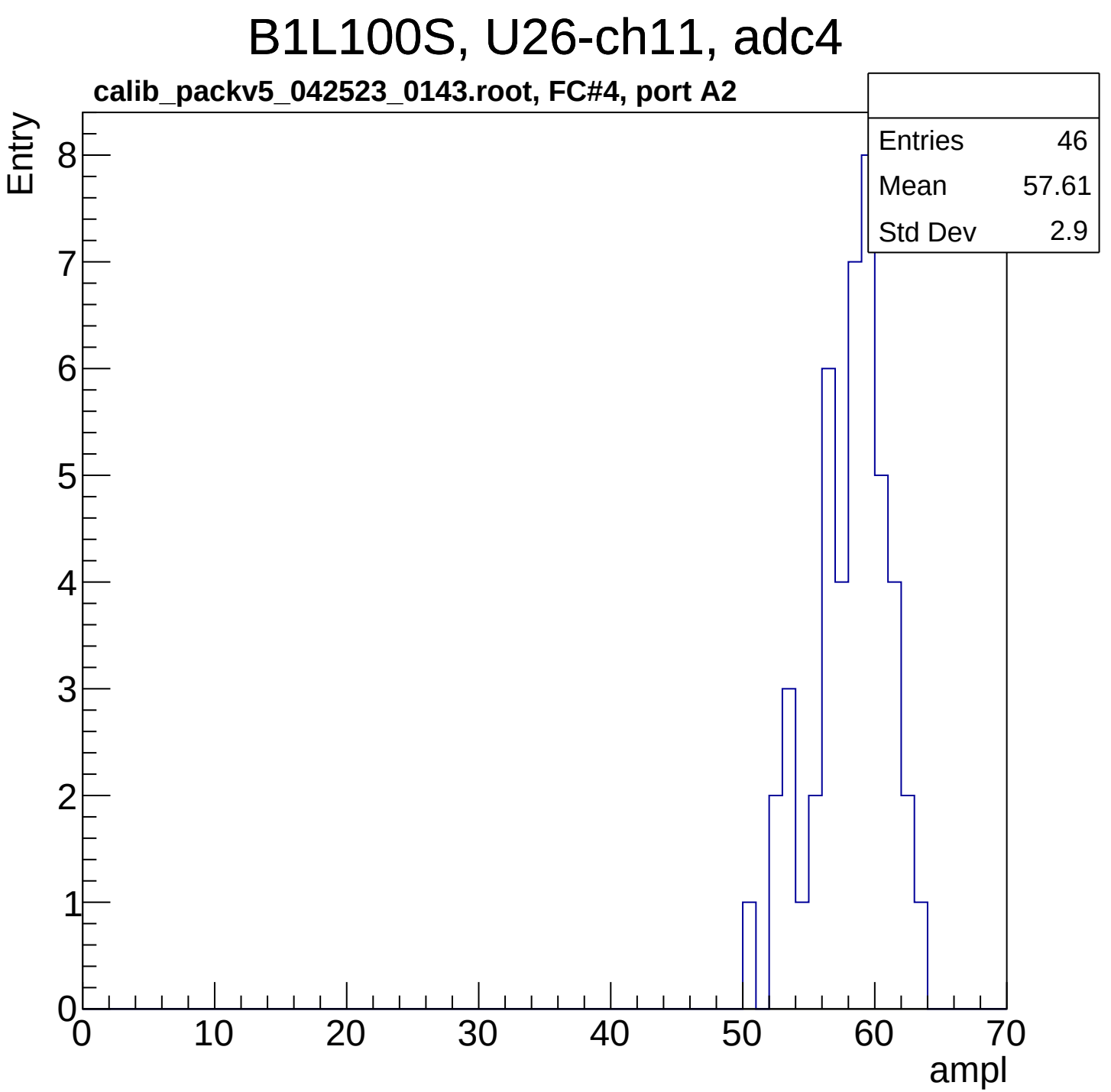
B1L100S, U26-ch11, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 57.61
Std Dev 2.9
Entry
ampl
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1
2
3
4
5
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7
8
0
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20
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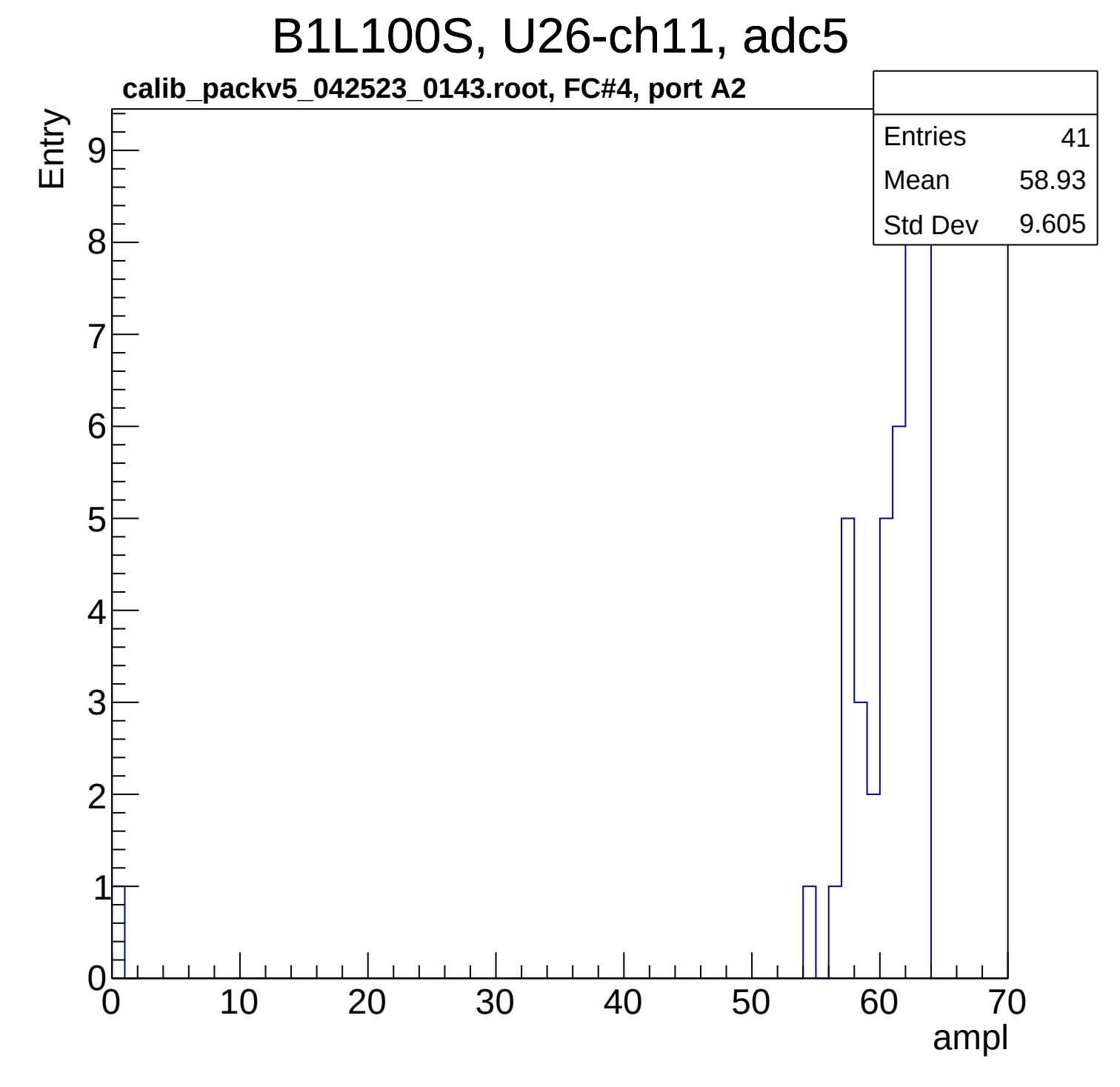
60
70


B1L100S, U26-ch11, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.93
Std Dev 9.605
Entry
ampl
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1
2
3
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6
7
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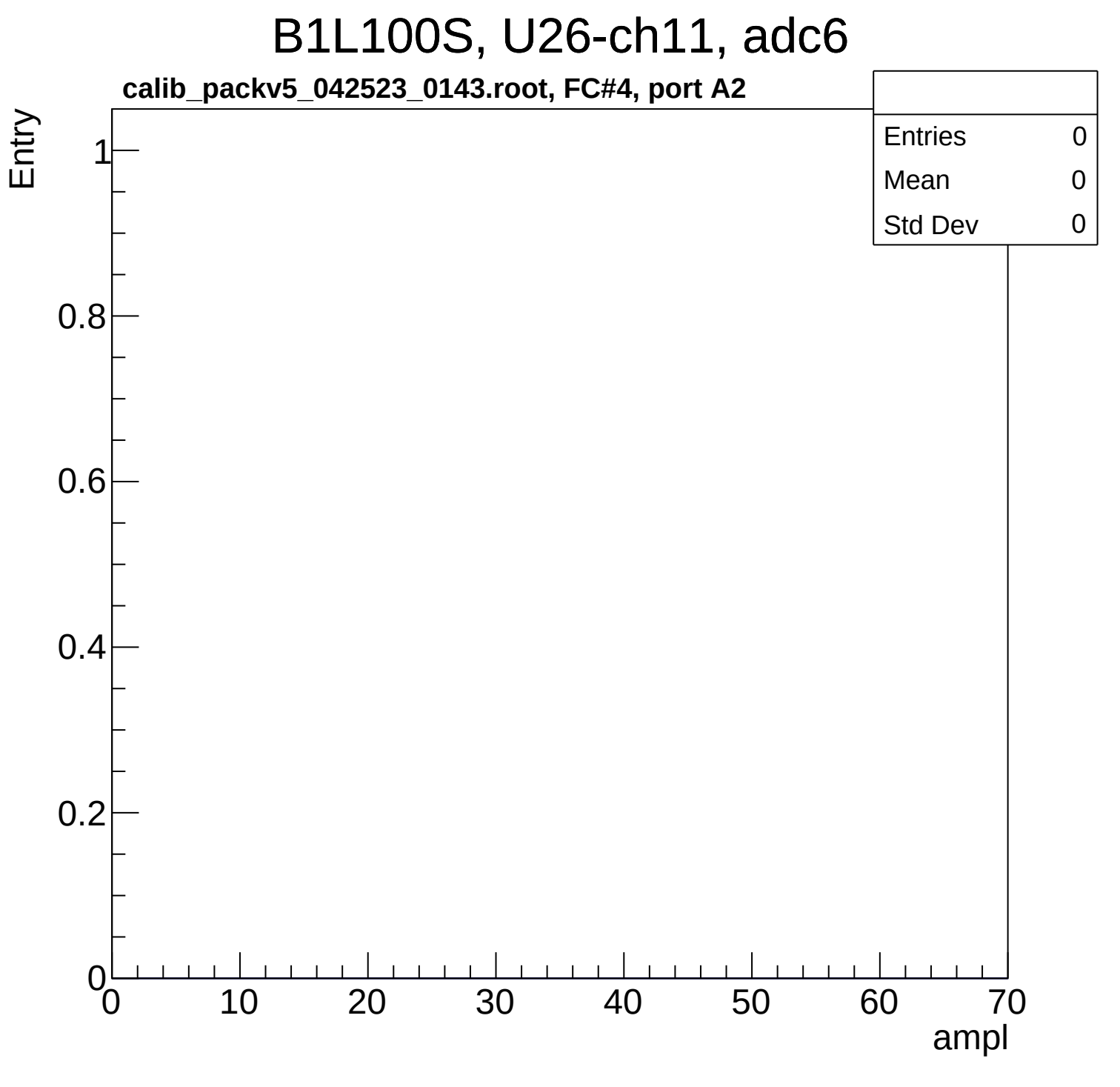
B1L100S, U26-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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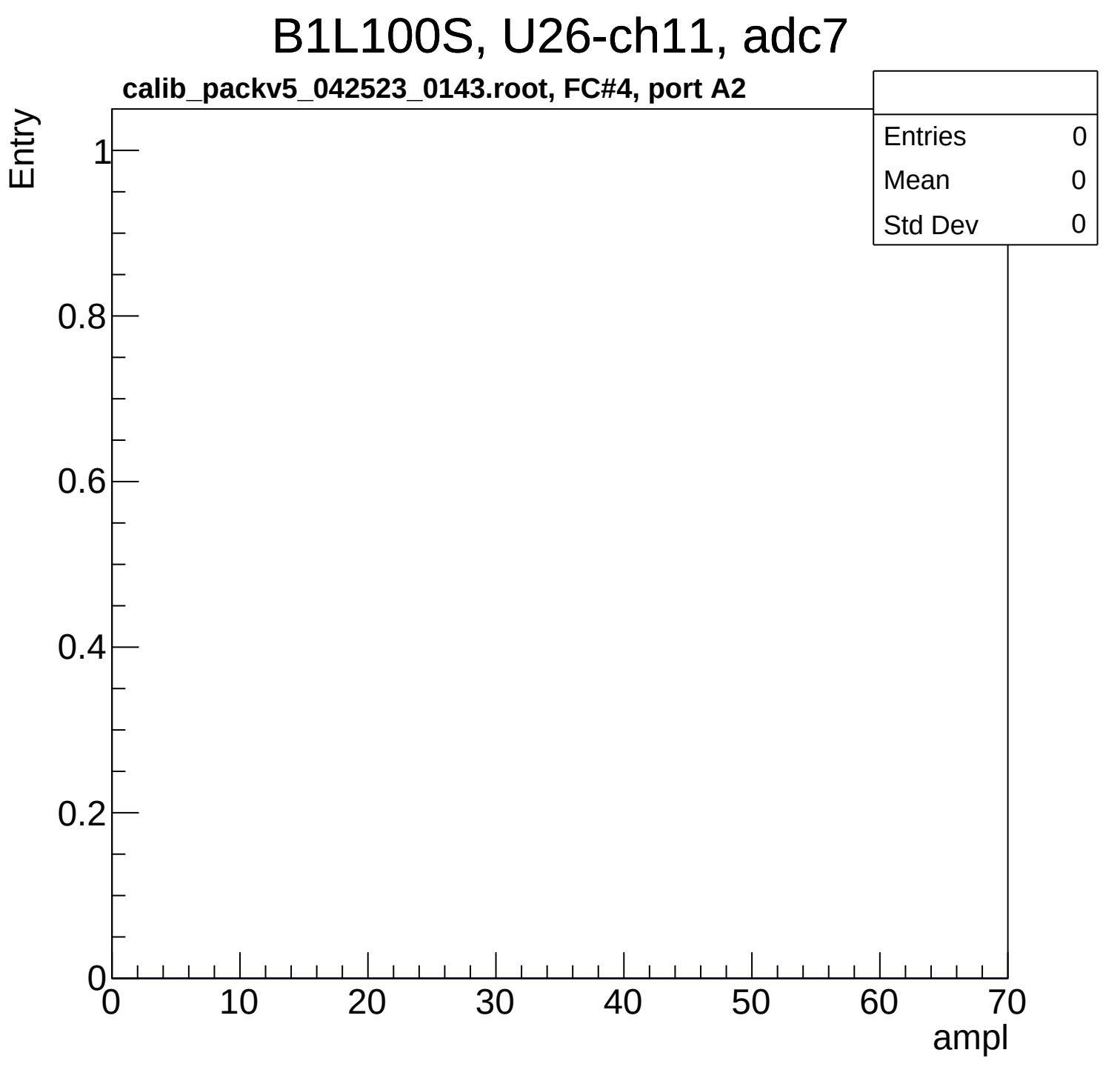
40
50
60
70
ampl


B1L100S, U26-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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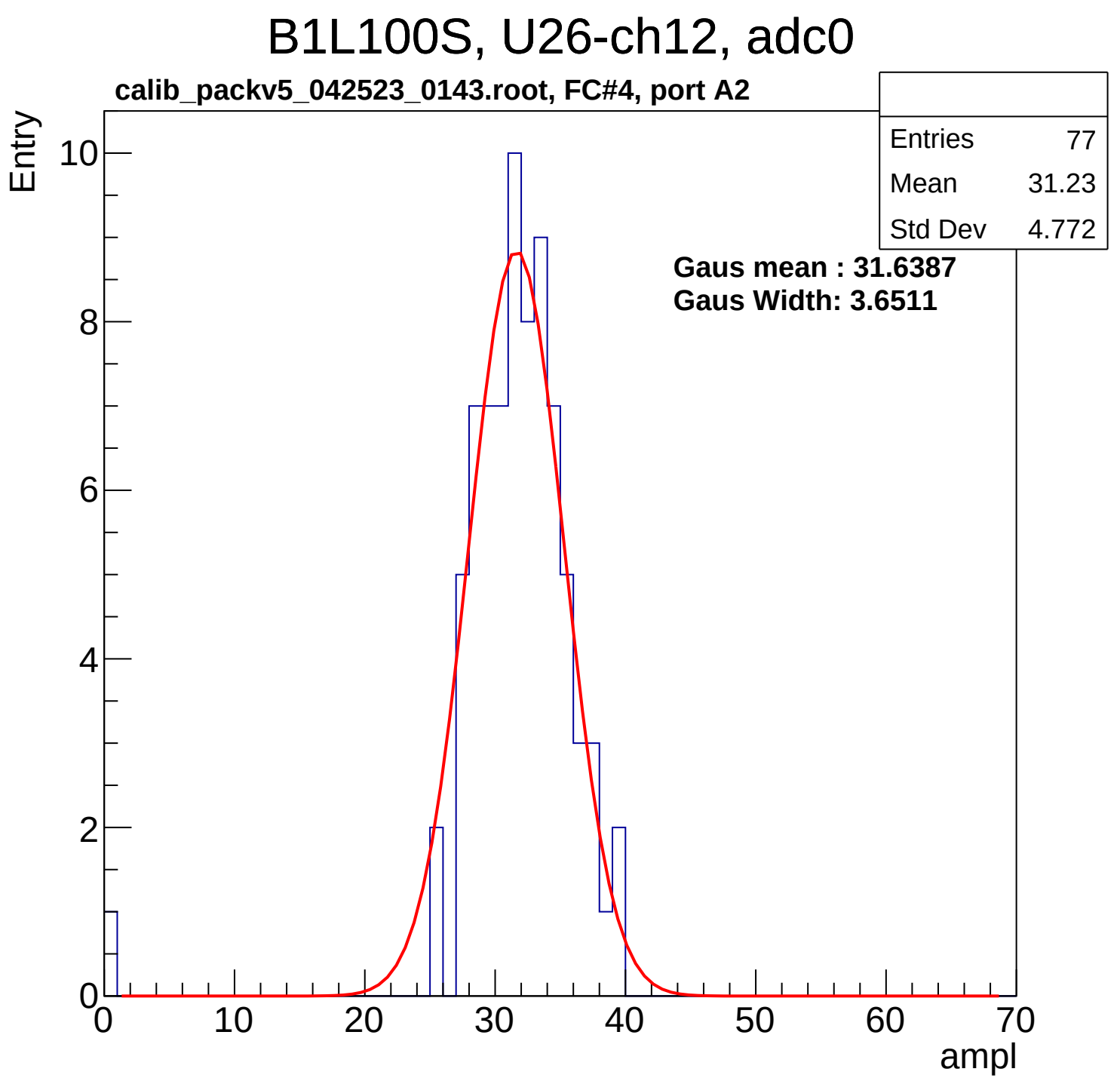
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 31.23
Std Dev 4.772
Gaus mean : 31.6387
Gaus Width: 3.6511
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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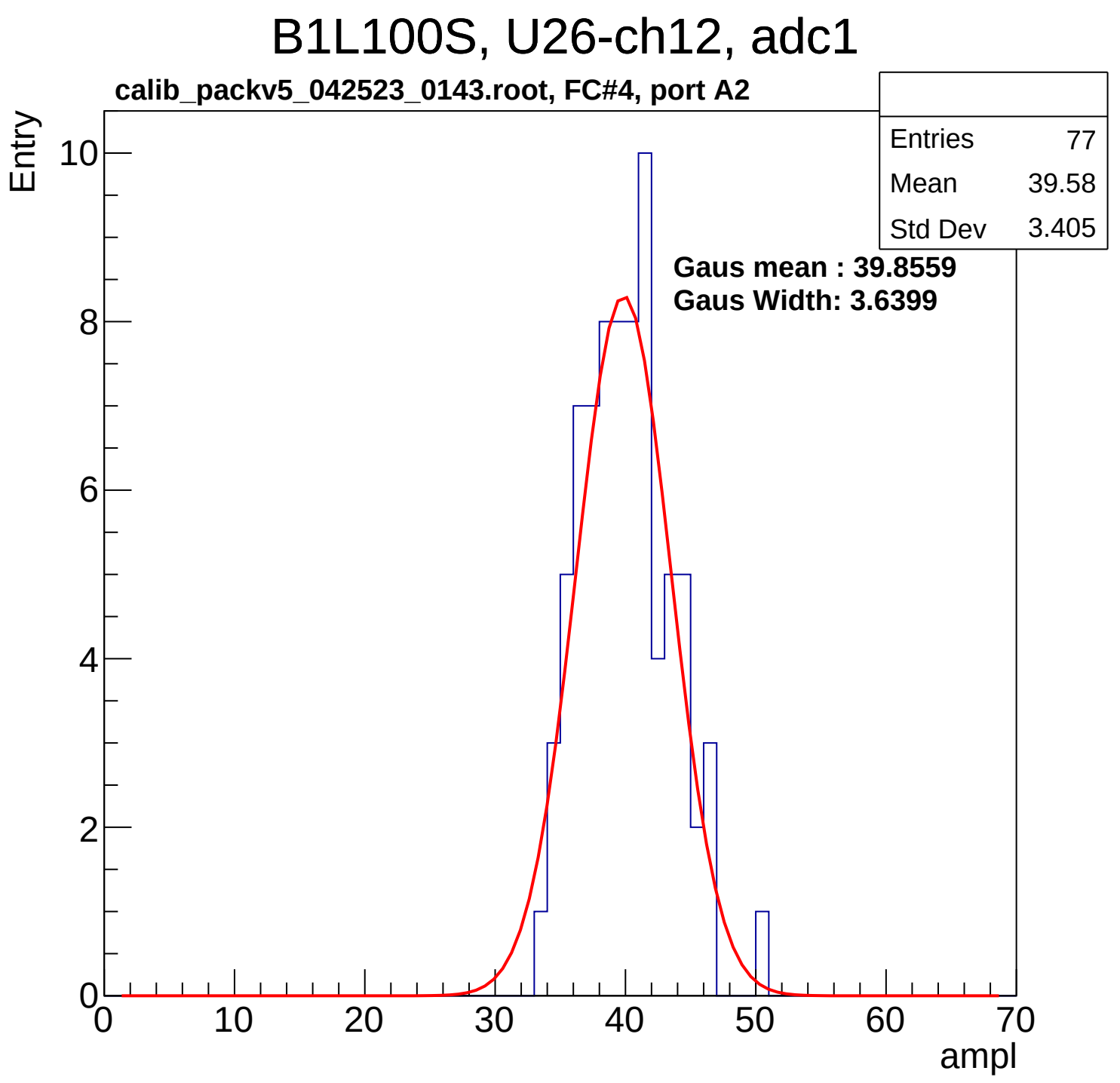
B1L100S, U26-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 39.58
Std Dev 3.405
Gaus mean : 39.8559
Gaus Width: 3.6399
Entry
ampl
0
2
4
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8
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0
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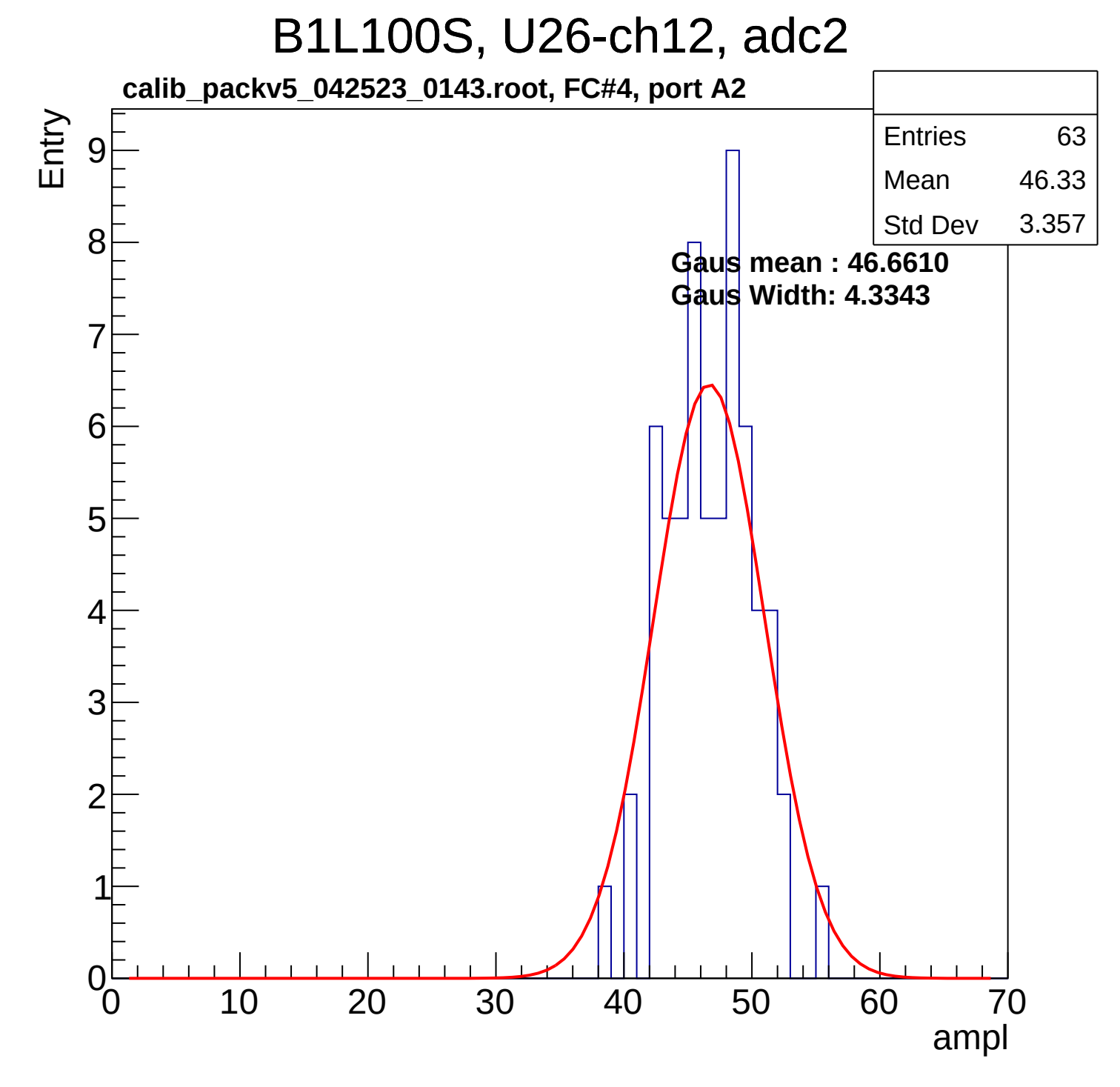
B1L100S, U26-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 46.33
Std Dev 3.357
Gaus mean : 46.6610
Gaus Width: 4.3343
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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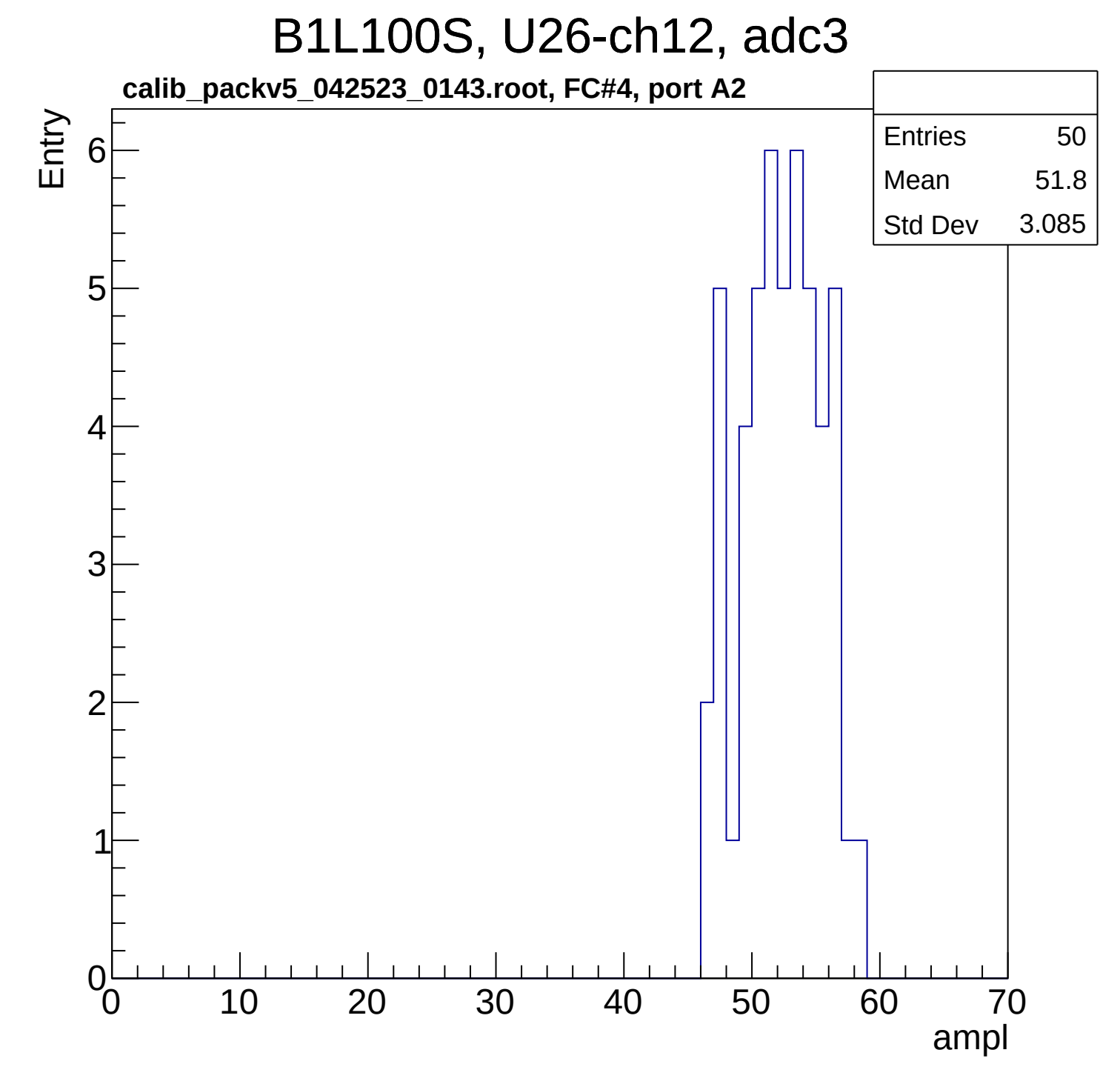
B1L100S, U26-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 51.8
Std Dev 3.085
Entry
0
1
2
3
4
5
6
0
10
20
30
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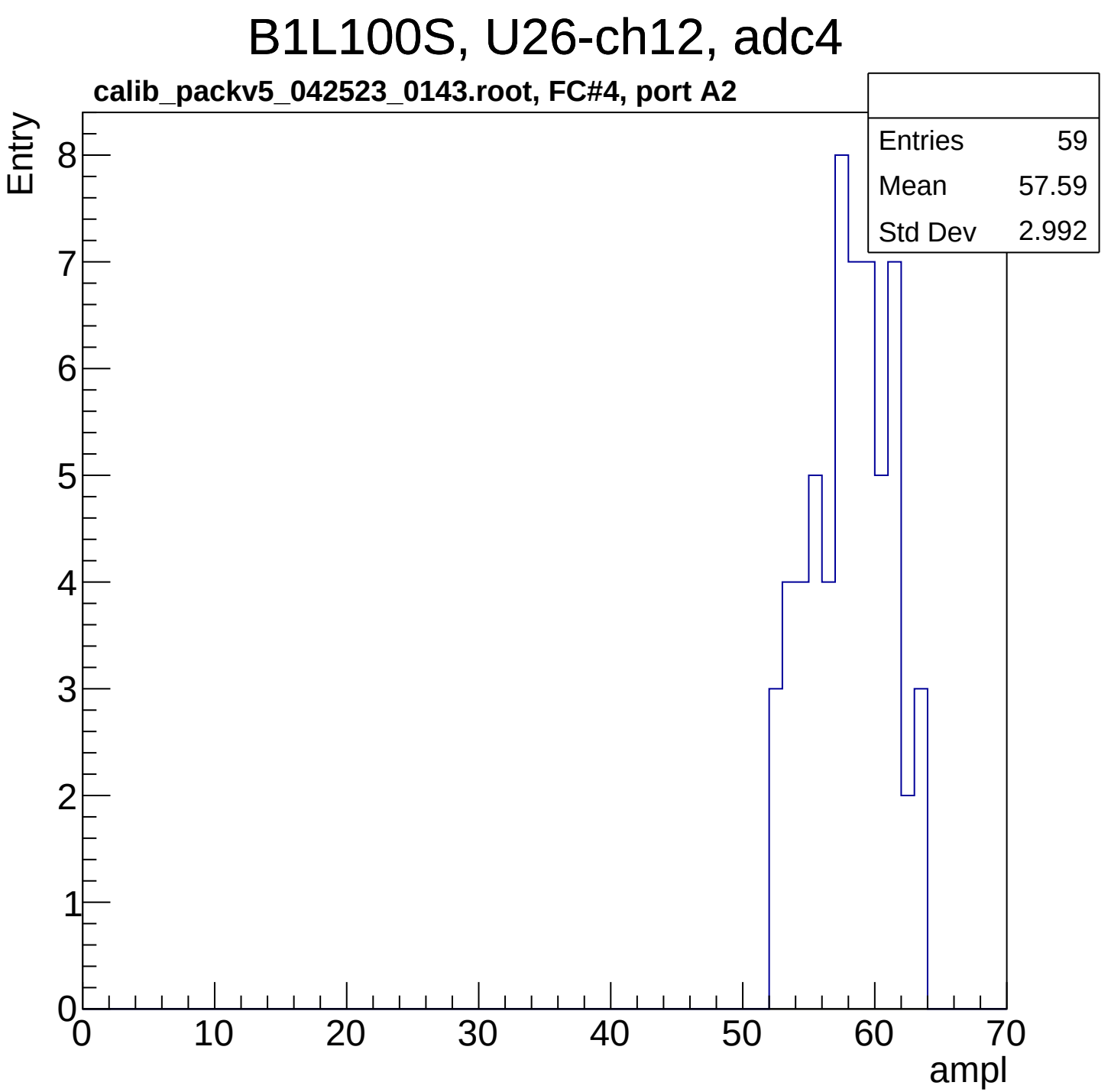
50
60
70
ampl


B1L100S, U26-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 57.59
Std Dev 2.992
Entry
ampl
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1
2
3
4
5
6
7
8
0
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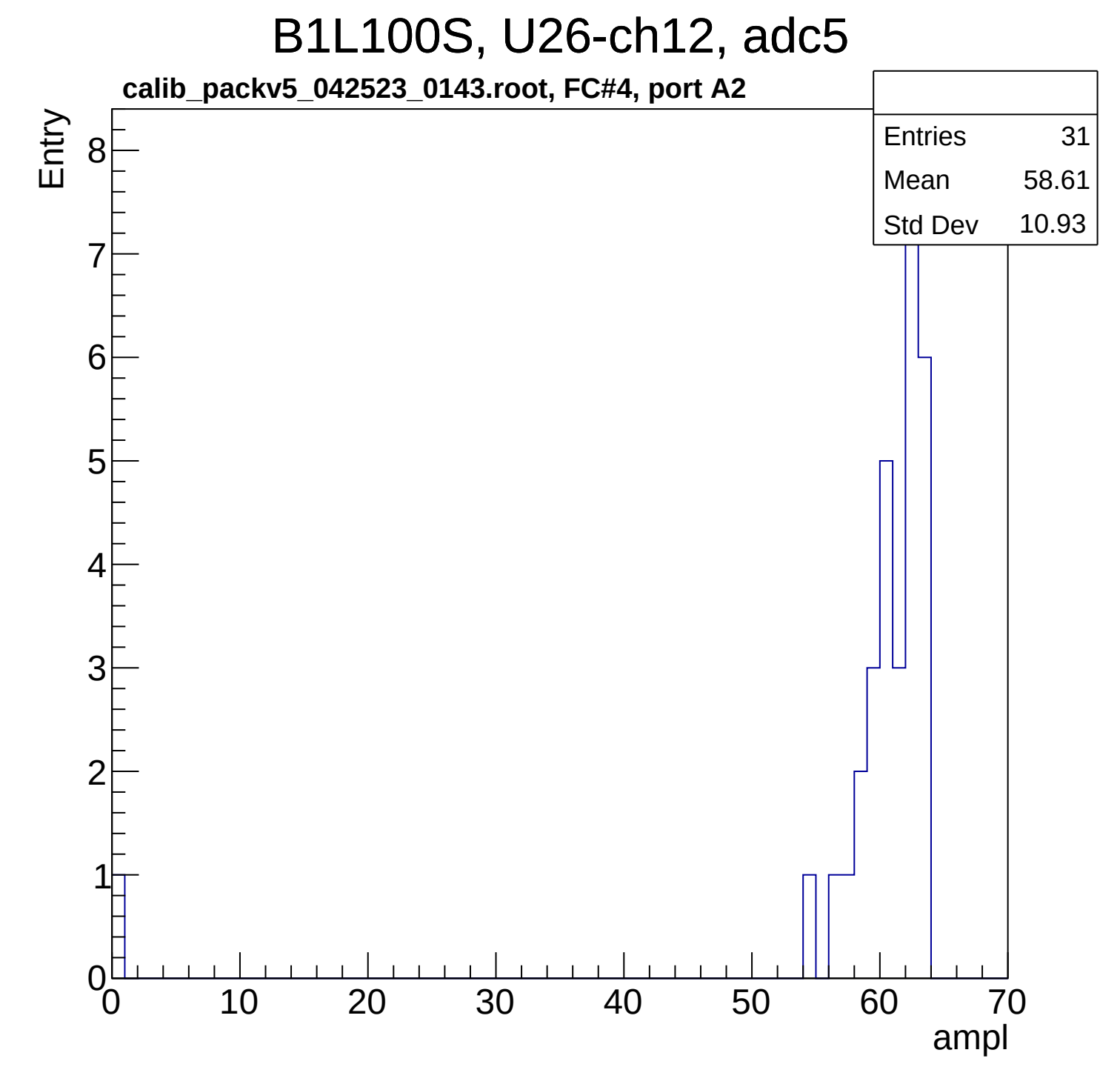
B1L100S, U26-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.61
Std Dev 10.93
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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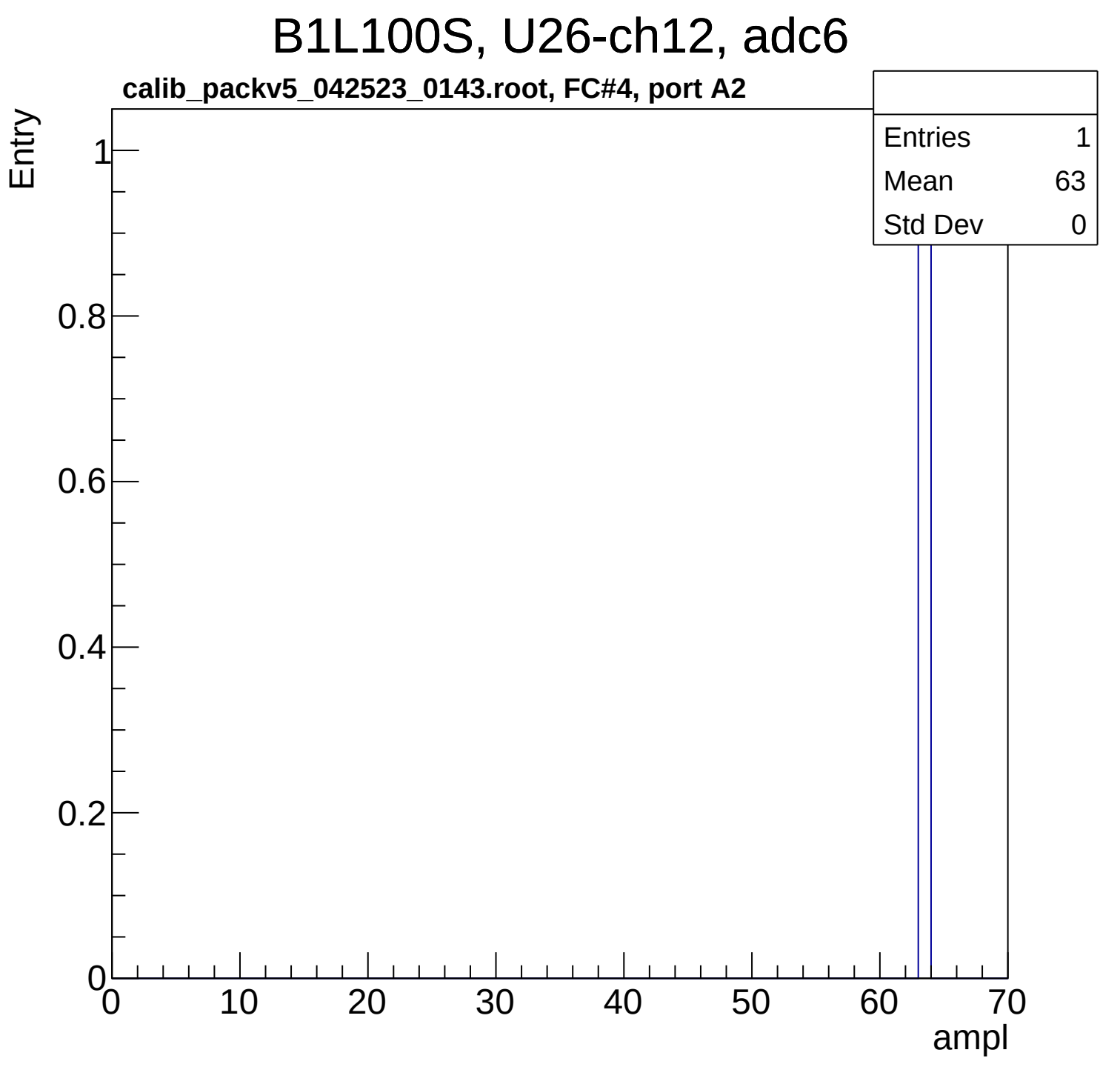
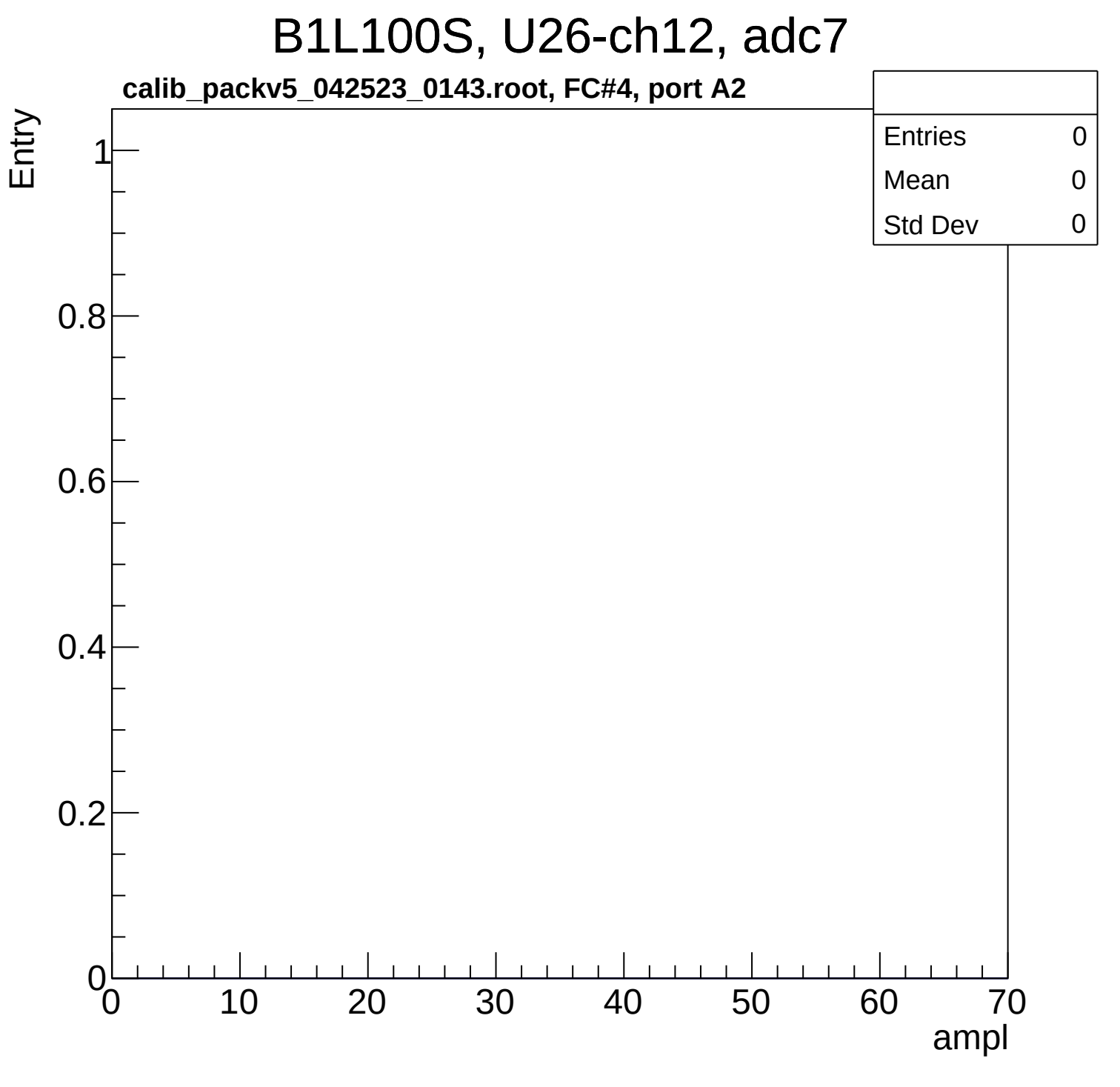
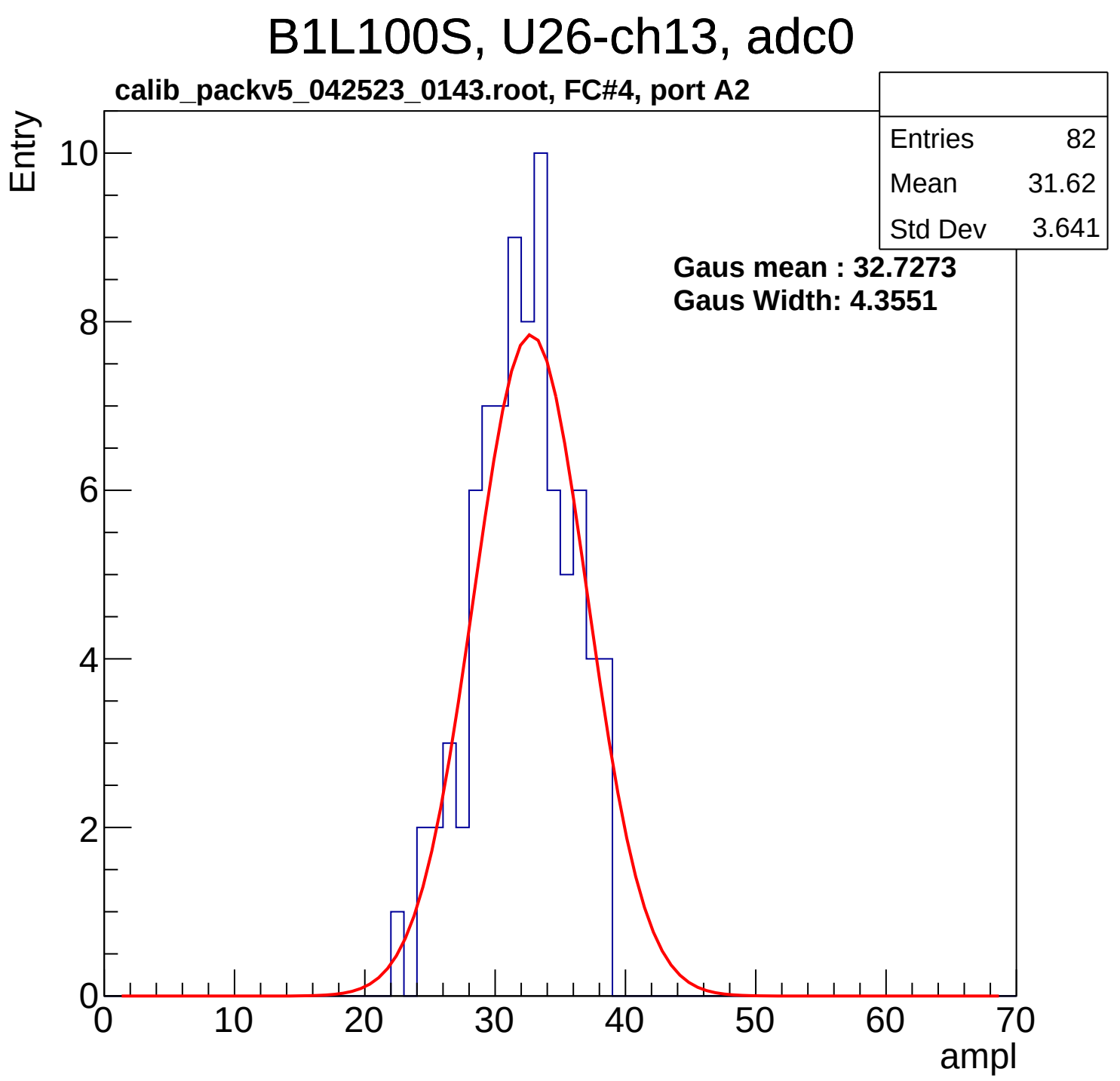
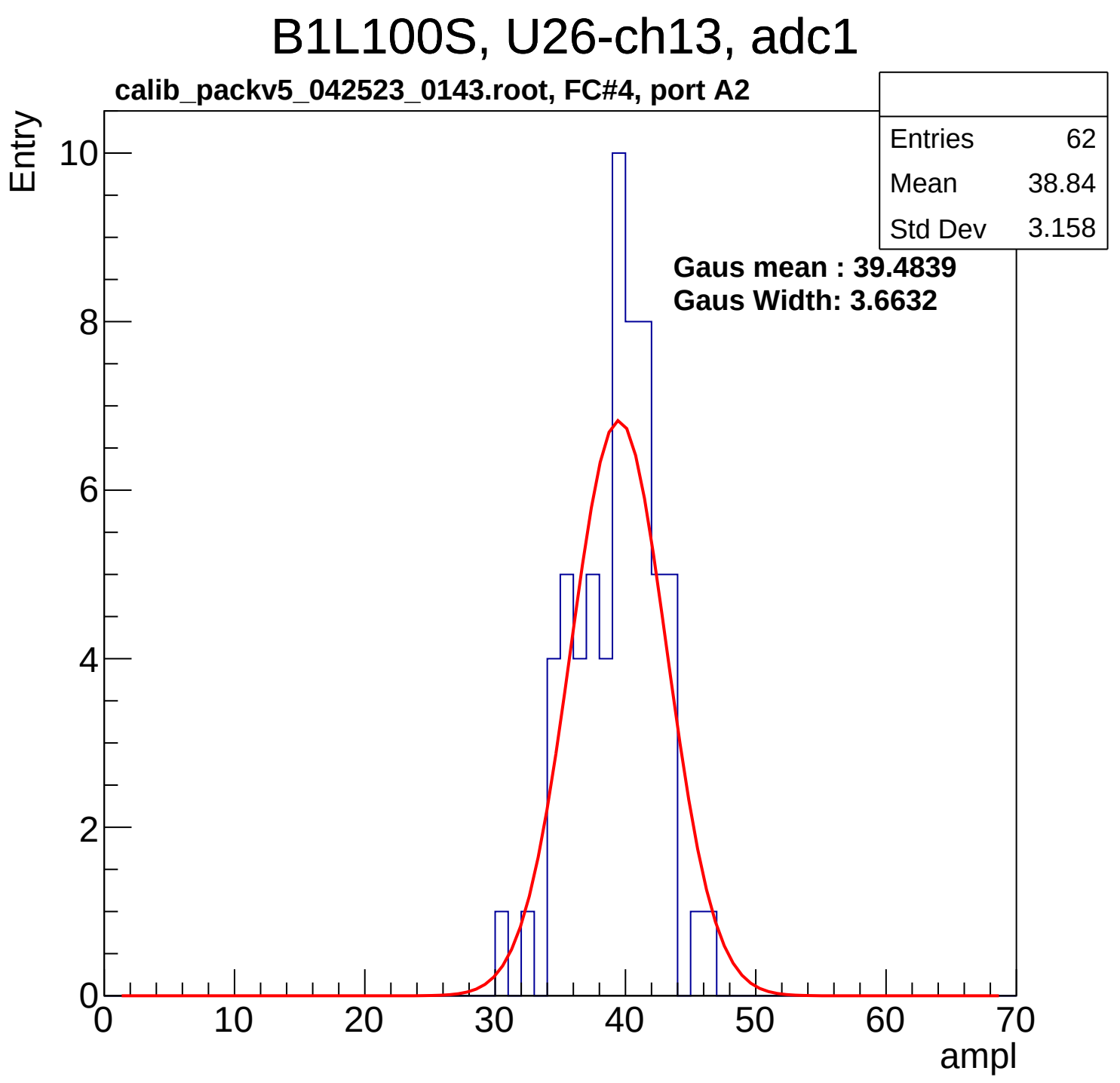
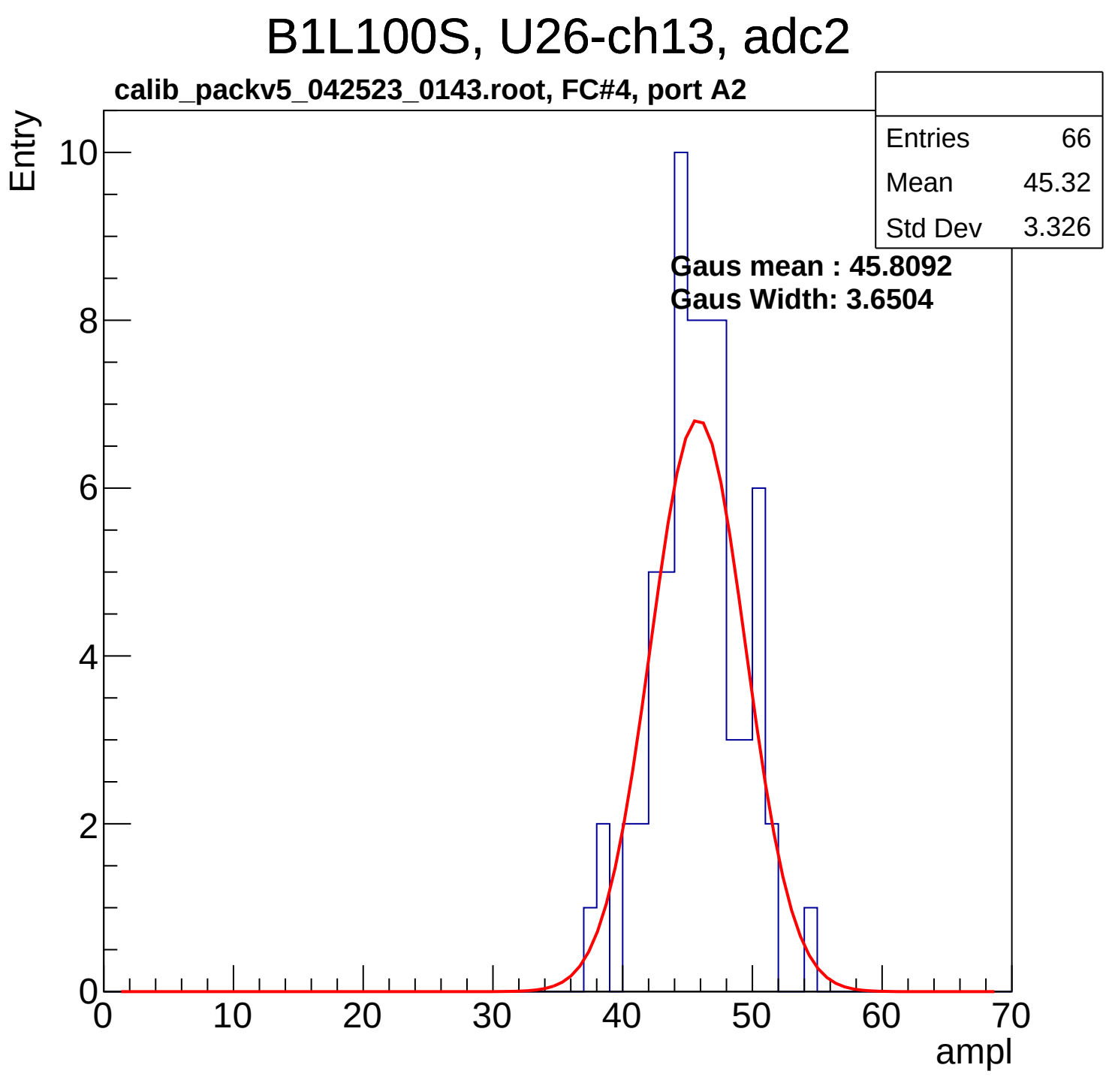
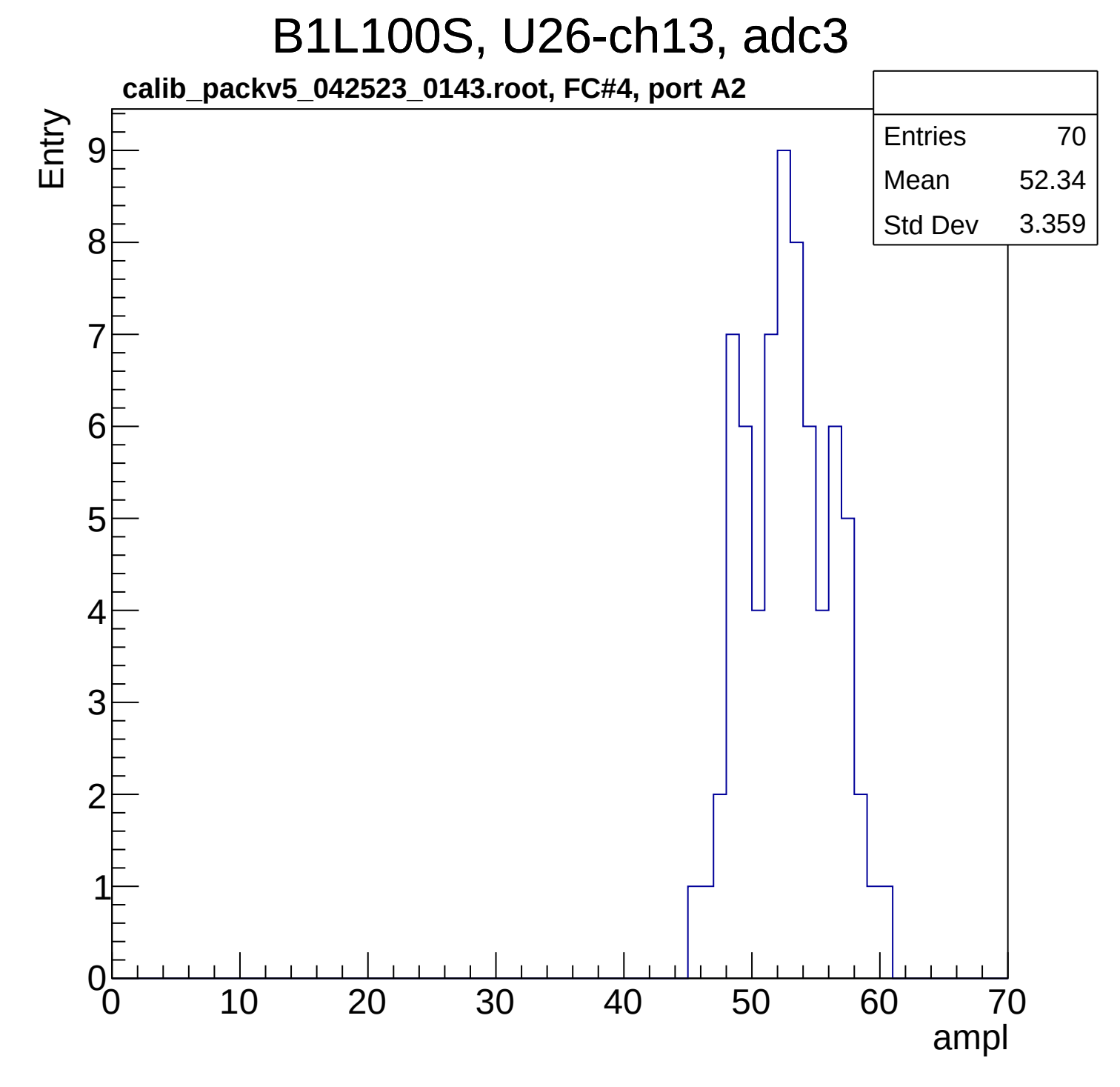

B1L100S, U26-ch12, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

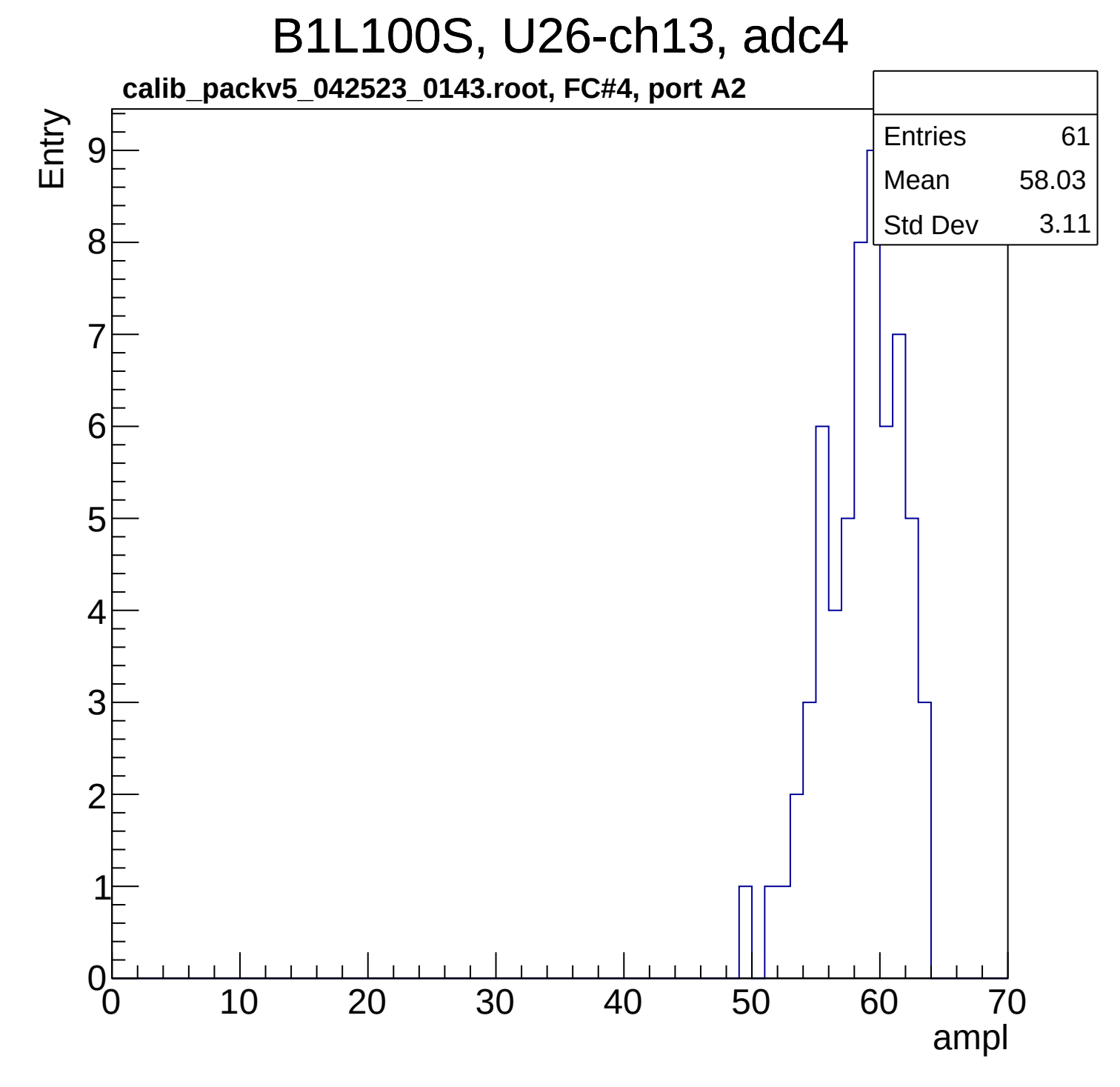

B1L100S, U26-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 31.62
Std Dev 3.641
Gaus mean : 32.7273
Gaus Width: 4.3551
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 38.84
Std Dev 3.158
Gaus mean : 39.4839
Gaus Width: 3.6632
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 45.32
Std Dev 3.326
Gaus mean : 45.8092
Gaus Width: 3.6504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 52.34
Std Dev 3.359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 58.03
Std Dev 3.11
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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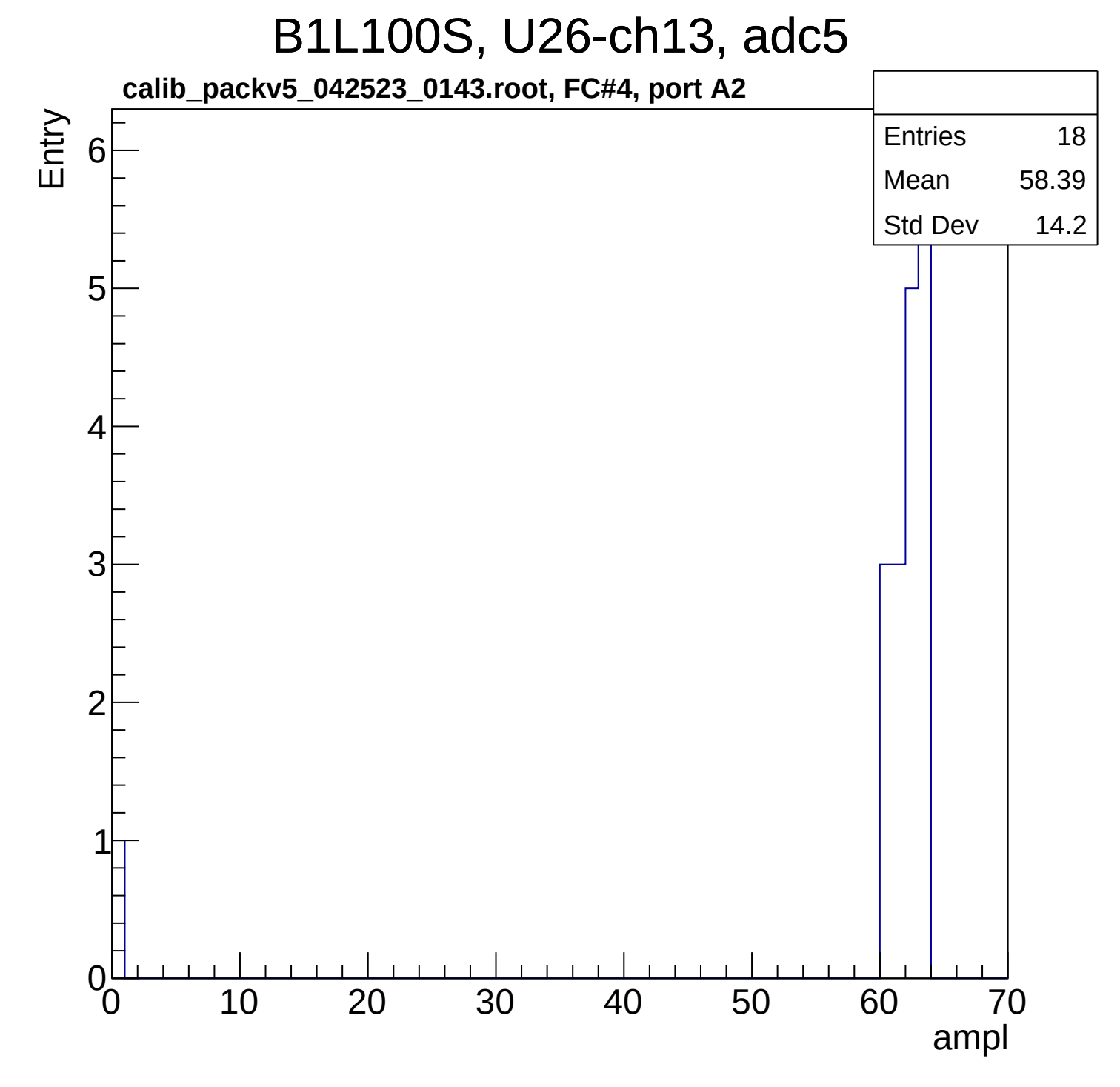
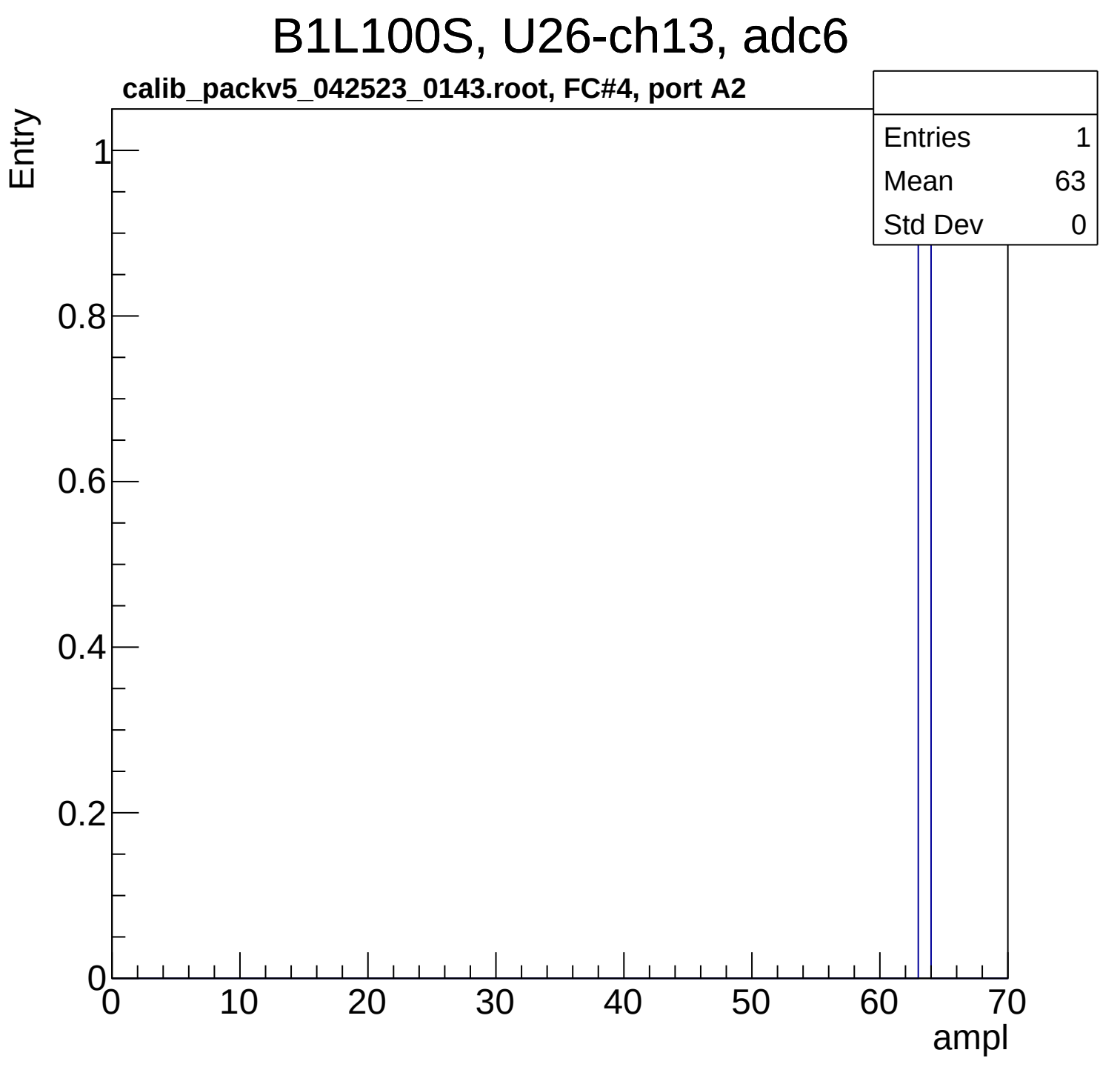
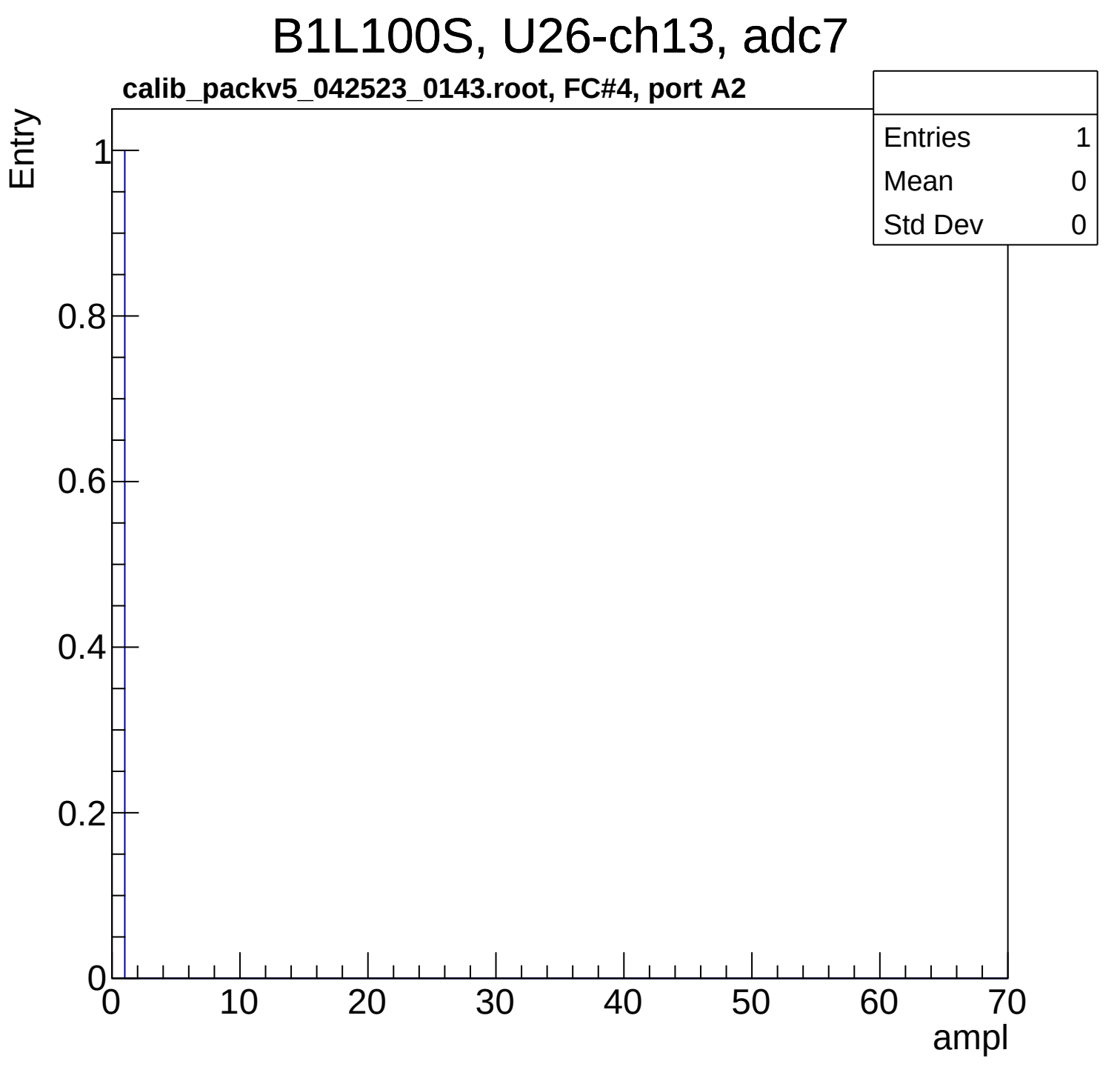
60
70


B1L100S, U26-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 18
Mean 58.39
Std Dev 14.2
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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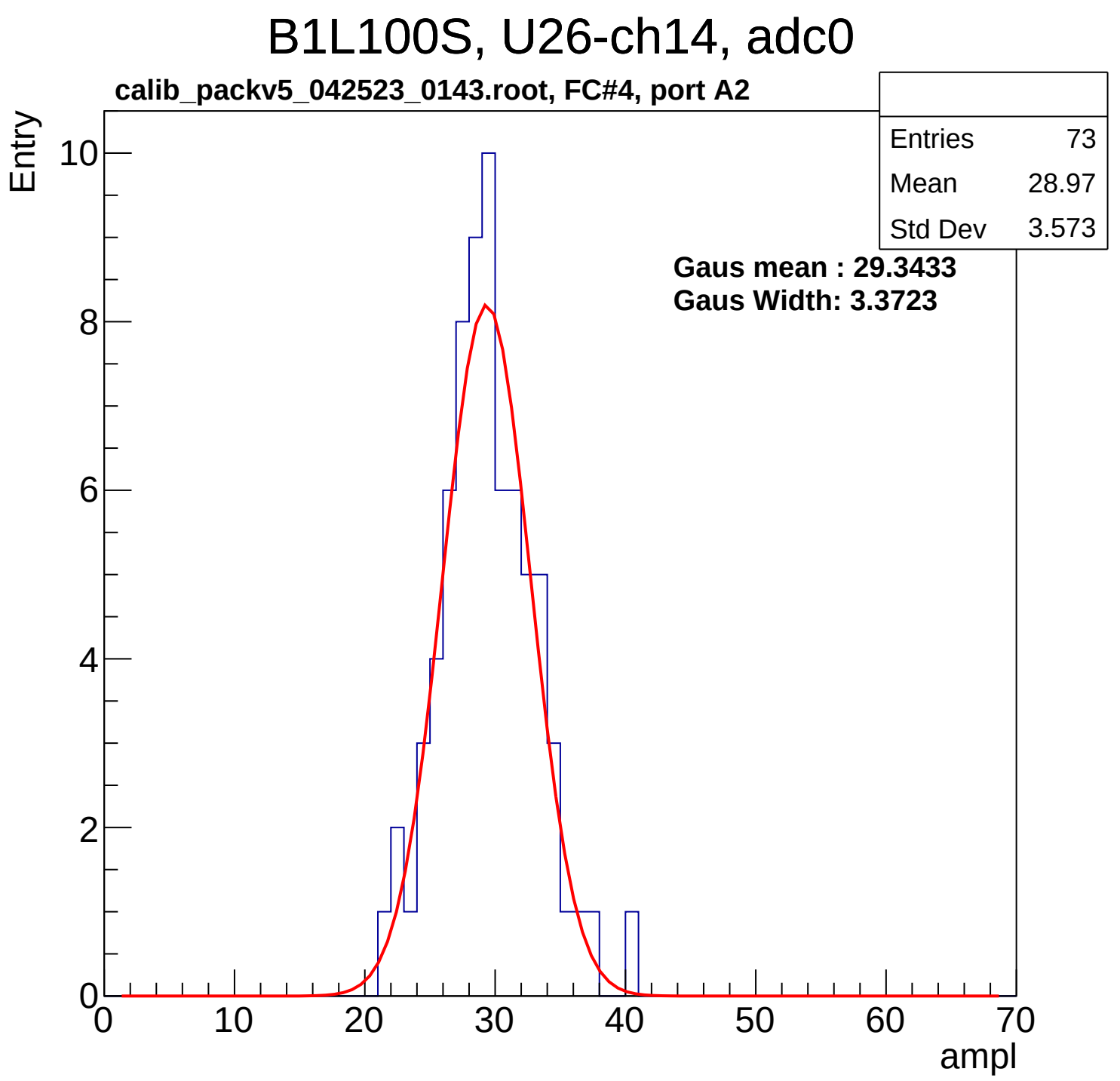
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 28.97
Std Dev 3.573
Gaus mean : 29.3433
Gaus Width: 3.3723
Entry
ampl
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2
4
6
8
10
0
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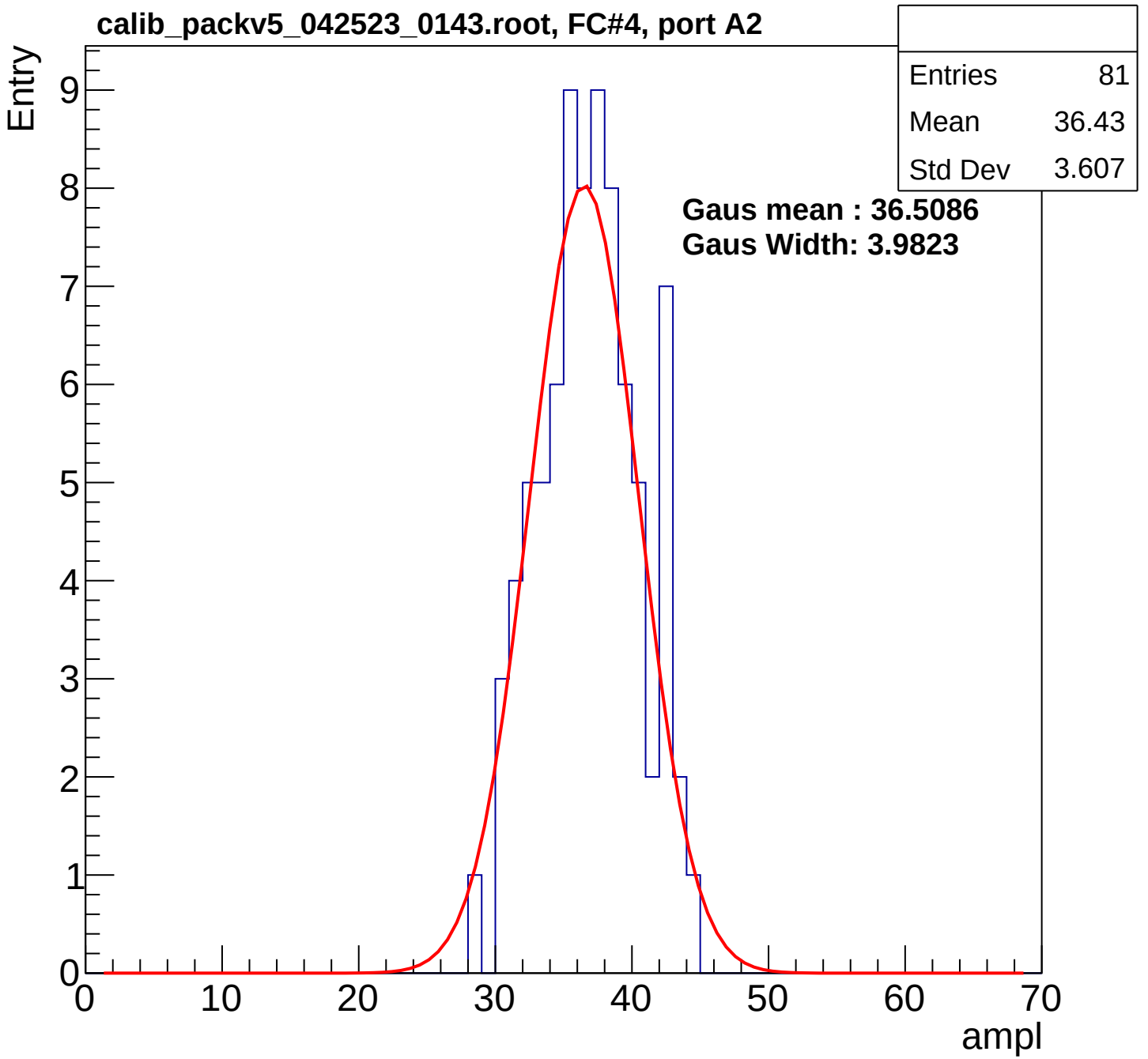
20
30
40
50
60
70


B1L100S, U26-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 36.43
Std Dev 3.607
Gaus mean : 36.5086
Gaus Width: 3.9823
Entry
ampl
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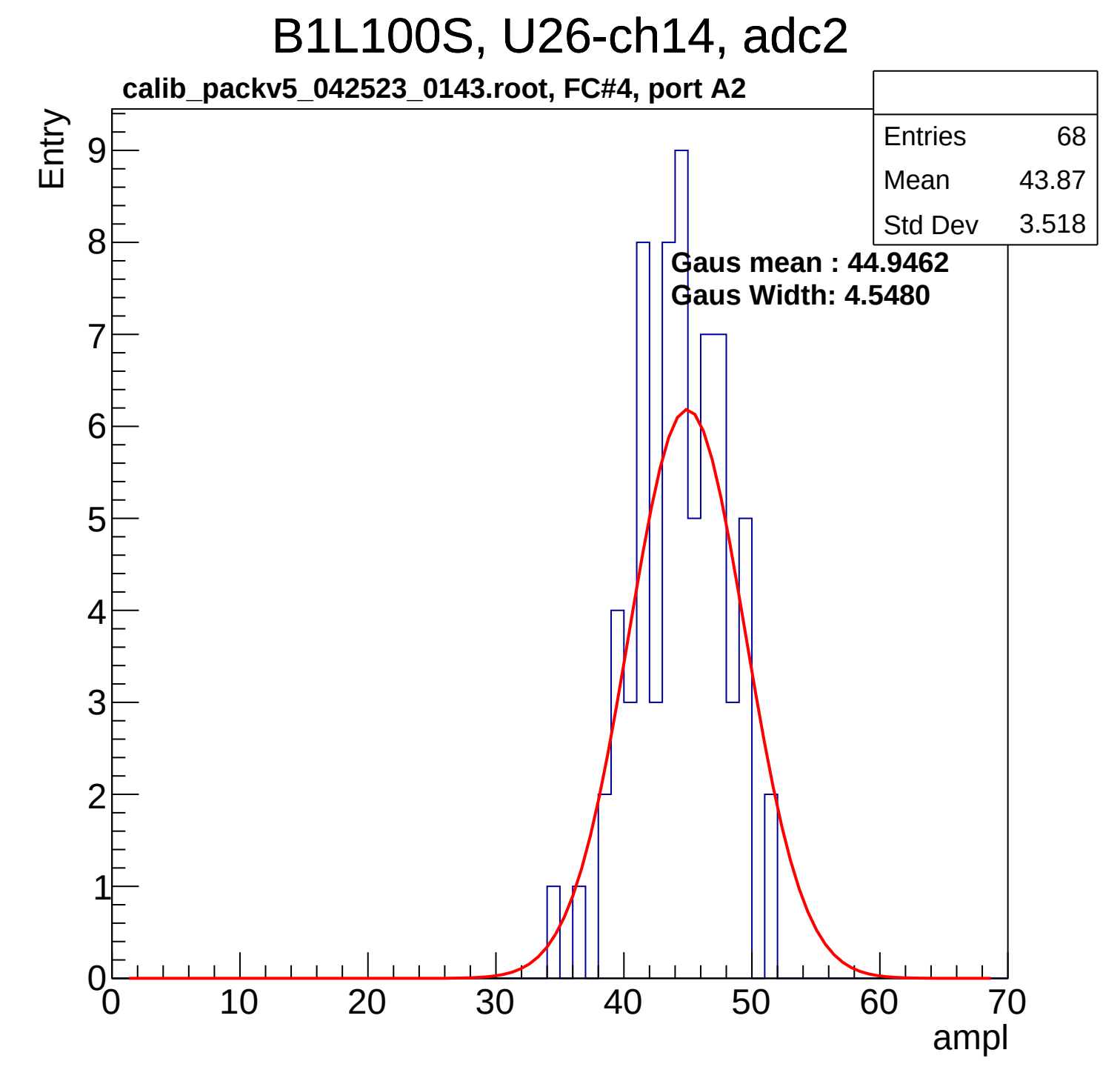
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U26-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.87
Std Dev 3.518
Gaus mean : 44.9462
Gaus Width: 4.5480
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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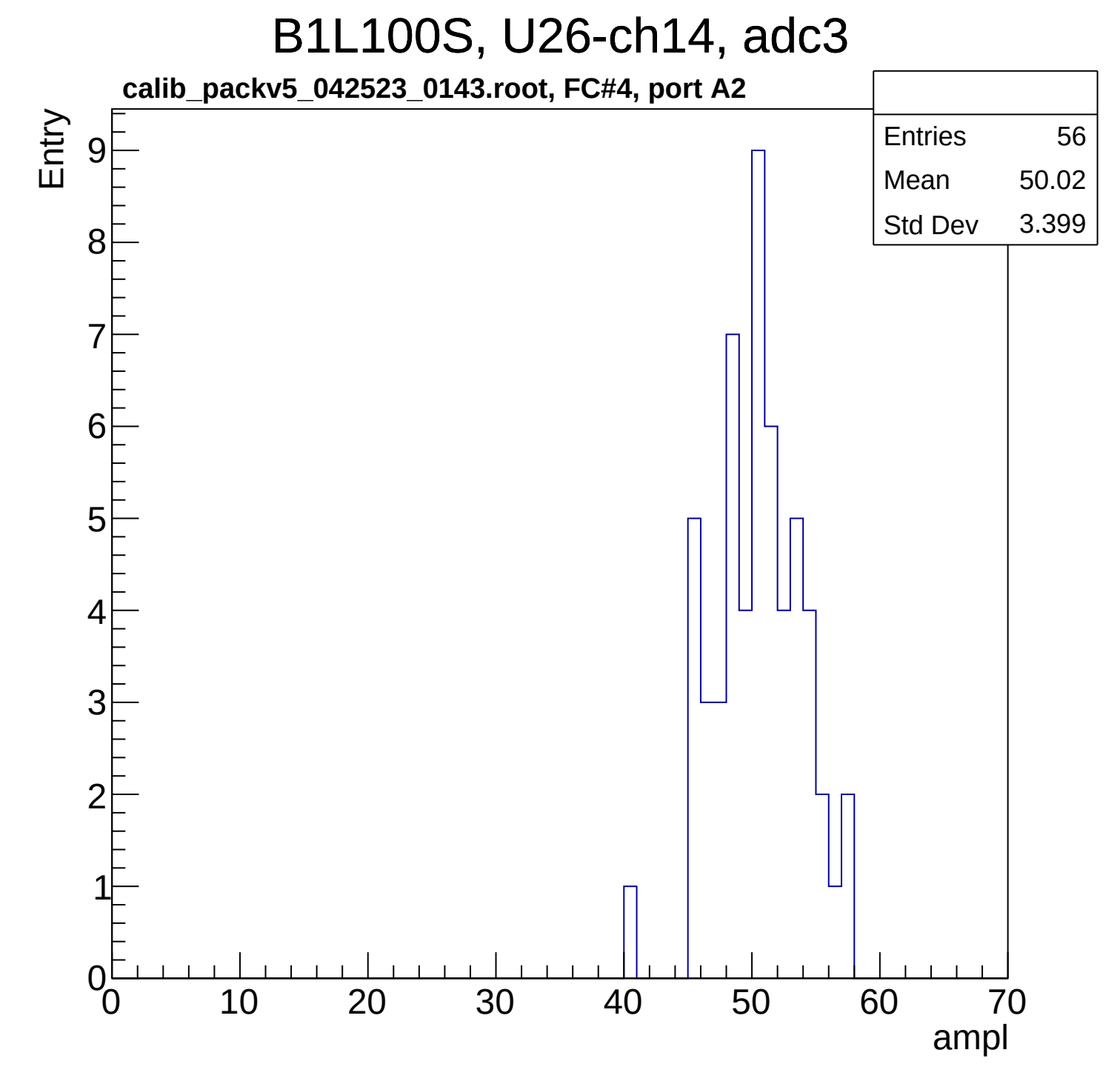
B1L100S, U26-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.02
Std Dev 3.399
Entry
ampl
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3
4
5
6
7
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0
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60
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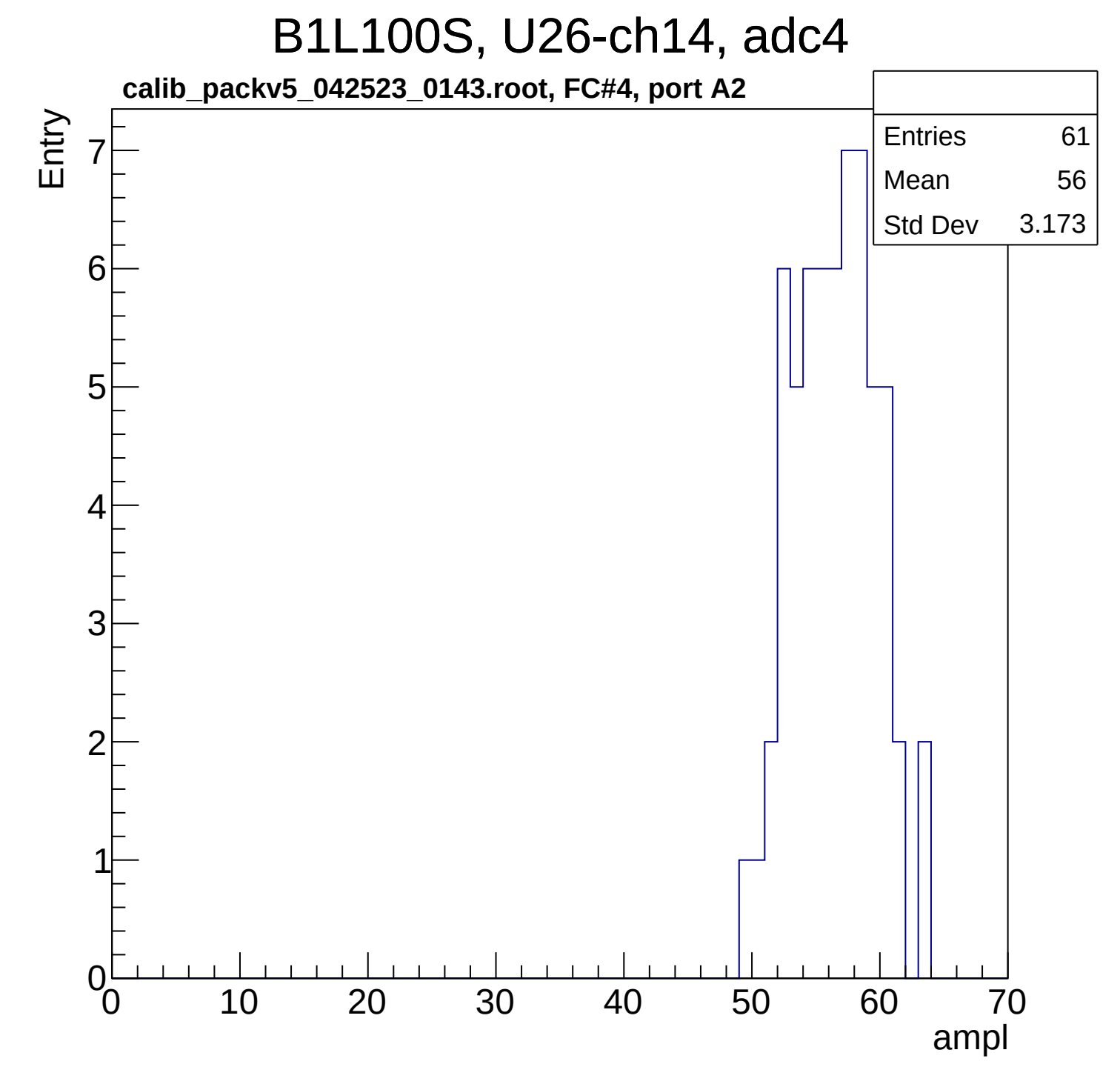
B1L100S, U26-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 56
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
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0
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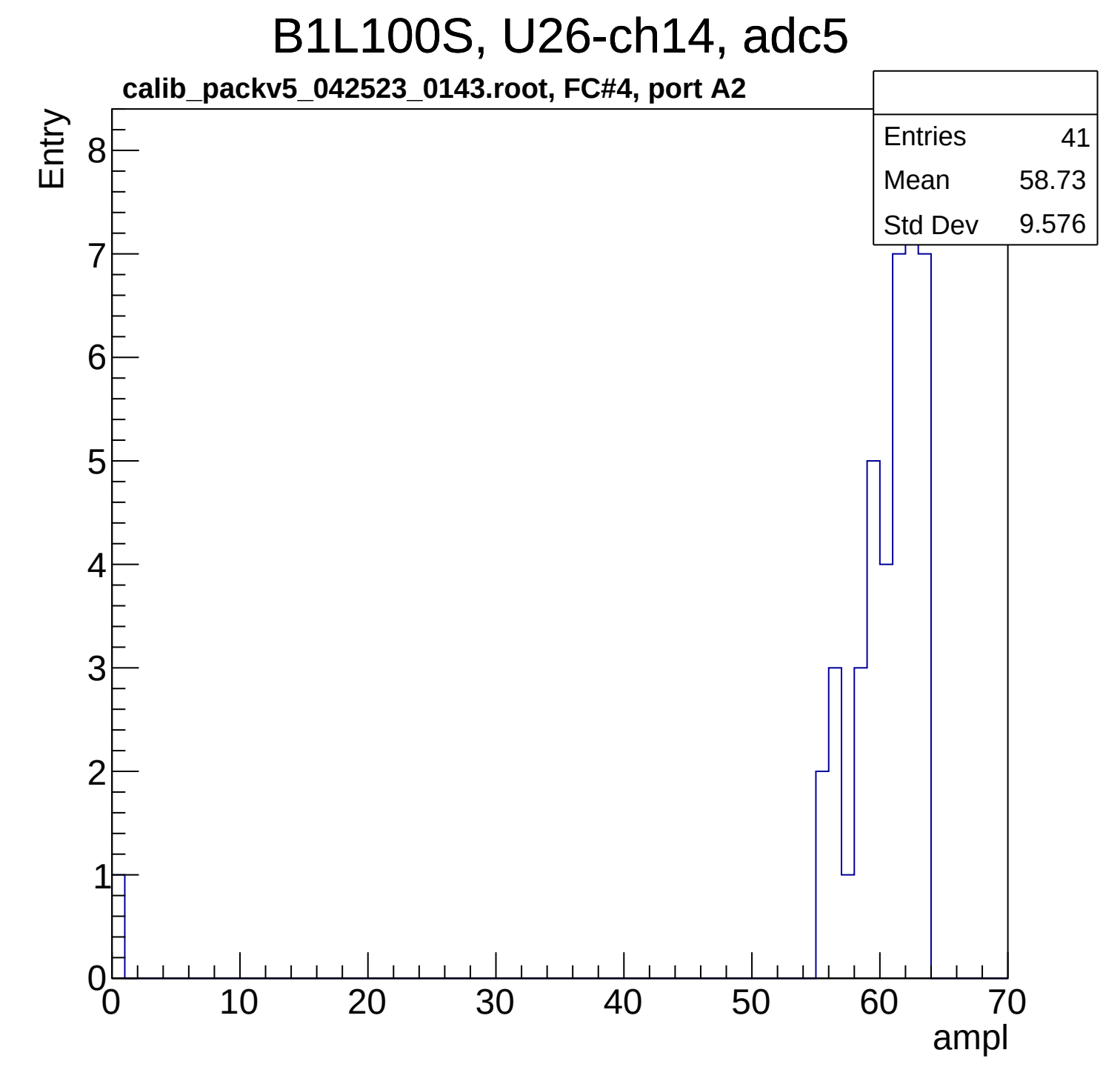
40
50
60
70


B1L100S, U26-ch14, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.73
Std Dev 9.576
Entry
ampl
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1
2
3
4
5
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0
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60
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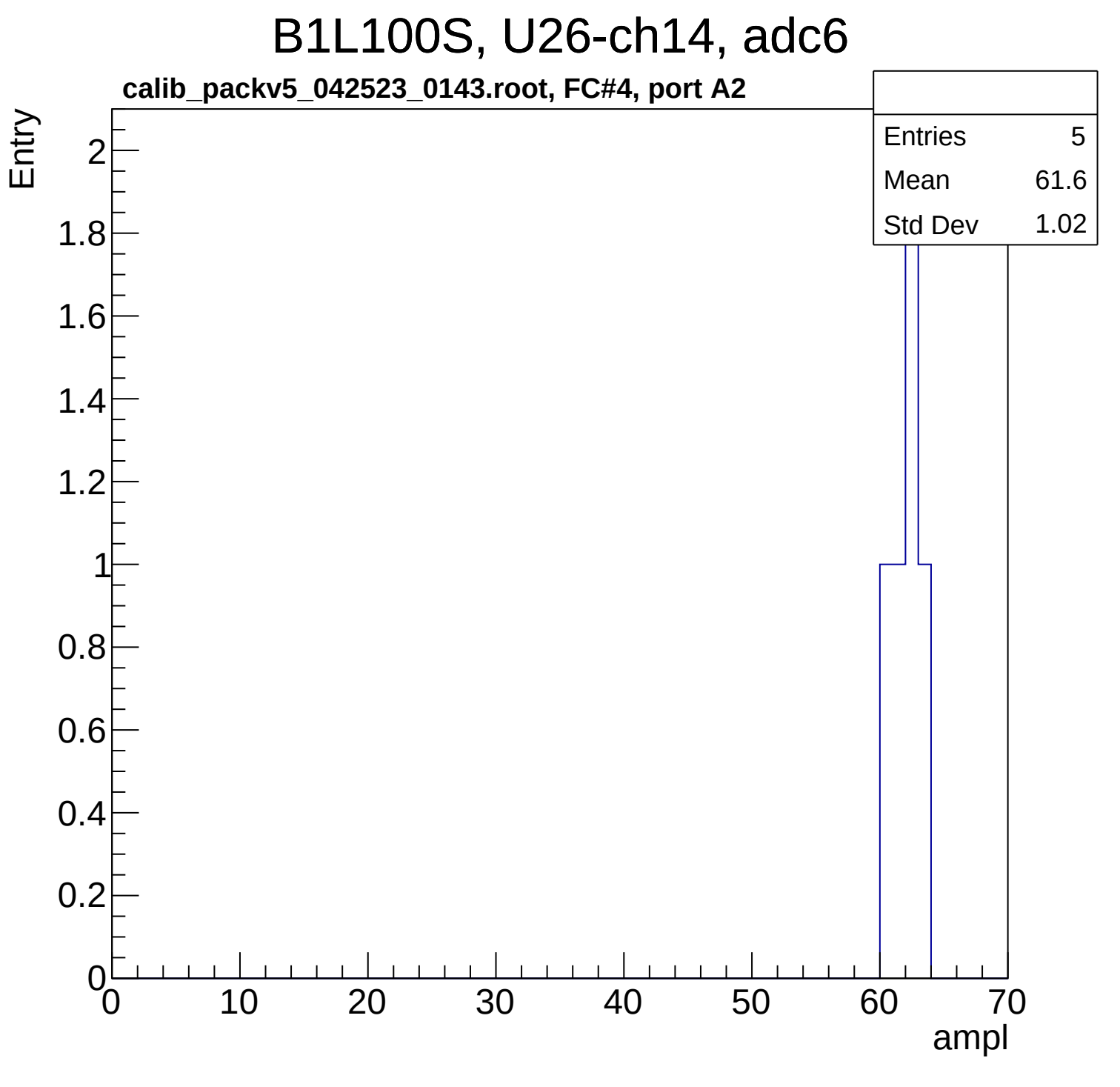
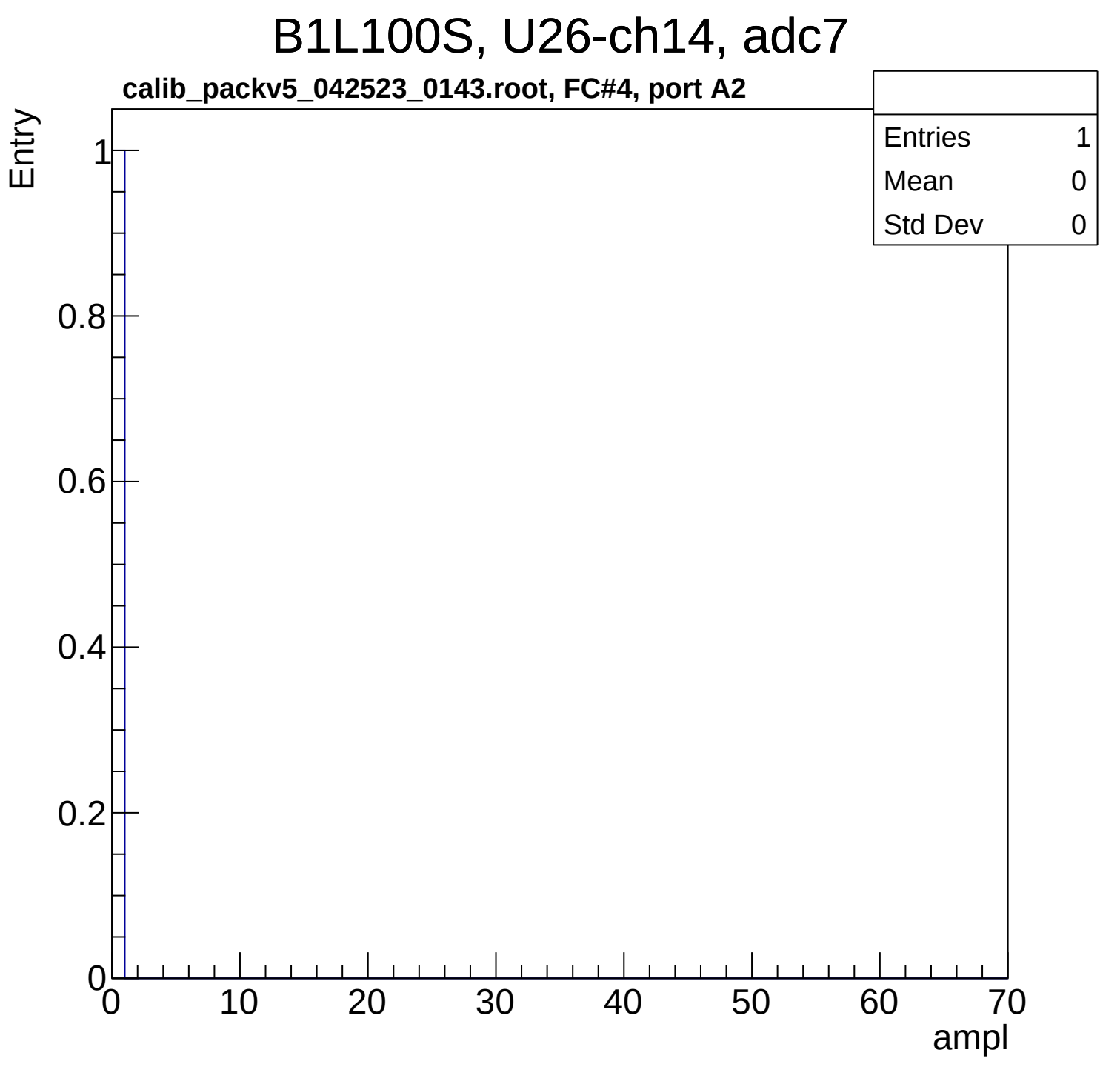
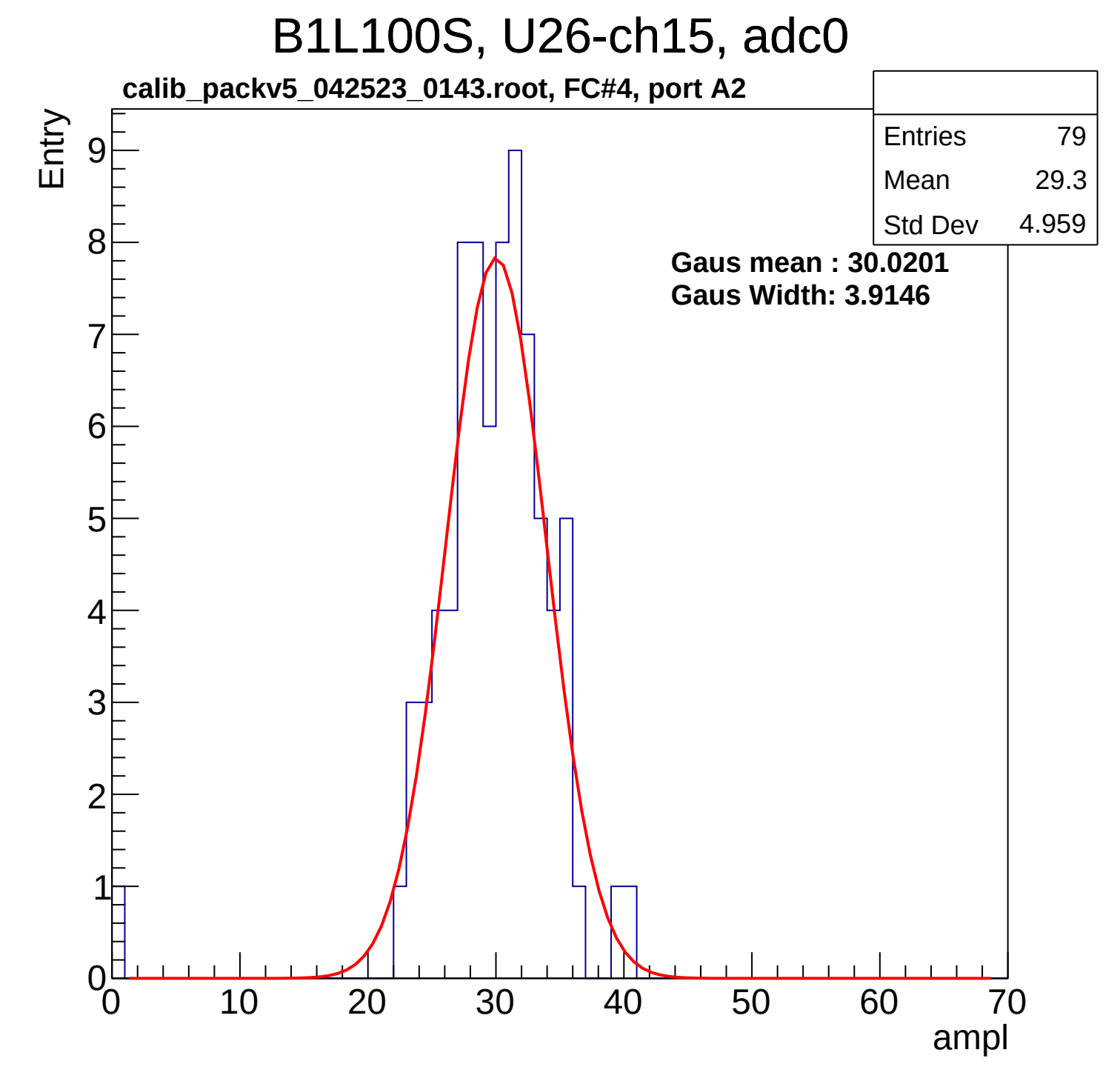

B1L100S, U26-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch14, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.3
Std Dev 4.959
Gaus mean : 30.0201
Gaus Width: 3.9146
Entry
ampl
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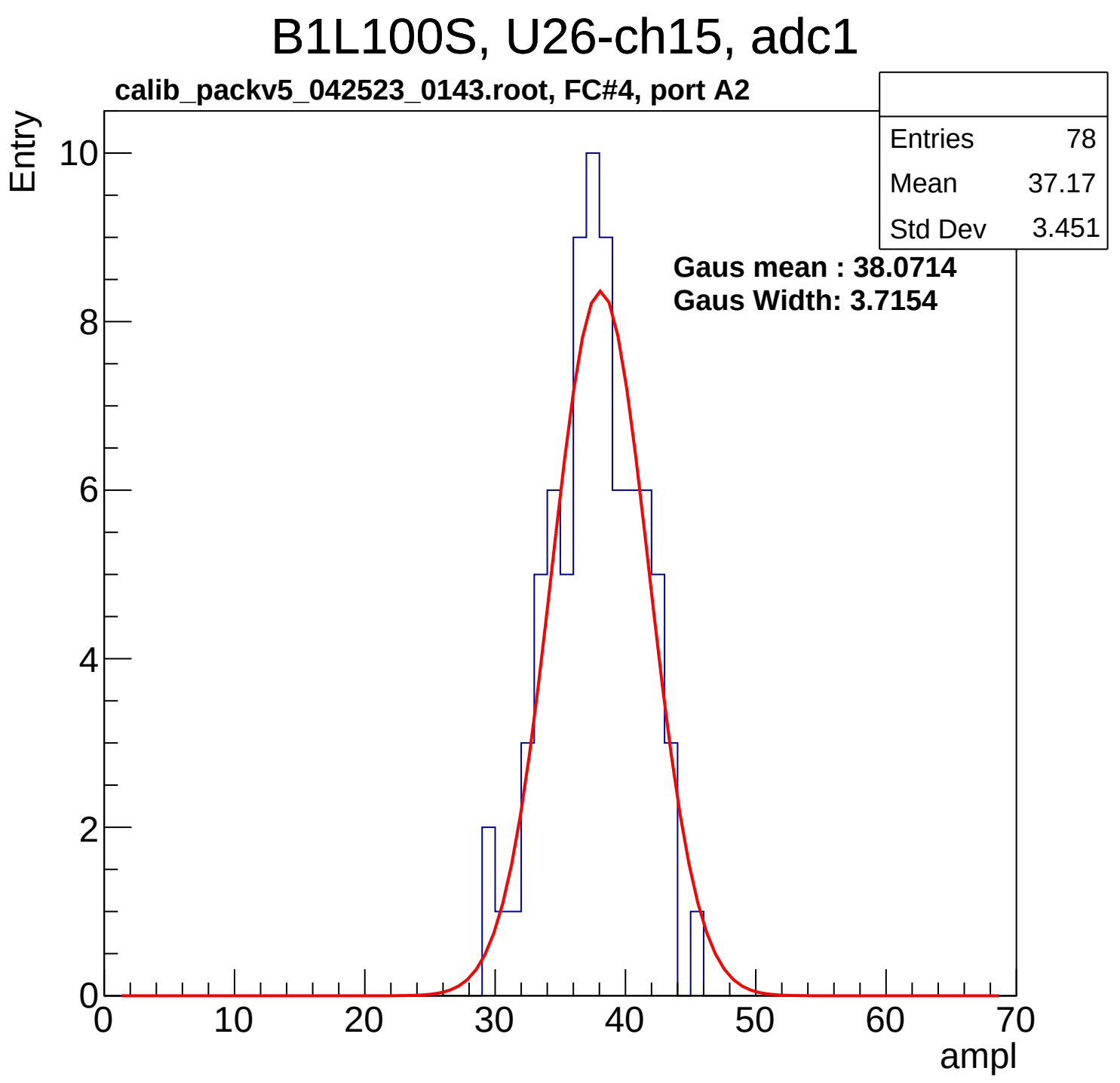
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.17
Std Dev 3.451
Gaus mean : 38.0714
Gaus Width: 3.7154
Entry
ampl
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6
8
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0
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60
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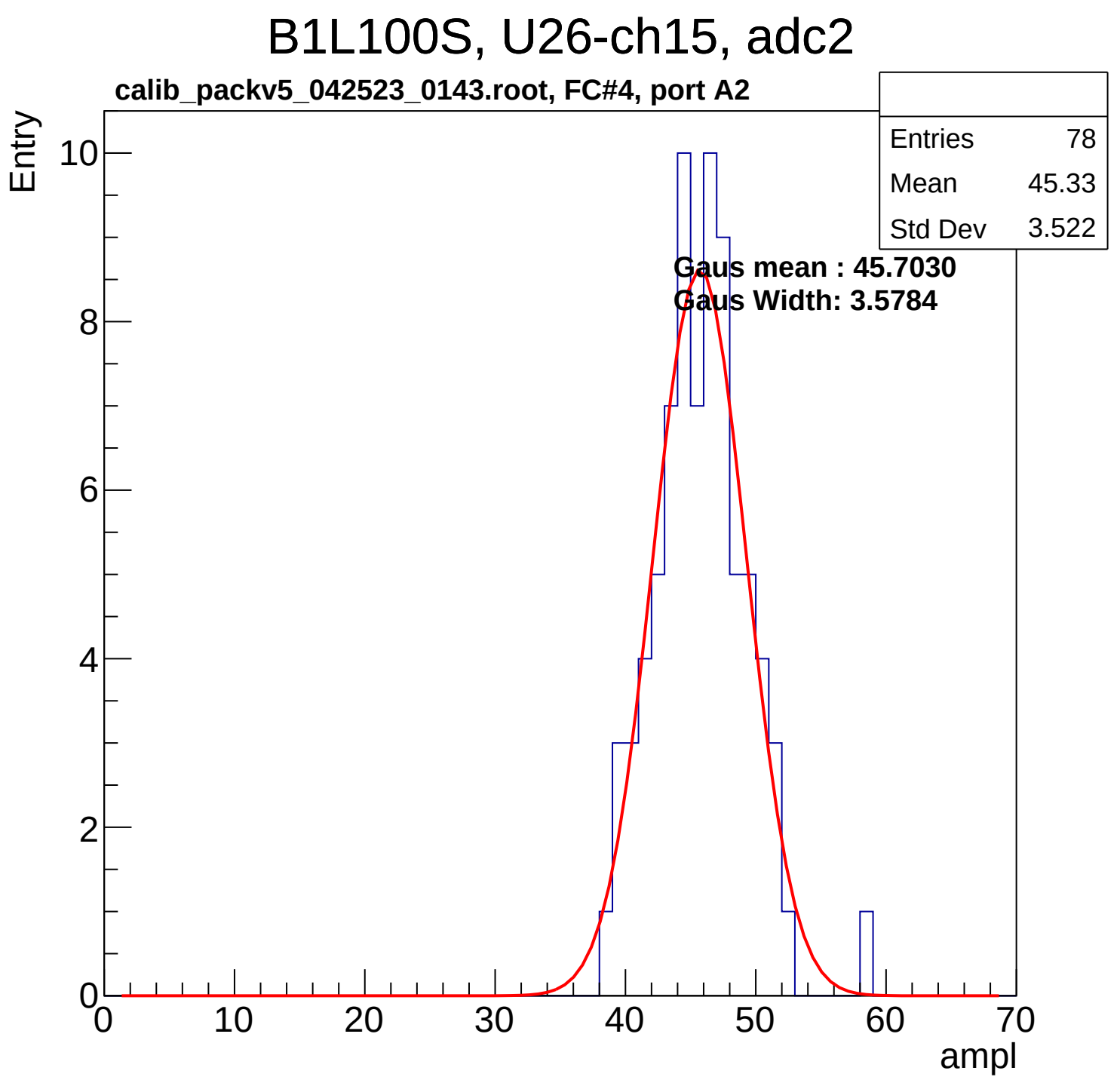
B1L100S, U26-ch15, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 45.33
Std Dev 3.522
Gaus mean : 45.7030
Gaus Width: 3.5784
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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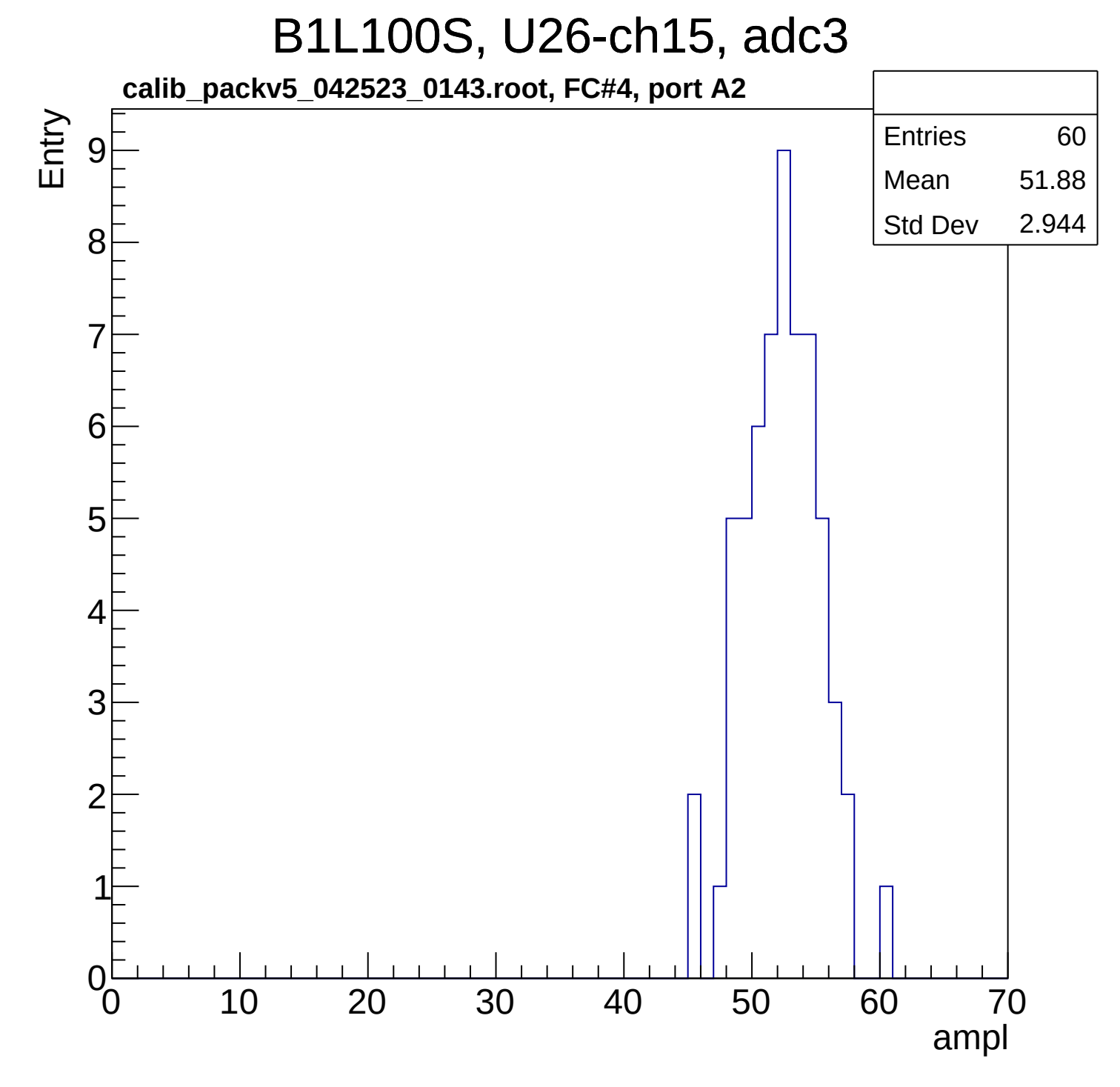
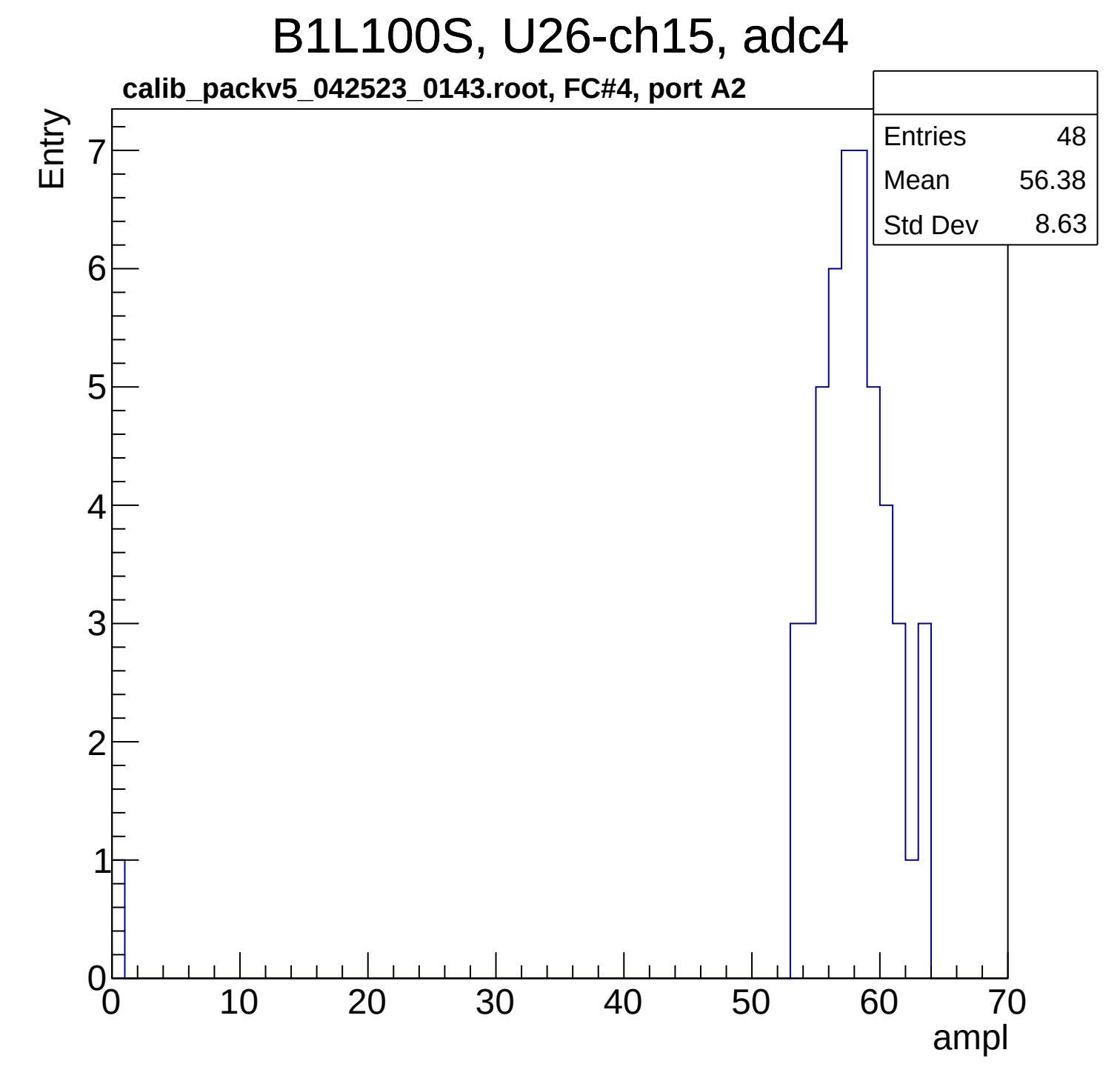

B1L100S, U26-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 51.88
Std Dev 2.944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.38
Std Dev 8.63
Entry
ampl
0
1
2
3
4
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60
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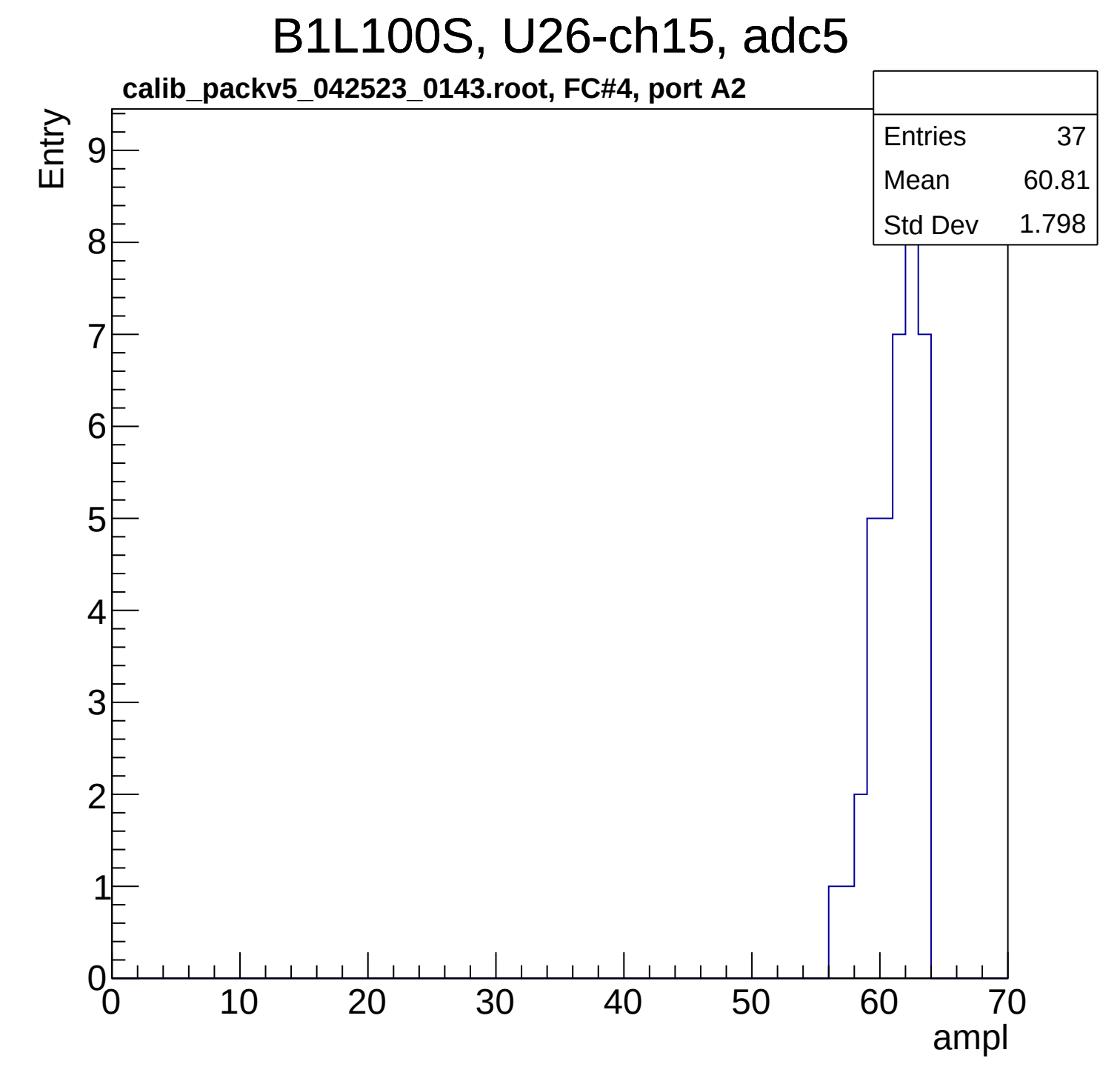
B1L100S, U26-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.81
Std Dev 1.798
Entry
ampl
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3
4
5
6
7
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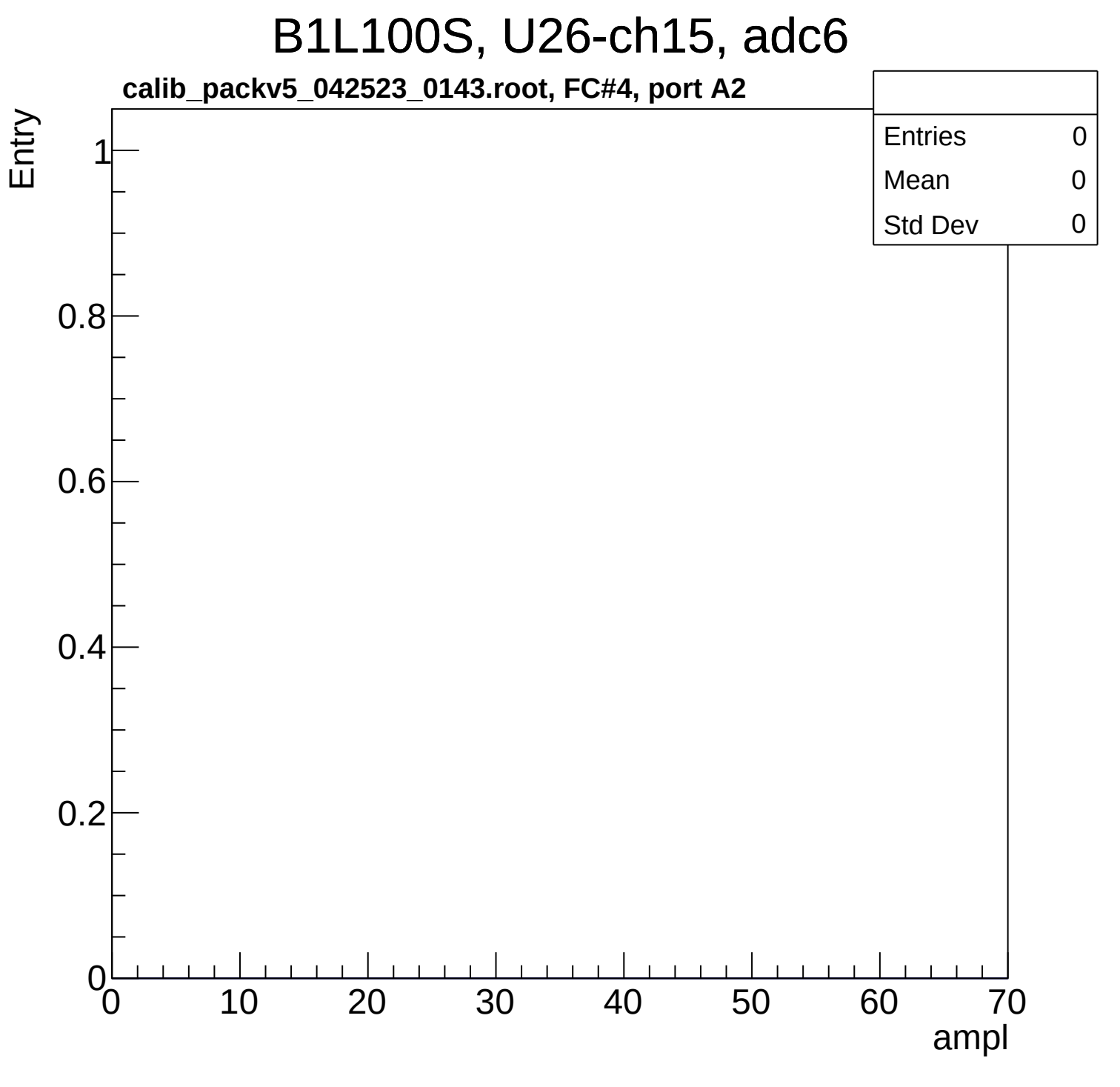
B1L100S, U26-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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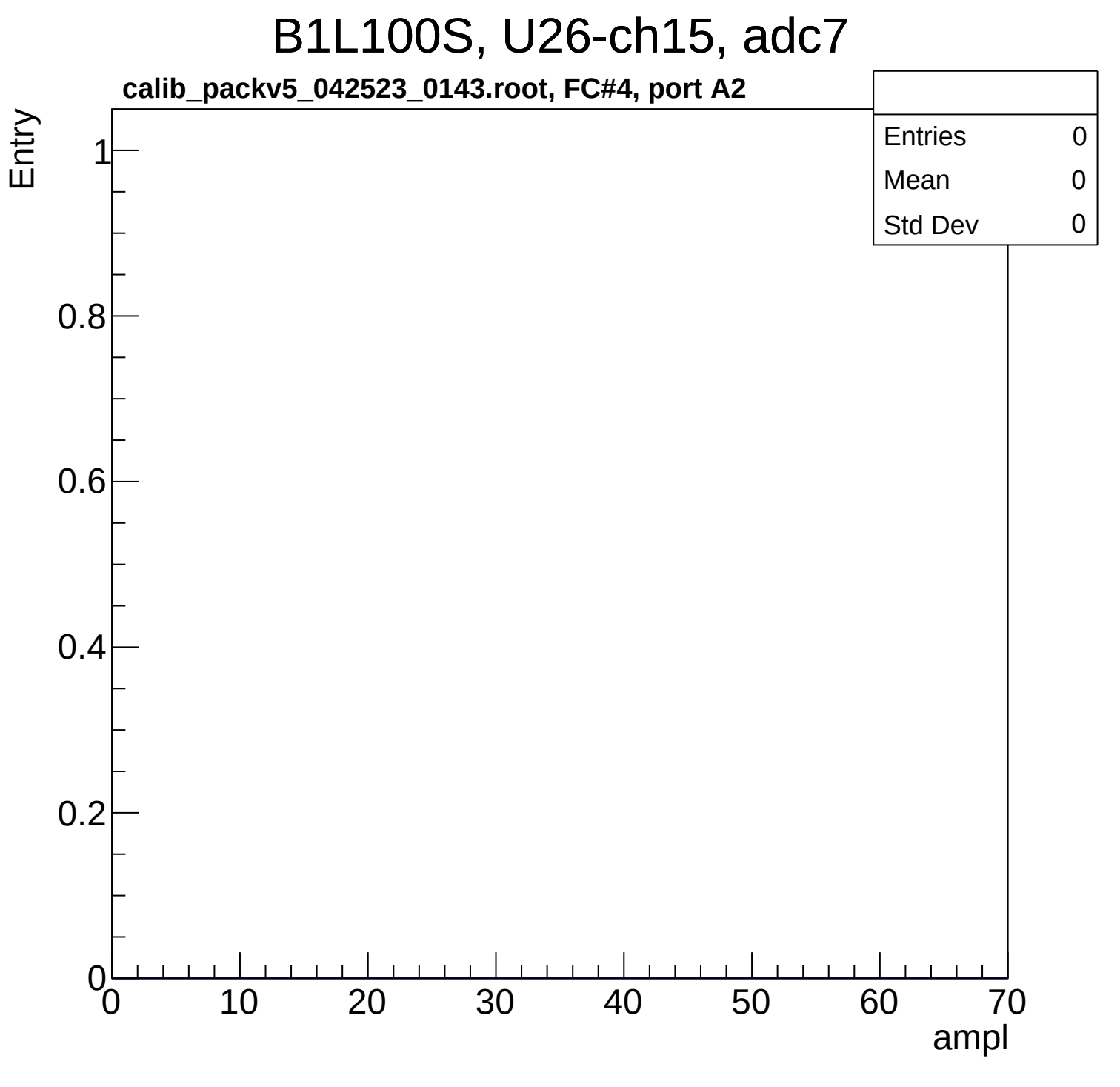
50
60
70
ampl


B1L100S, U26-ch15, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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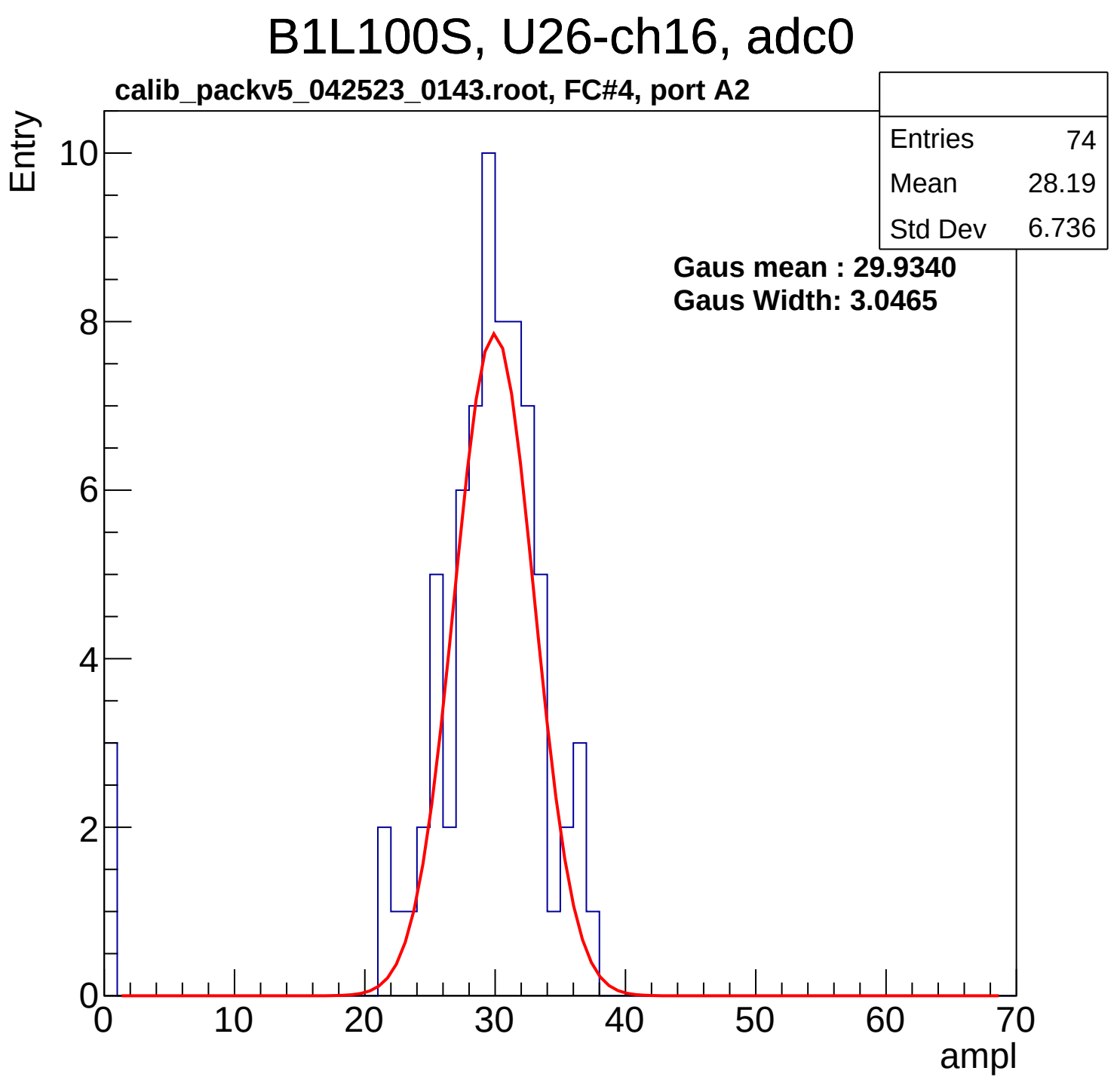
50
60
70
ampl


B1L100S, U26-ch16, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.19
Std Dev 6.736
Gaus mean : 29.9340
Gaus Width: 3.0465
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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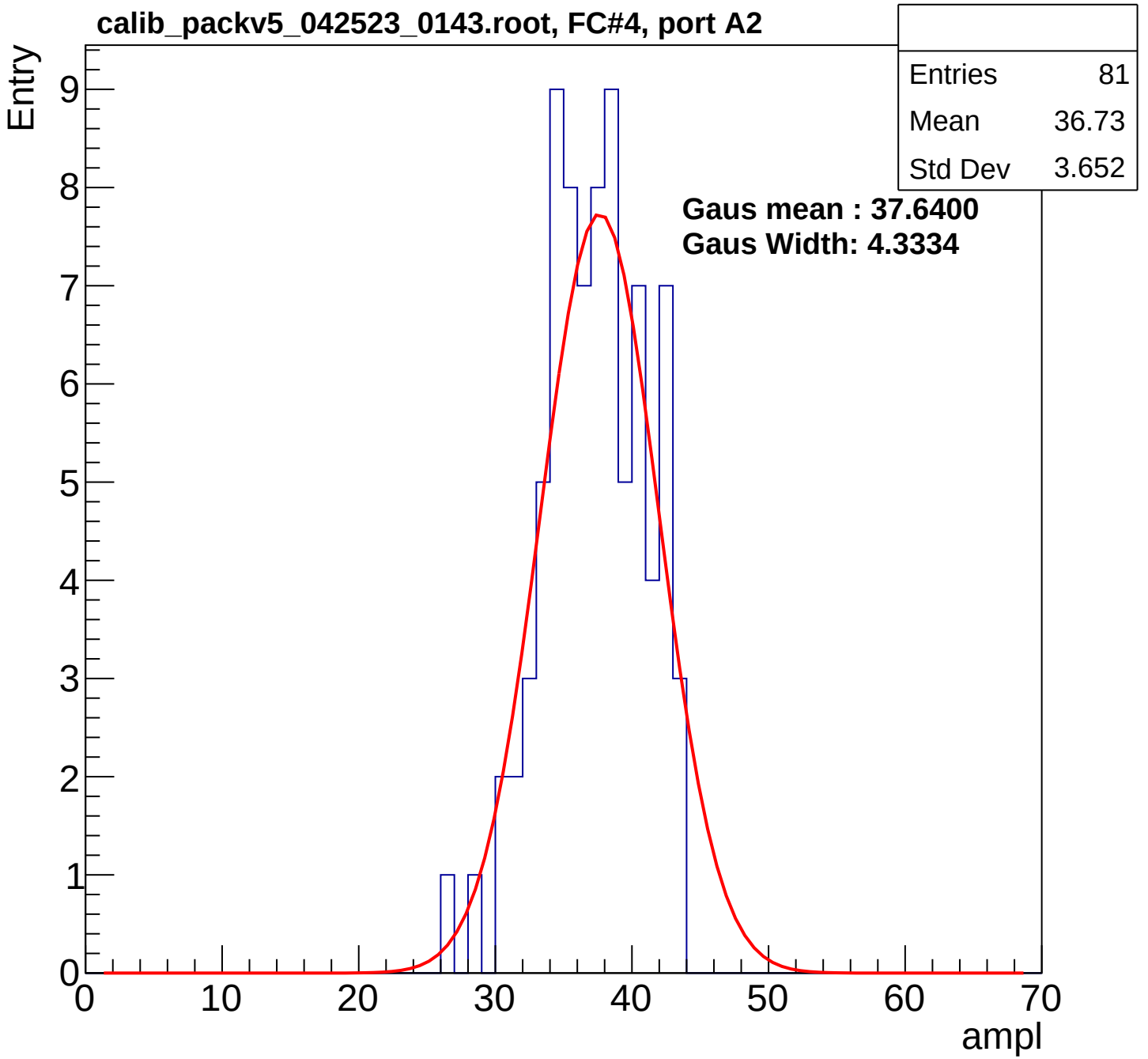
B1L100S, U26-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 36.73
Std Dev 3.652
Gaus mean : 37.6400
Gaus Width: 4.3334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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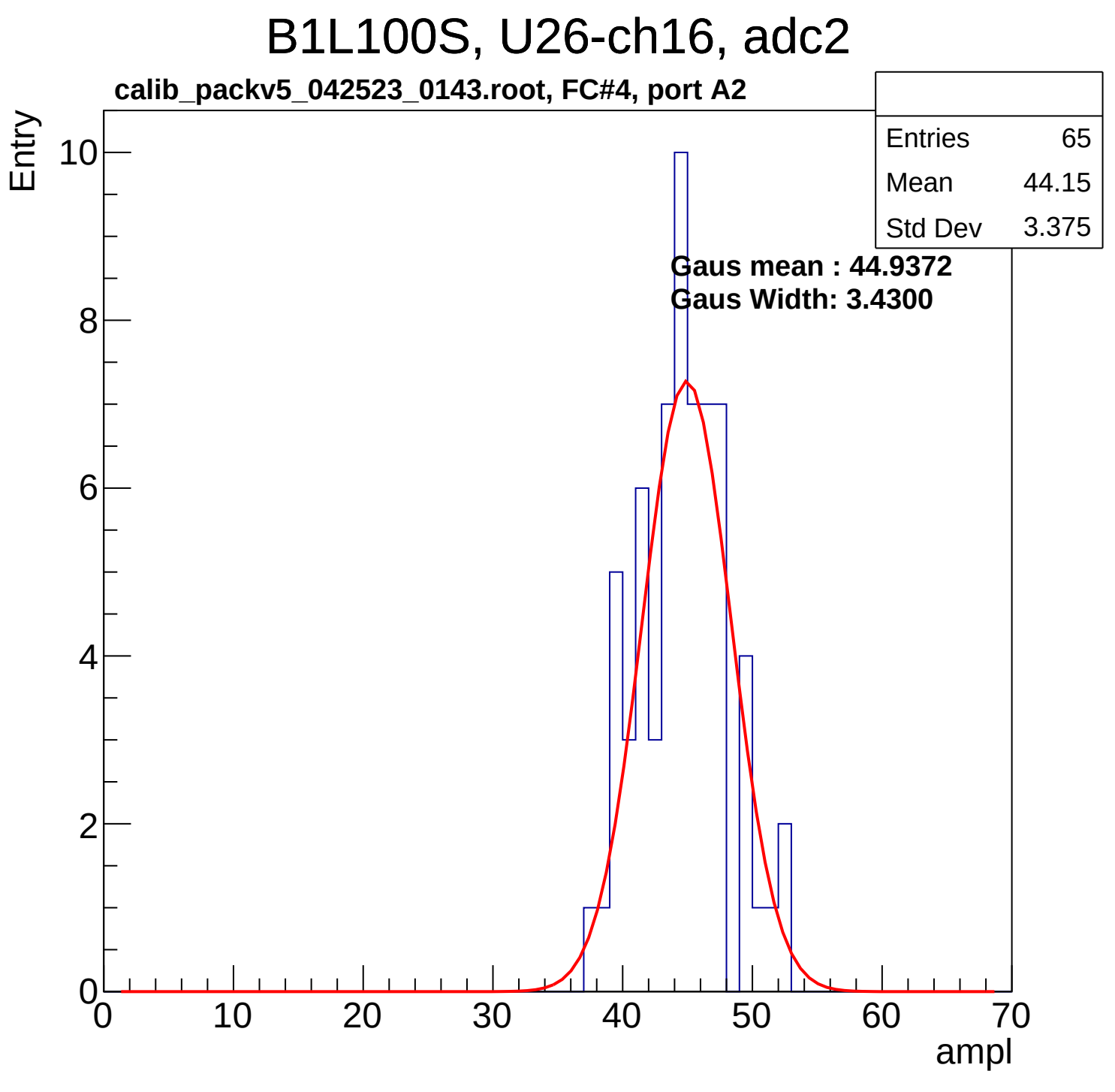
B1L100S, U26-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.15
Std Dev 3.375
Gaus mean : 44.9372
Gaus Width: 3.4300
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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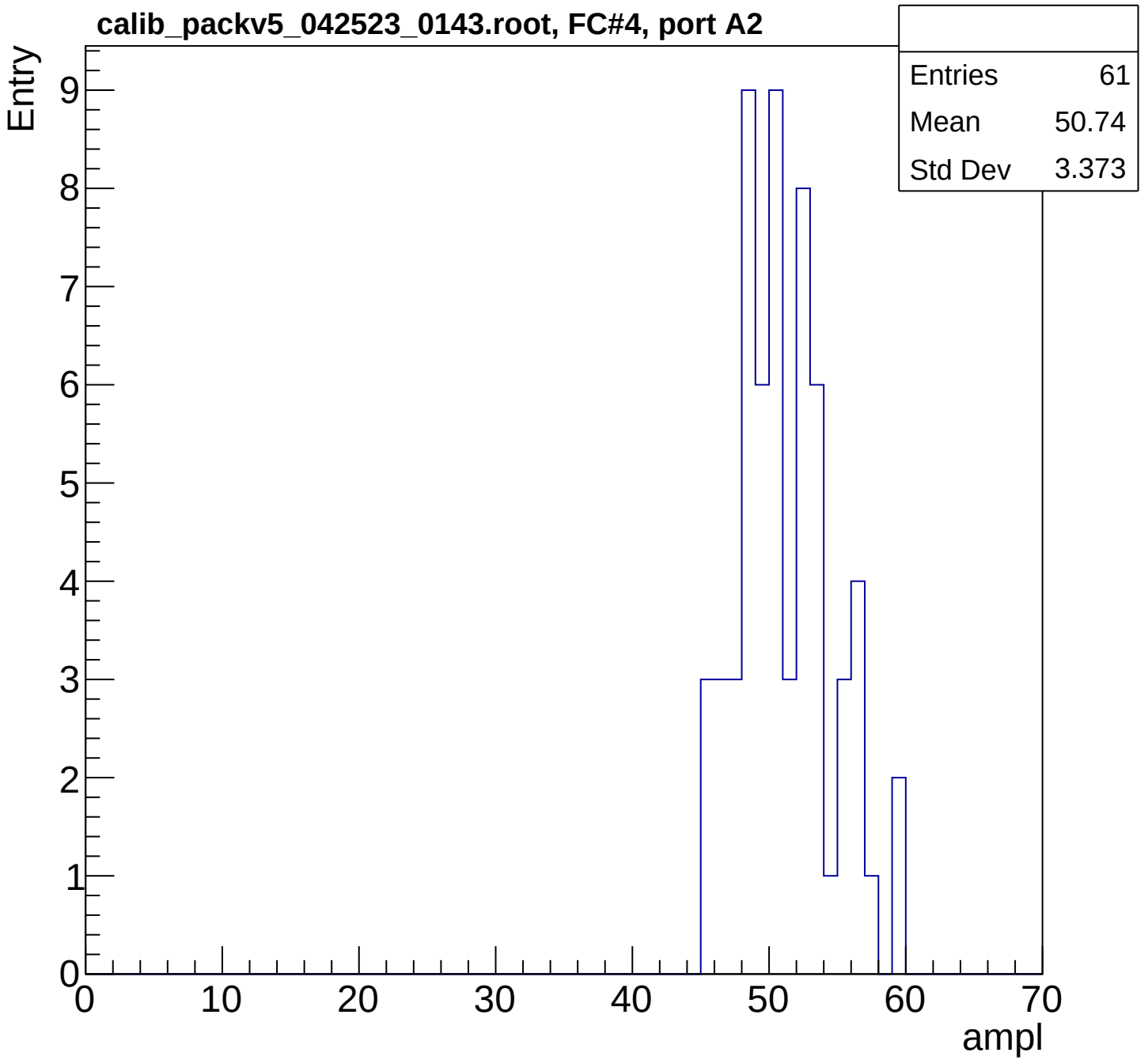
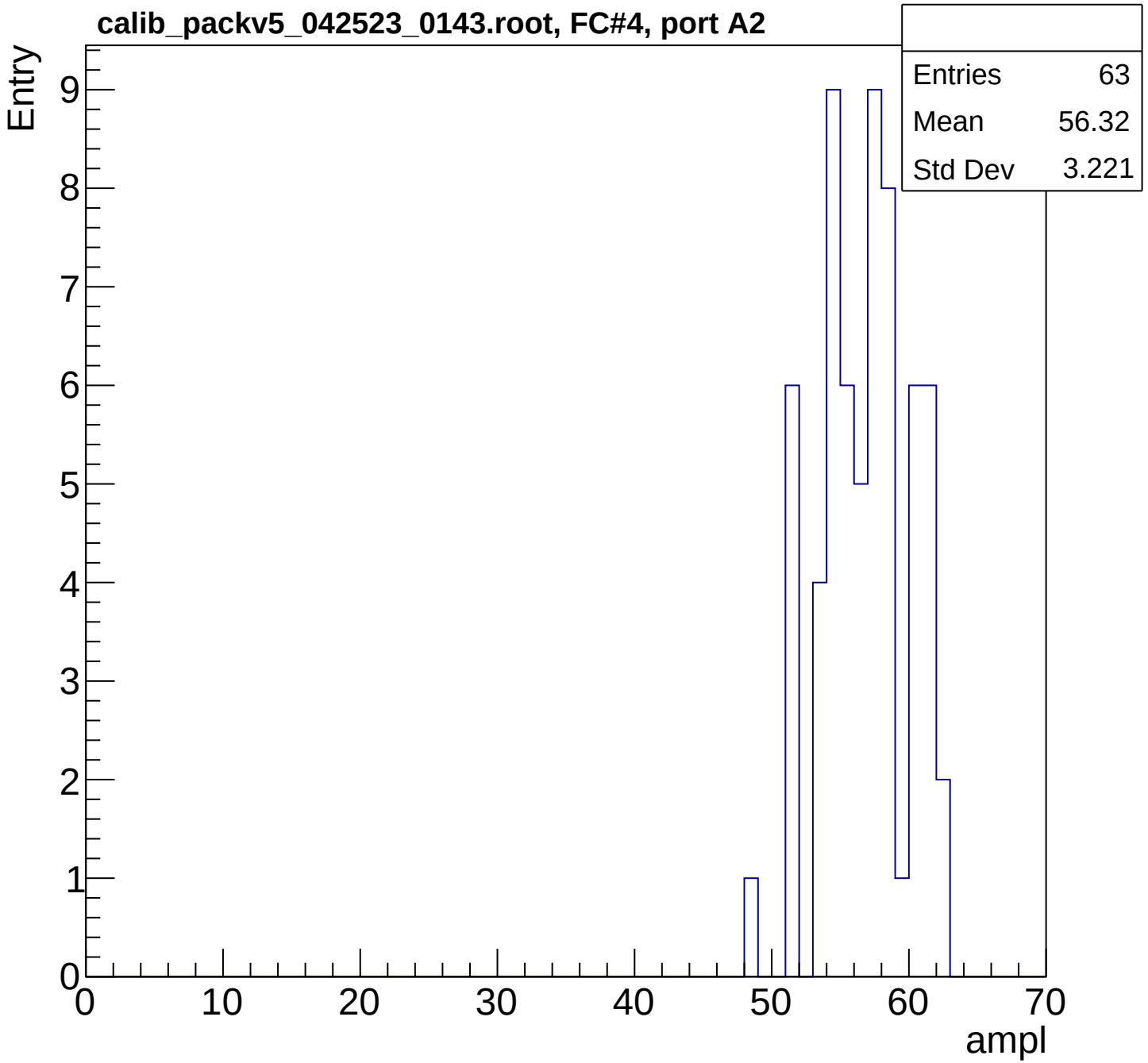

B1L100S, U26-ch16, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.74
Std Dev 3.373
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch16, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.32
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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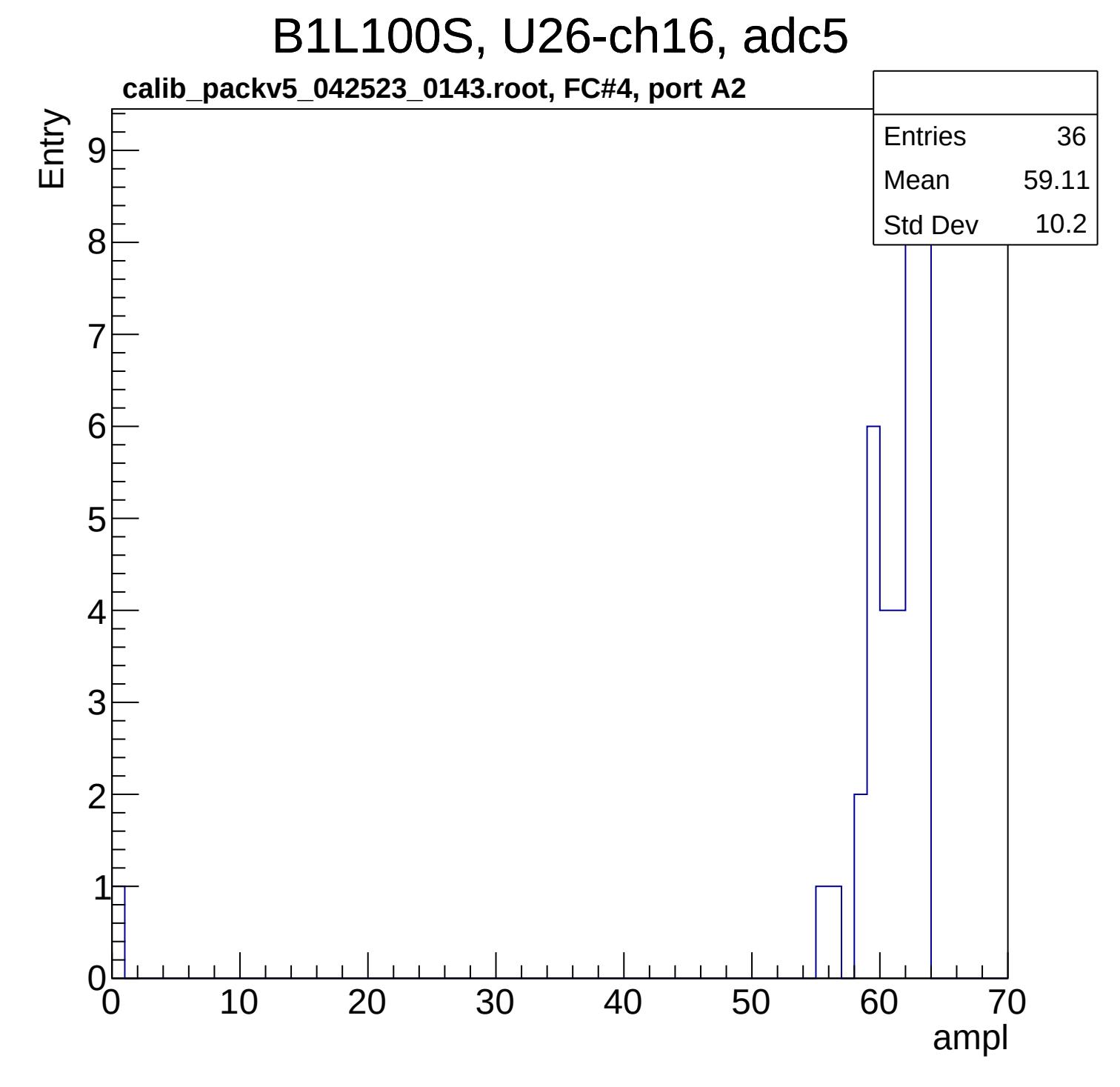
B1L100S, U26-ch16, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.11
Std Dev 10.2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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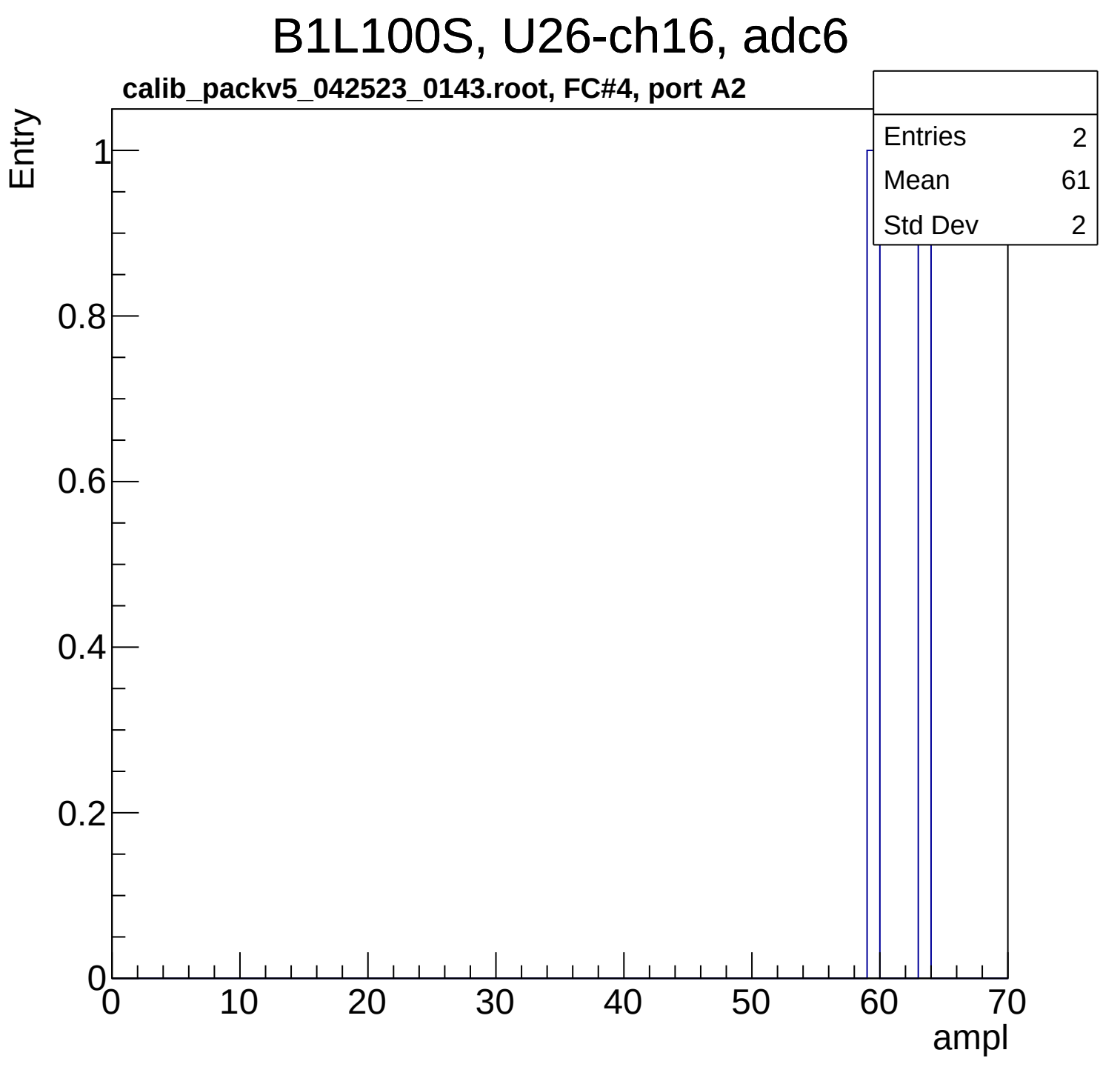
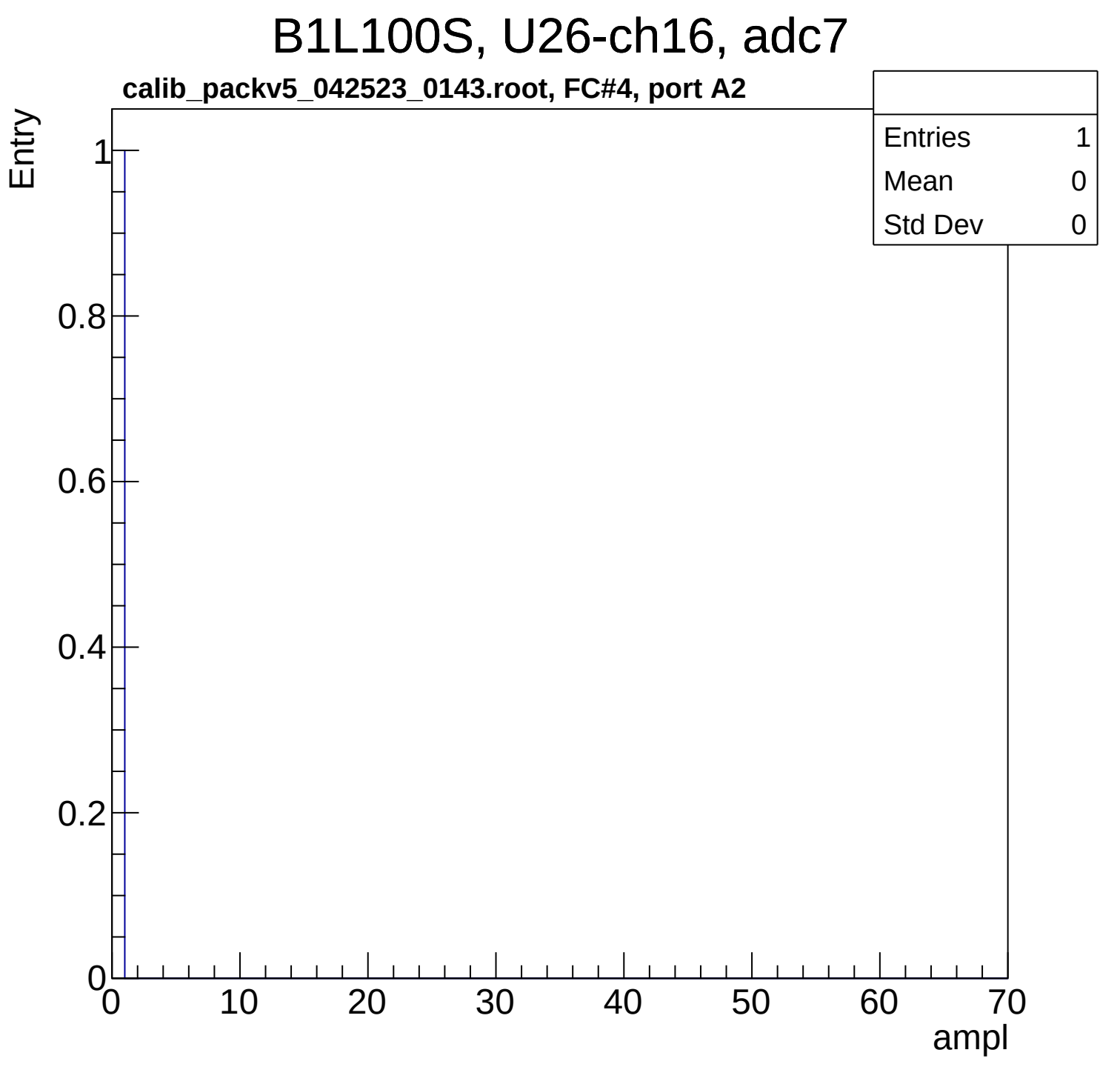
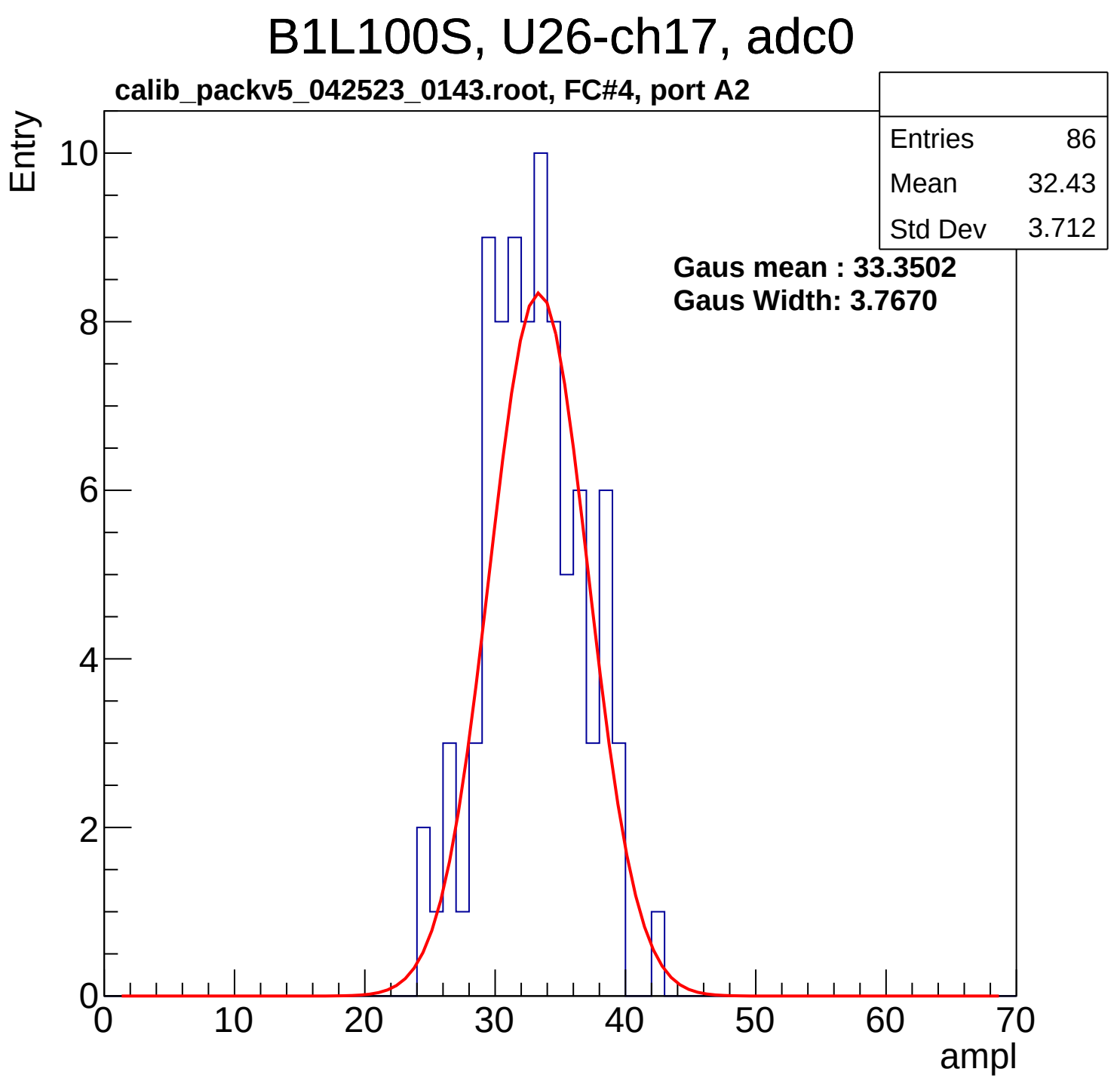
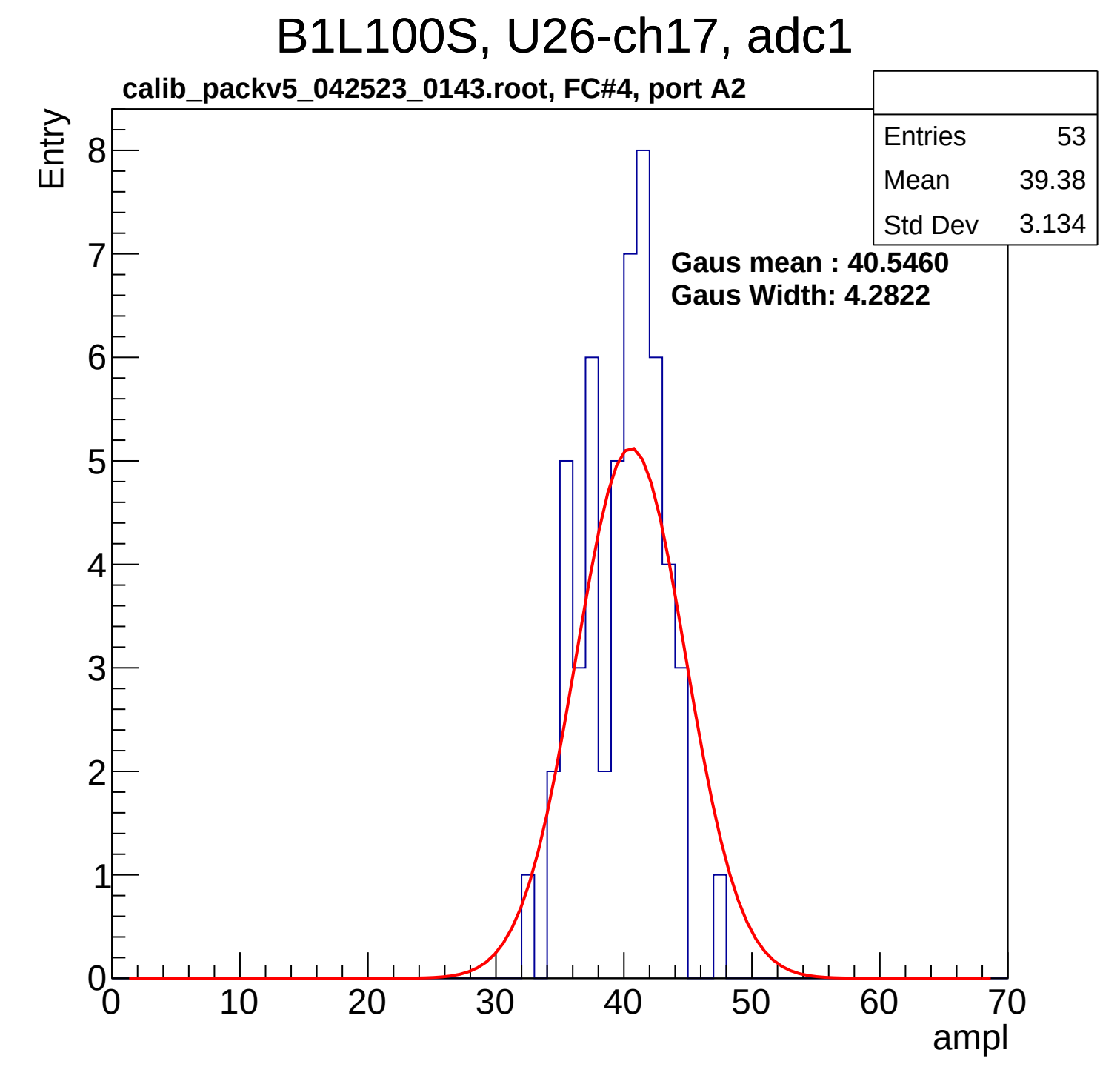

B1L100S, U26-ch16, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 32.43
Std Dev 3.712
Gaus mean : 33.3502
Gaus Width: 3.7670
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 39.38
Std Dev 3.134
Gaus mean : 40.5460
Gaus Width: 4.2822
Entry
ampl
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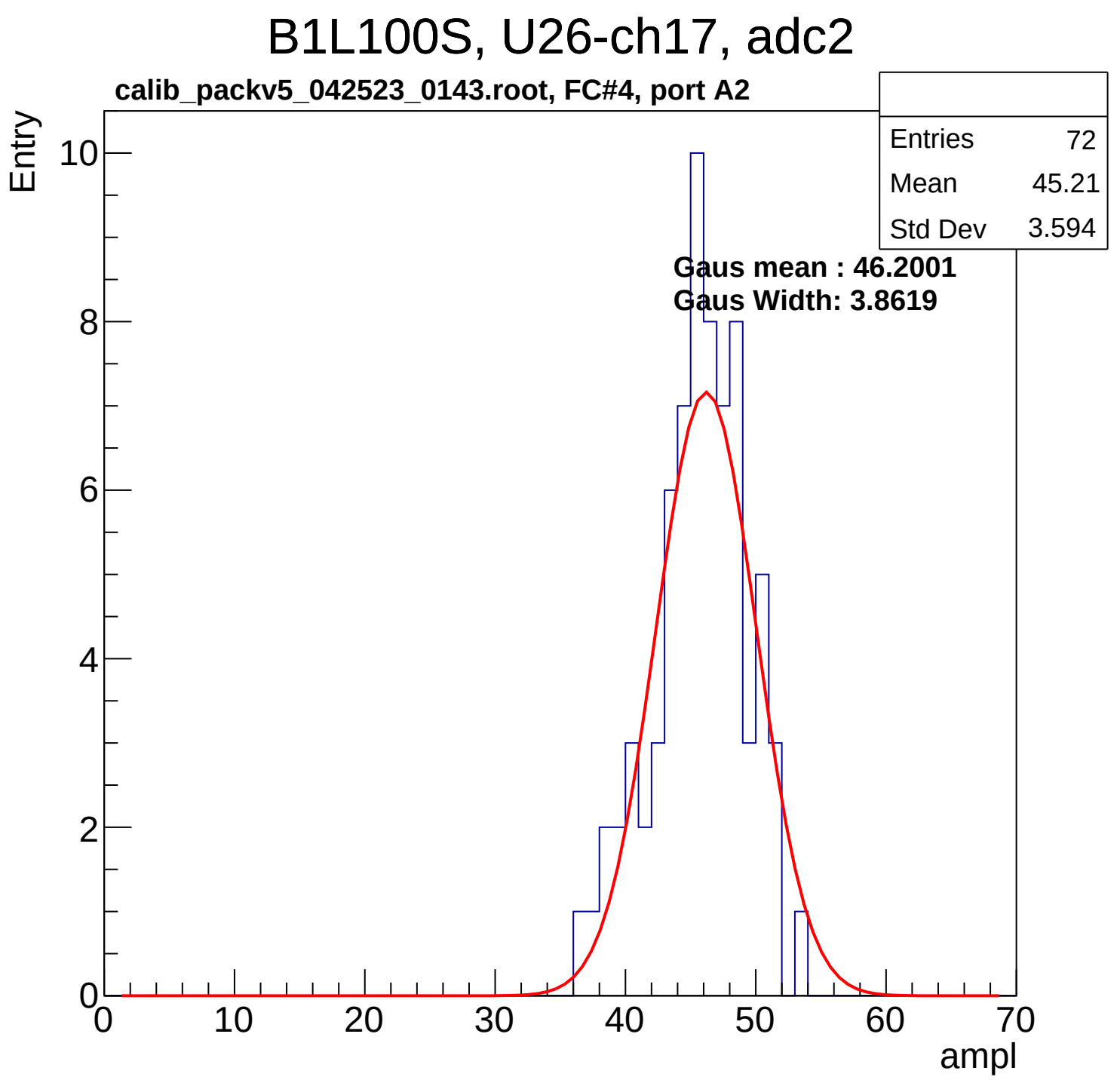
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 45.21
Std Dev 3.594
Gaus mean : 46.2001
Gaus Width: 3.8619
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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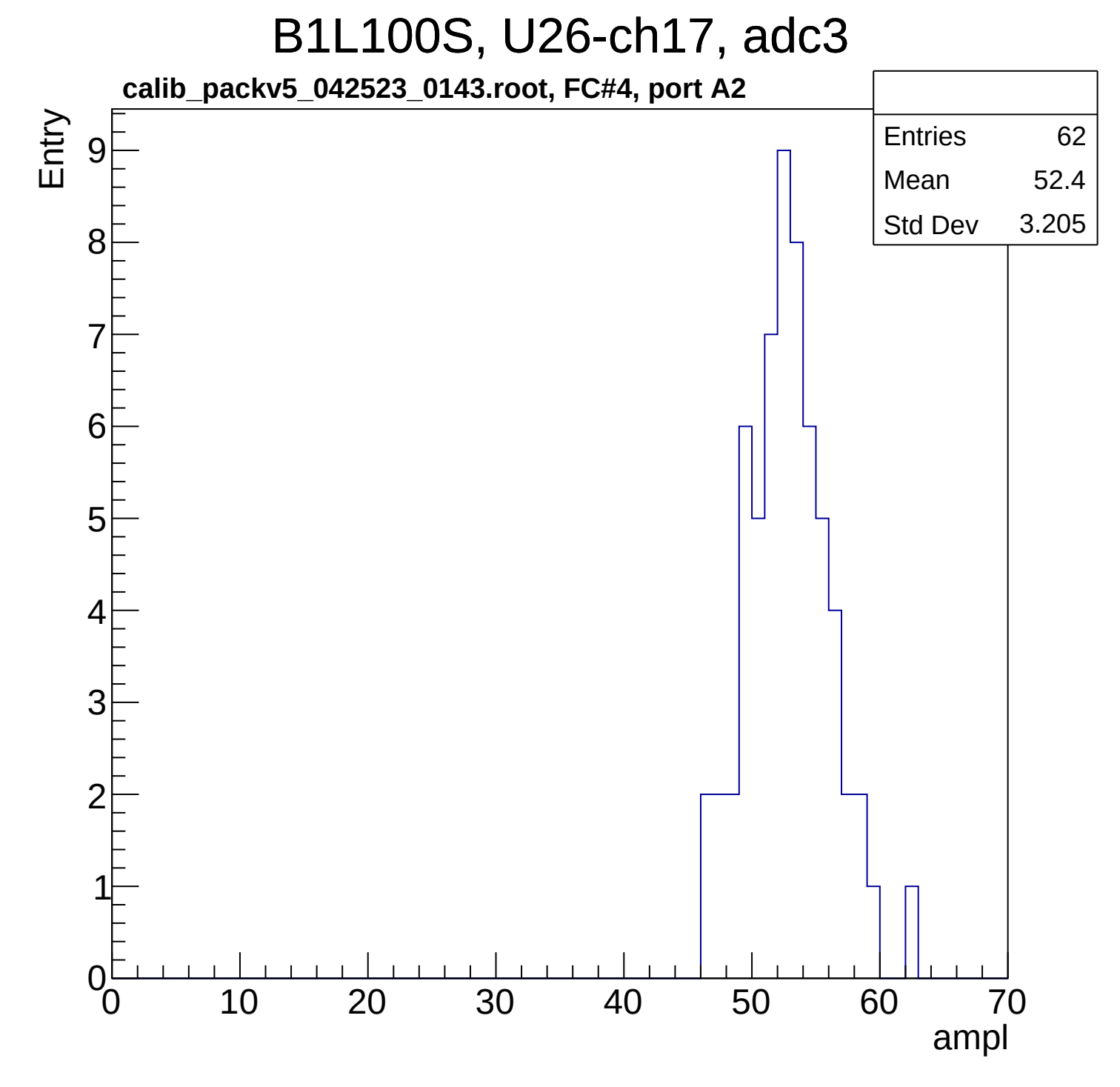
B1L100S, U26-ch17, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 52.4
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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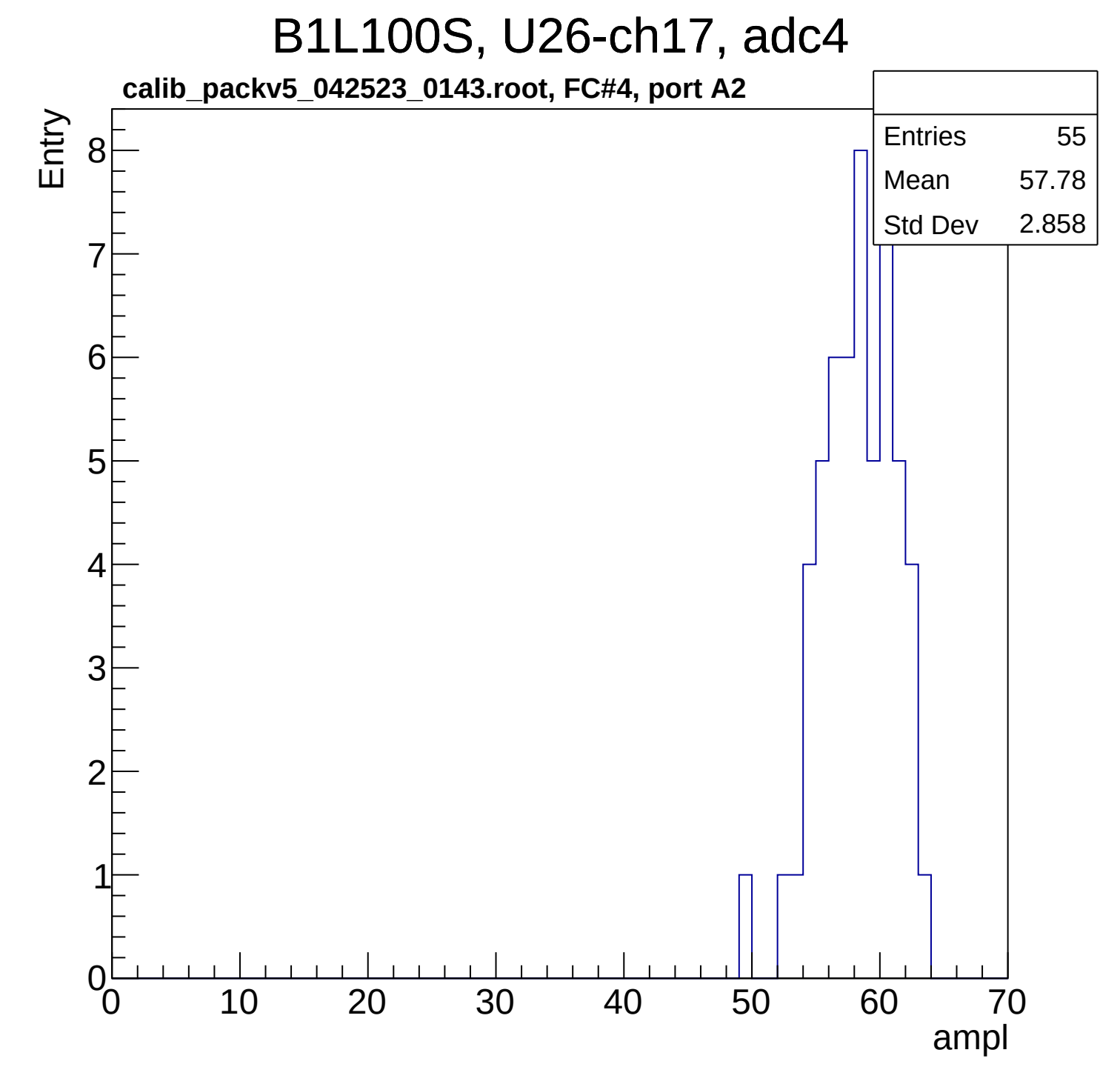
B1L100S, U26-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 57.78
Std Dev 2.858
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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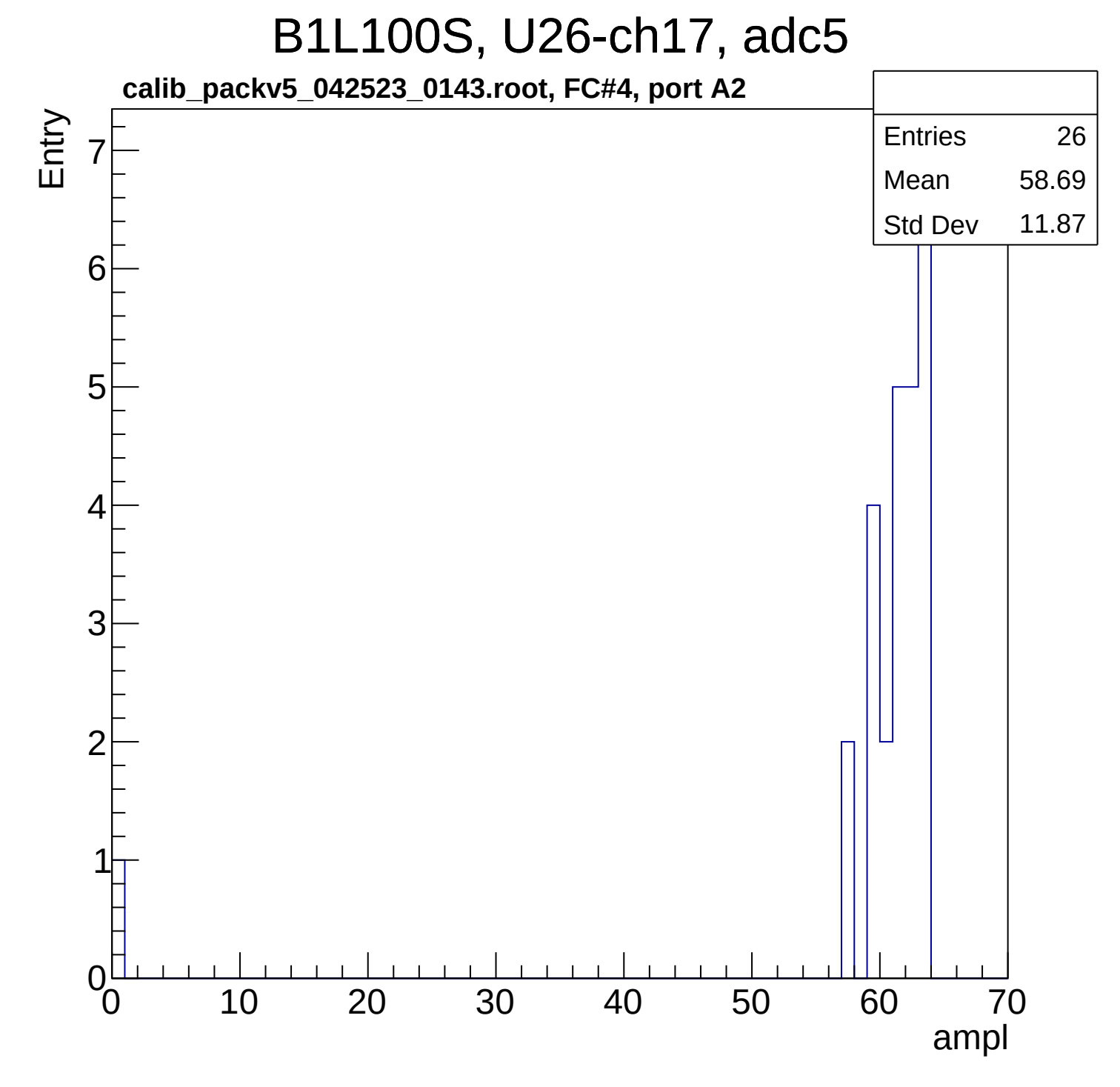
B1L100S, U26-ch17, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.69
Std Dev 11.87
Entry
ampl
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1
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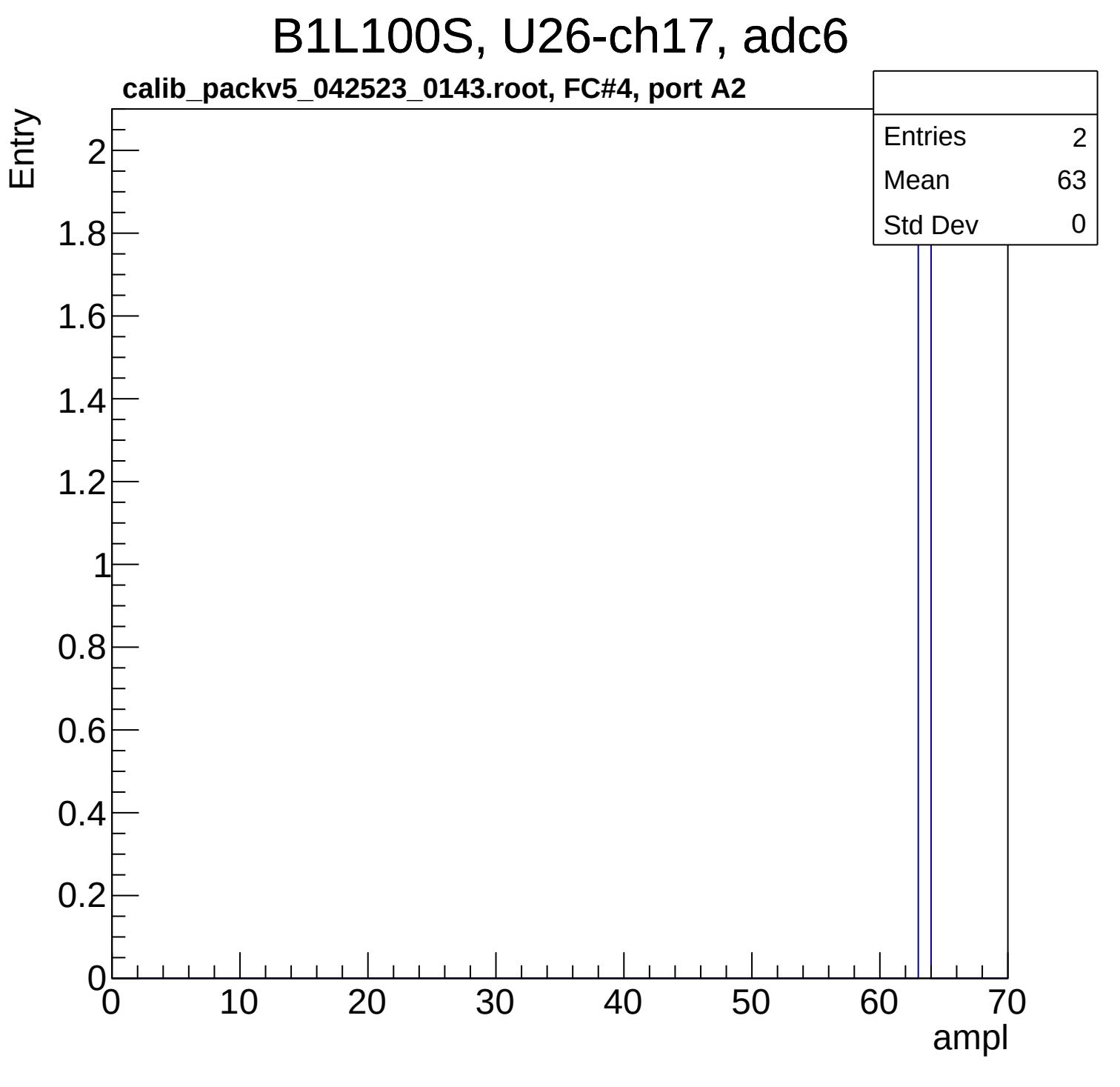
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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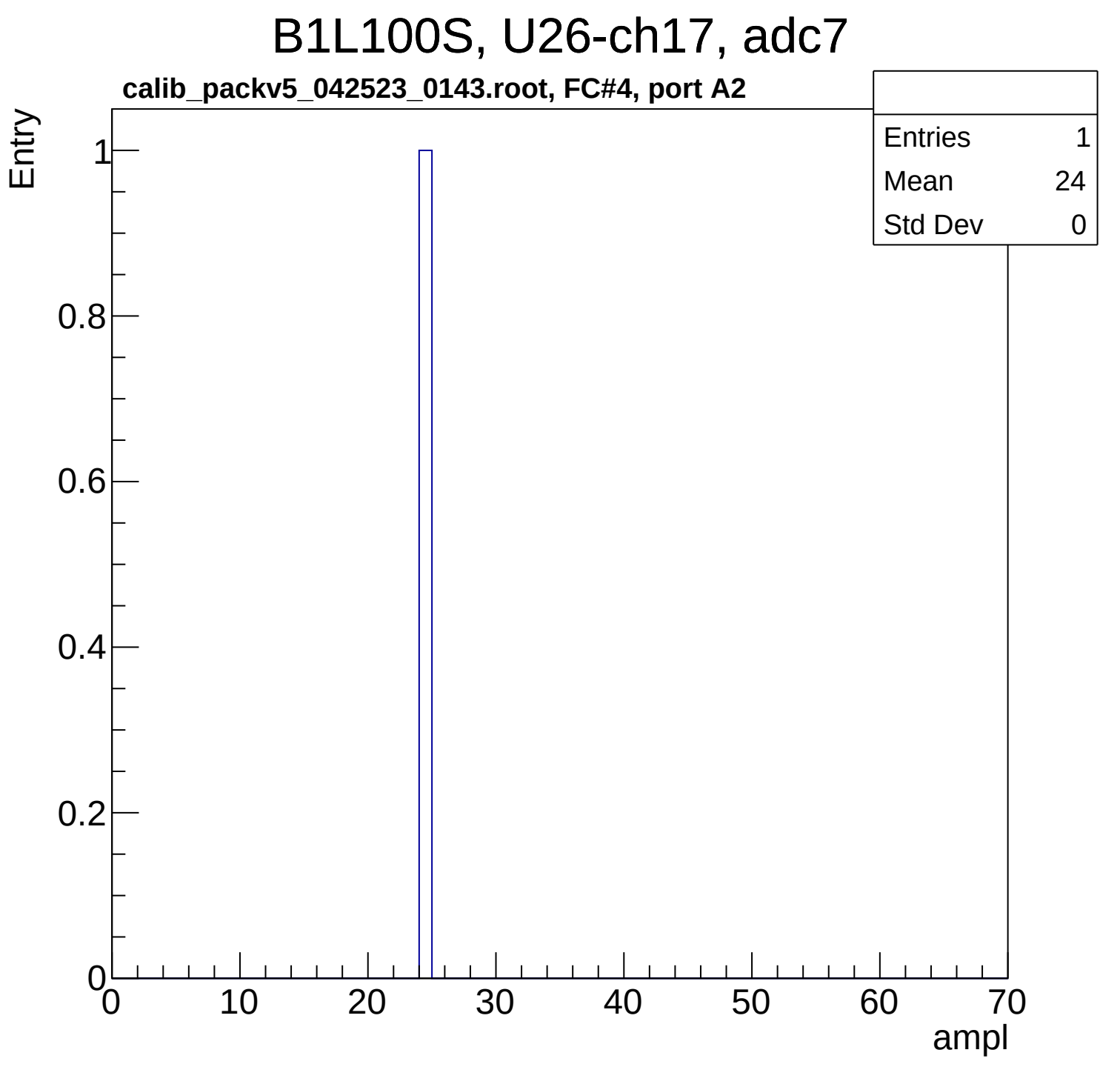
B1L100S, U26-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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10
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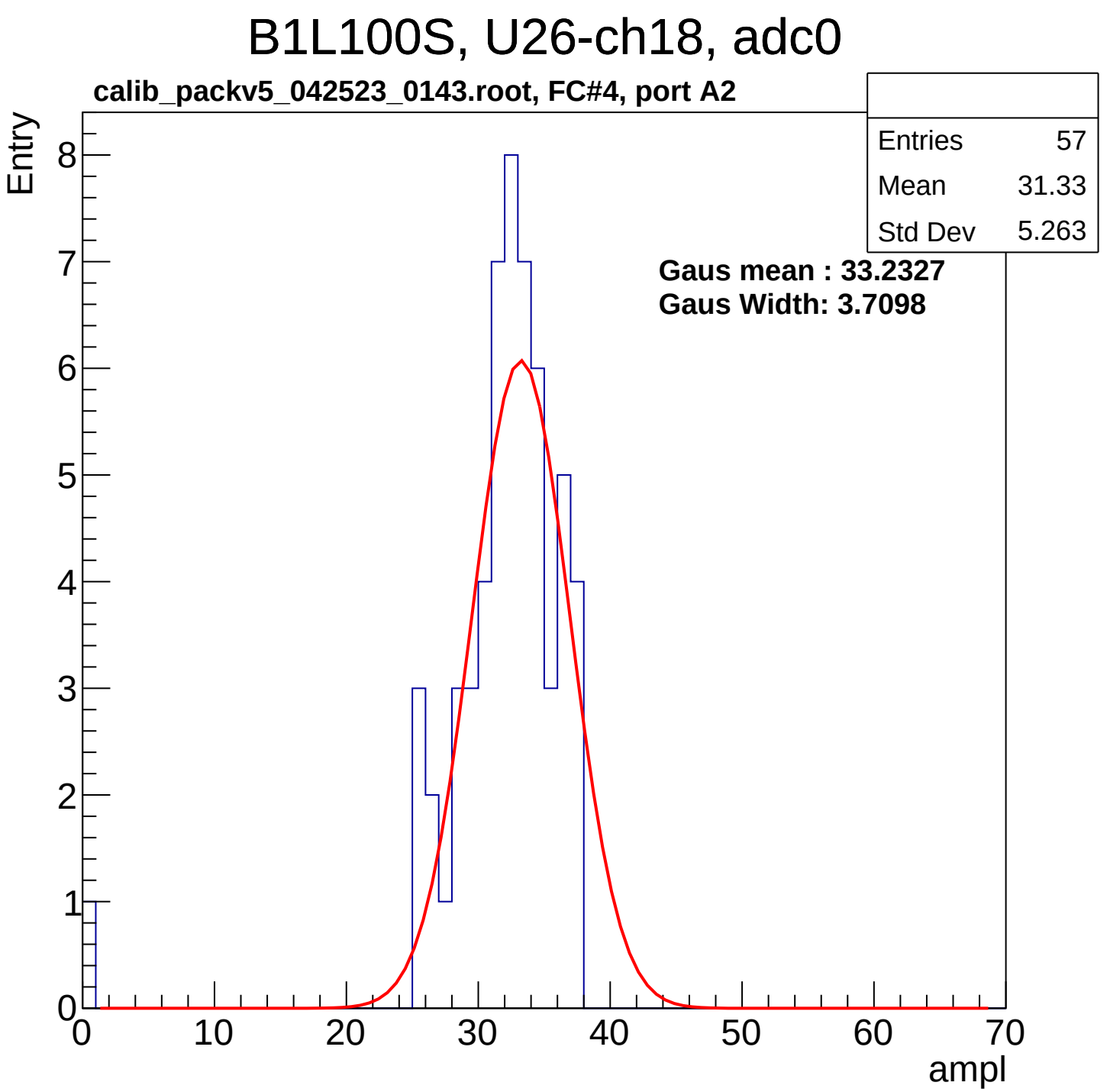
50
60
70
ampl


B1L100S, U26-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 31.33
Std Dev 5.263
Gaus mean : 33.2327
Gaus Width: 3.7098
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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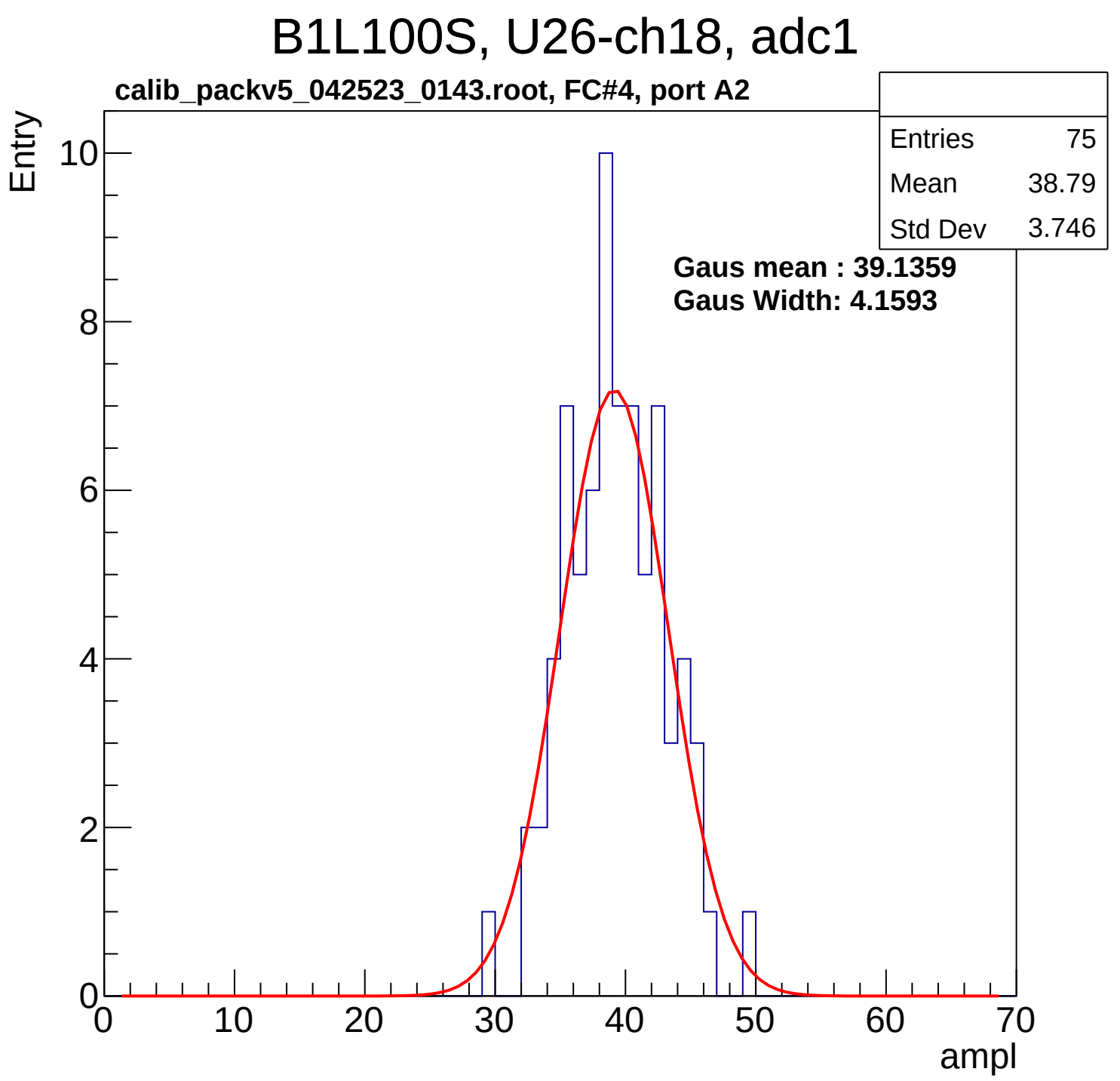
B1L100S, U26-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 38.79
Std Dev 3.746
Gaus mean : 39.1359
Gaus Width: 4.1593
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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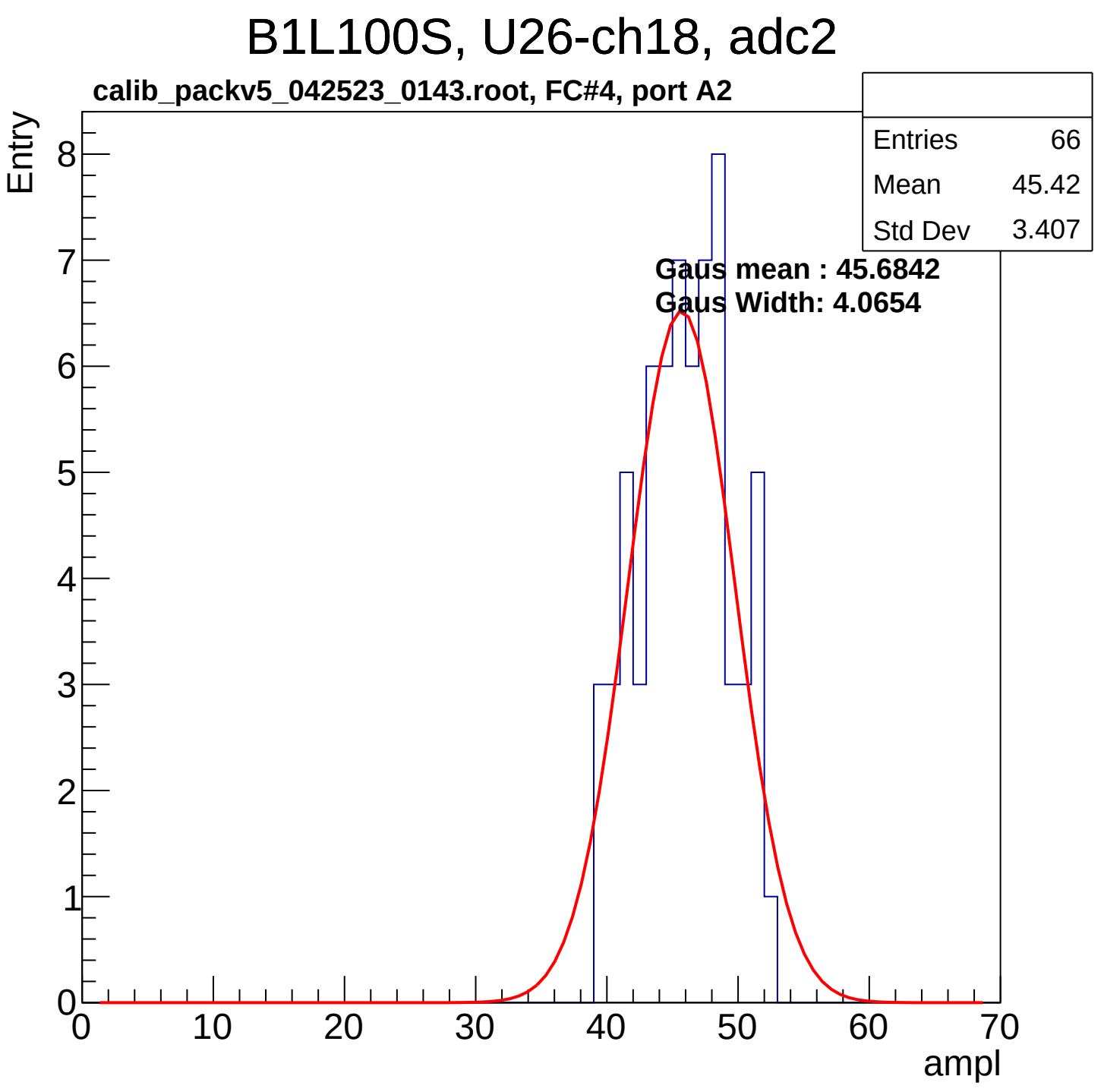
60
70


B1L100S, U26-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 45.42
Std Dev 3.407
Gaus mean : 45.6842
Gaus Width: 4.0654
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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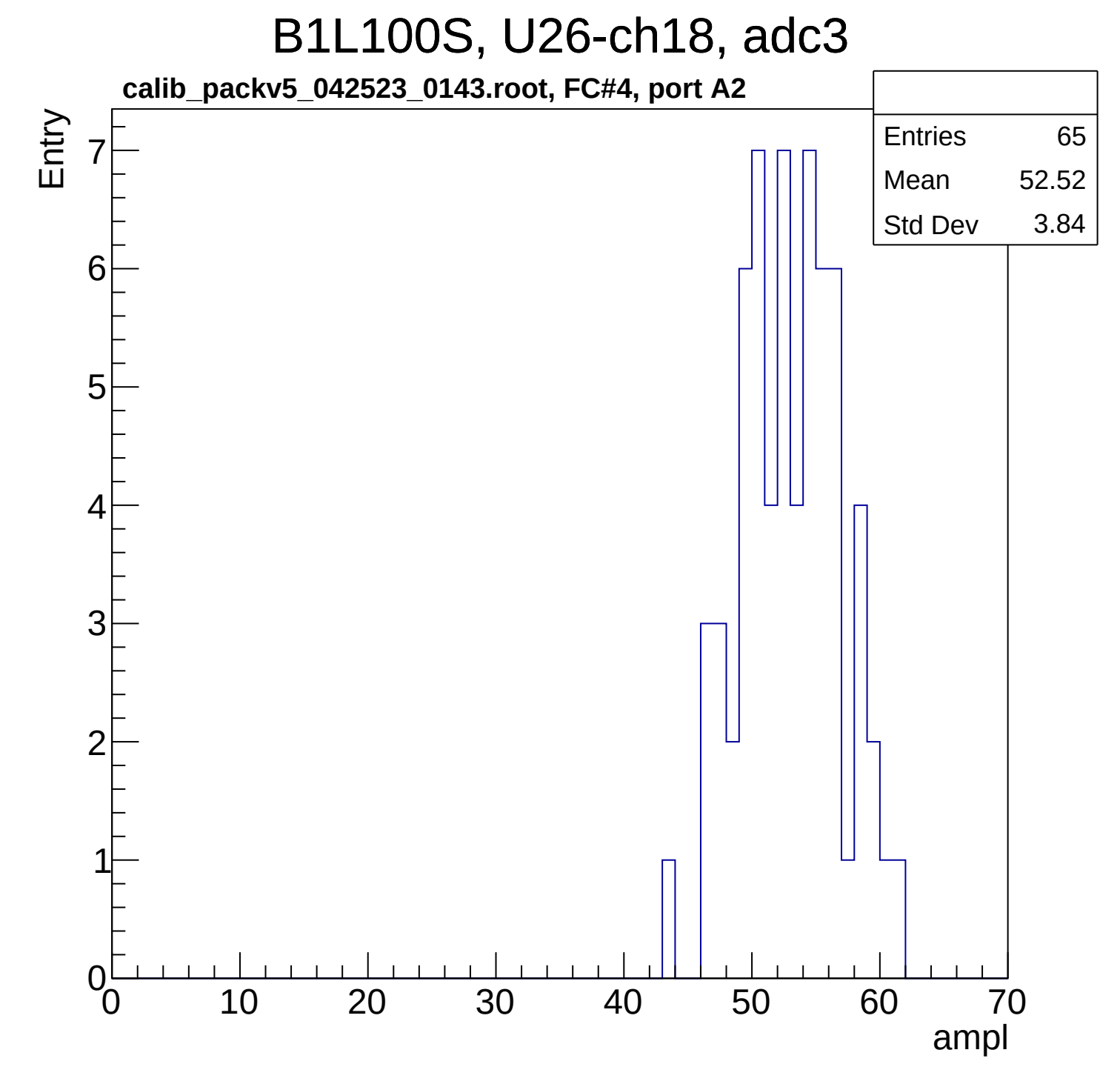
B1L100S, U26-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 52.52
Std Dev 3.84
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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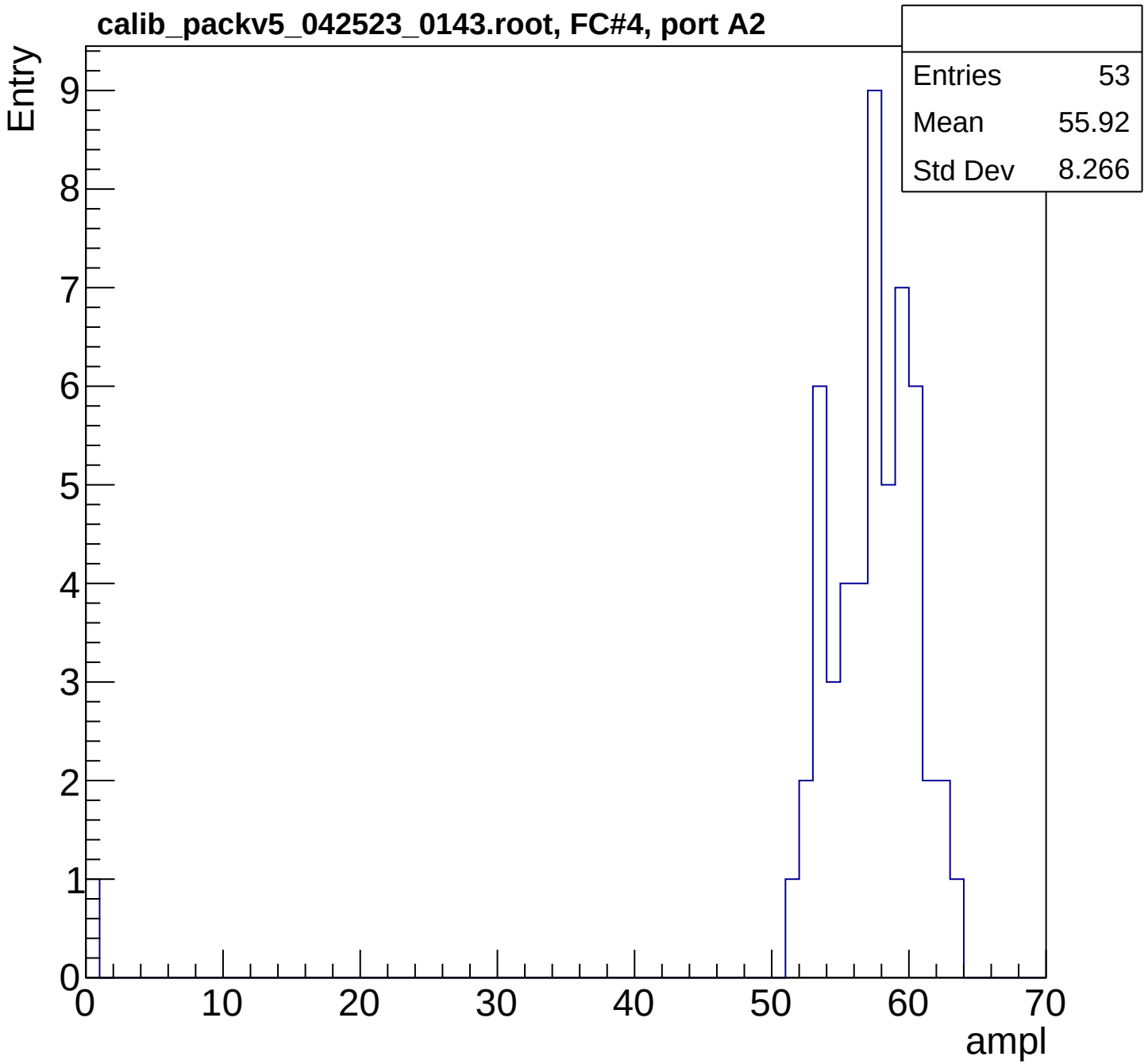
40
50
60
70


B1L100S, U26-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.92
Std Dev 8.266
Entry
ampl
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0
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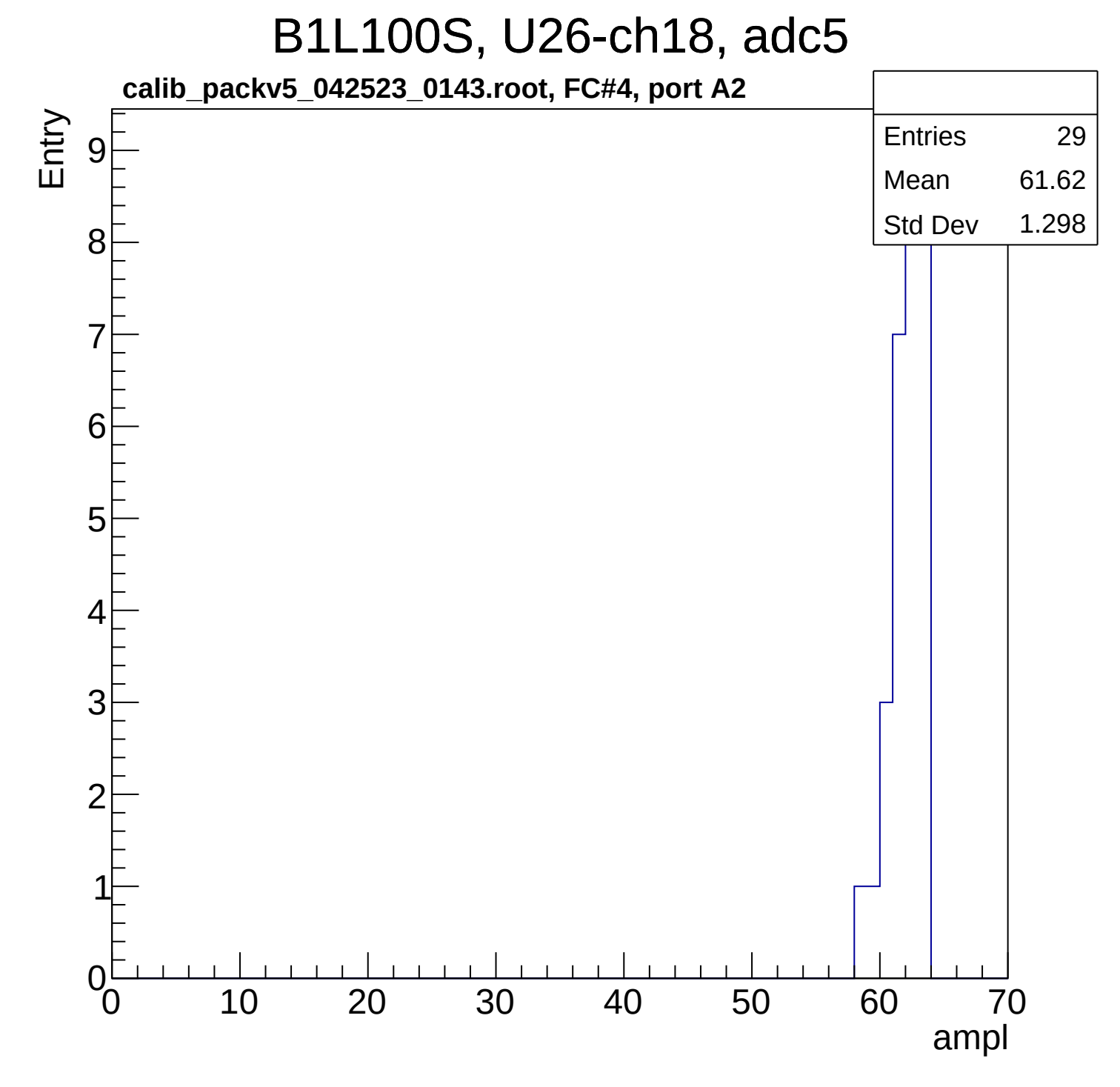
B1L100S, U26-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 61.62
Std Dev 1.298
Entry
ampl
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2
3
4
5
6
7
8
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0
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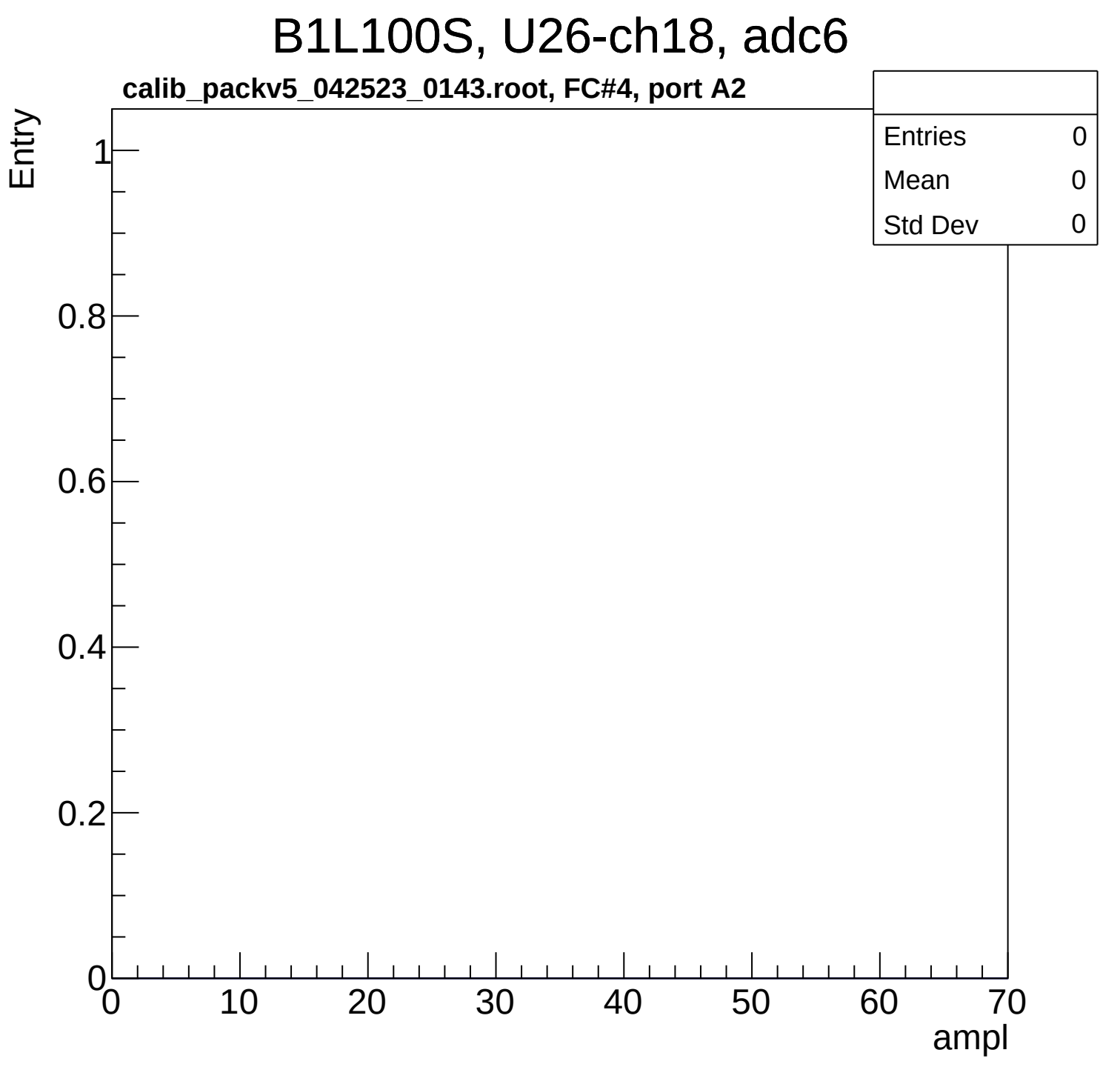
B1L100S, U26-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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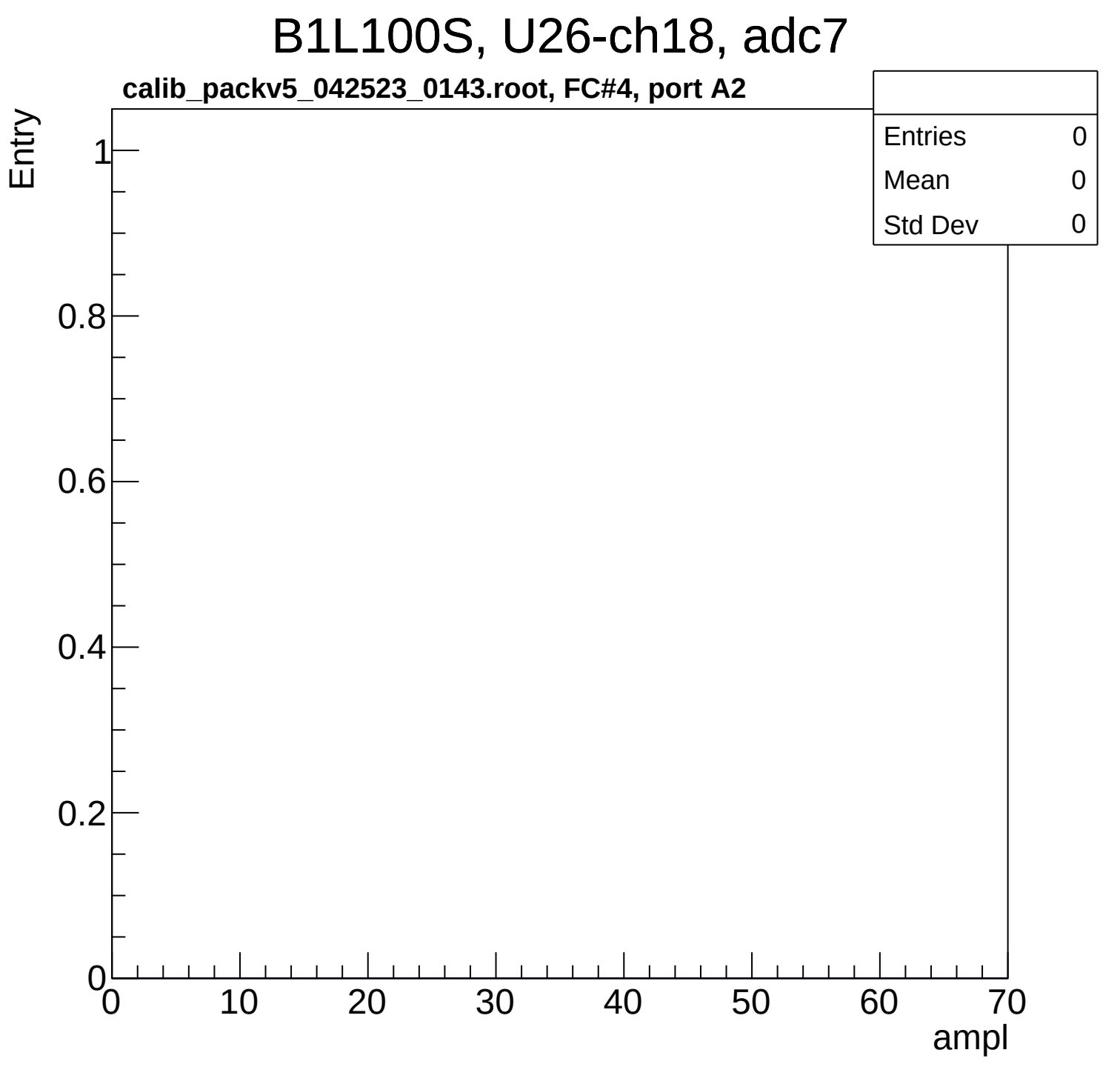
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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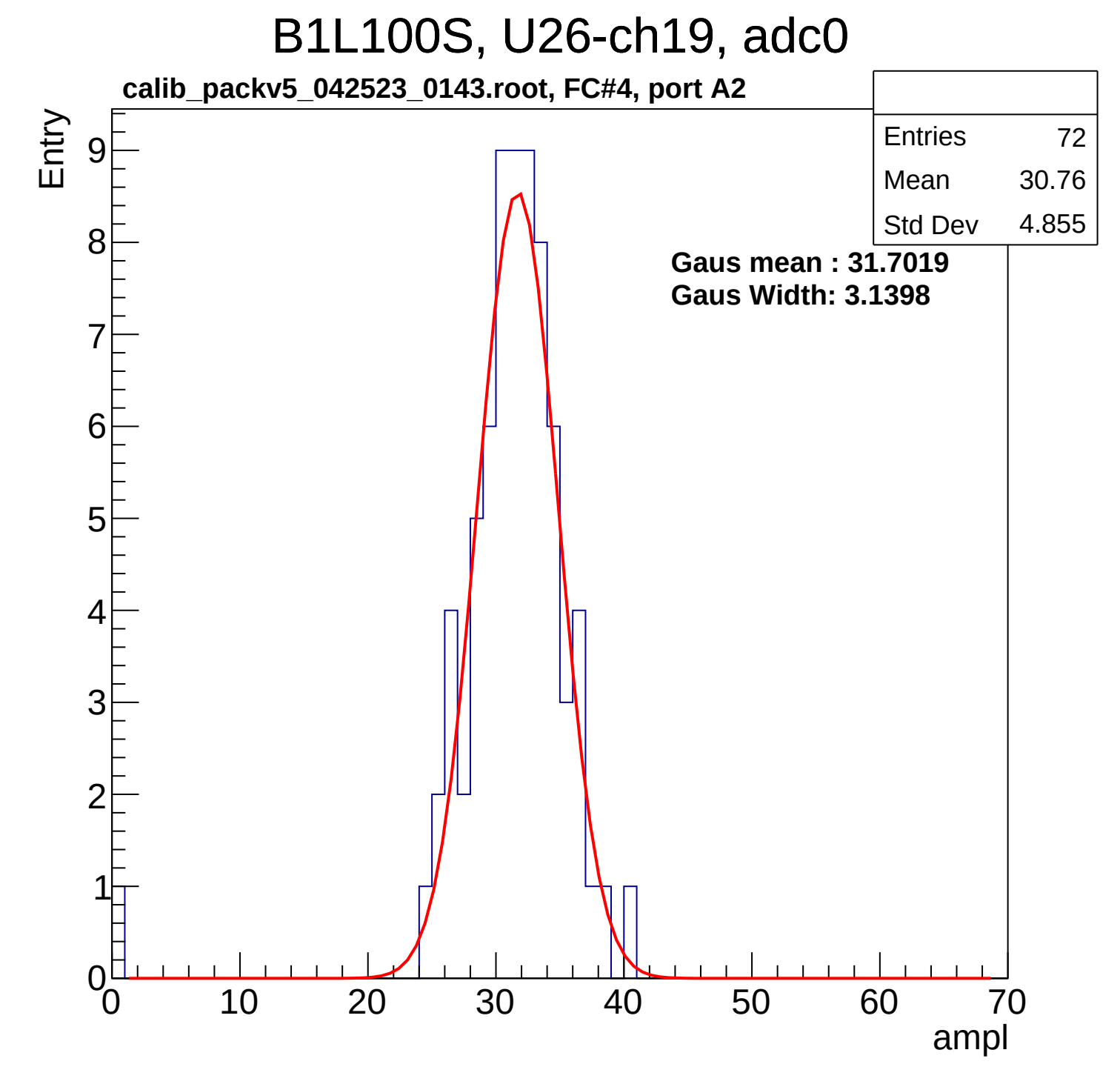
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.76
Std Dev 4.855
Gaus mean : 31.7019
Gaus Width: 3.1398
Entry
ampl
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3
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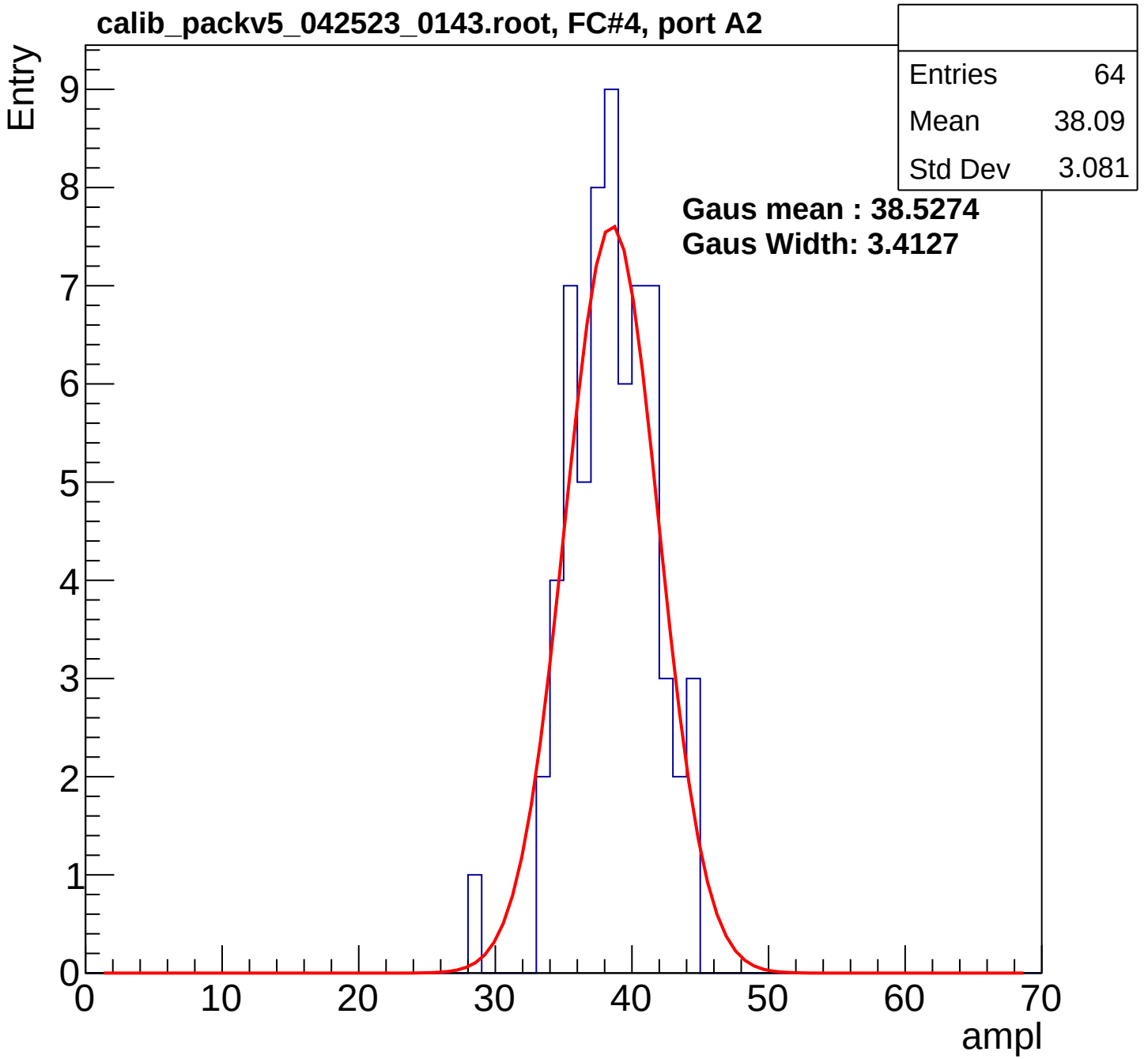
B1L100S, U26-ch19, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 38.09
Std Dev 3.081
Gaus mean : 38.5274
Gaus Width: 3.4127
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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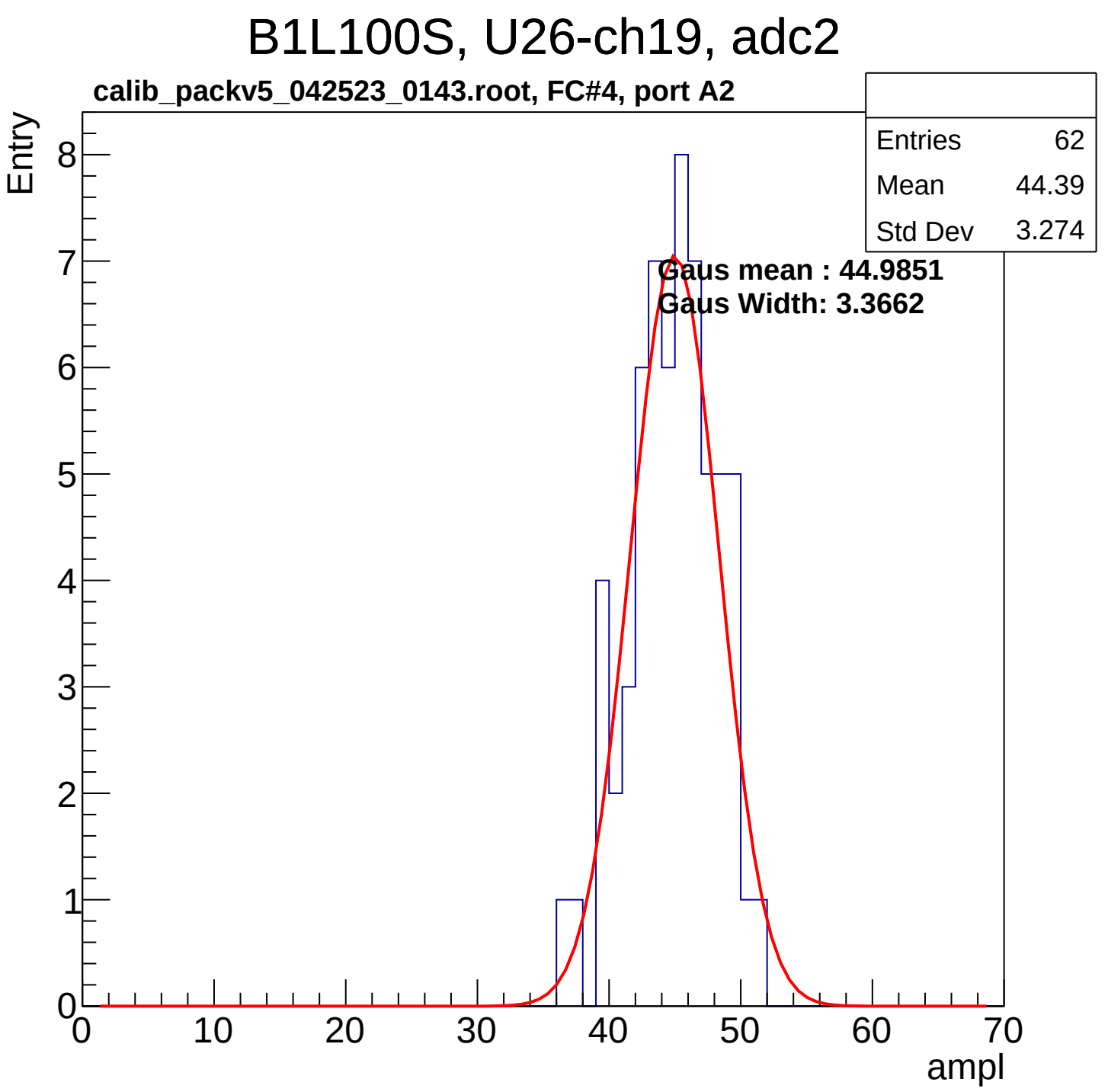
B1L100S, U26-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44.39
Std Dev 3.274
Gaus mean : 44.9851
Gaus Width: 3.3662
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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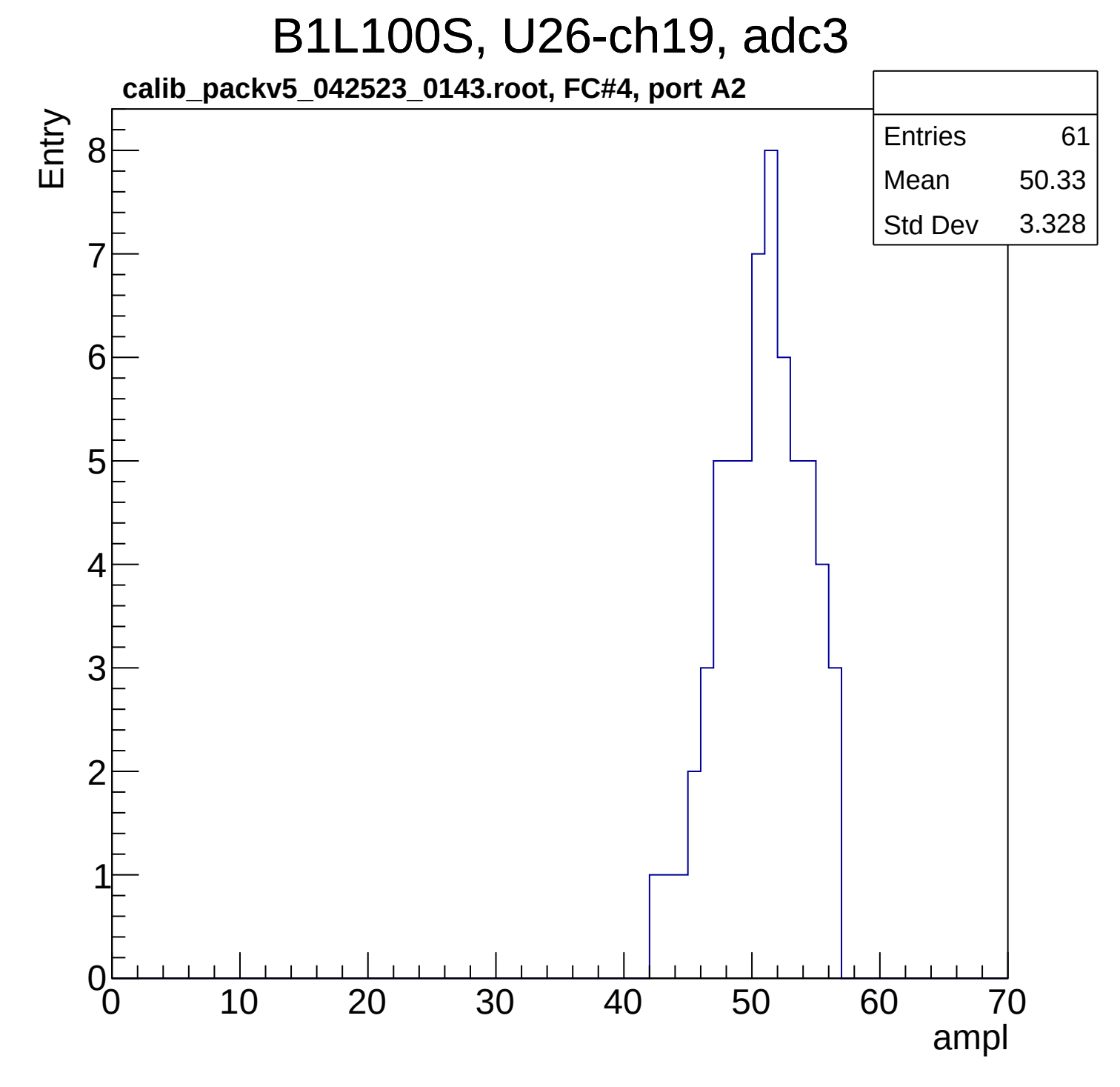
B1L100S, U26-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.33
Std Dev 3.328
Entry
ampl
0
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2
3
4
5
6
7
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0
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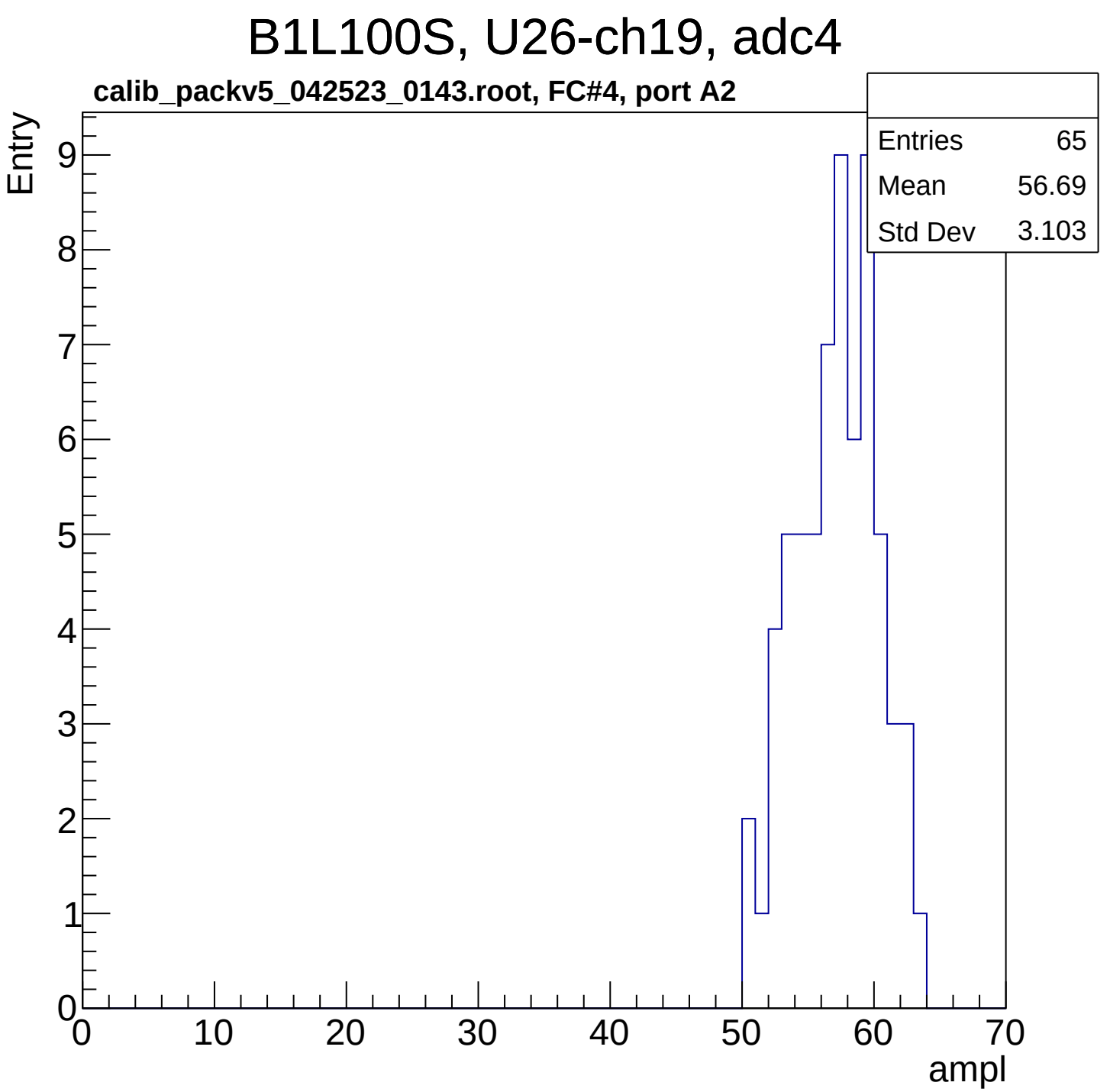
B1L100S, U26-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.69
Std Dev 3.103
Entry
ampl
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2
3
4
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6
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0
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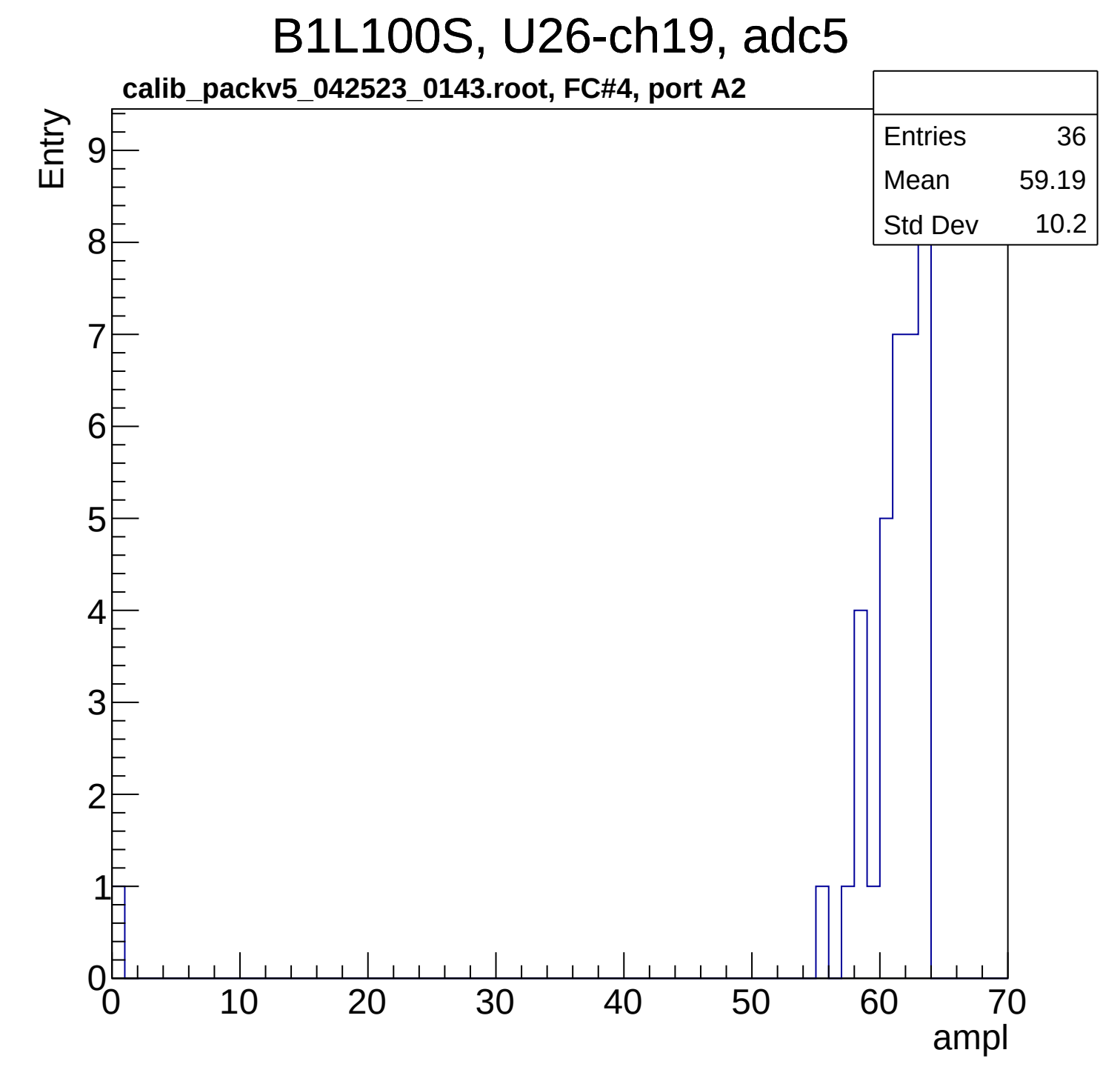
B1L100S, U26-ch19, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 59.19
Std Dev 10.2
Entry
ampl
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2
3
4
5
6
7
8
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0
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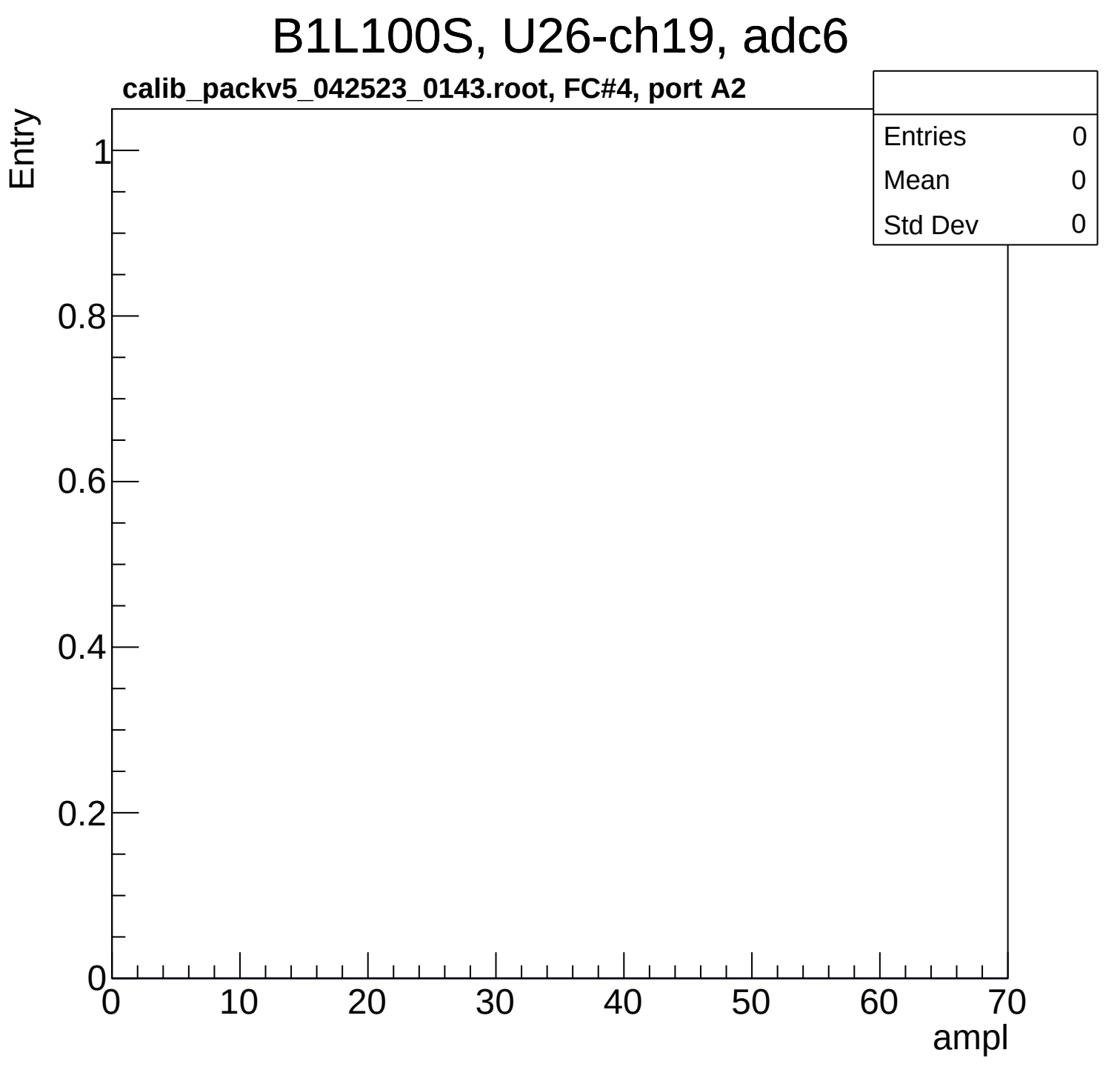
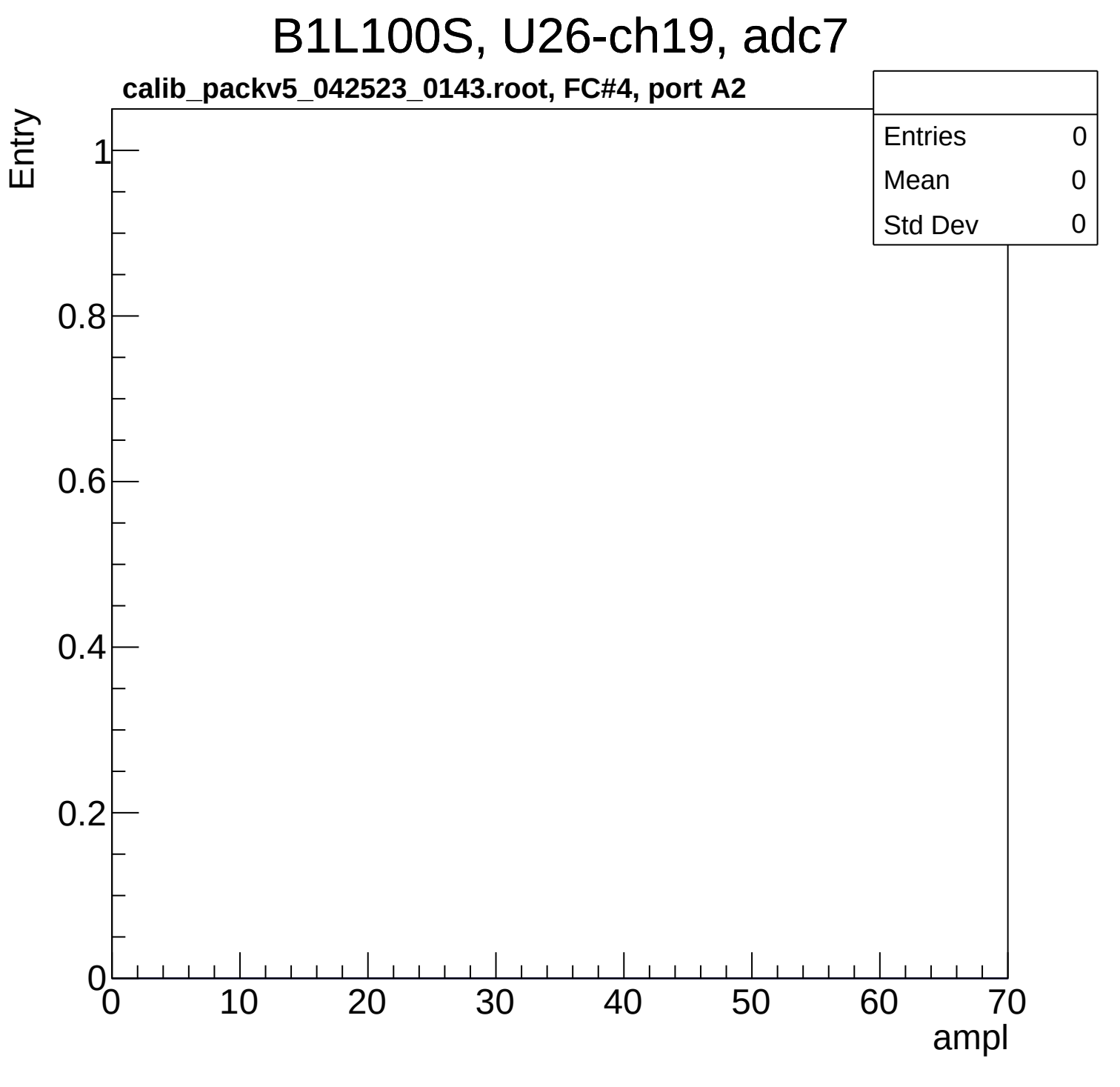
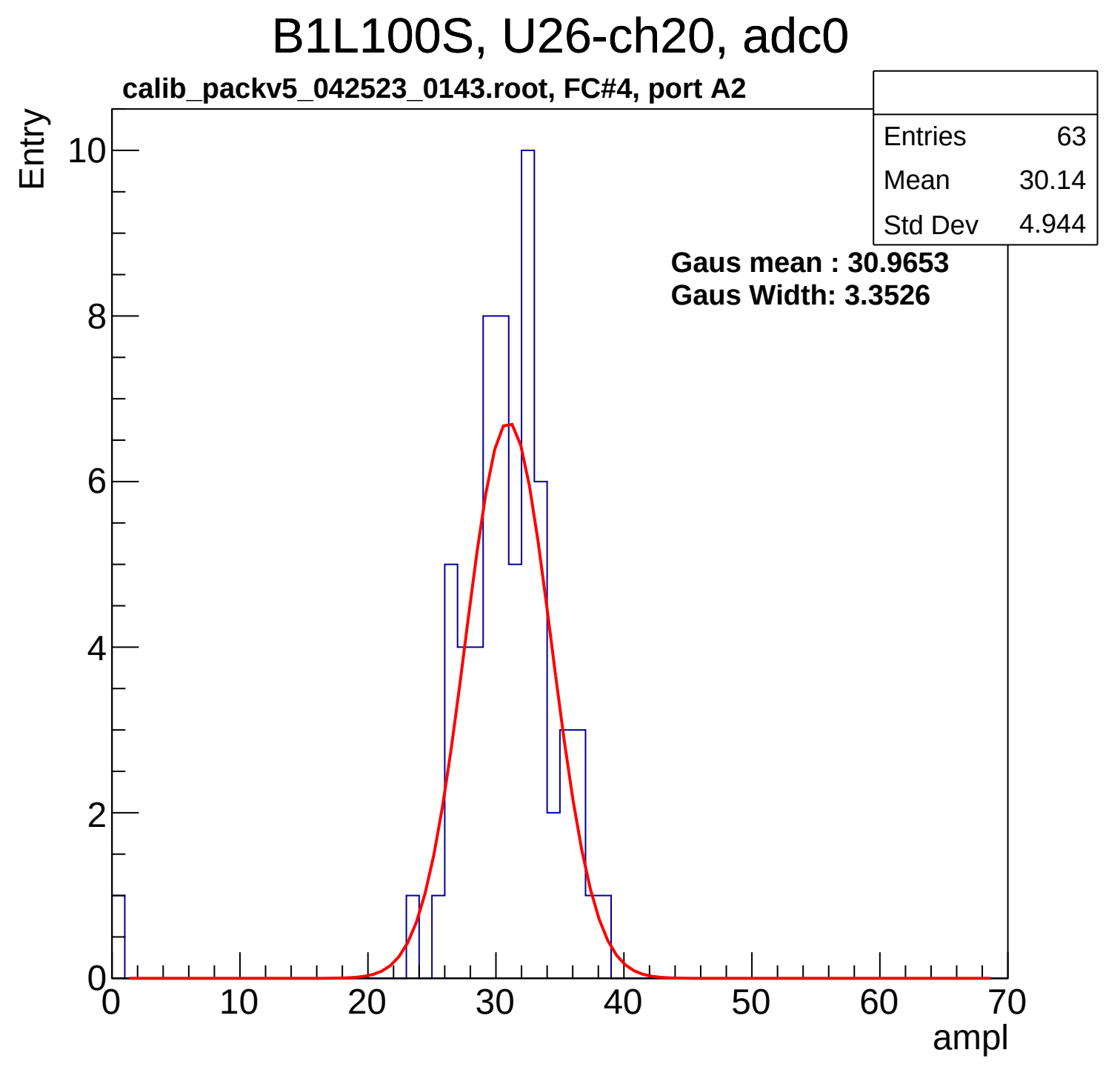

B1L100S, U26-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 30.14
Std Dev 4.944
Gaus mean : 30.9653
Gaus Width: 3.3526
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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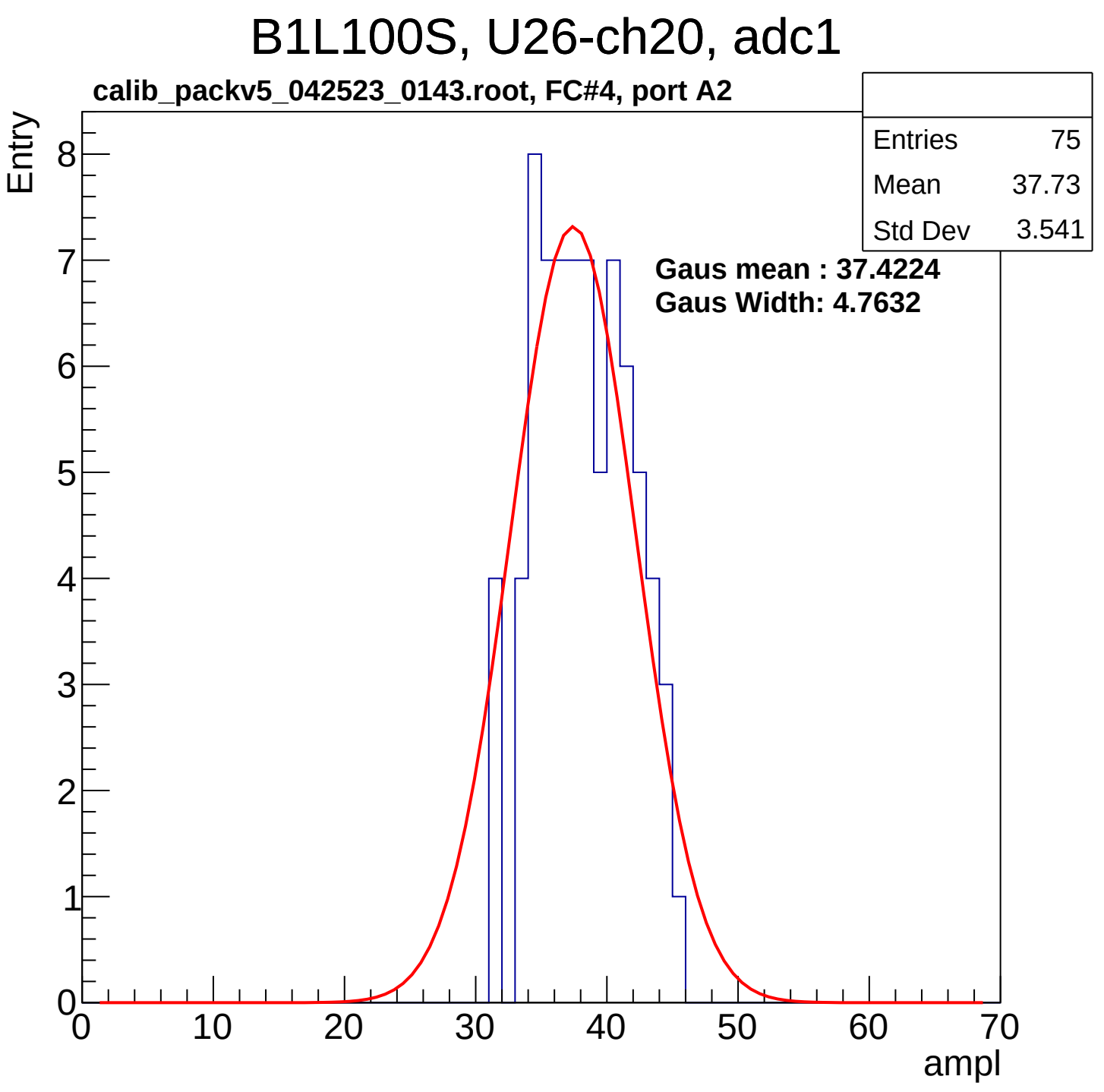
B1L100S, U26-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.73
Std Dev 3.541
Gaus mean : 37.4224
Gaus Width: 4.7632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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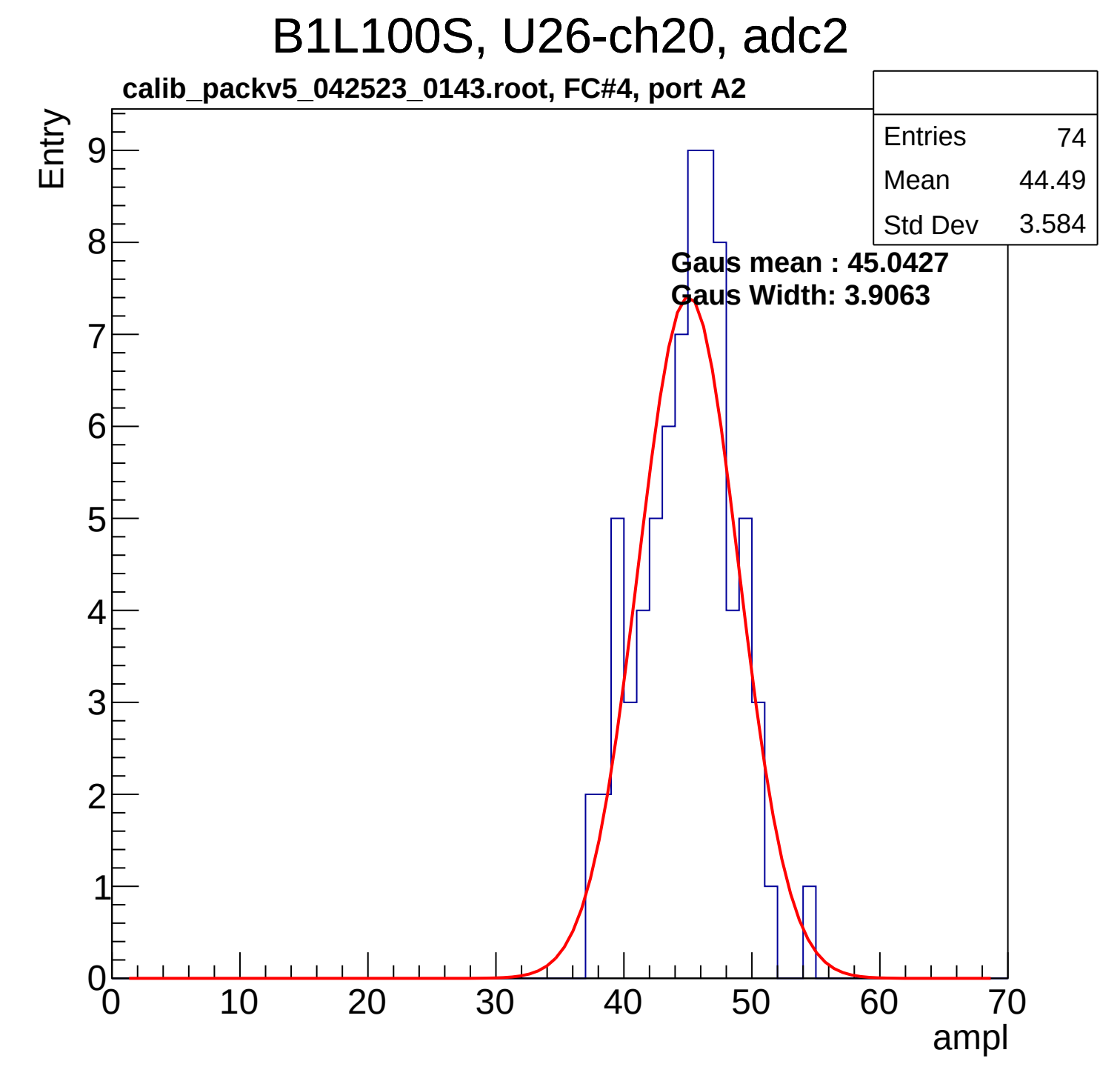
B1L100S, U26-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 44.49
Std Dev 3.584
Gaus mean : 45.0427
Gaus Width: 3.9063
Entry
ampl
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2
3
4
5
6
7
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0
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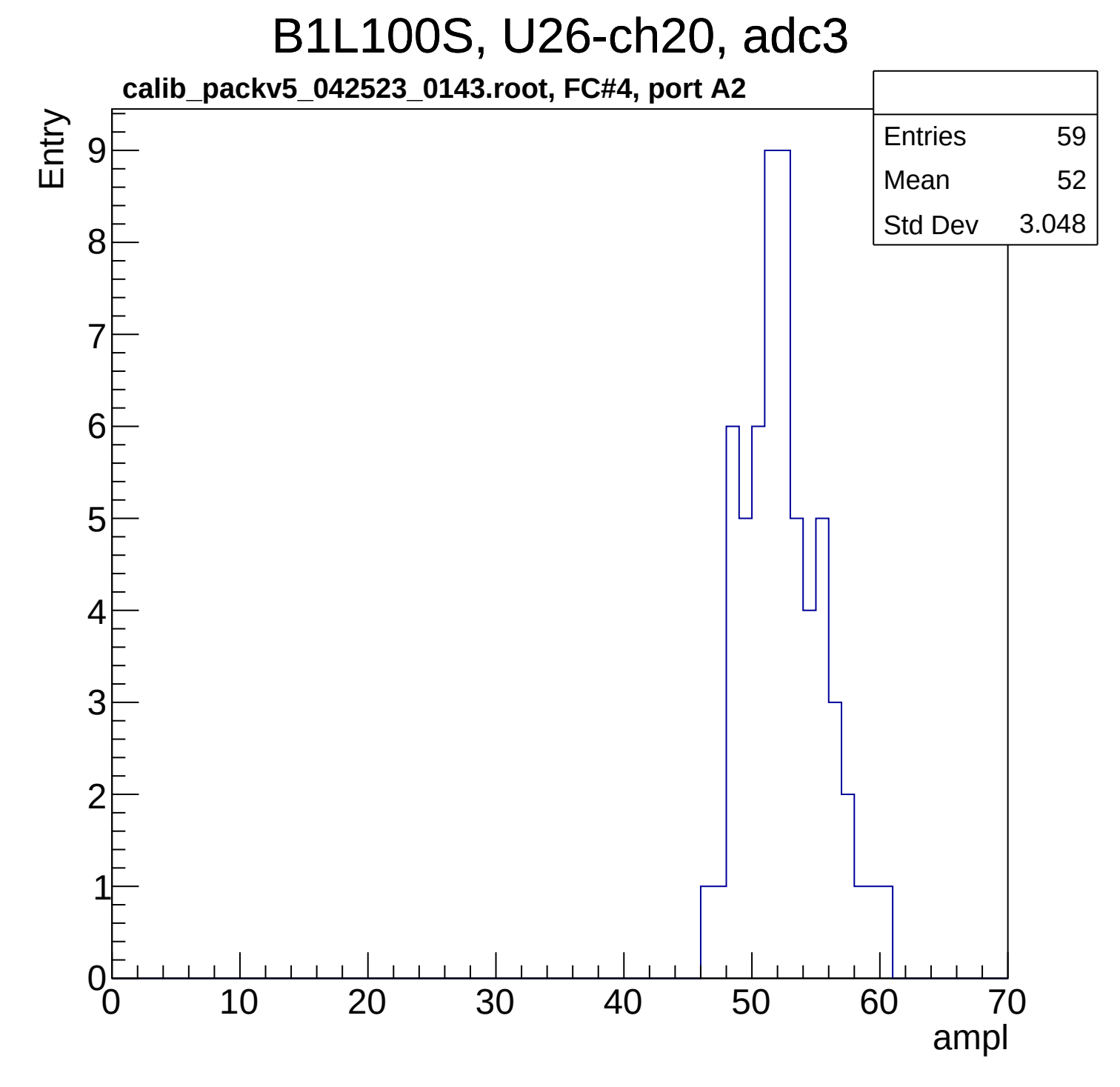
B1L100S, U26-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 52
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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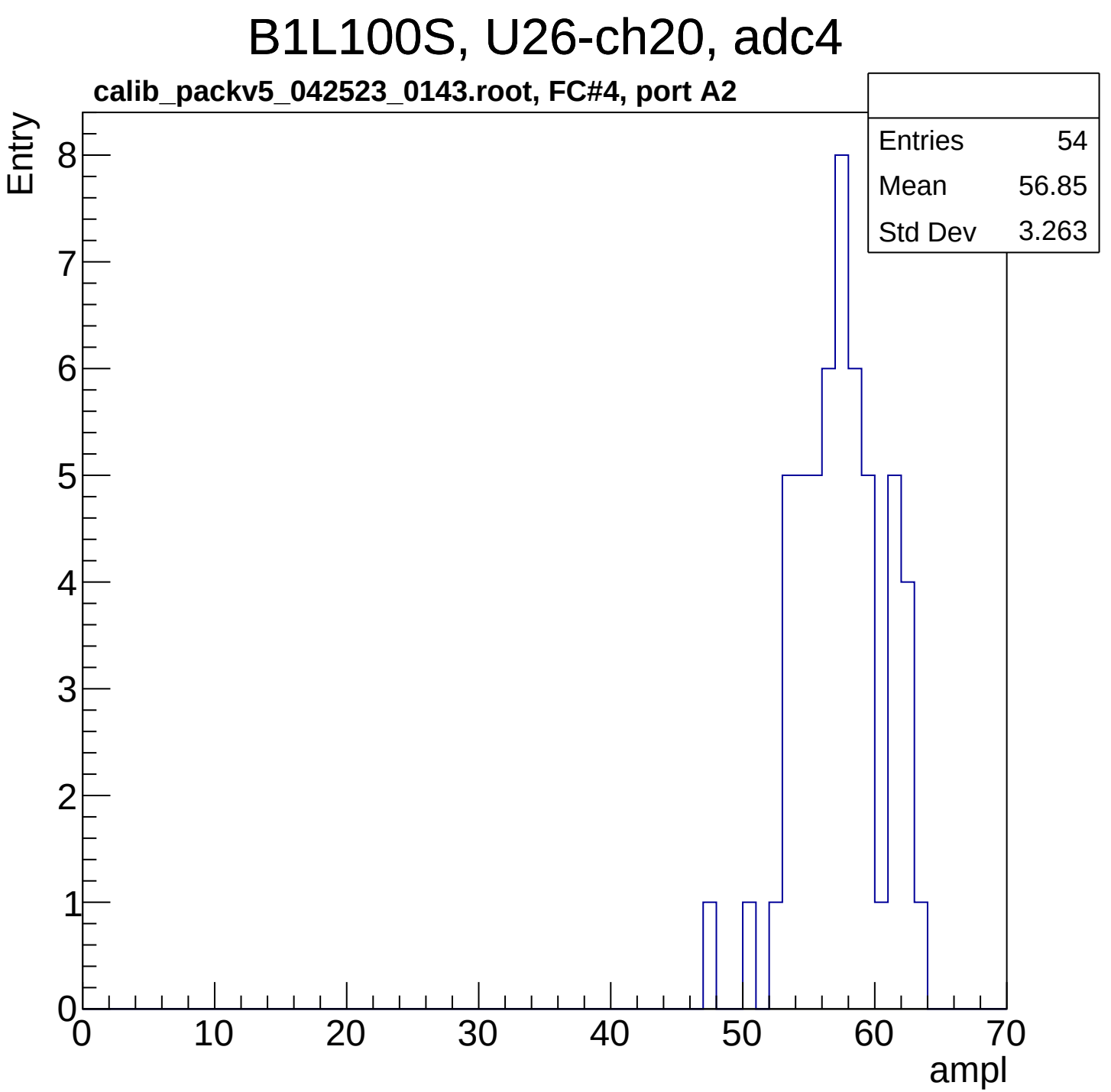
B1L100S, U26-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.85
Std Dev 3.263
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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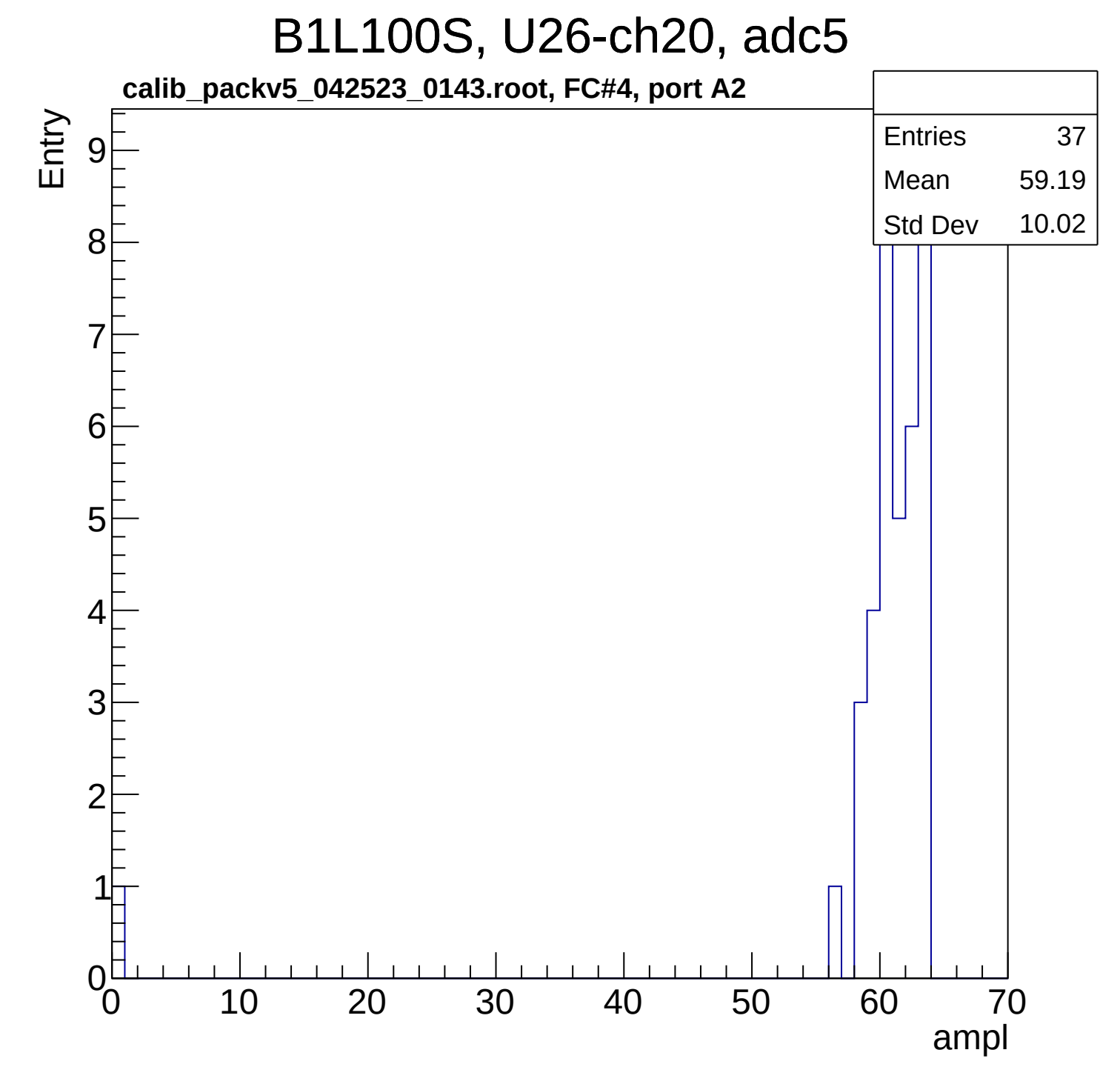
B1L100S, U26-ch20, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.19
Std Dev 10.02
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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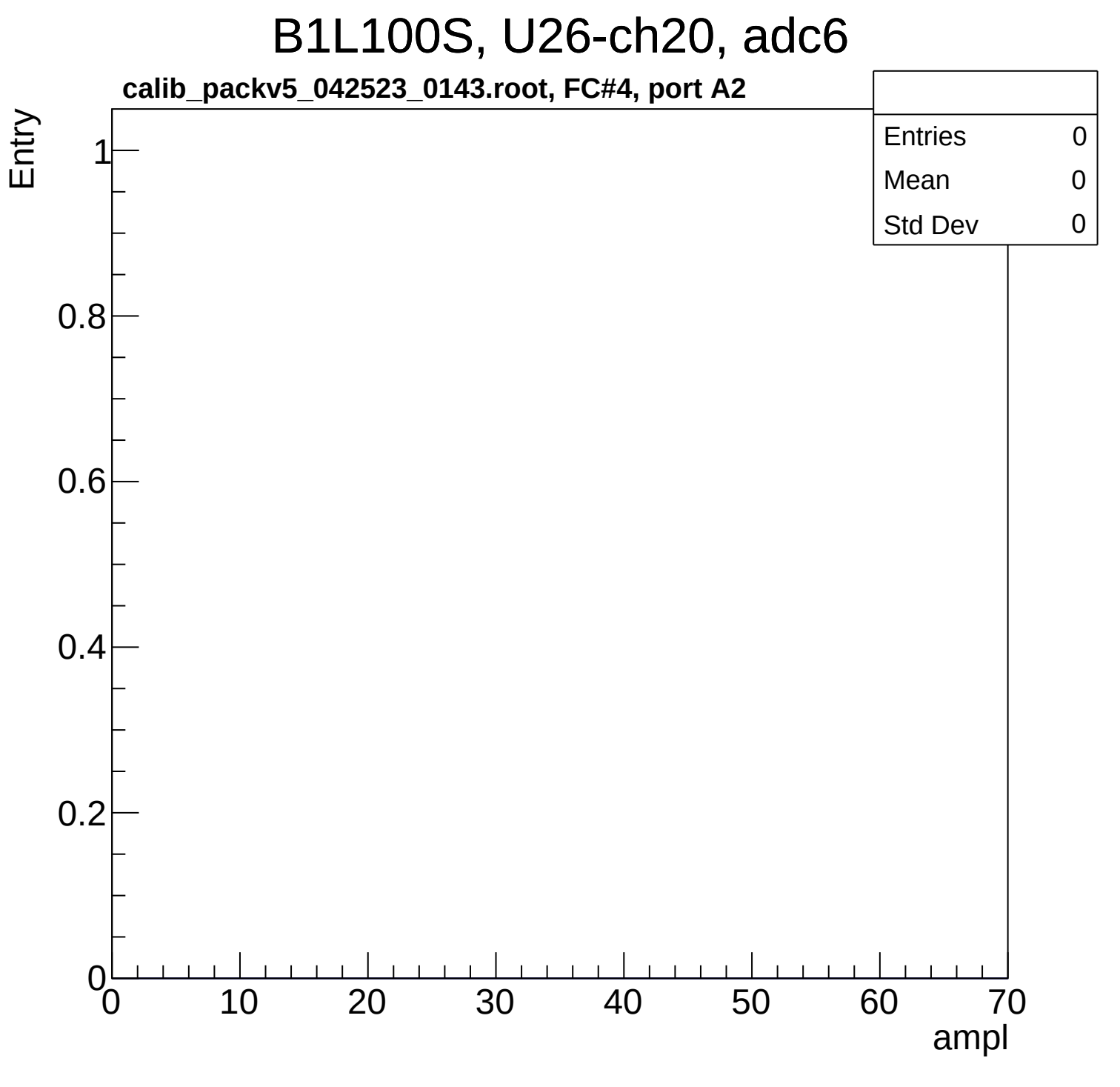
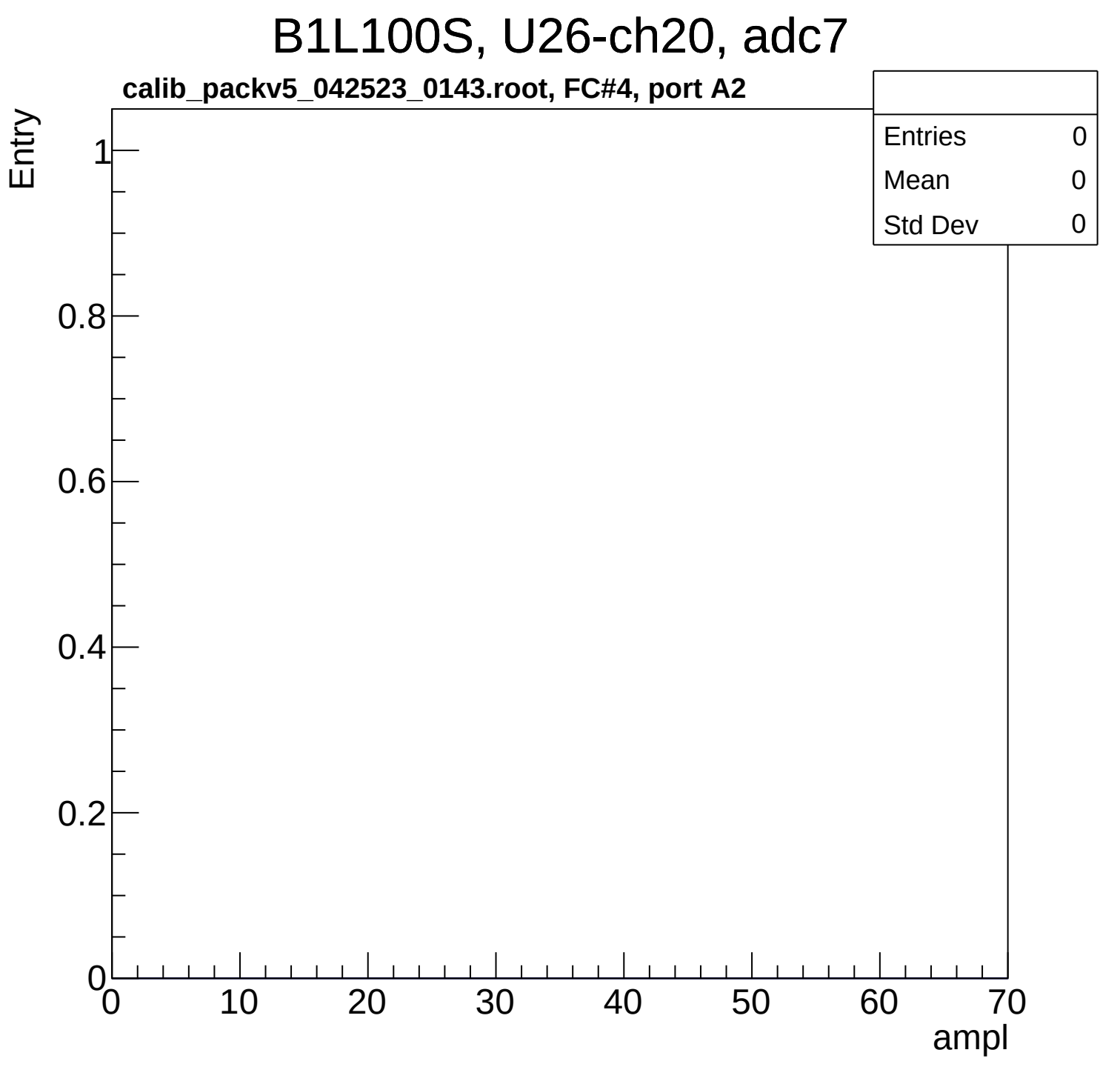
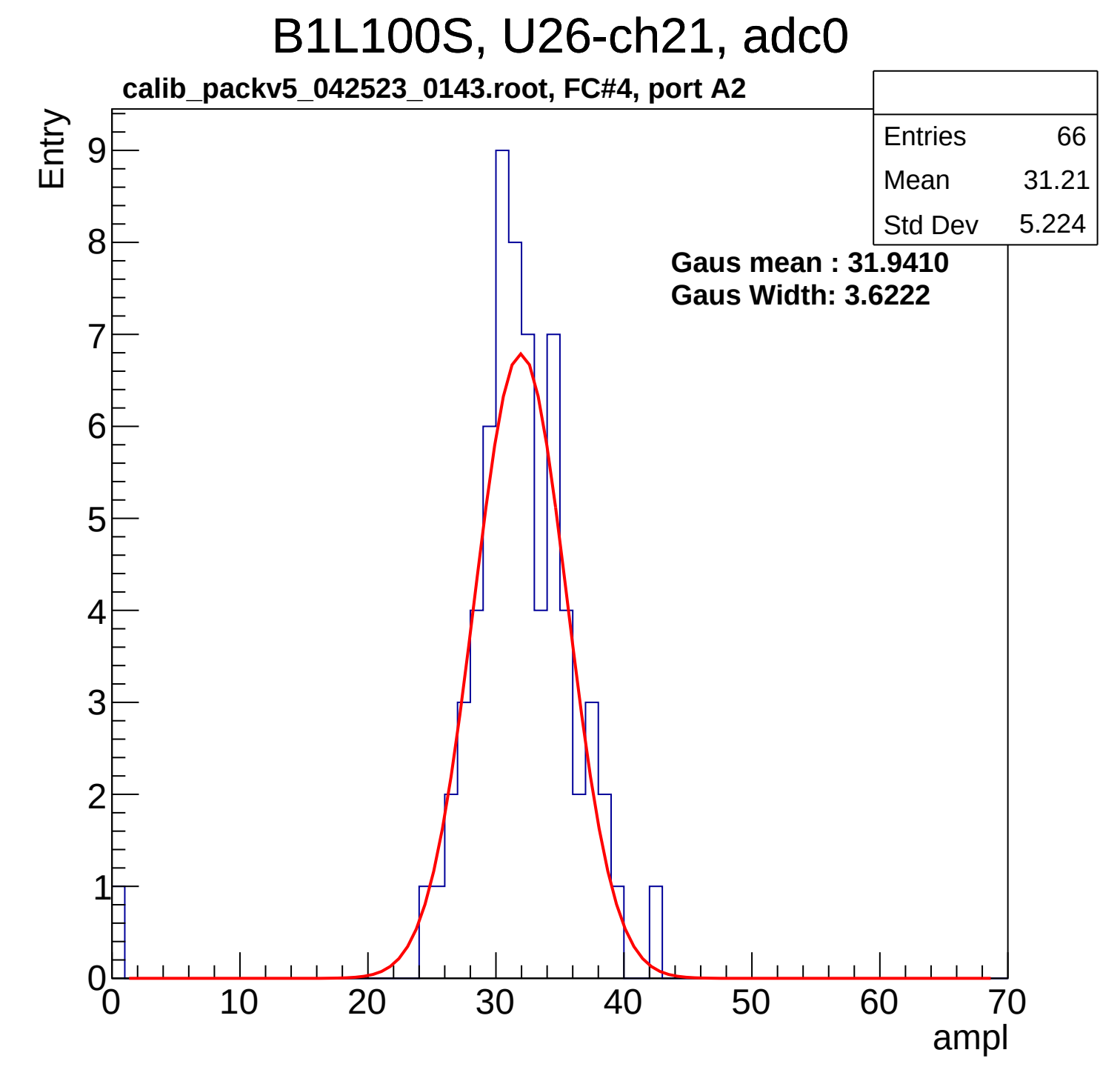

B1L100S, U26-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch20, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 31.21
Std Dev 5.224
Gaus mean : 31.9410
Gaus Width: 3.6222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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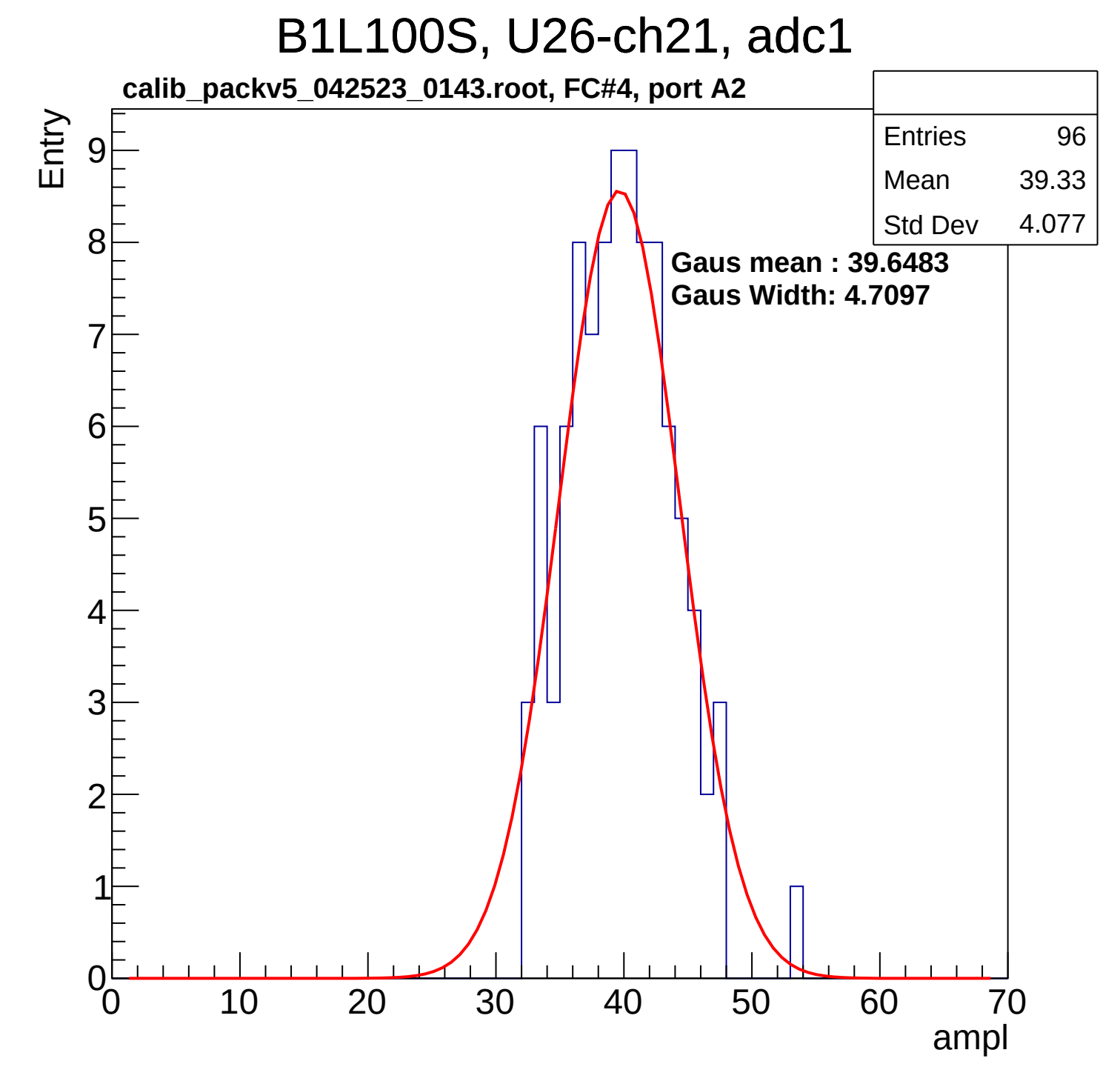
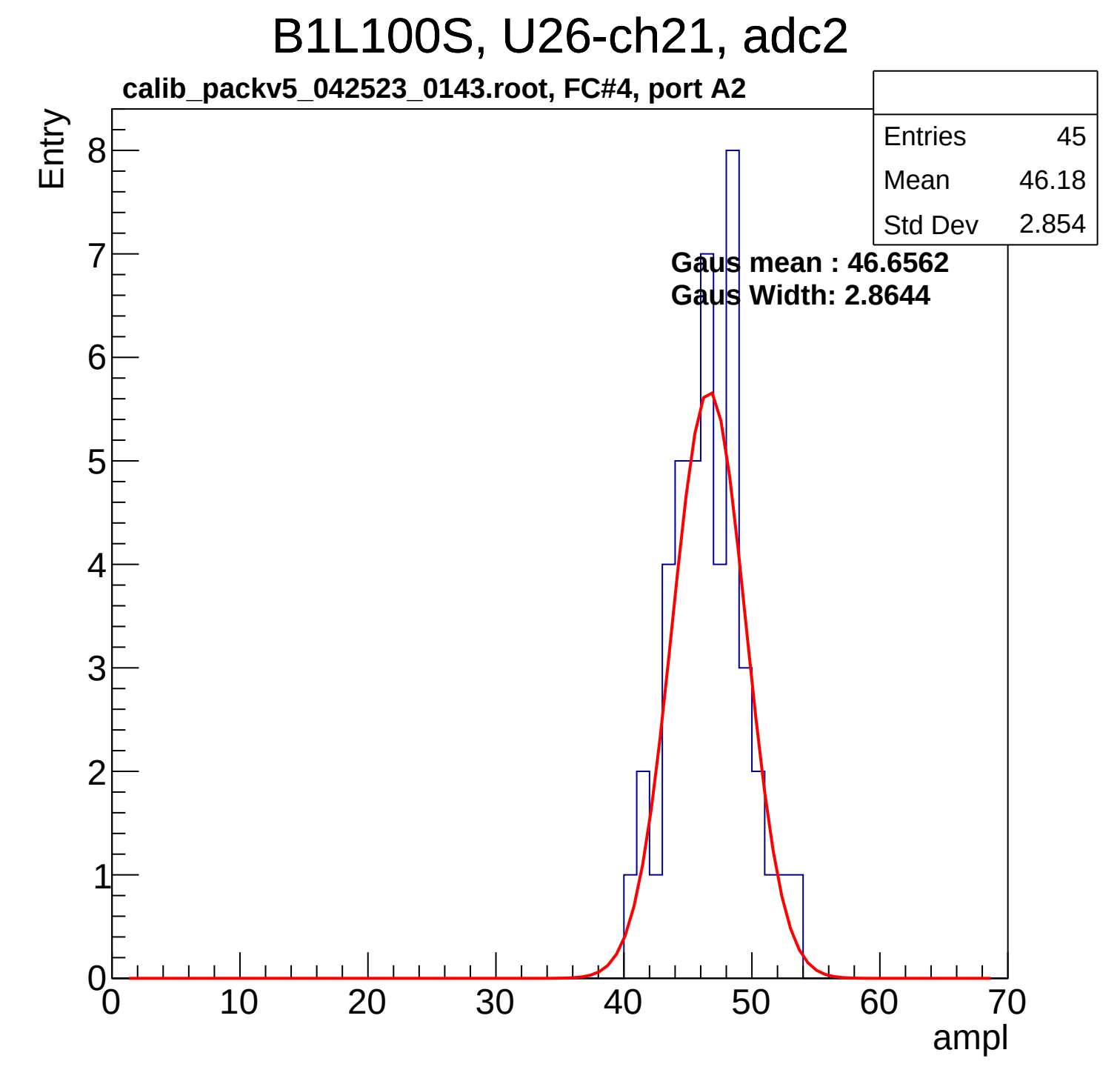
60
70


B1L100S, U26-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 96
Mean 39.33
Std Dev 4.077
Gaus mean : 39.6483
Gaus Width: 4.7097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 46.18
Std Dev 2.854
Gaus mean : 46.6562
Gaus Width: 2.8644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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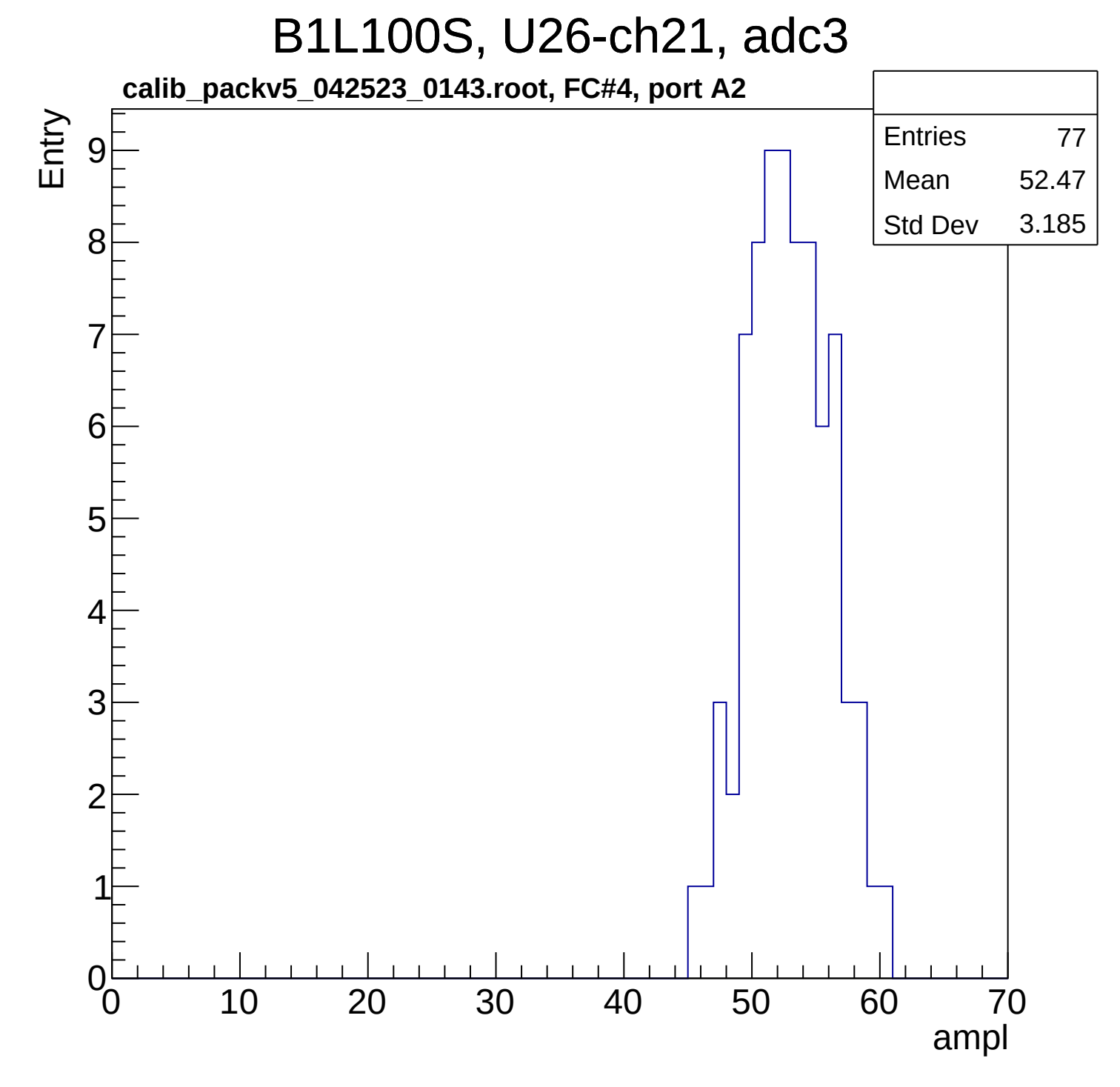
B1L100S, U26-ch21, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 52.47
Std Dev 3.185
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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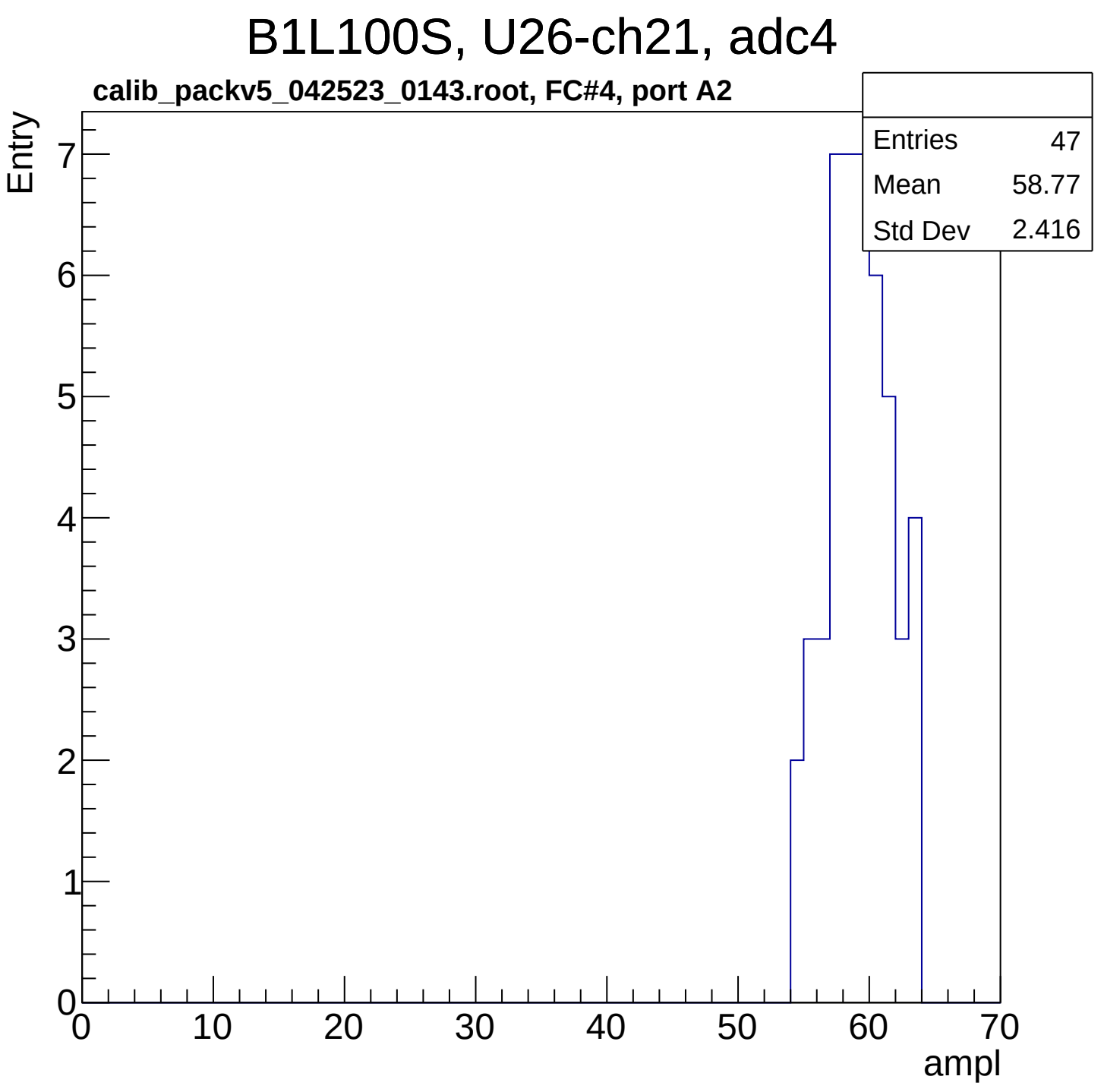
60
70


B1L100S, U26-ch21, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.77
Std Dev 2.416
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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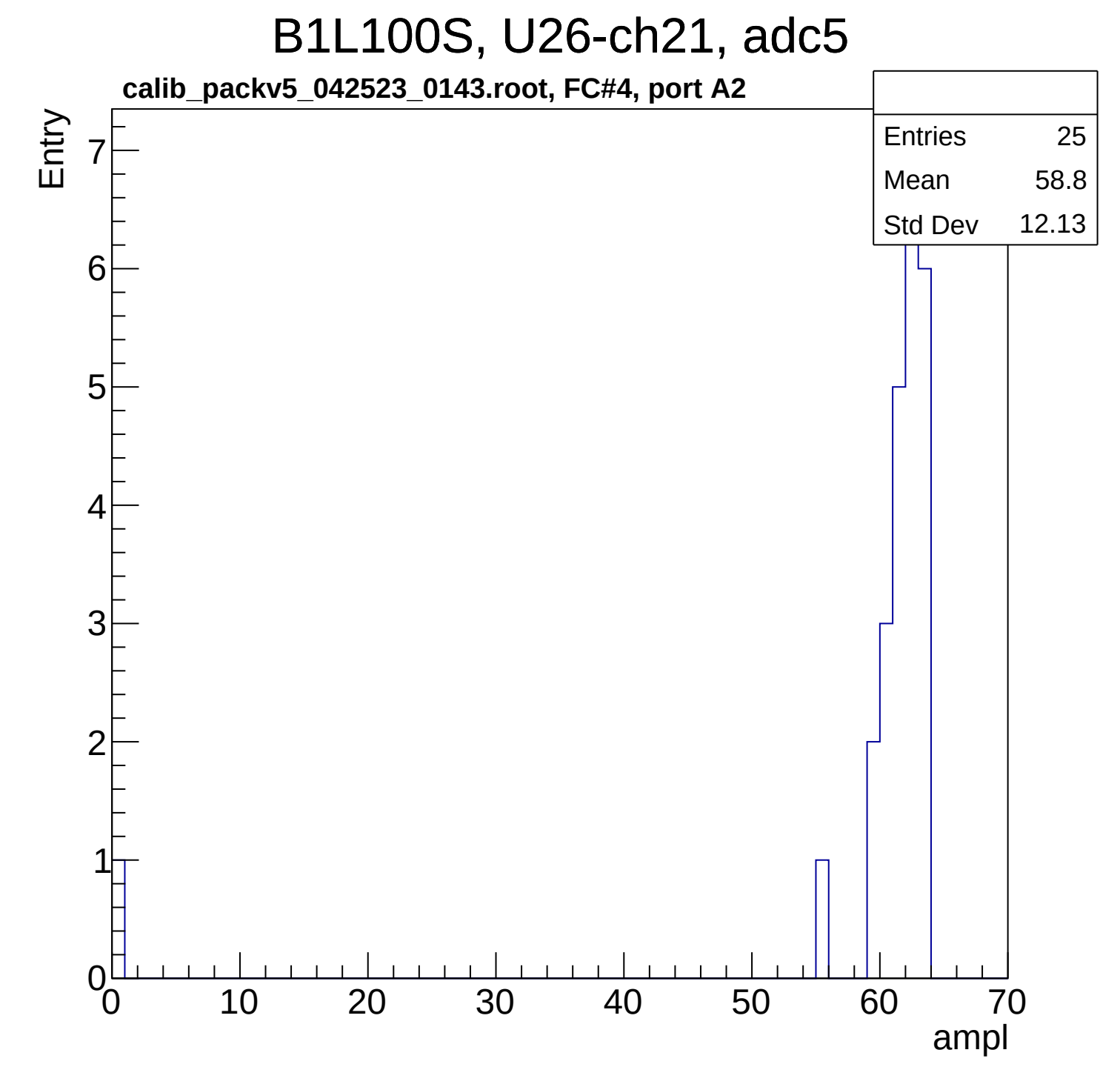
40
50
60
70


B1L100S, U26-ch21, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.8
Std Dev 12.13
Entry
ampl
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4
5
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0
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40
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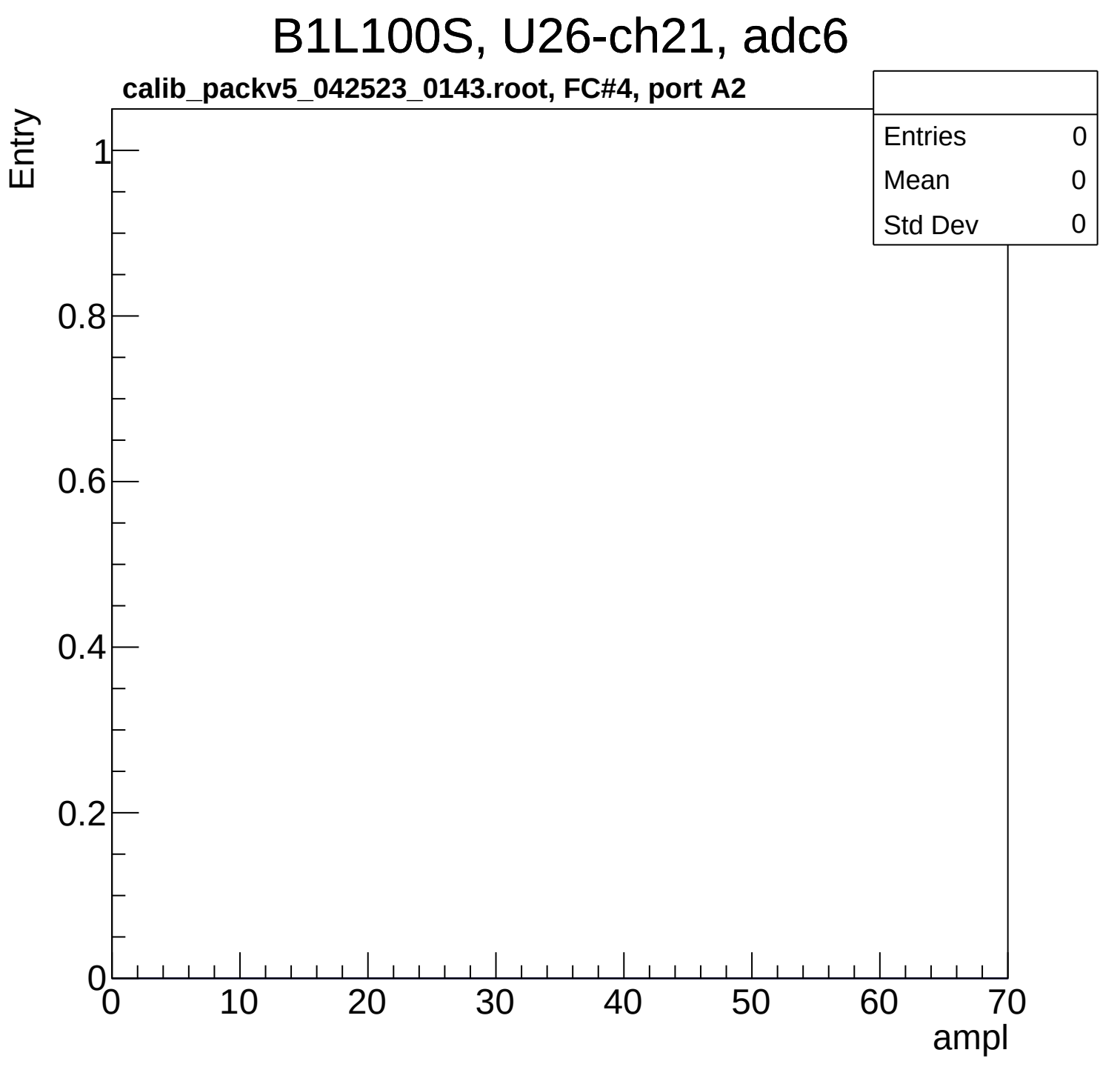
60
70


B1L100S, U26-ch21, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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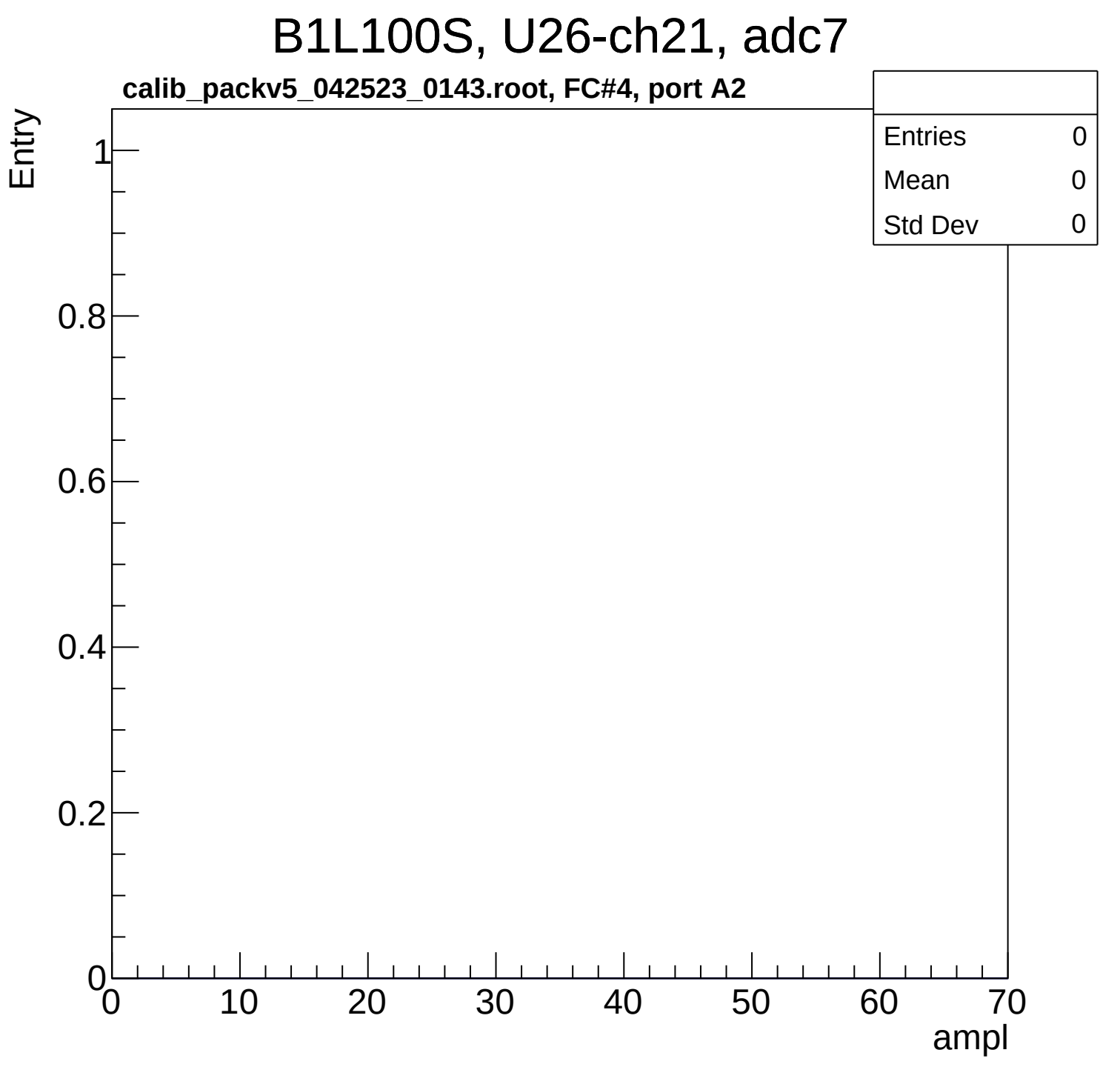
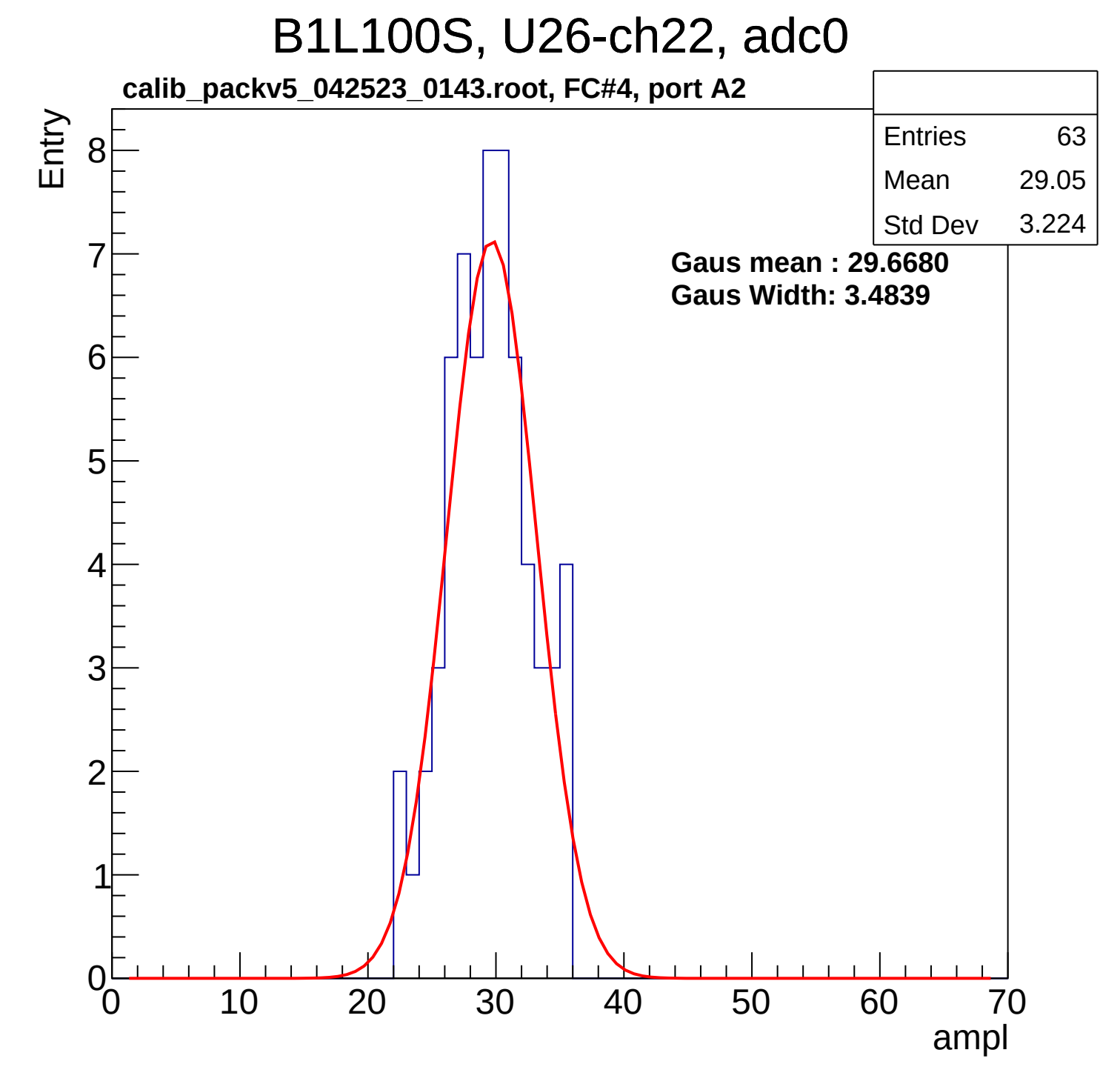
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch22, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.05
Std Dev 3.224
Gaus mean : 29.6680
Gaus Width: 3.4839
Entry
ampl
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3
4
5
6
7
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0
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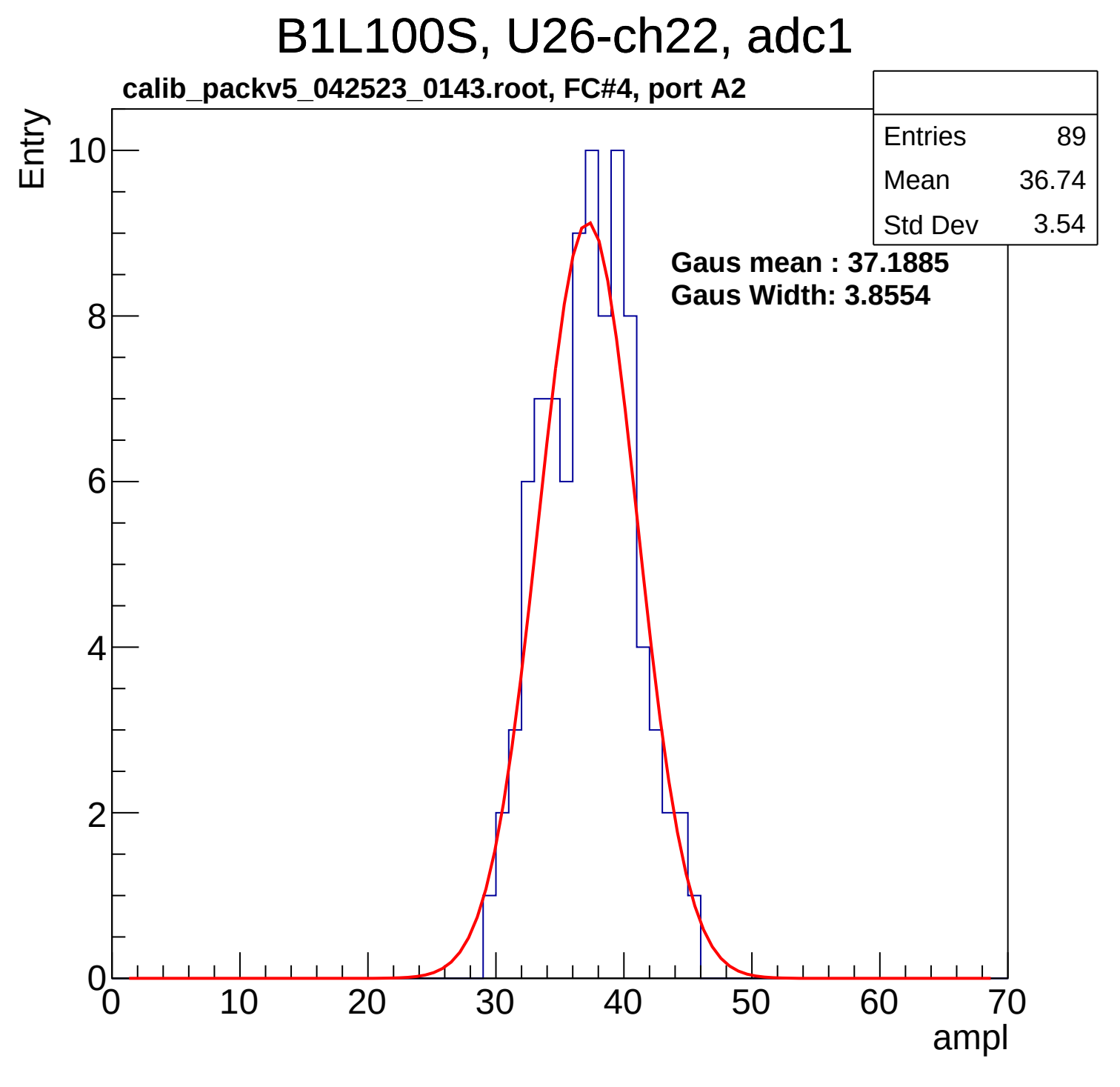
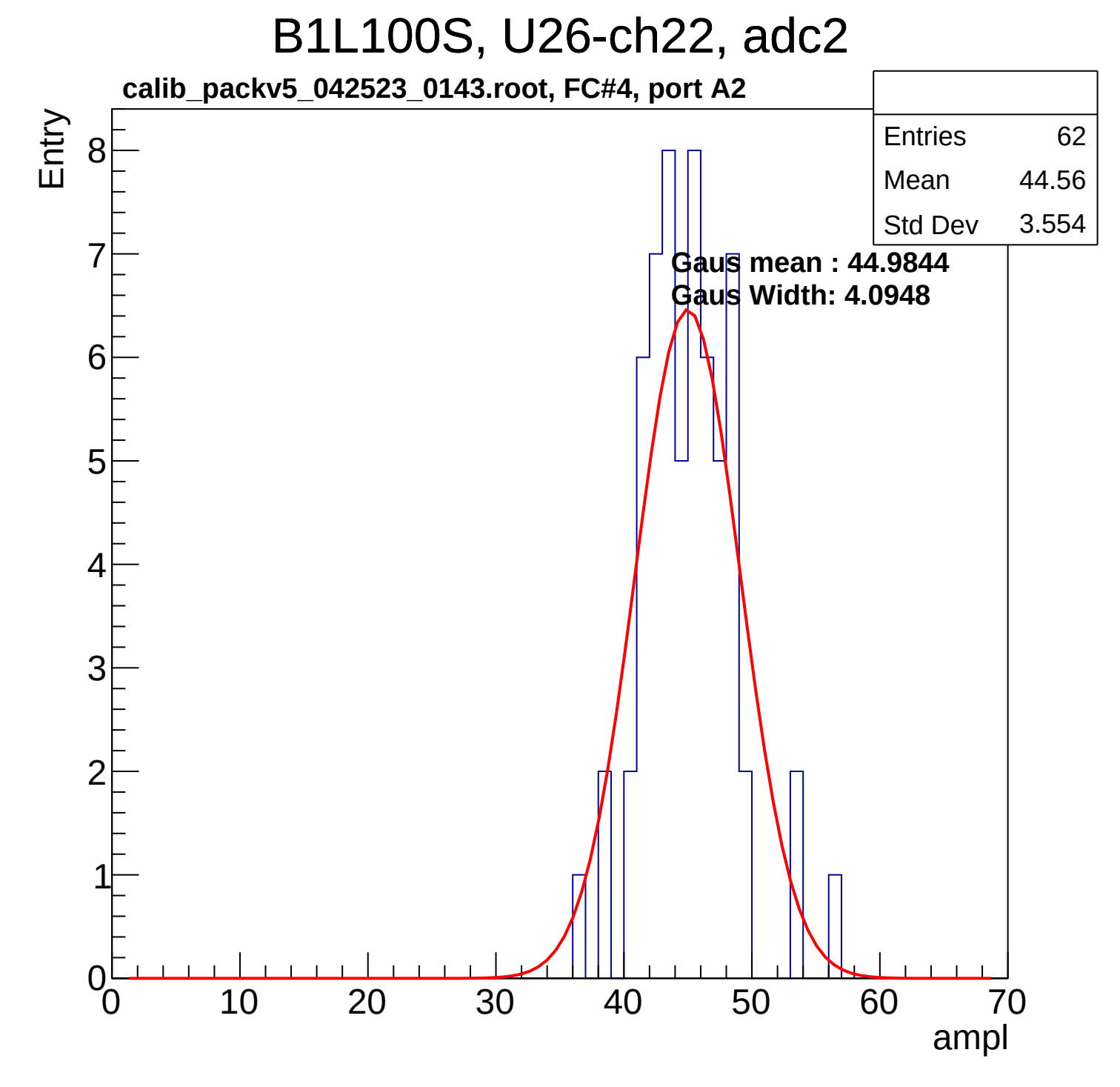

B1L100S, U26-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 36.74
Std Dev 3.54
Gaus mean : 37.1885
Gaus Width: 3.8554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44.56
Std Dev 3.554
Gaus mean : 44.9844
Gaus Width: 4.0948
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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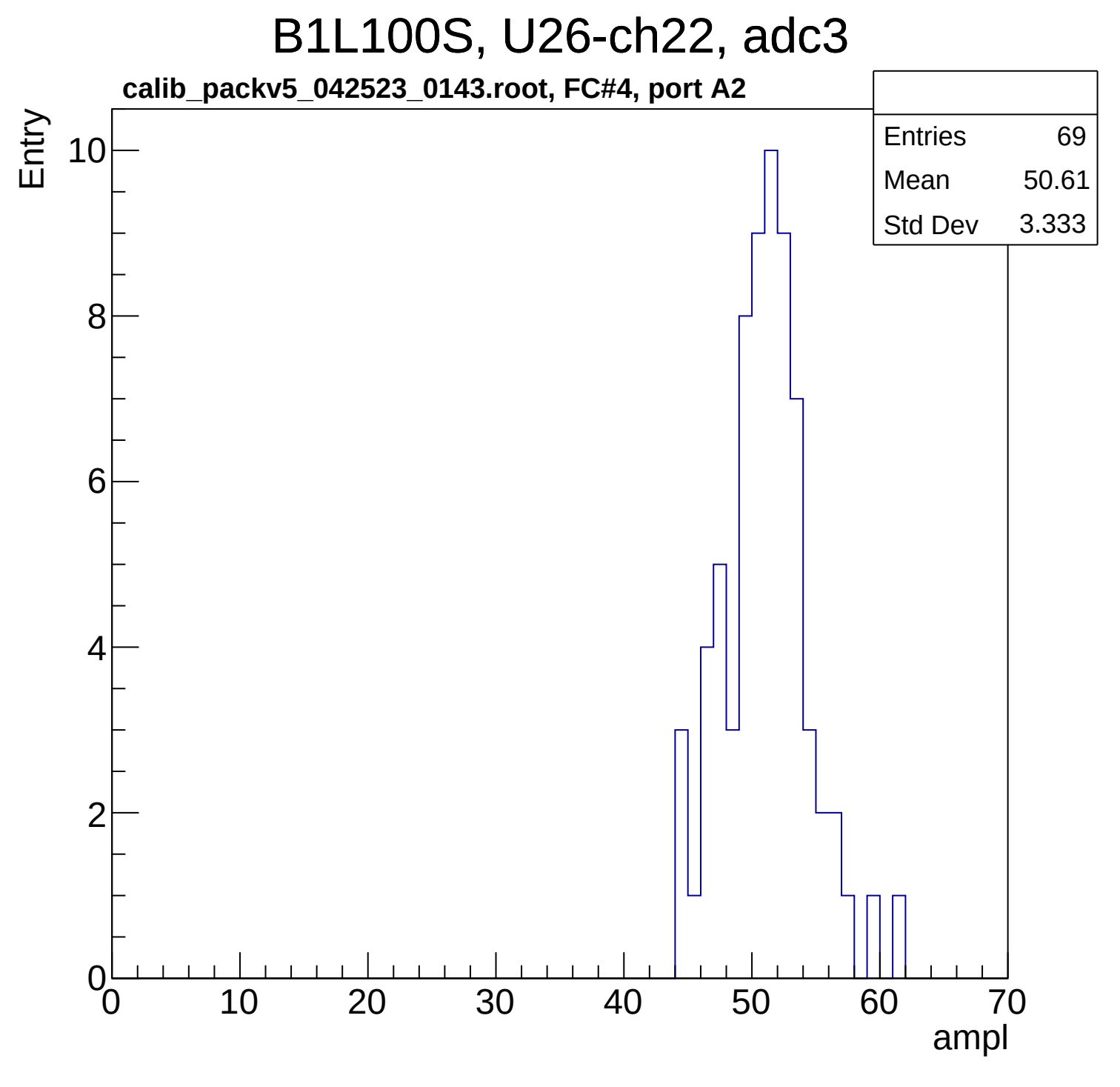
B1L100S, U26-ch22, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 50.61
Std Dev 3.333
Entry
ampl
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2
4
6
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10
0
10
20
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40
50
60
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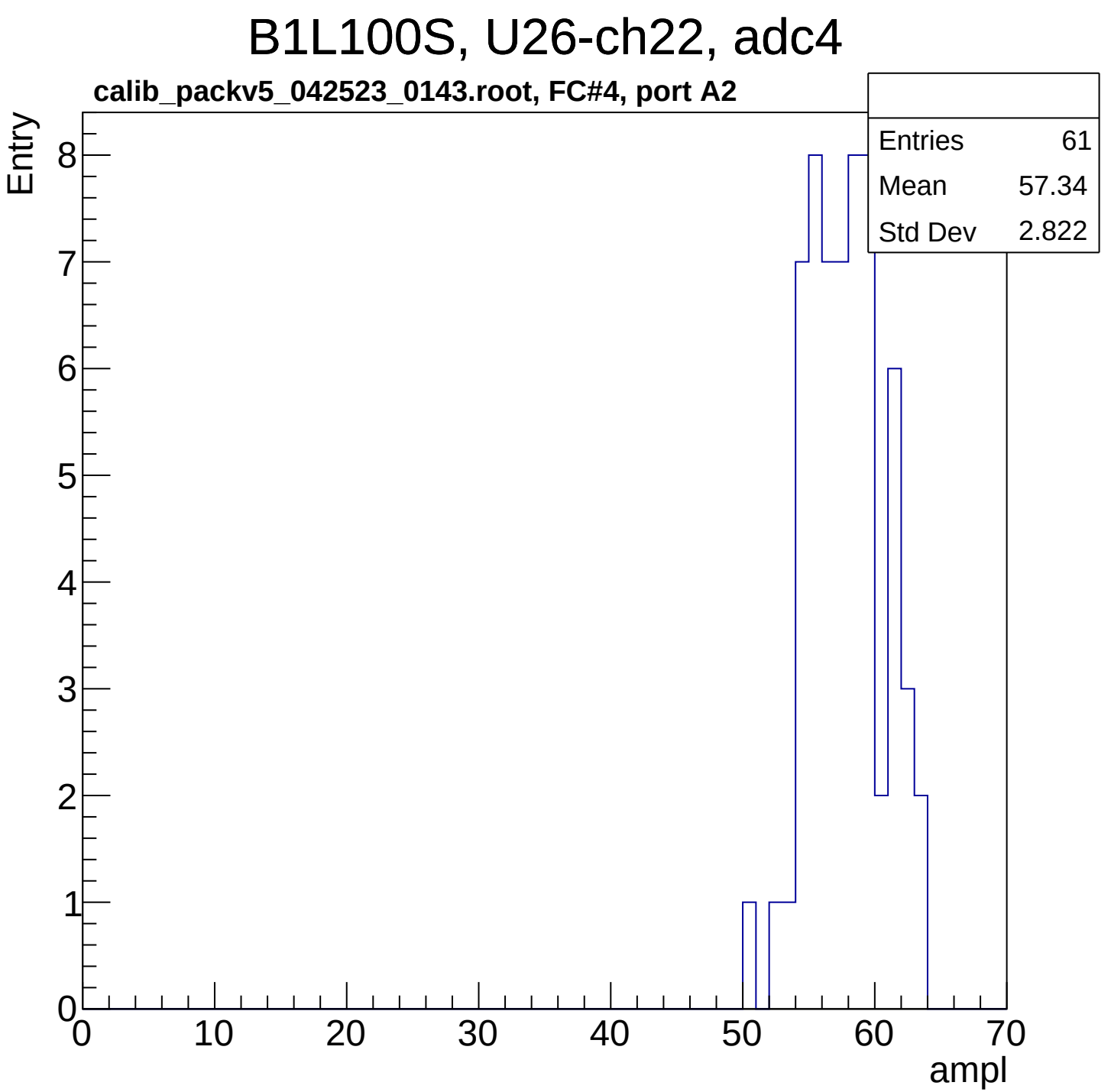
B1L100S, U26-ch22, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.34
Std Dev 2.822
Entry
ampl
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0
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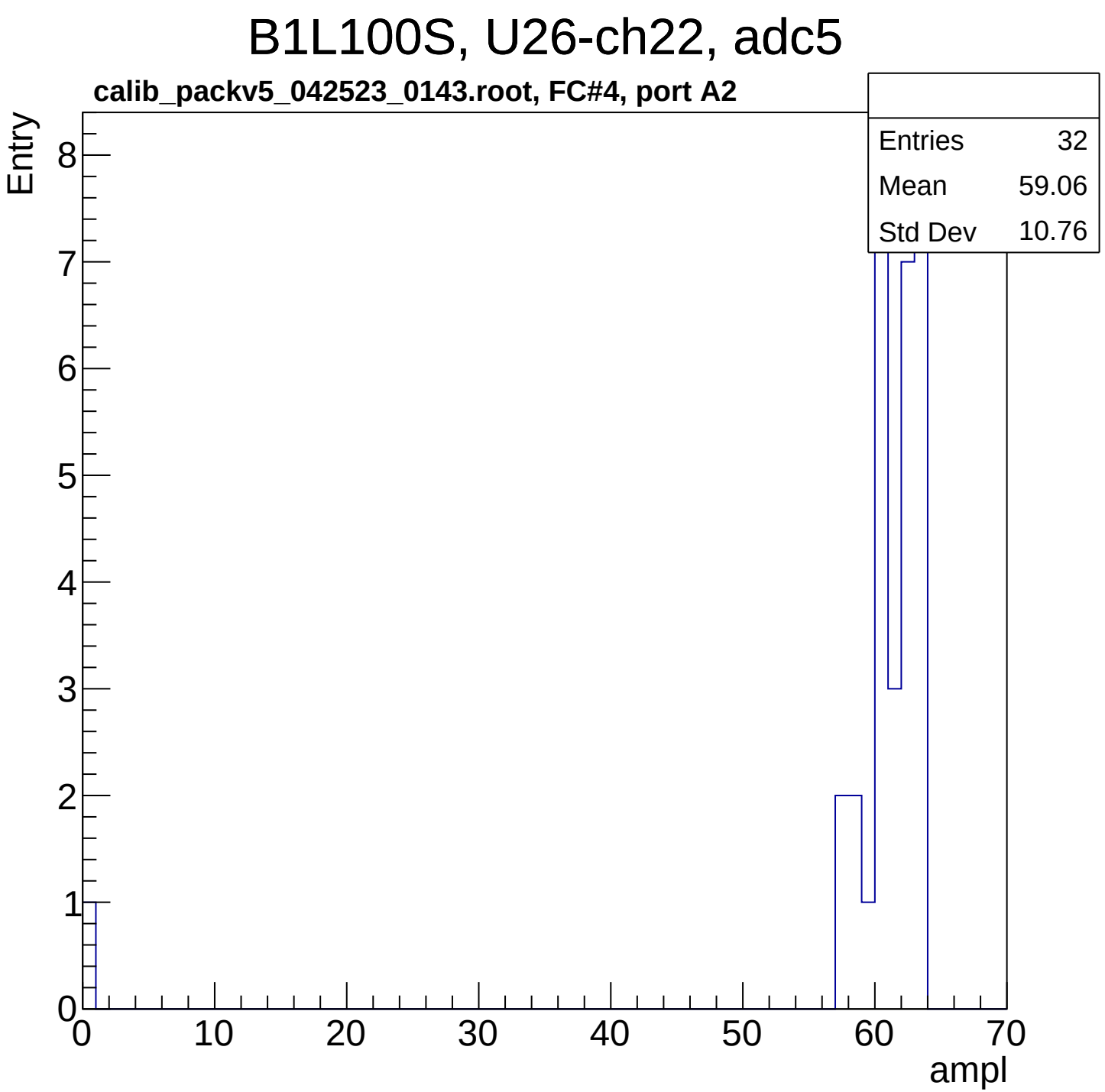
B1L100S, U26-ch22, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.06
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
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0
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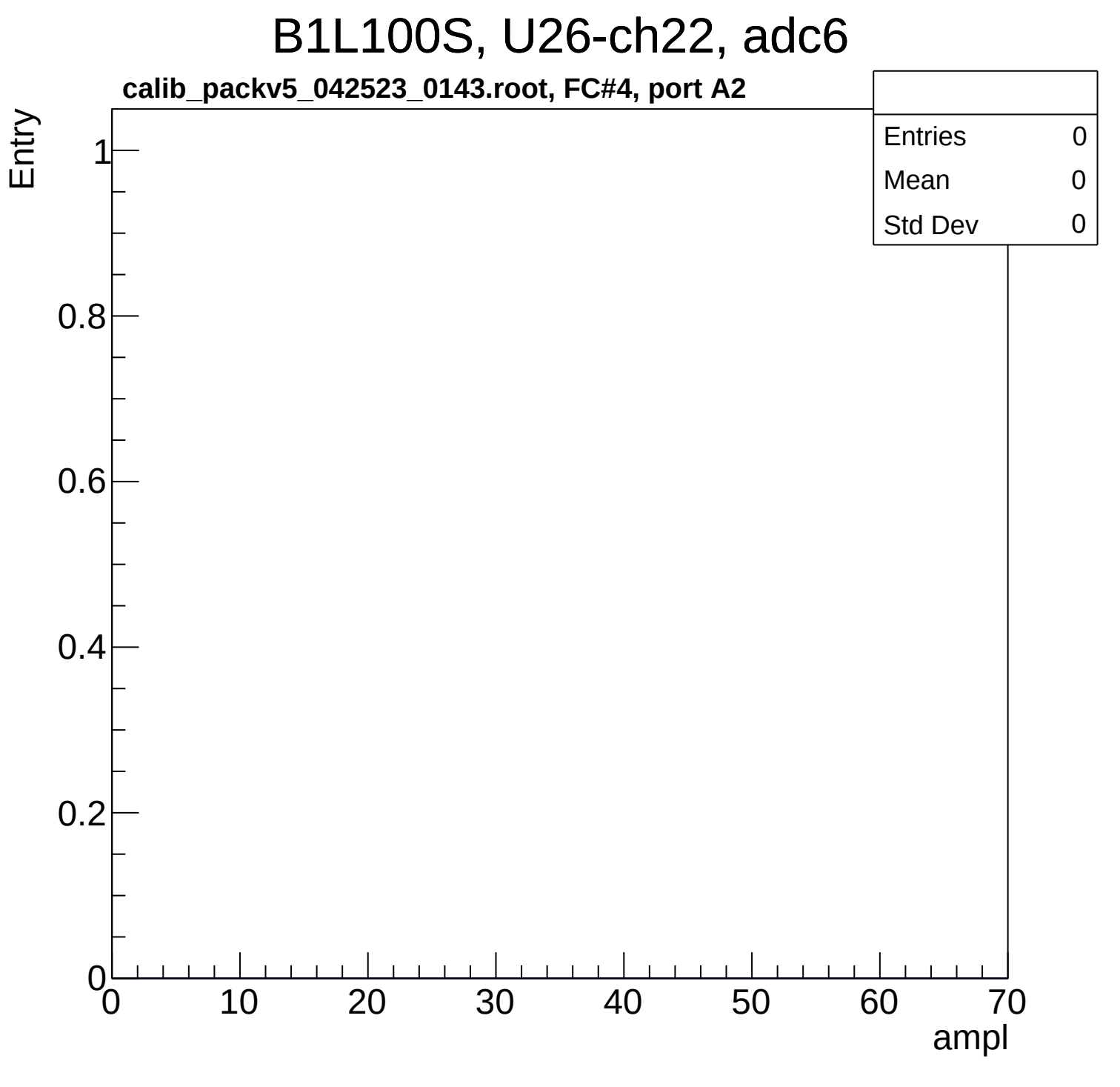
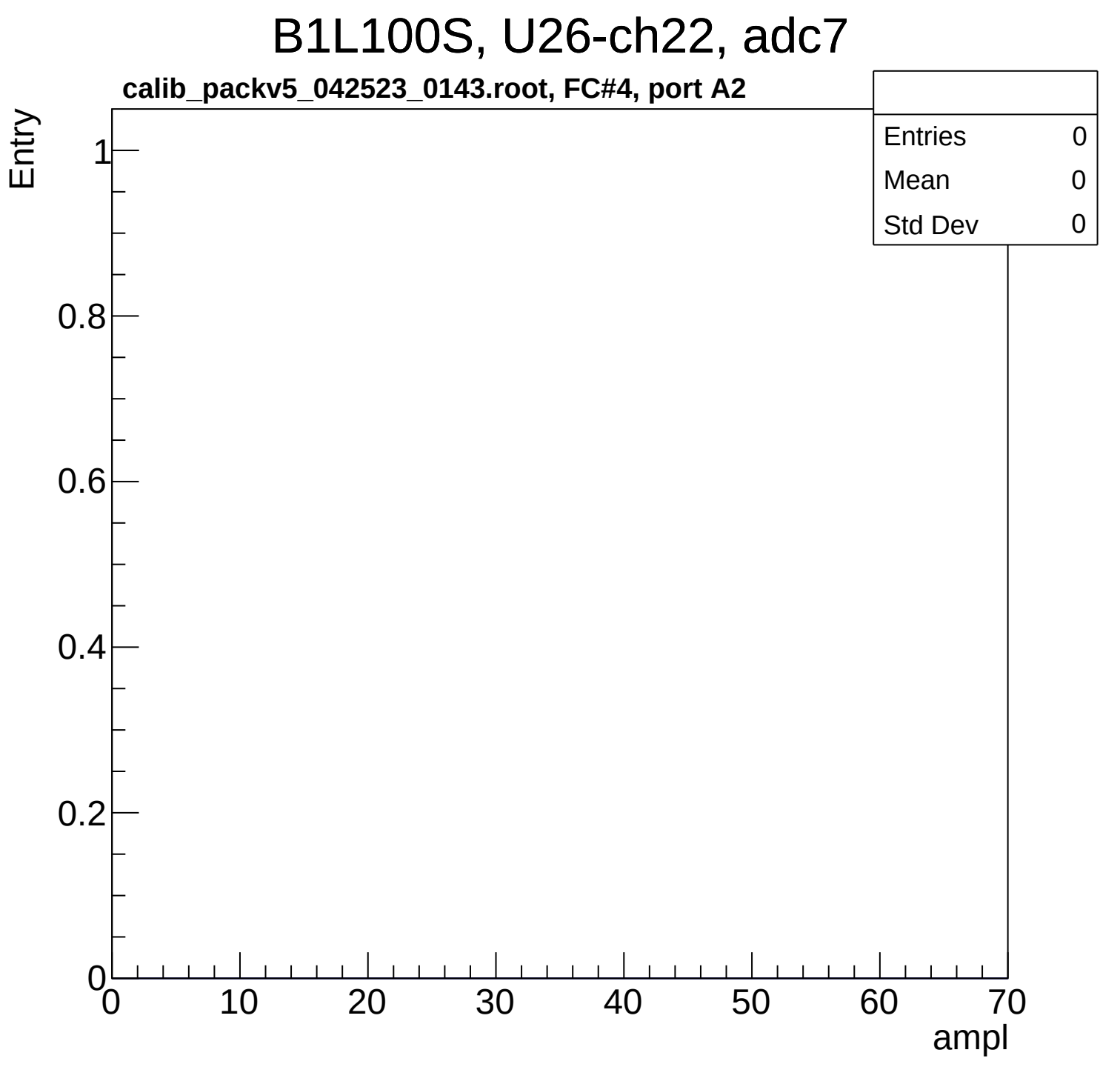

B1L100S, U26-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch22, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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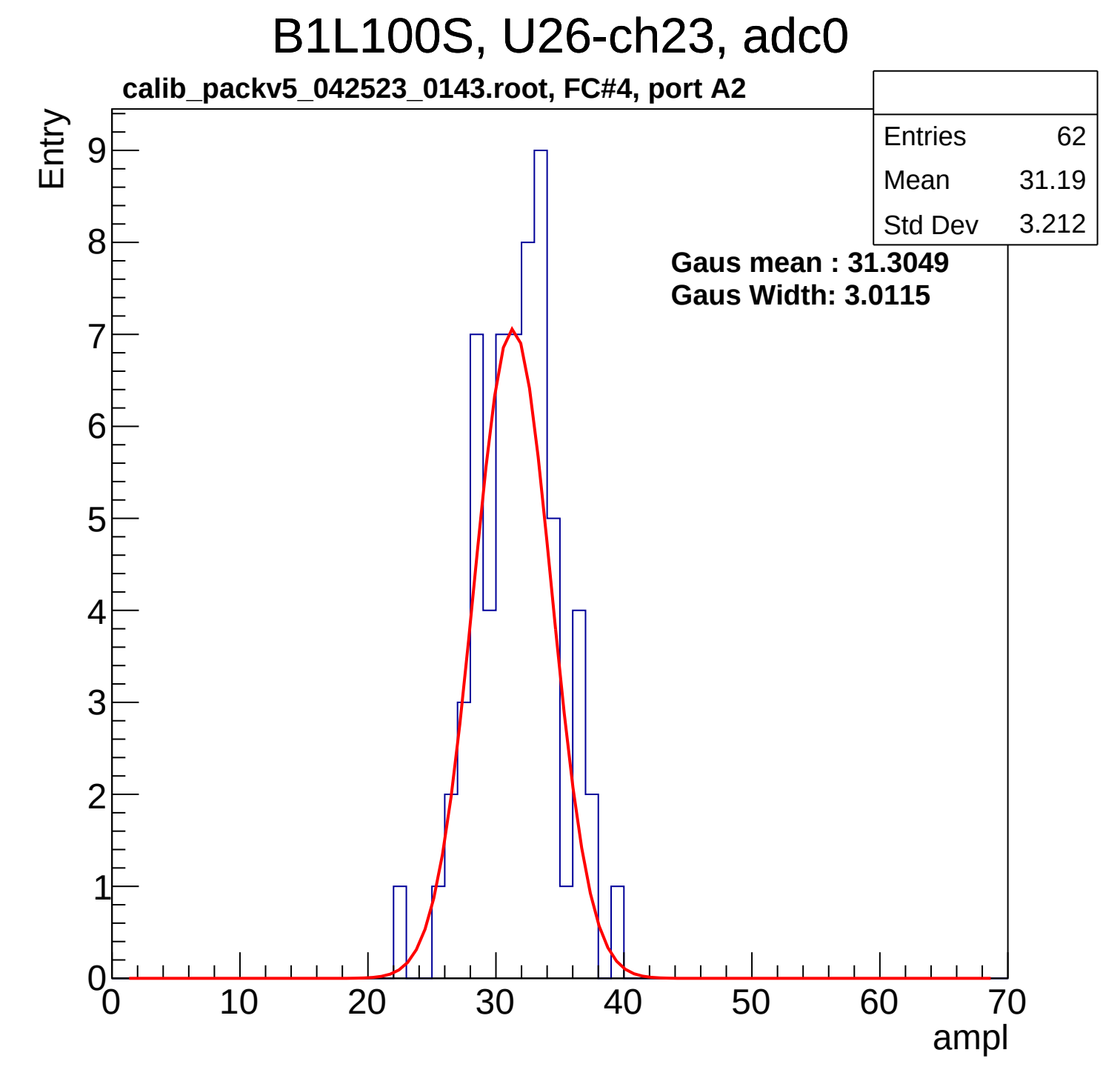
50
60
70
ampl


B1L100S, U26-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 31.19
Std Dev 3.212
Gaus mean : 31.3049
Gaus Width: 3.0115
Entry
ampl
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3
4
5
6
7
8
9
0
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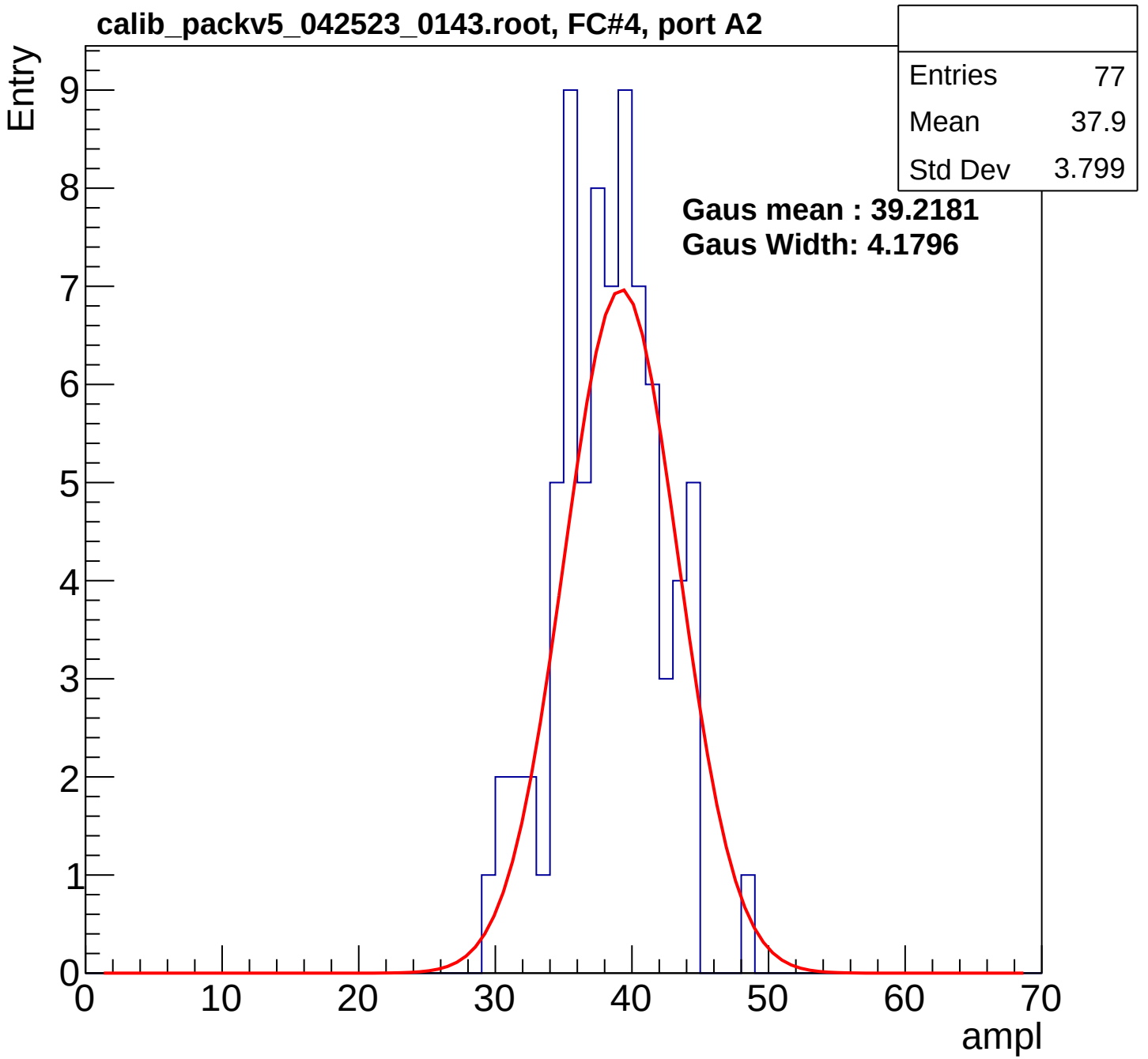
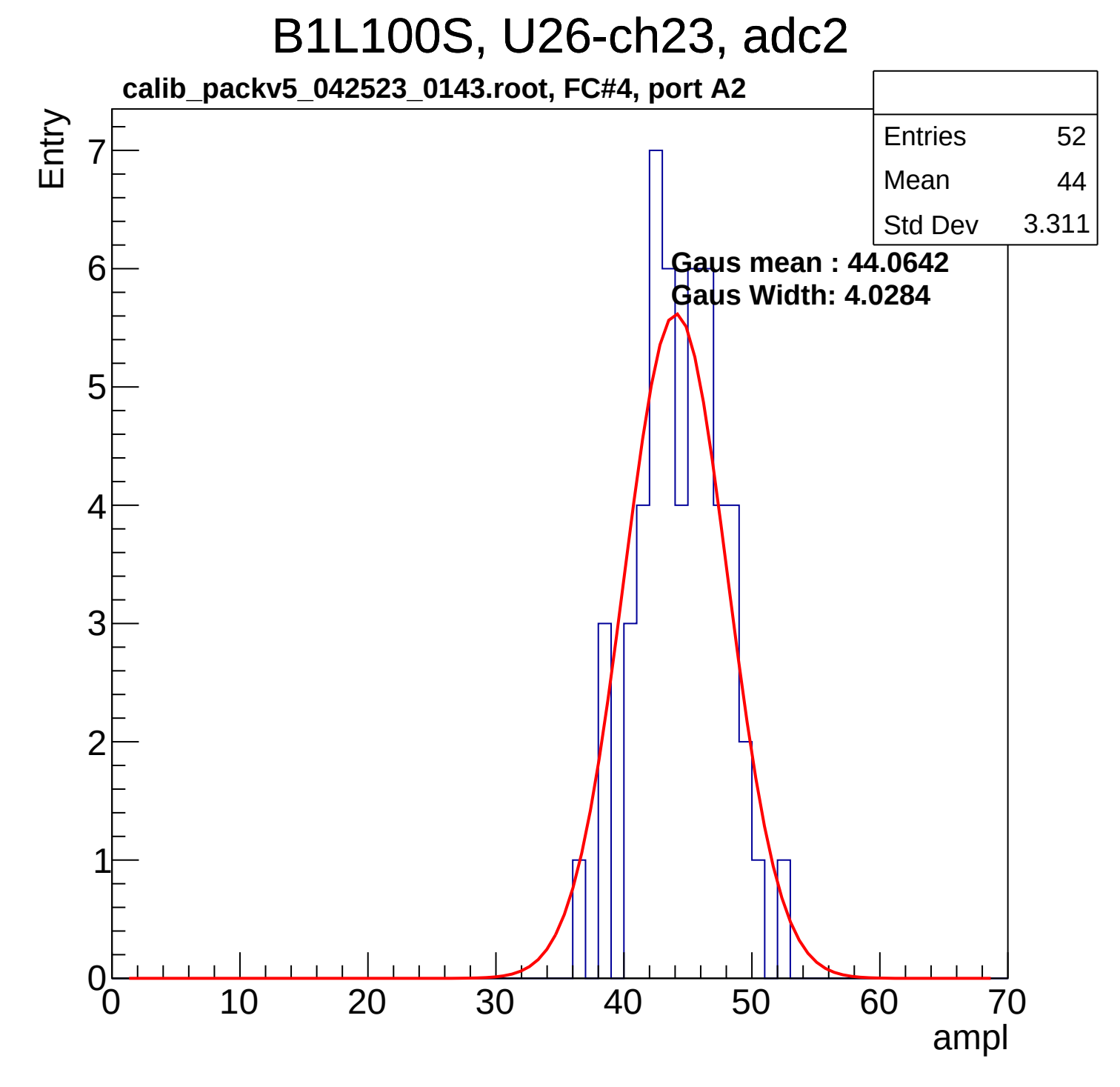

B1L100S, U26-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.9
Std Dev 3.799
Gaus mean : 39.2181
Gaus Width: 4.1796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 44
Std Dev 3.311
Gaus mean : 44.0642
Gaus Width: 4.0284
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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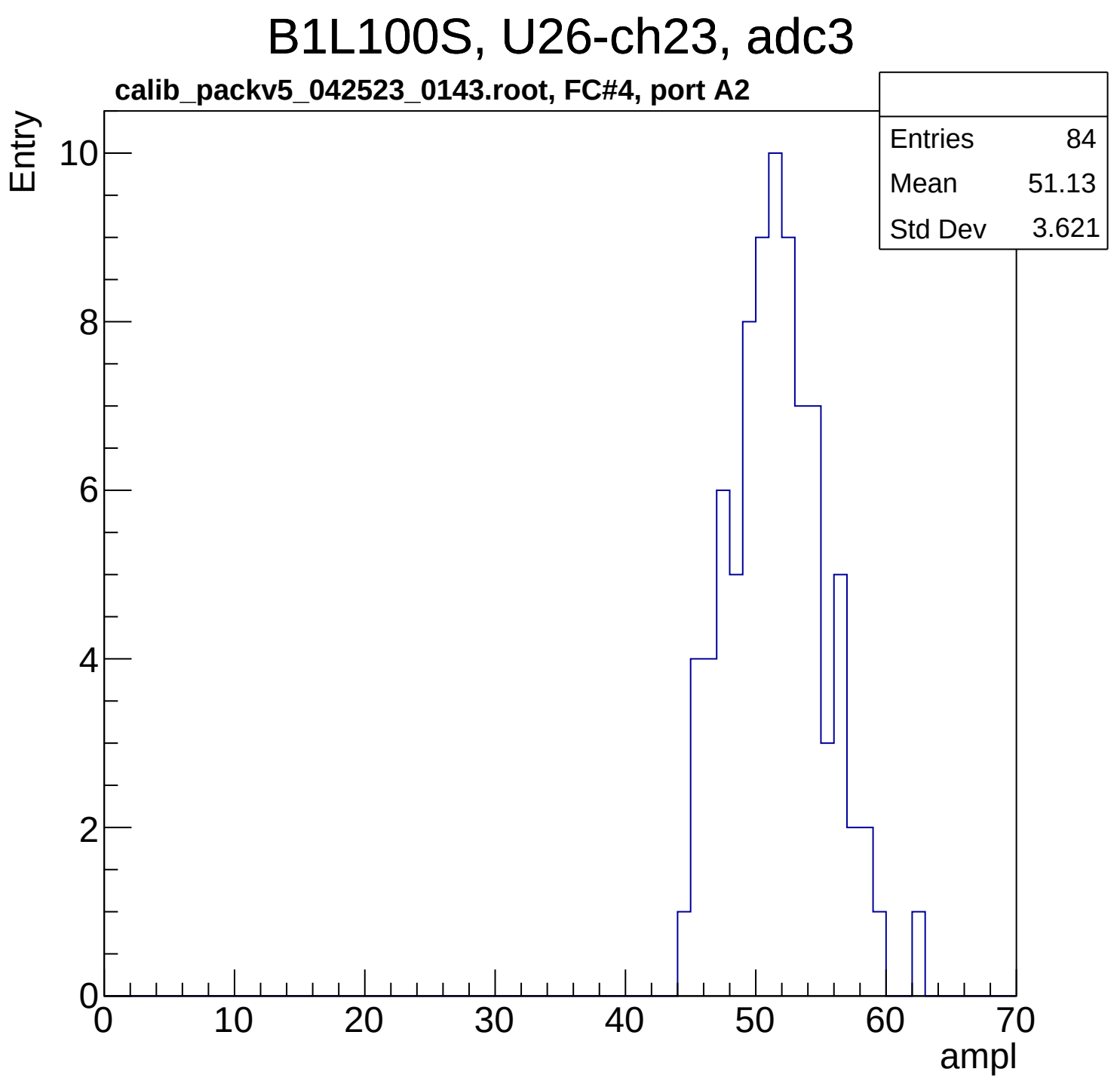
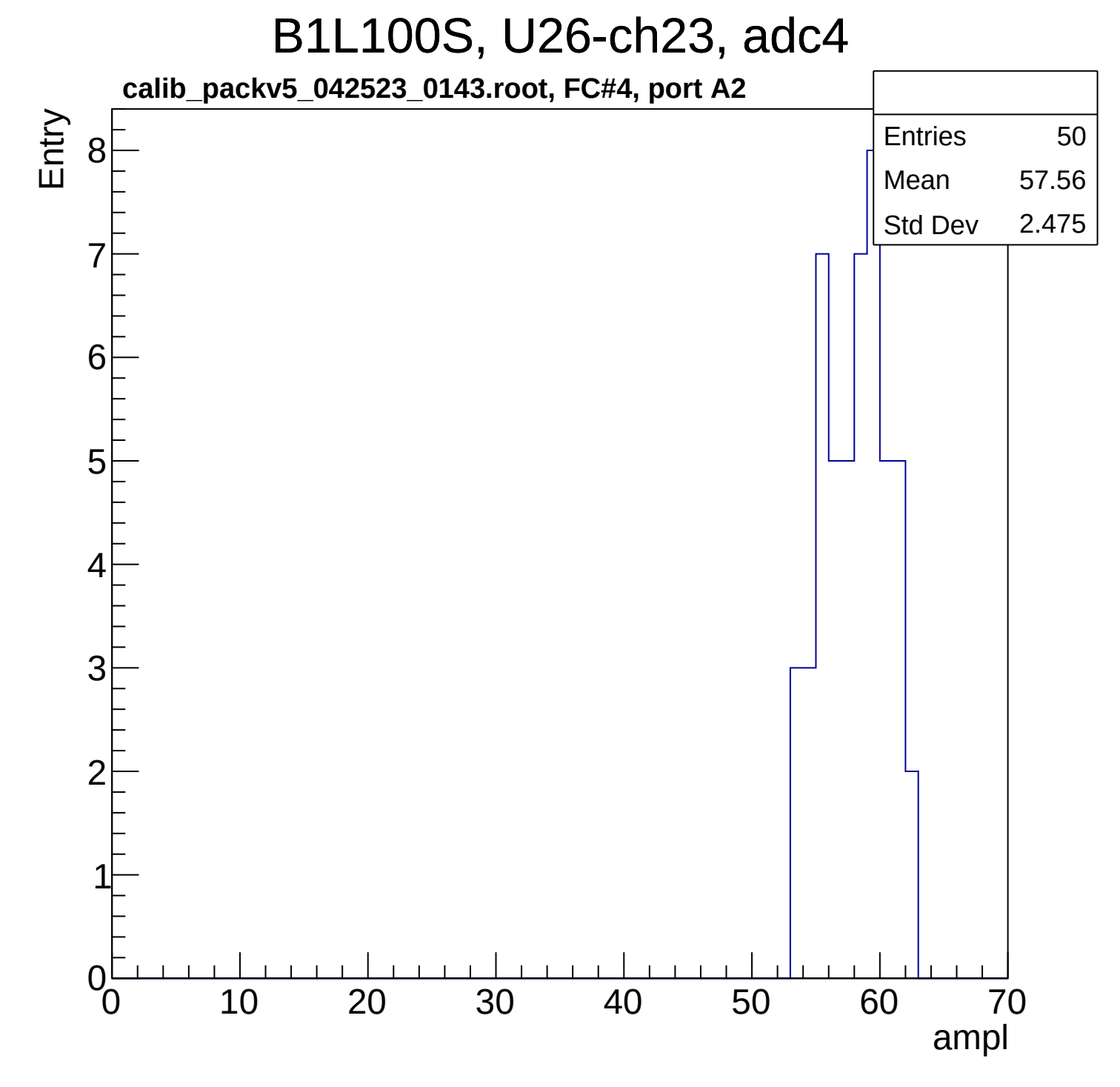
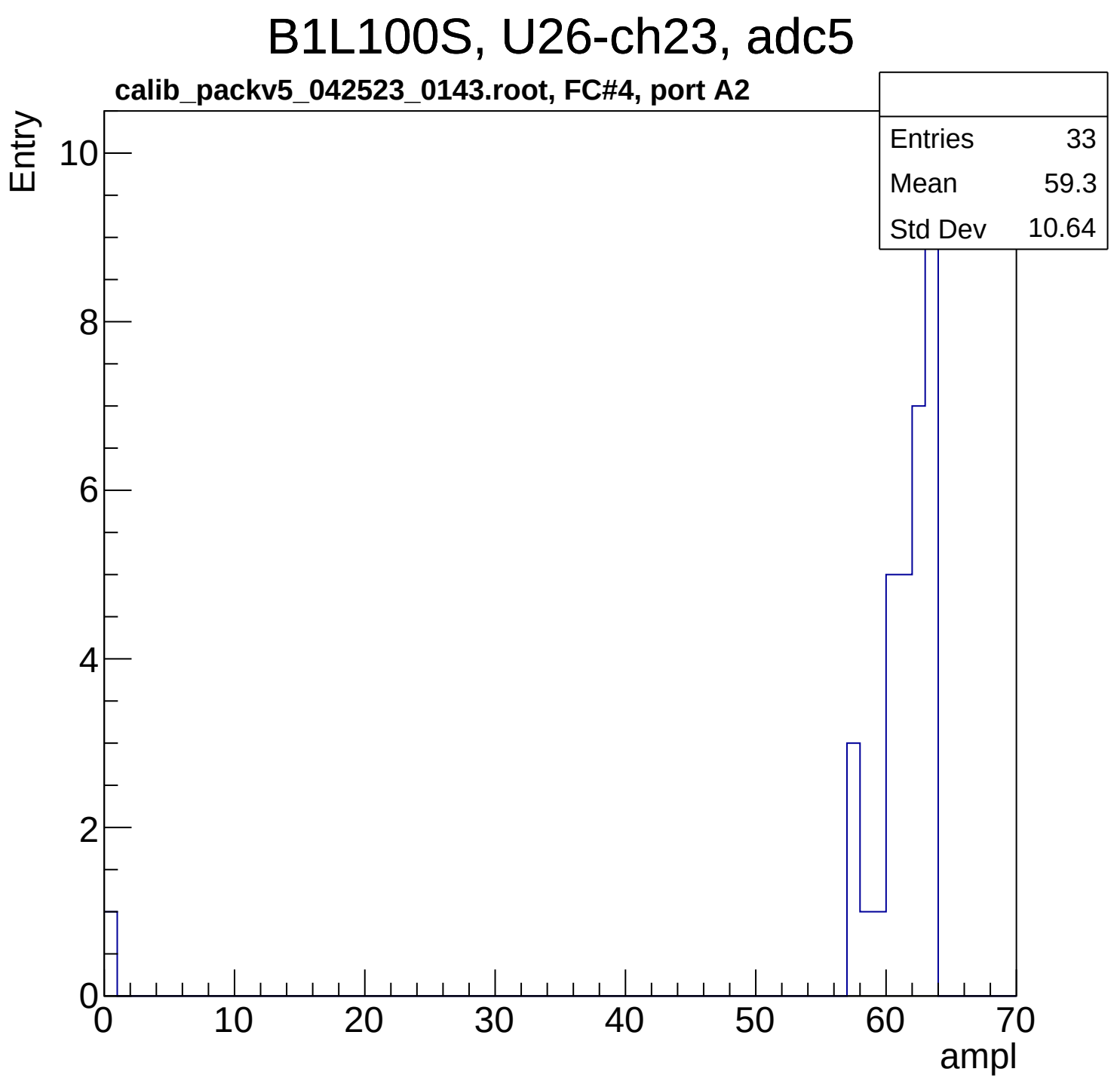
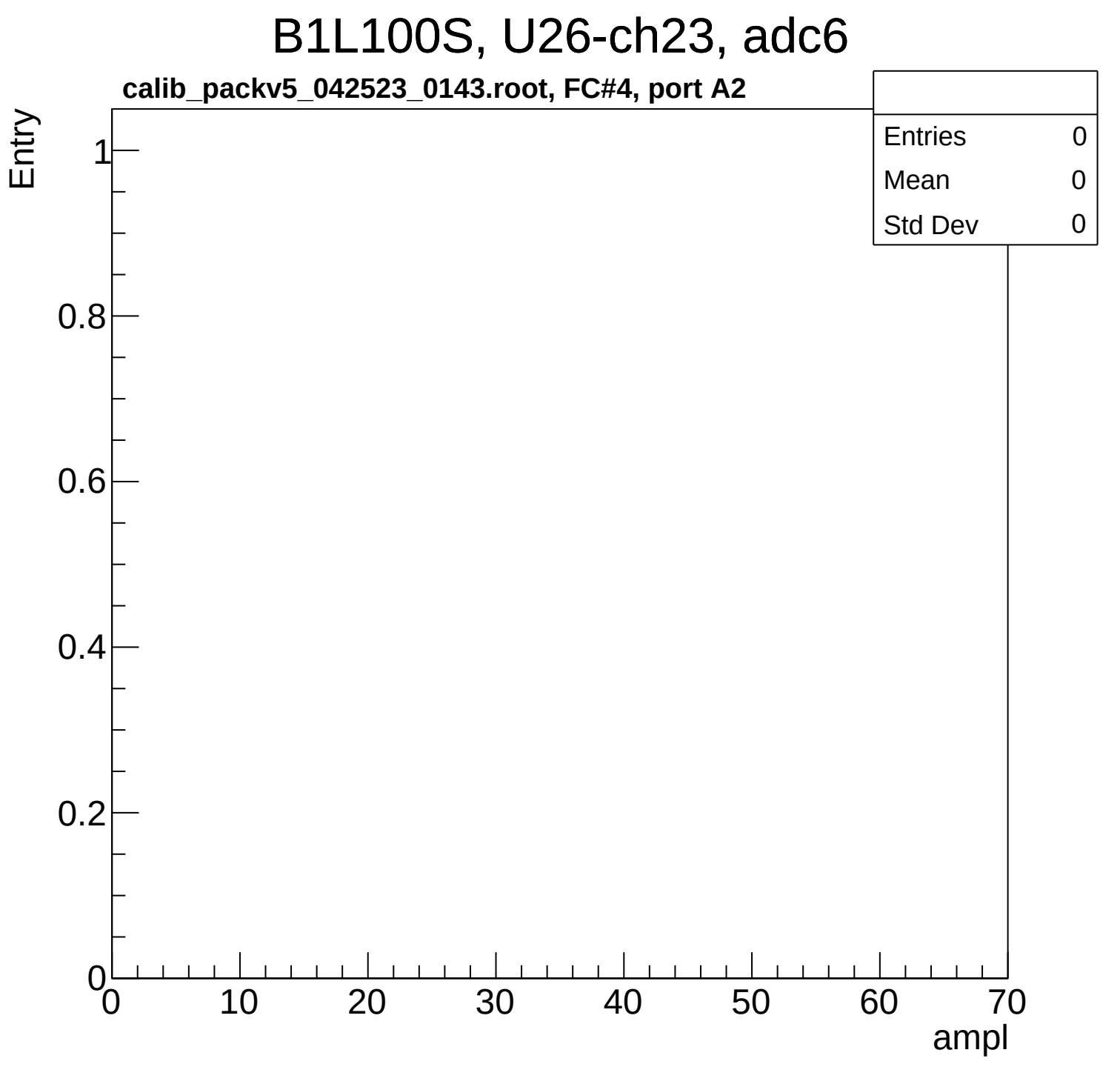
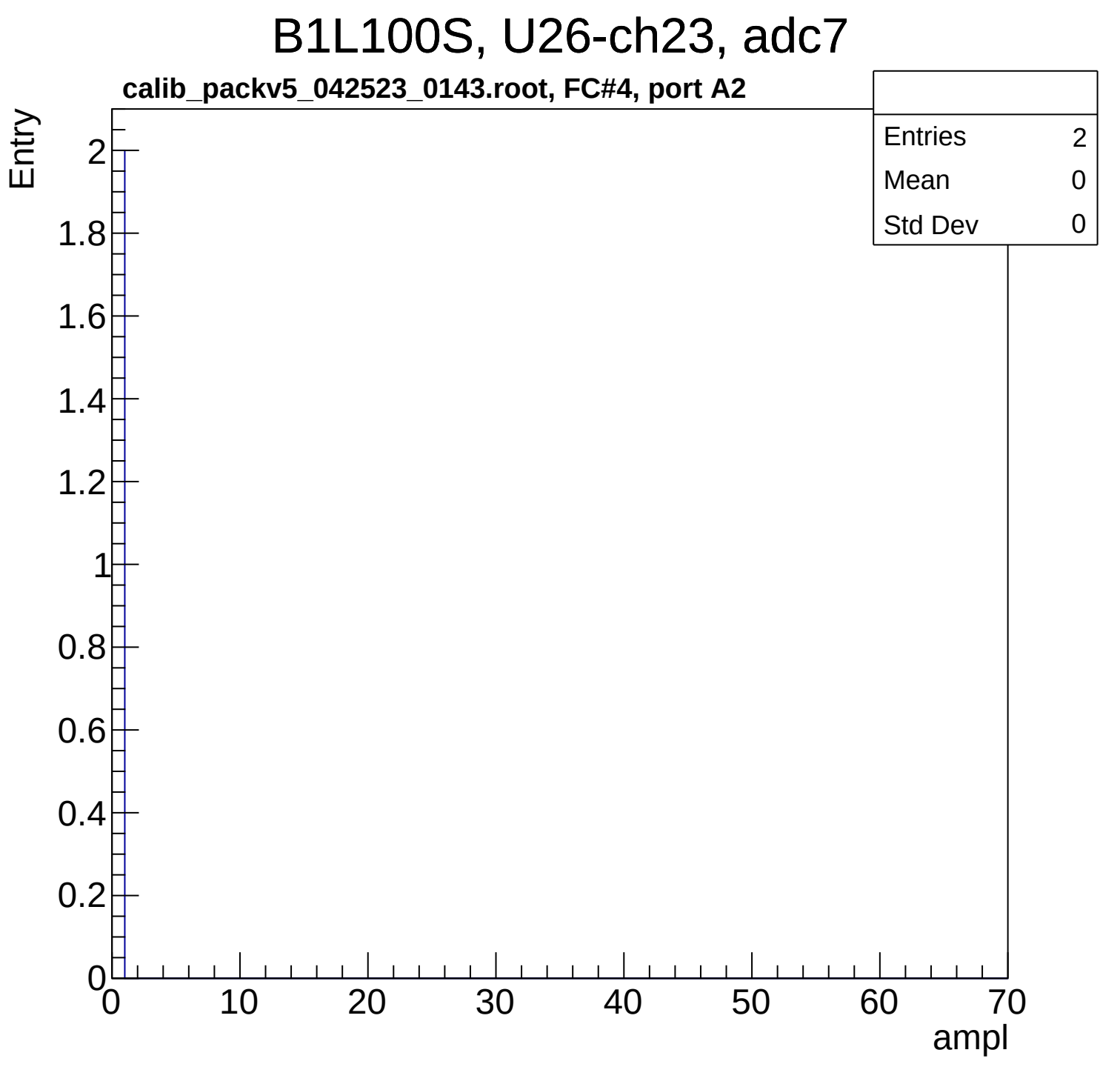
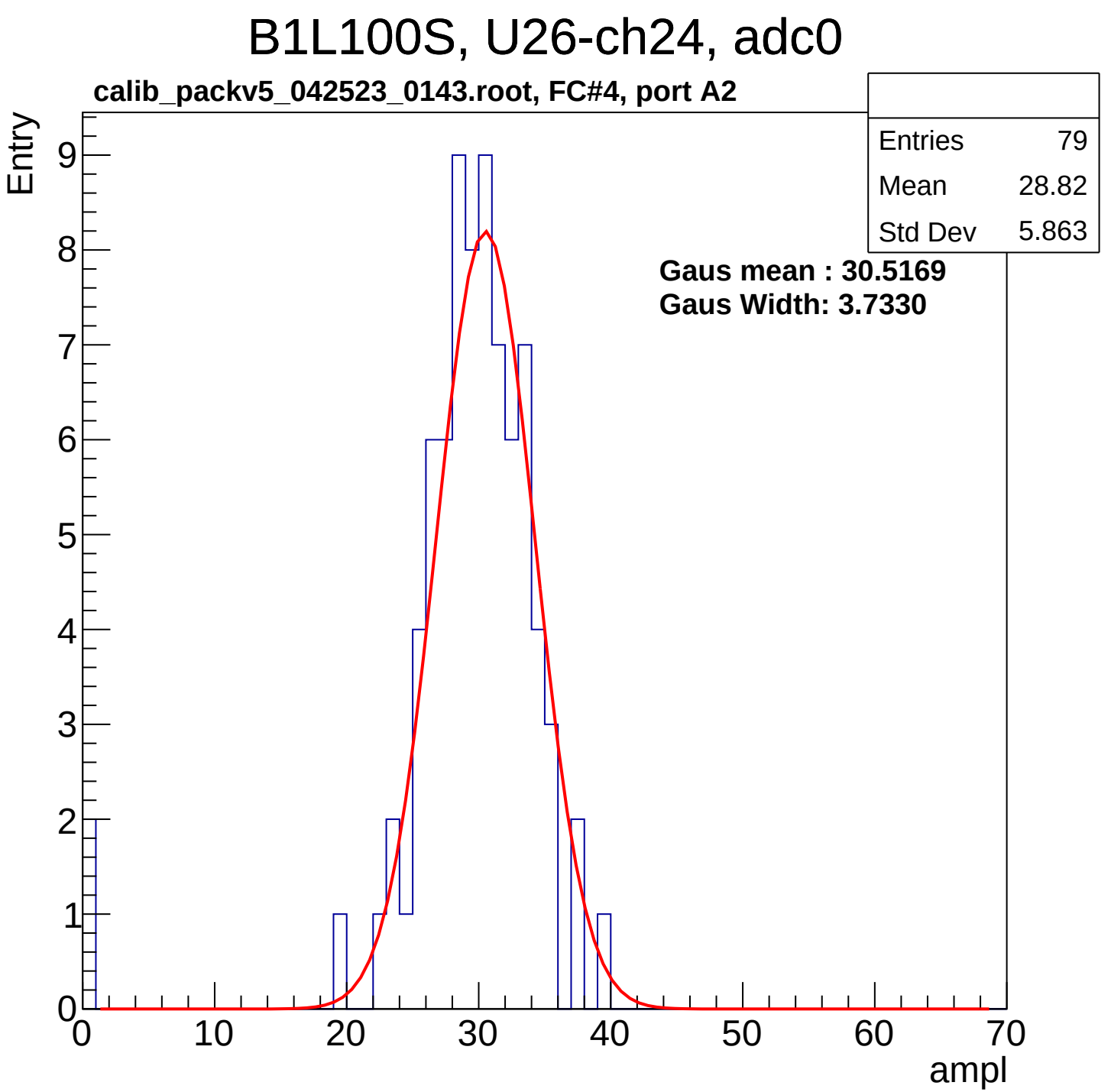

B1L100S, U26-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 51.13
Std Dev 3.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

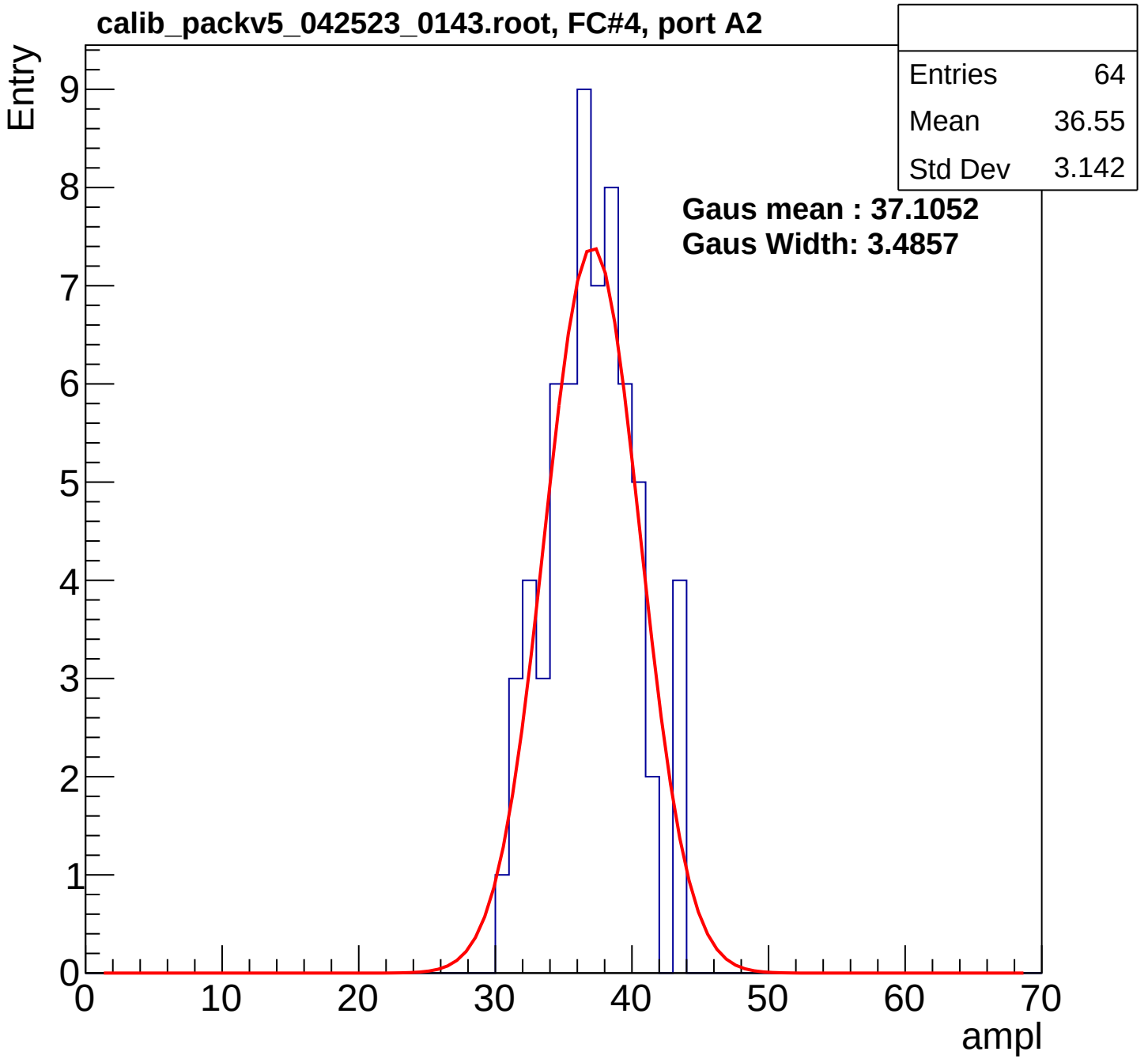

B1L100S, U26-ch23, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.56
Std Dev 2.475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

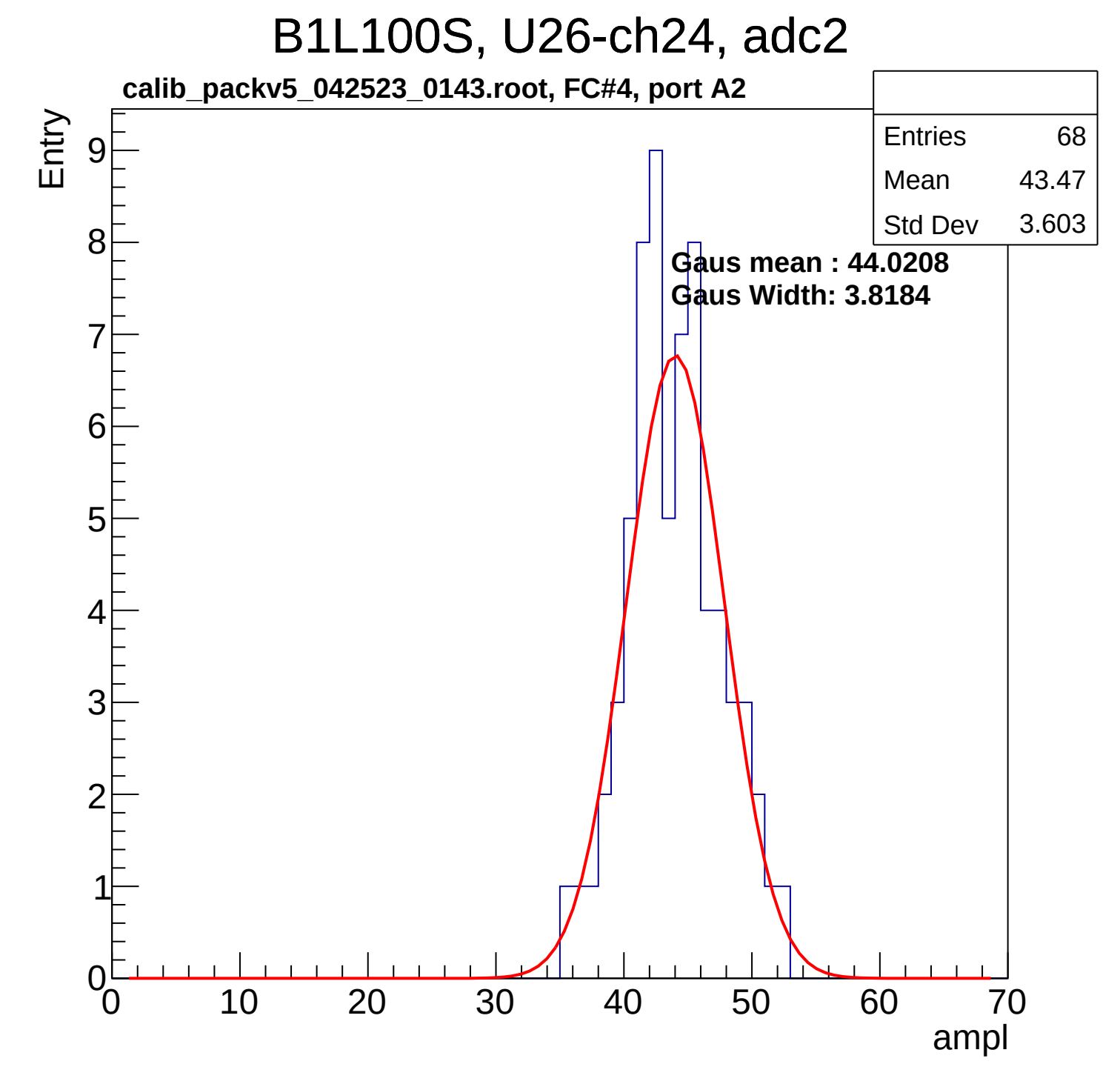

B1L100S, U26-ch23, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.3
Std Dev 10.64
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

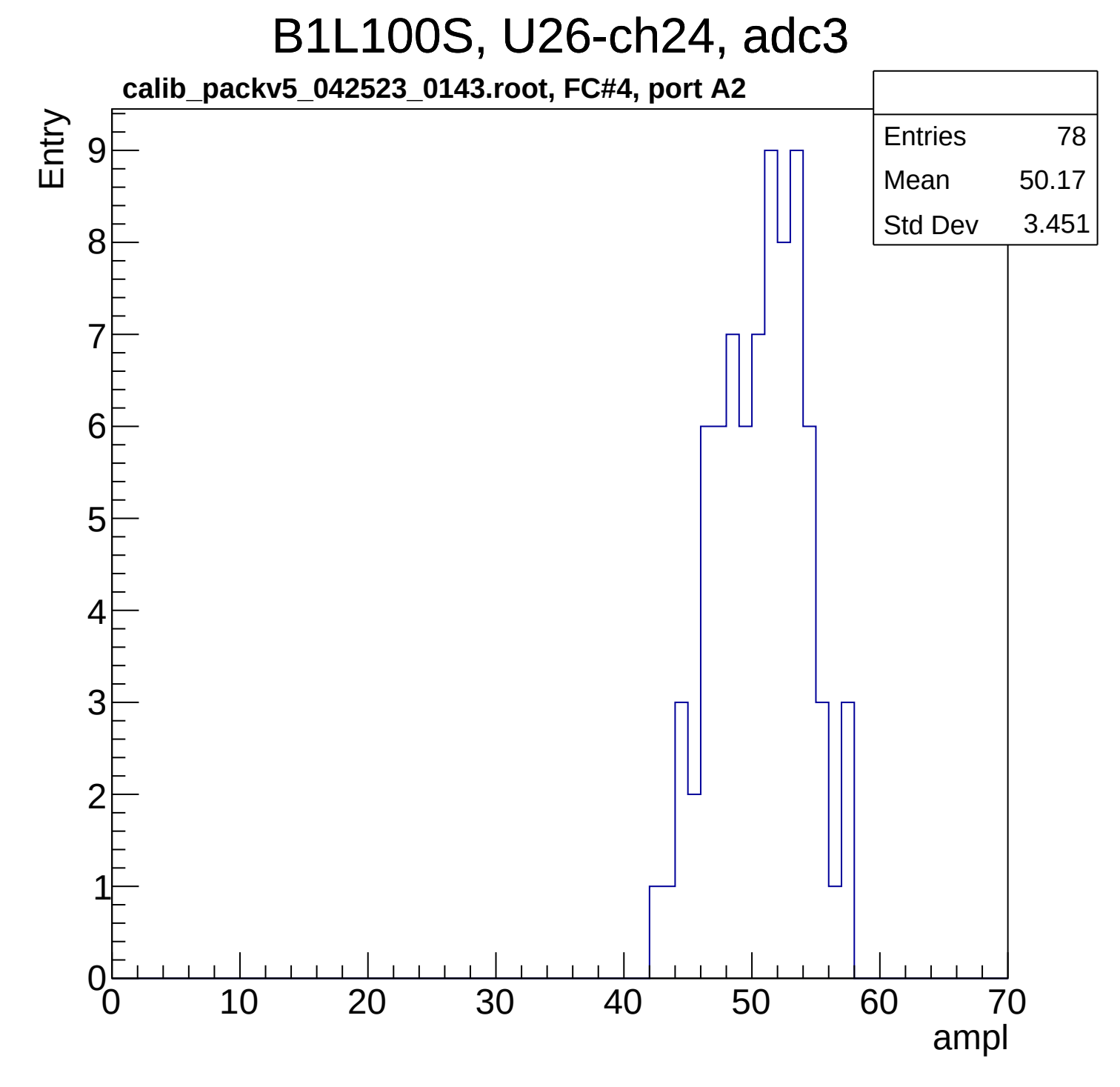

B1L100S, U26-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

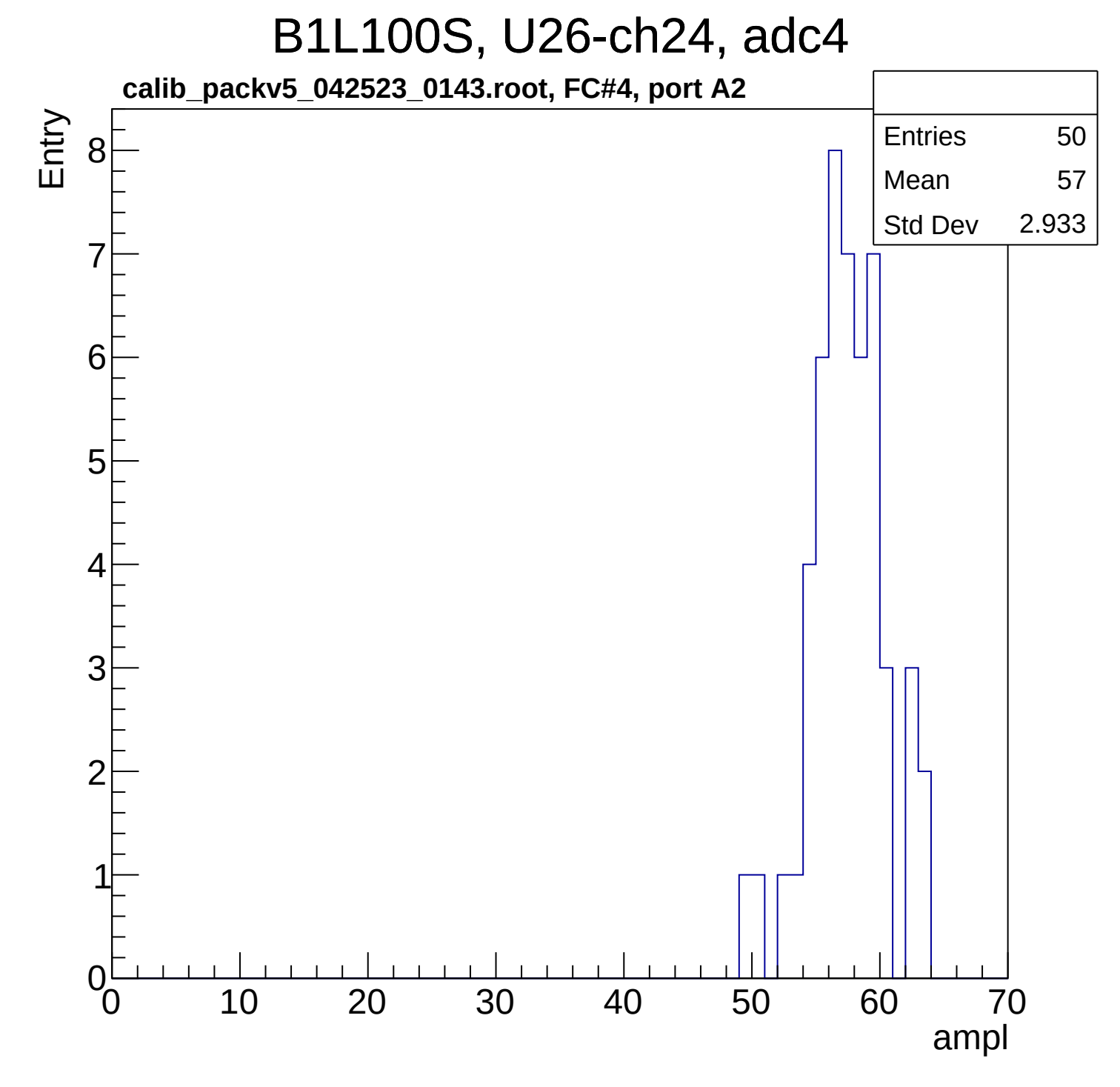

B1L100S, U26-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

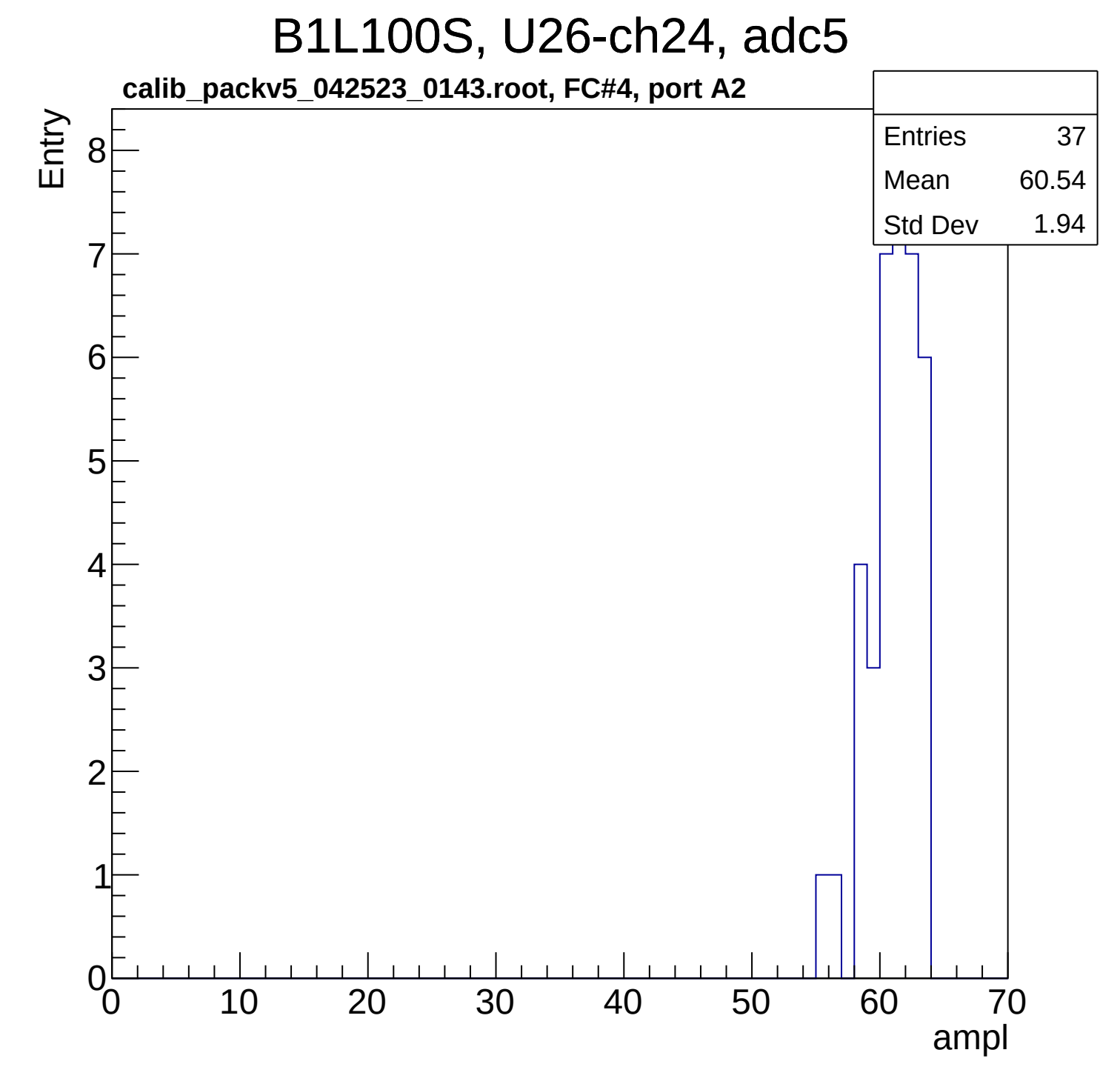

B1L100S, U26-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.82
Std Dev 5.863
Gaus mean : 30.5169
Gaus Width: 3.7330
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

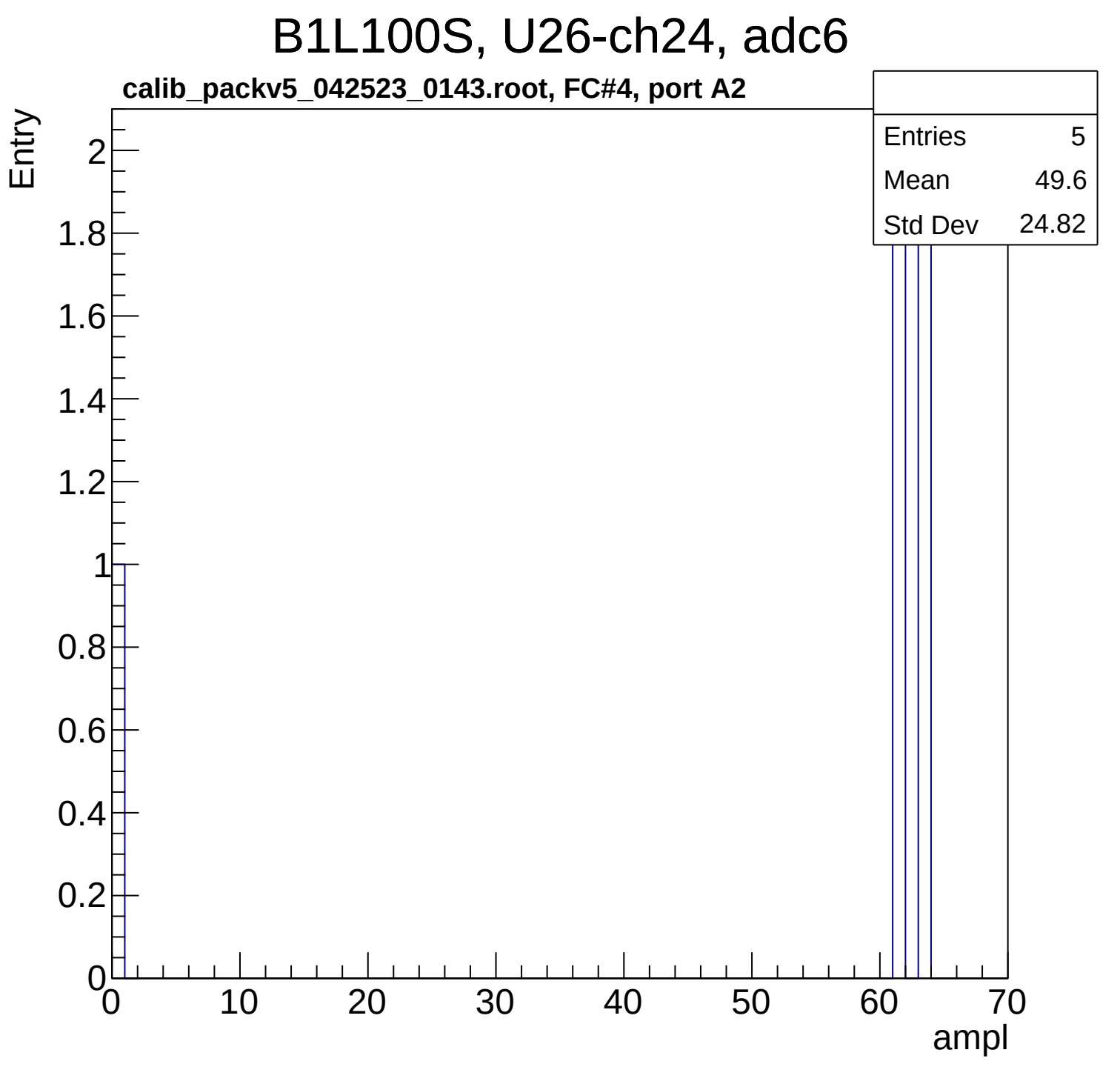

B1L100S, U26-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.55
Std Dev 3.142
Gaus mean : 37.1052
Gaus Width: 3.4857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

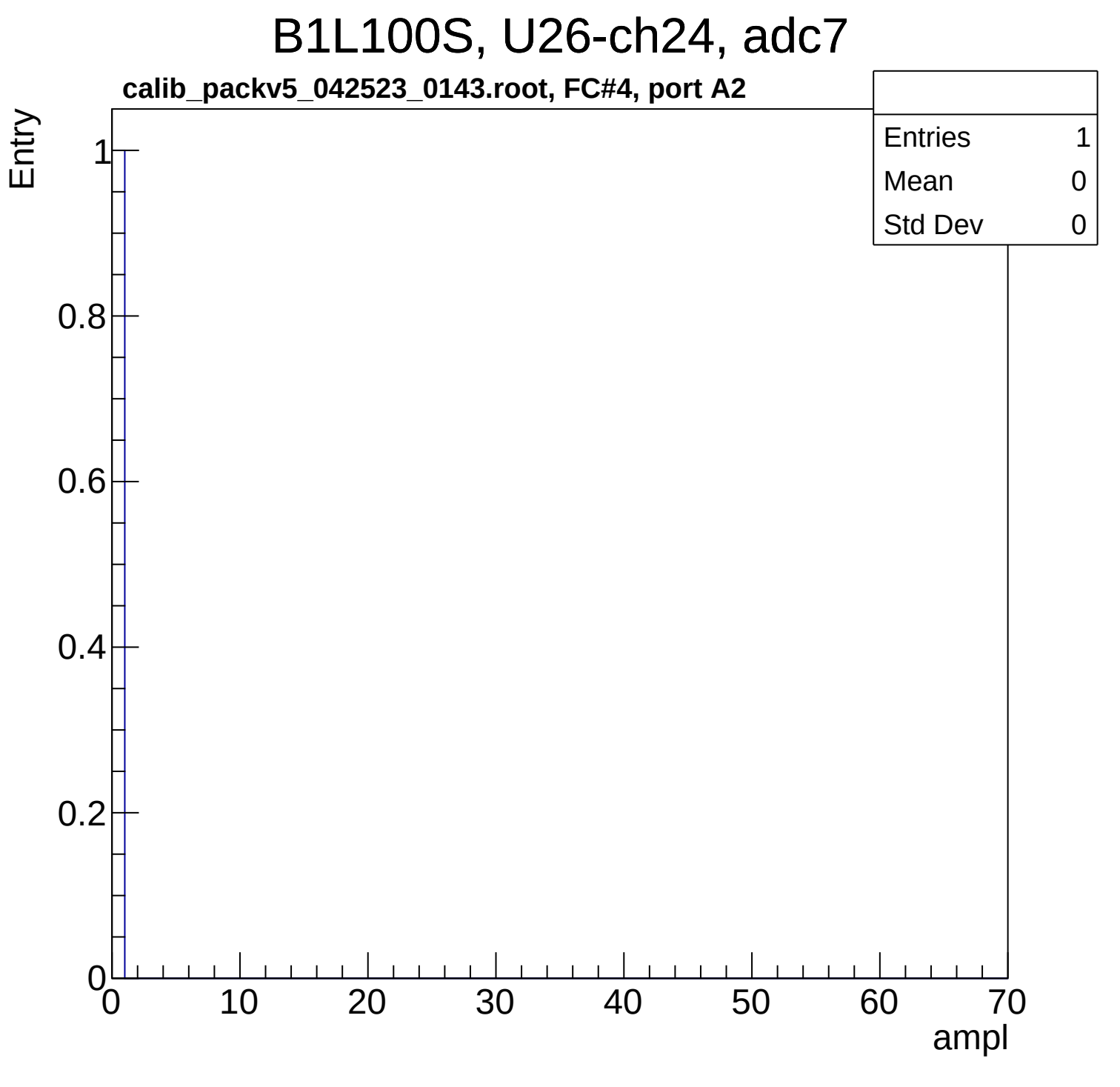

B1L100S, U26-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.47
Std Dev 3.603
Gaus mean : 44.0208
Gaus Width: 3.8184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

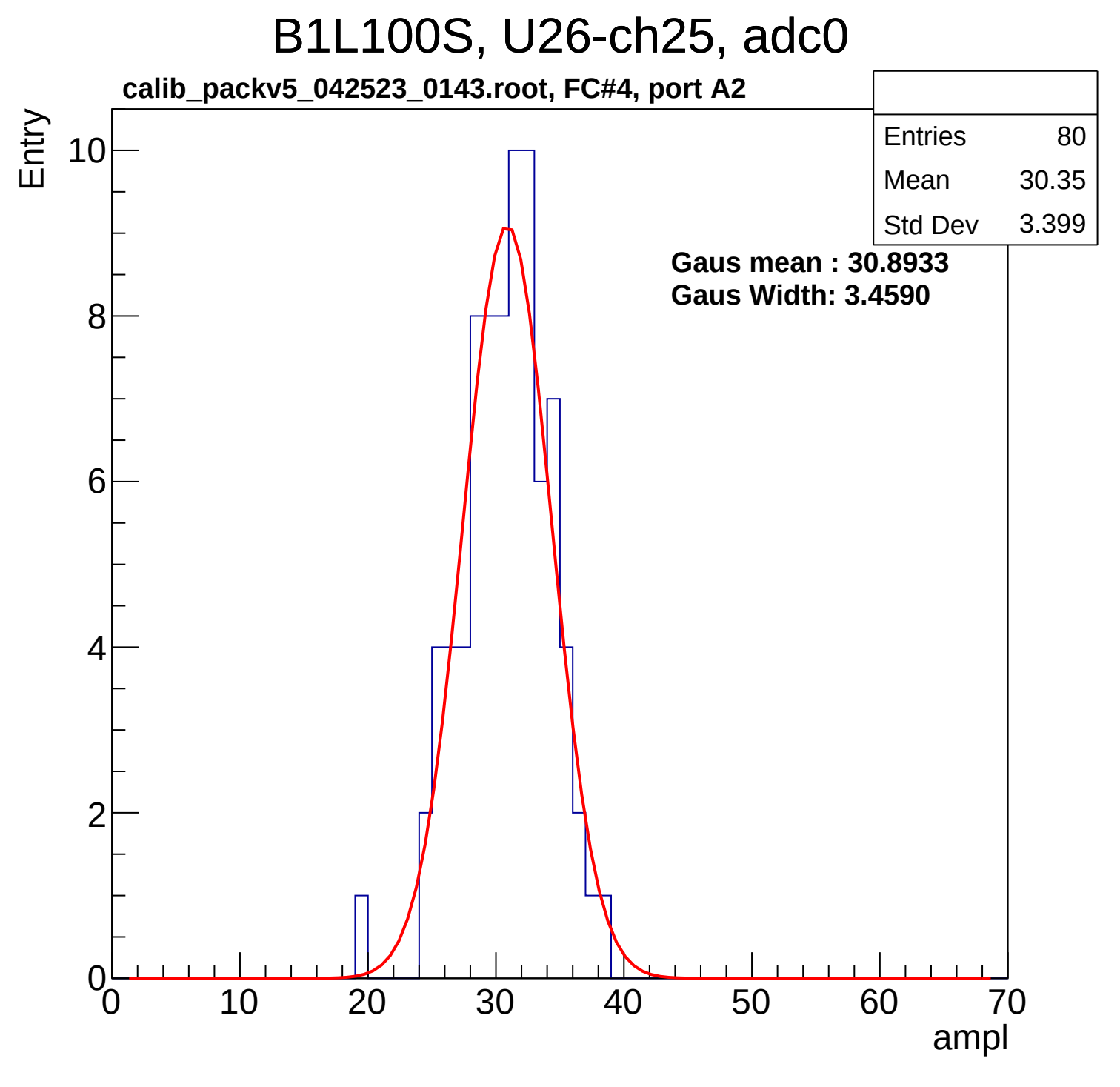

B1L100S, U26-ch24, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 50.17
Std Dev 3.451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

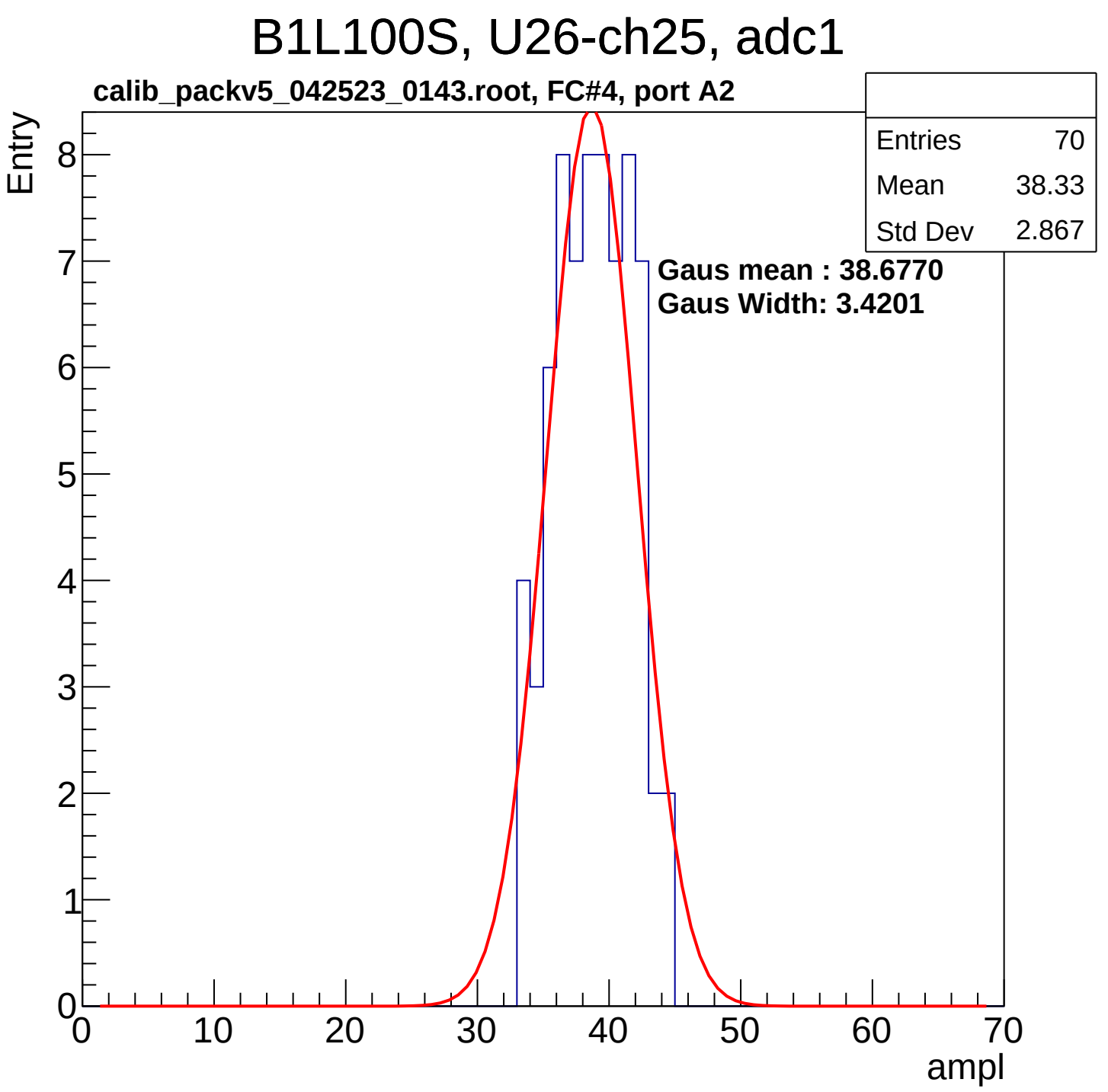

B1L100S, U26-ch24, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57
Std Dev 2.933
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

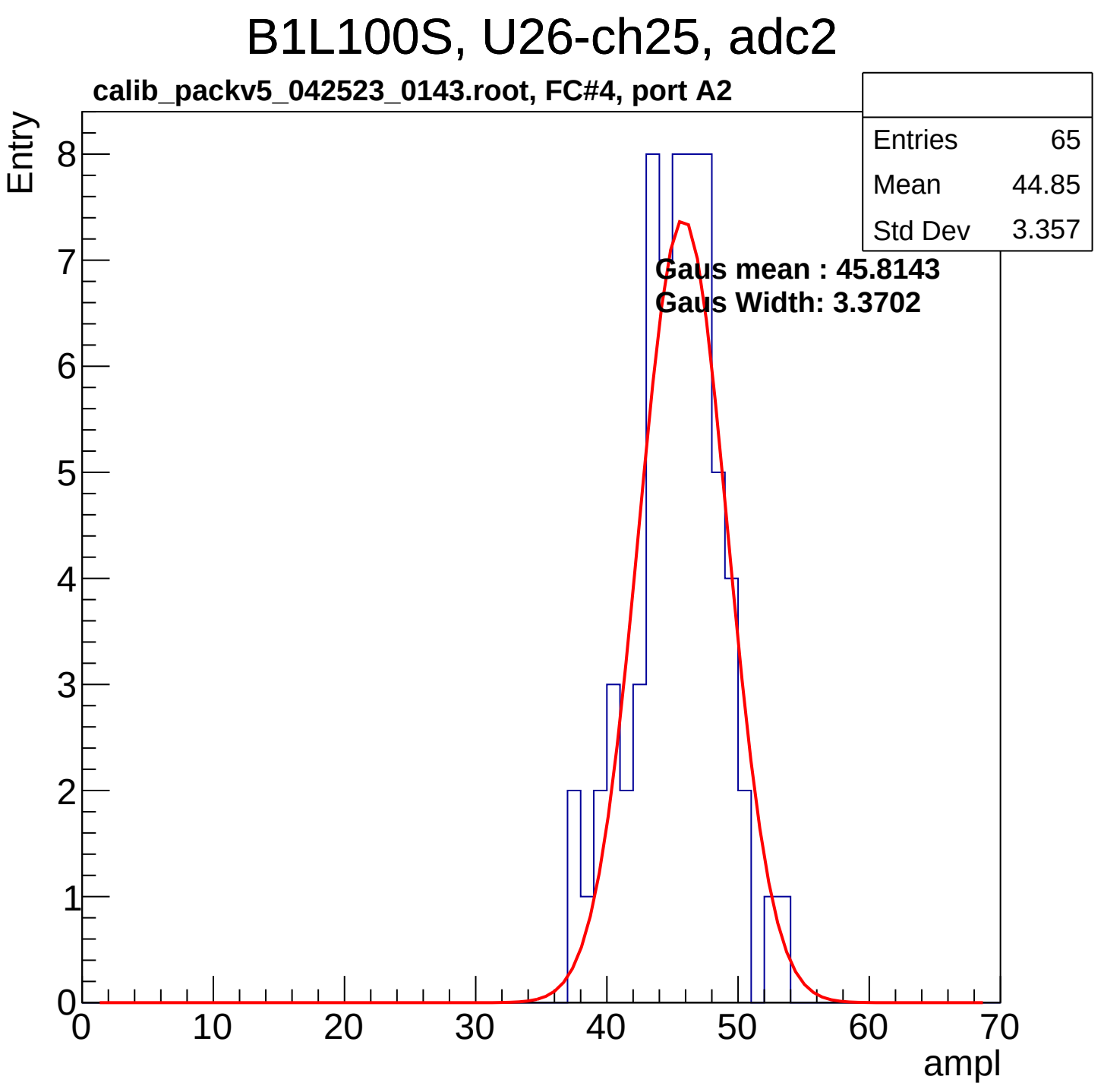

B1L100S, U26-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.54
Std Dev 1.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

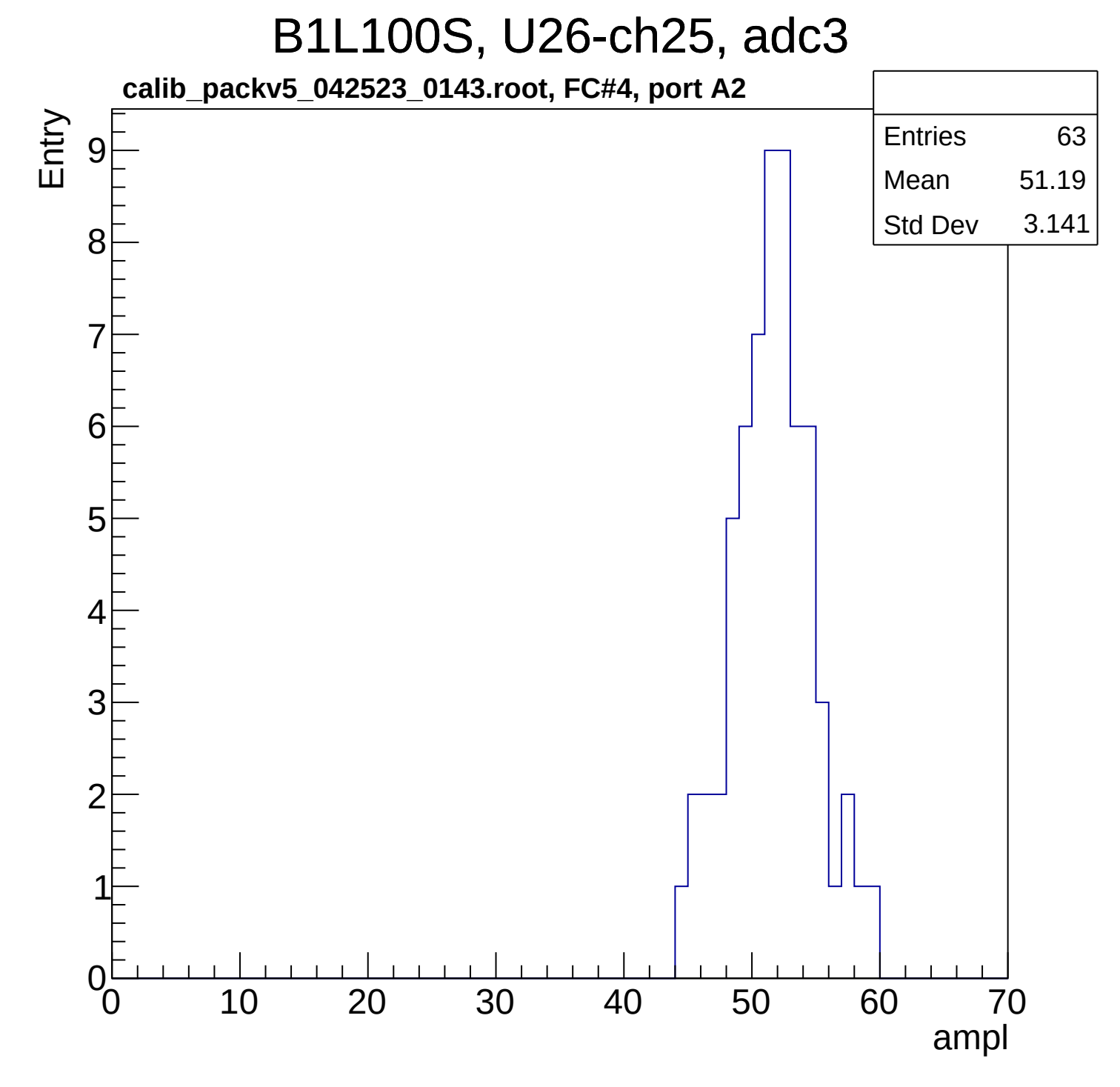

B1L100S, U26-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

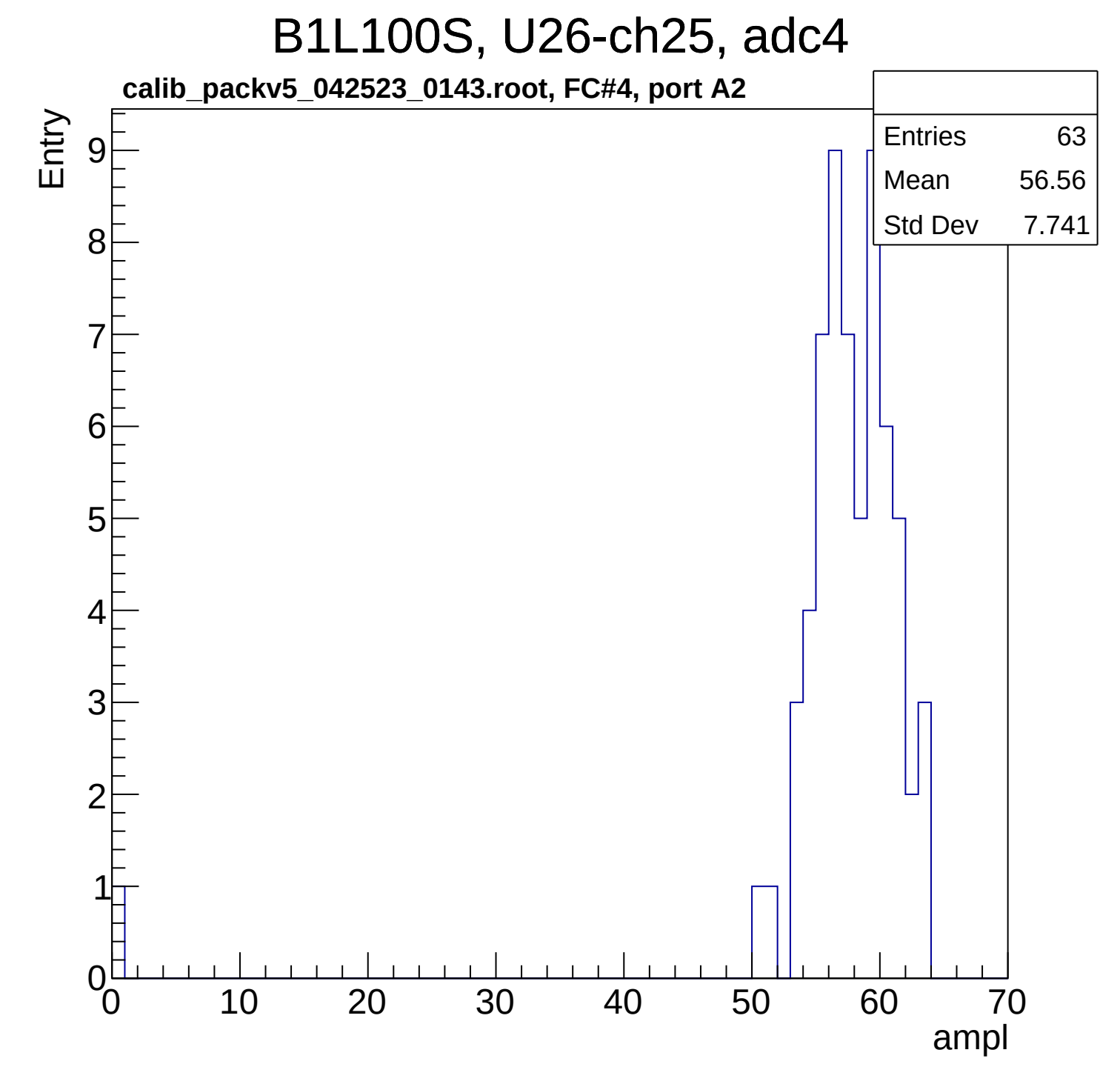

B1L100S, U26-ch24, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 30.35
Std Dev 3.399
Gaus mean : 30.8933
Gaus Width: 3.4590
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 38.33
Std Dev 2.867
Gaus mean : 38.6770
Gaus Width: 3.4201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.85
Std Dev 3.357
Gaus mean : 45.8143
Gaus Width: 3.3702
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch25, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.19
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.56
Std Dev 7.741
Entry
ampl
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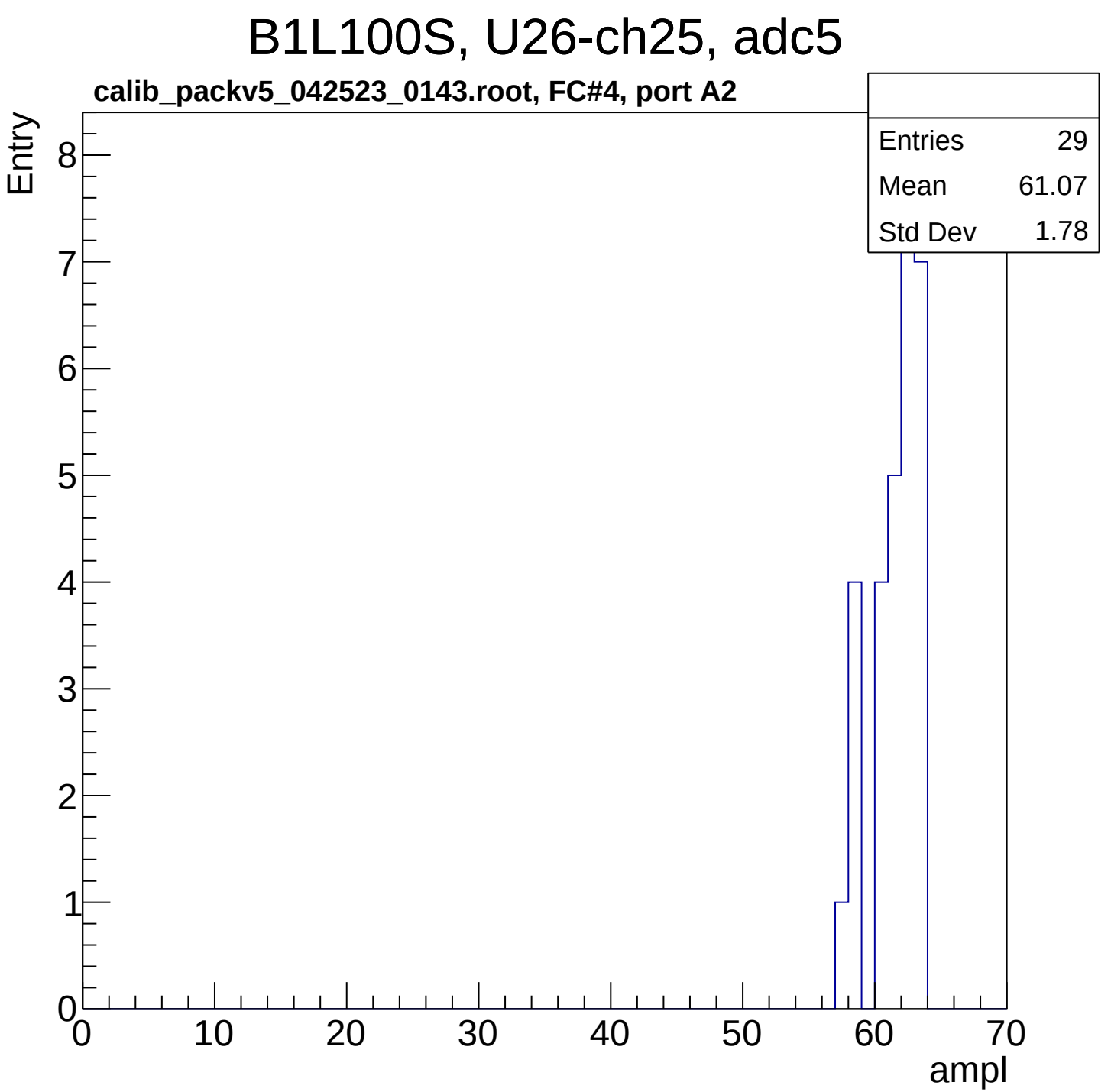
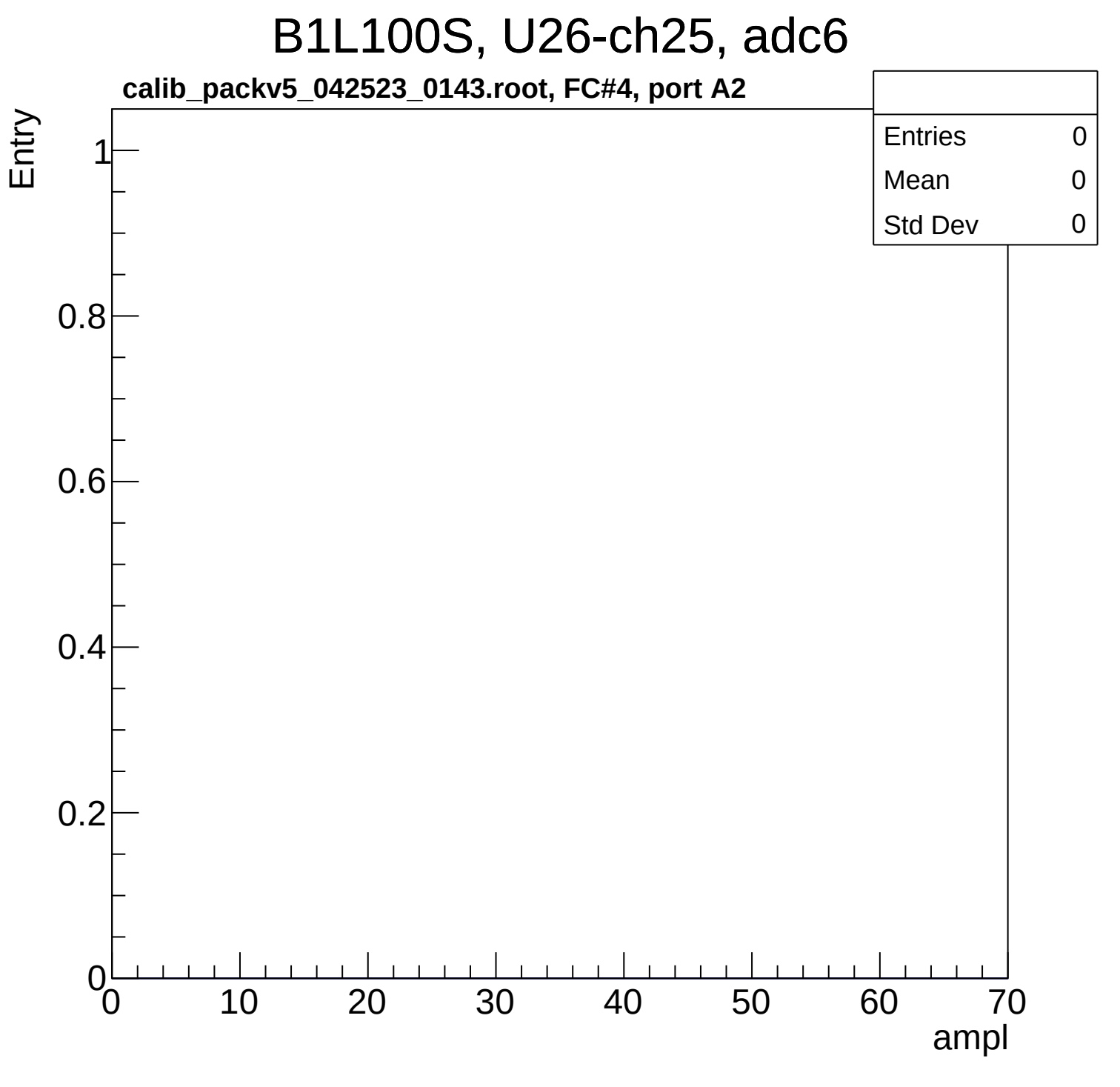
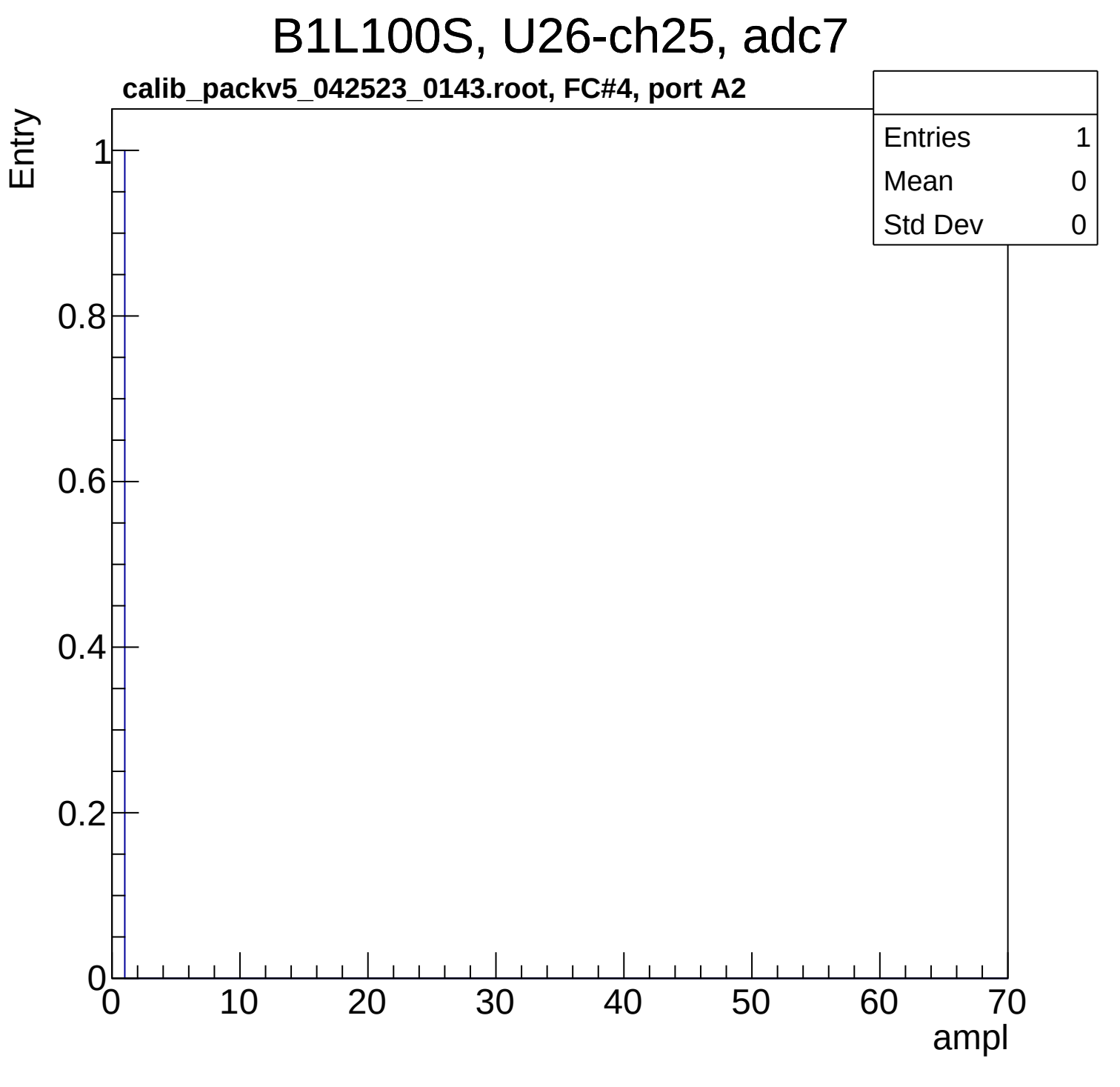
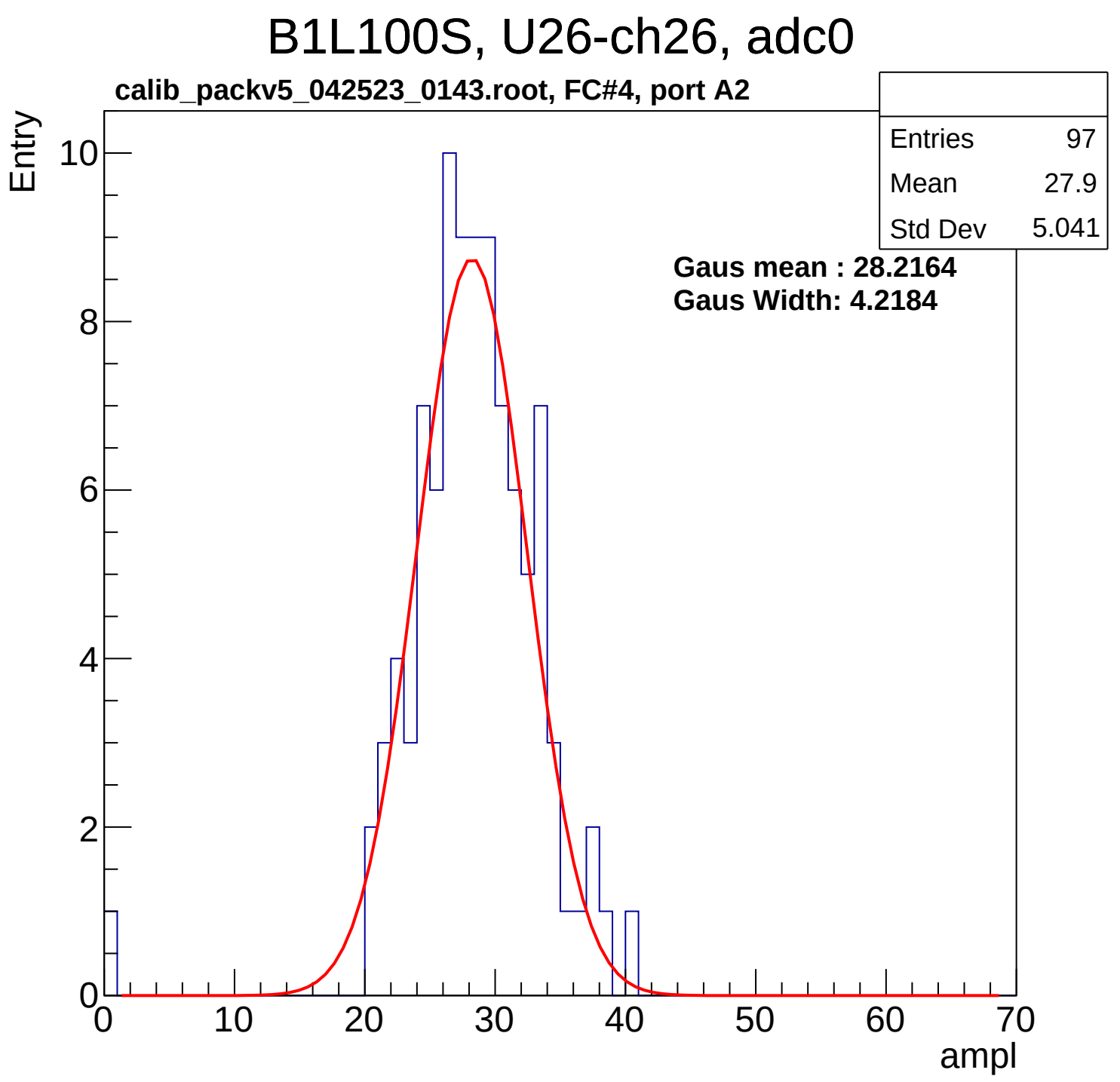
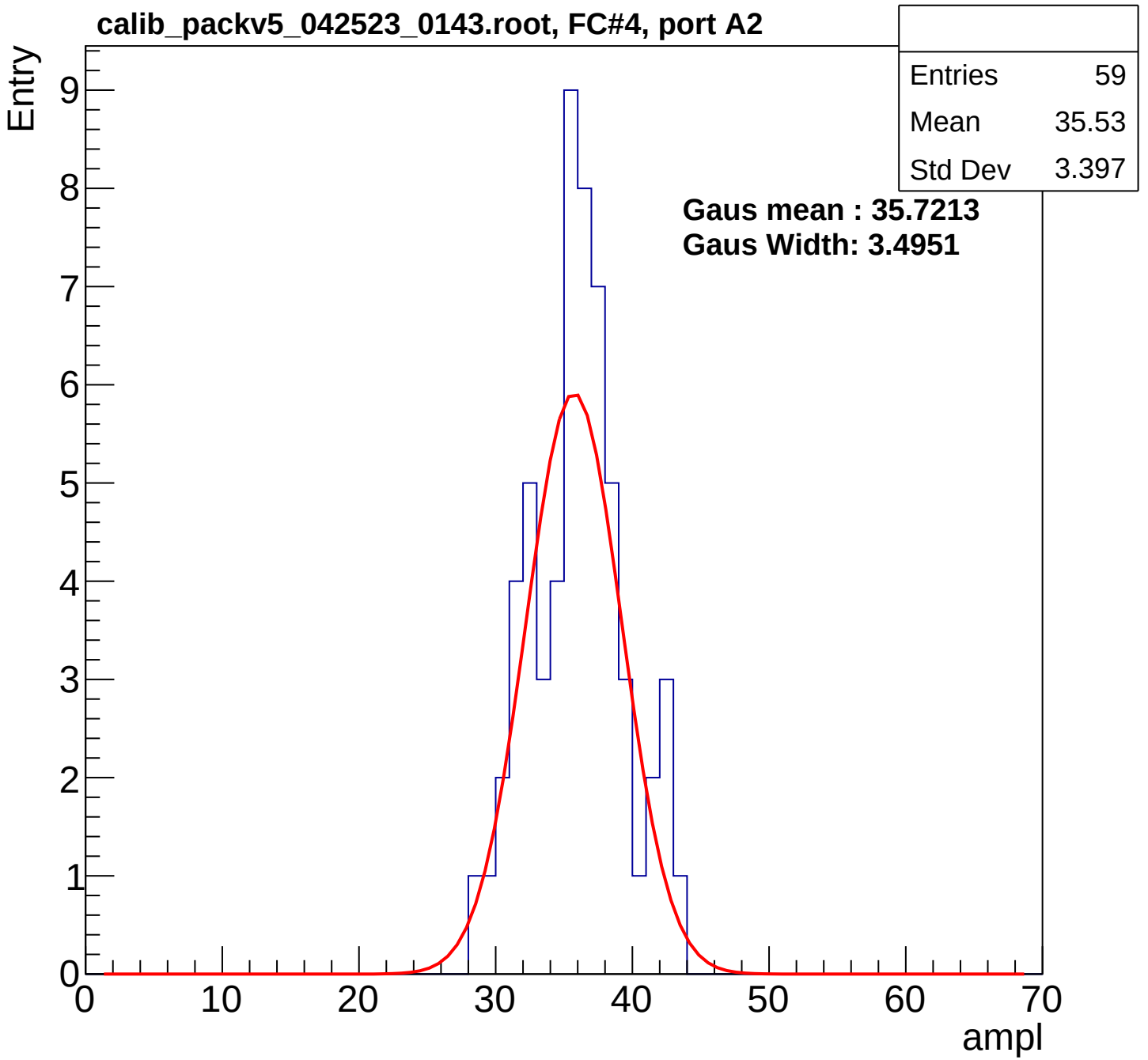
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

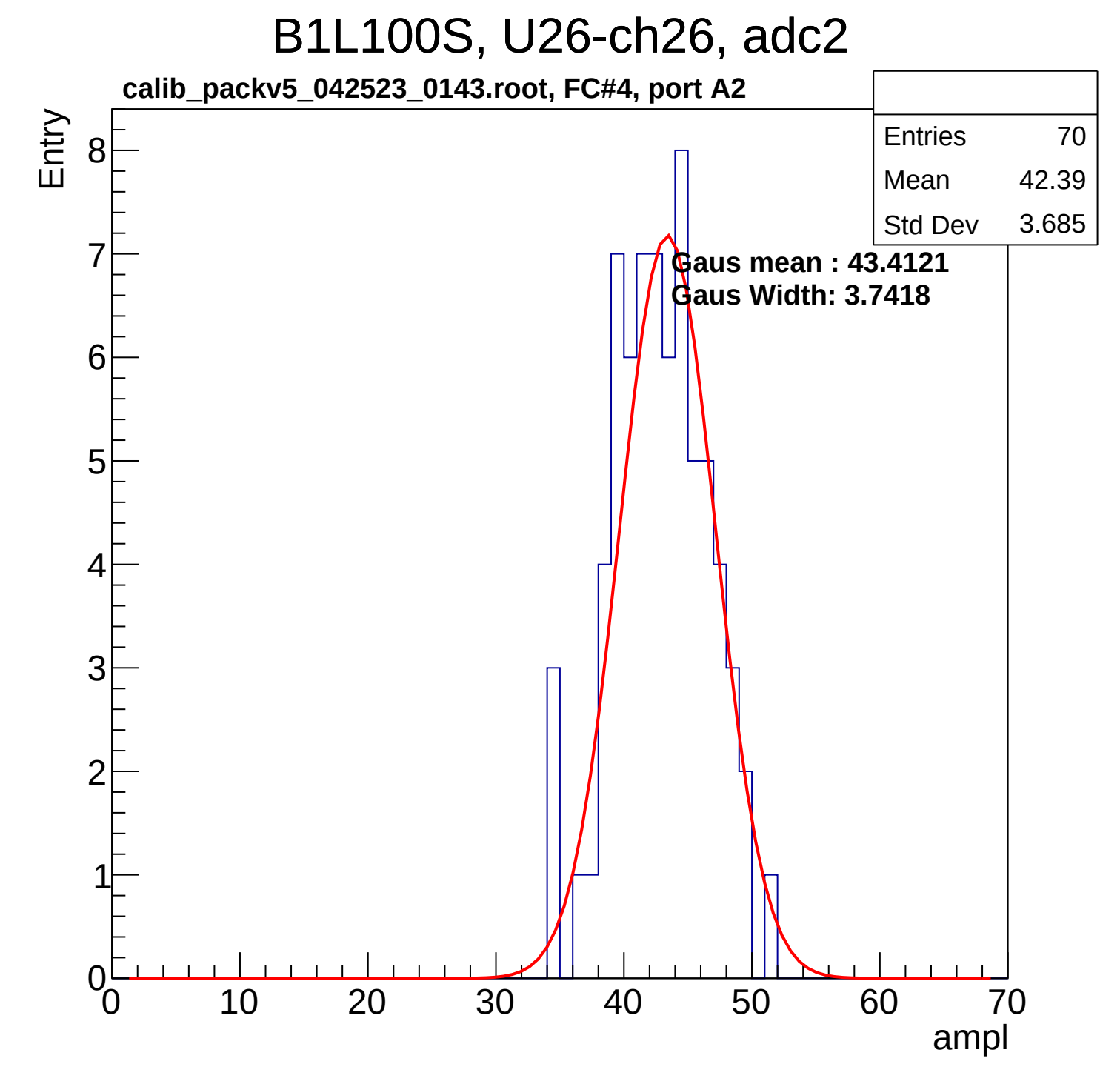

B1L100S, U26-ch25, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 61.07
Std Dev 1.78
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch25, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 97
Mean 27.9
Std Dev 5.041
Gaus mean : 28.2164
Gaus Width: 4.2184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.53
Std Dev 3.397
Gaus mean : 35.7213
Gaus Width: 3.4951
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 42.39
Std Dev 3.685
Gaus mean : 43.4121
Gaus Width: 3.7418
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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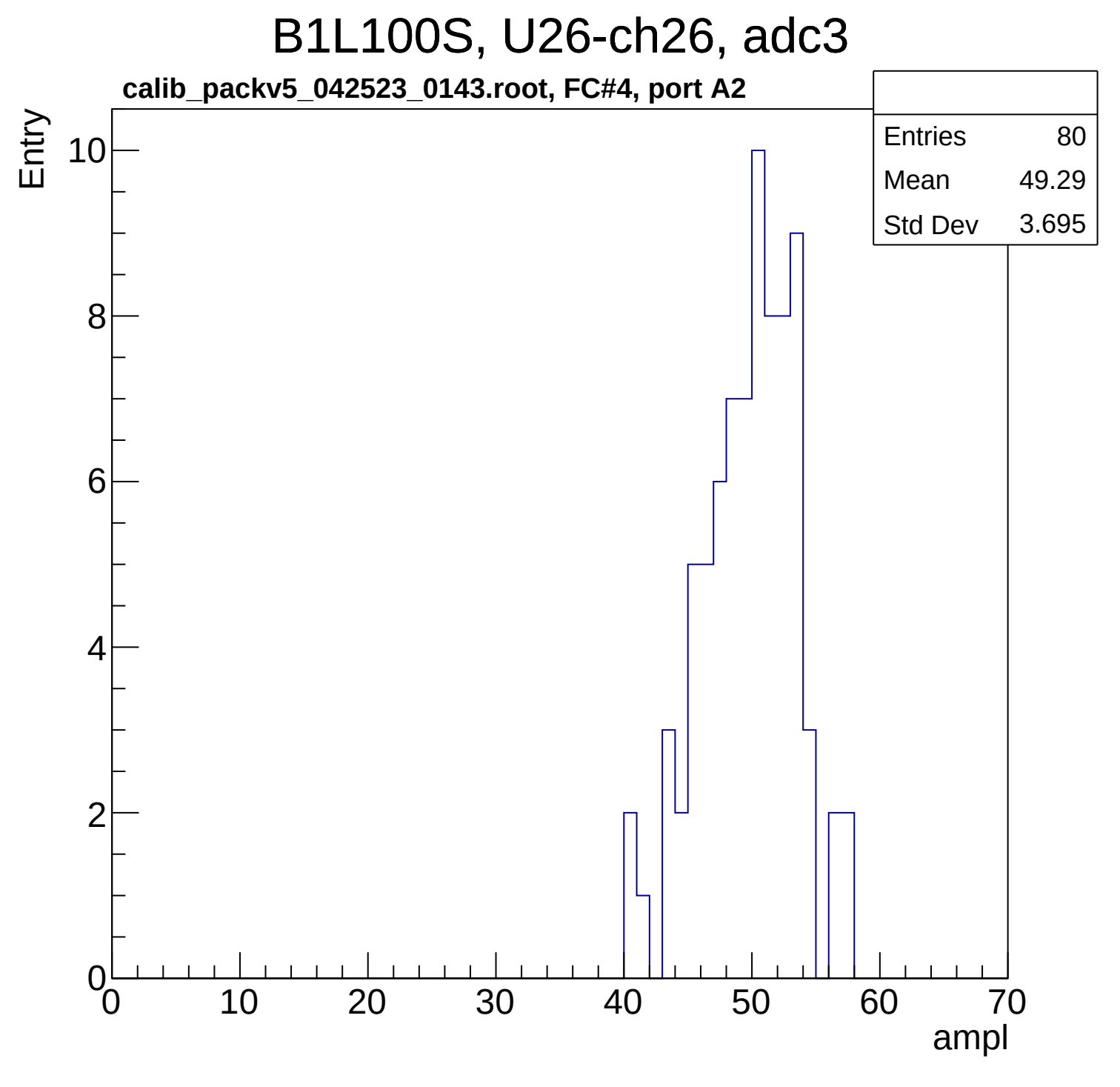
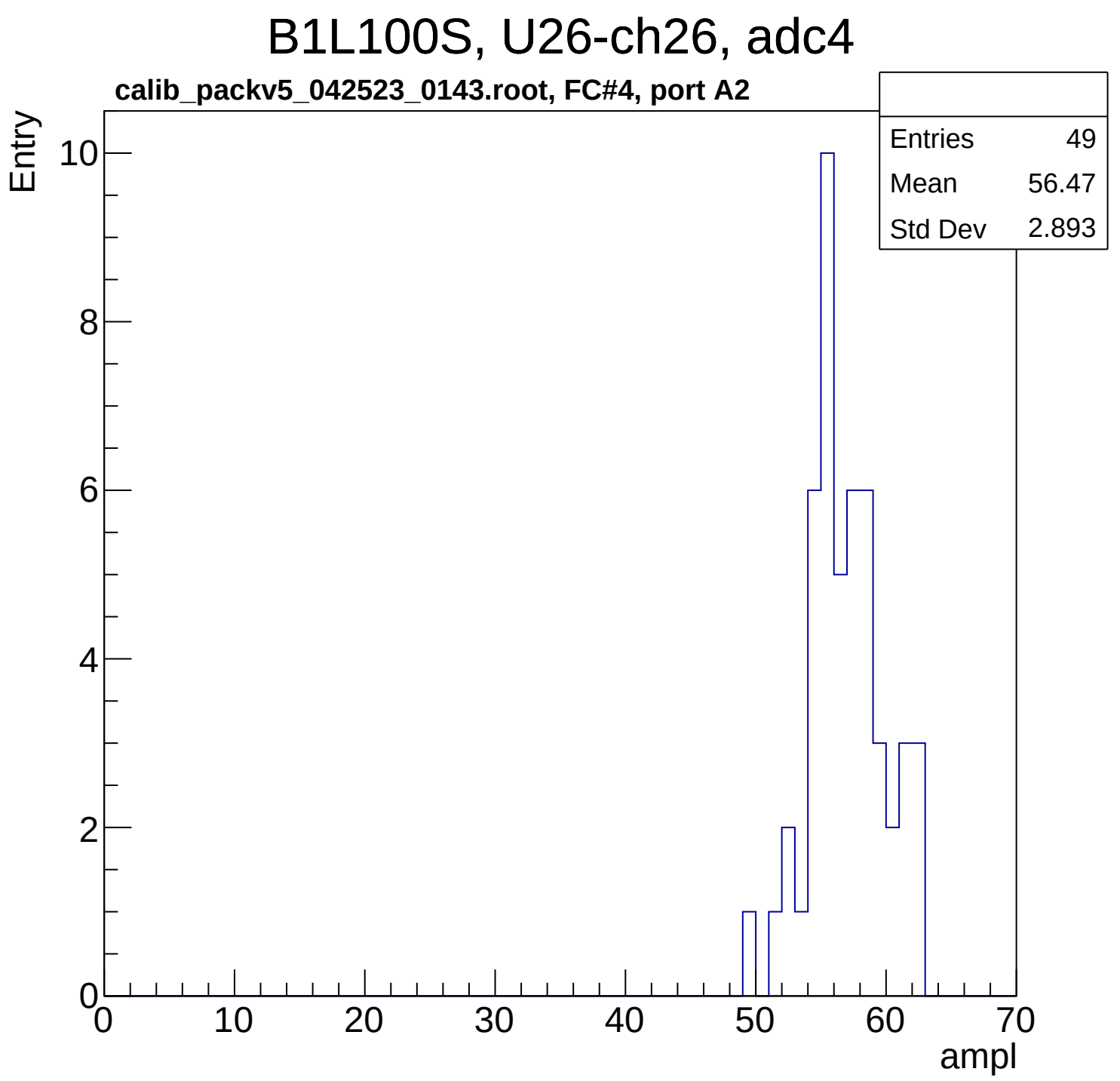
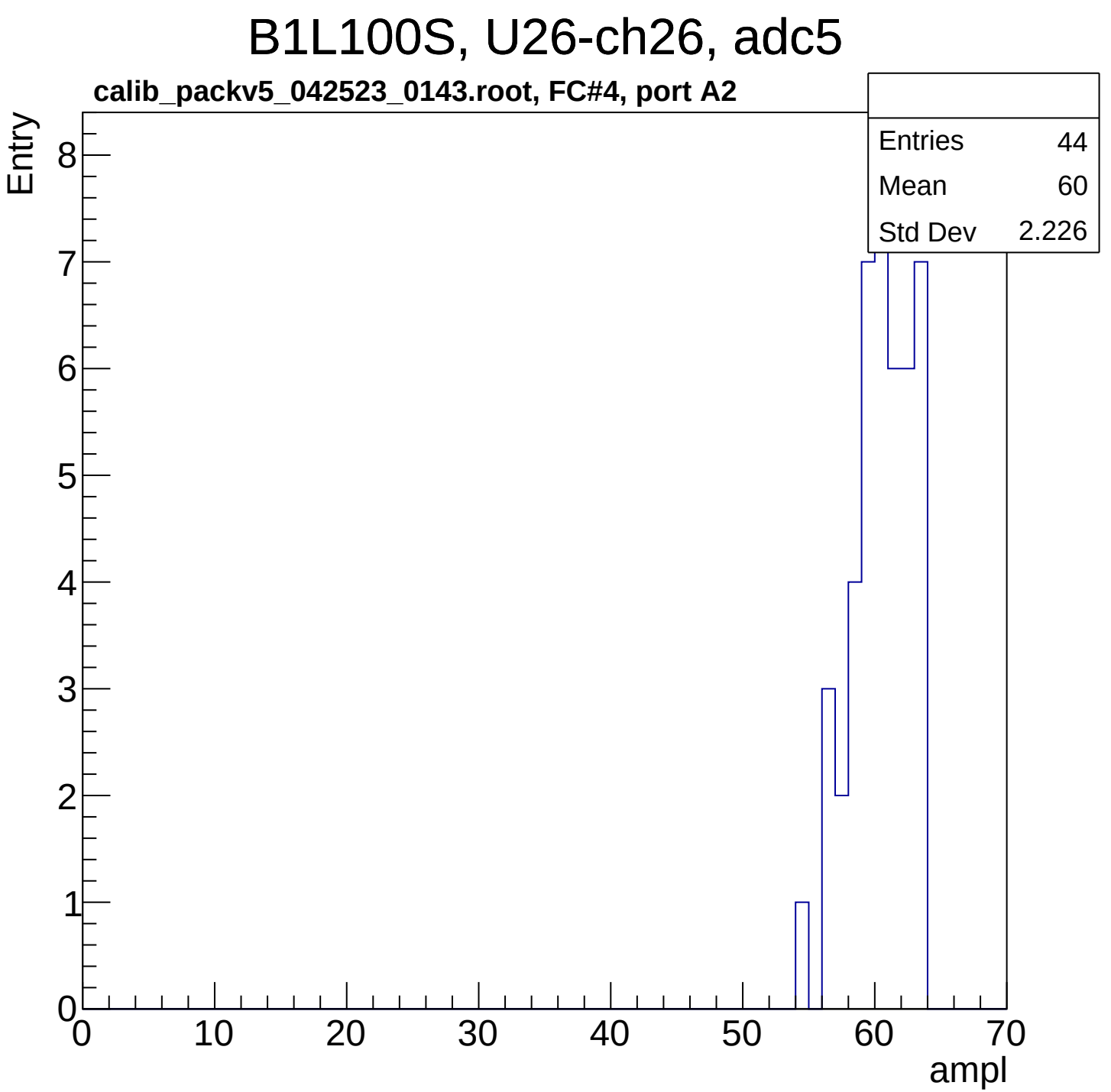

B1L100S, U26-ch26, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 49.29
Std Dev 3.695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.47
Std Dev 2.893
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch26, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 60
Std Dev 2.226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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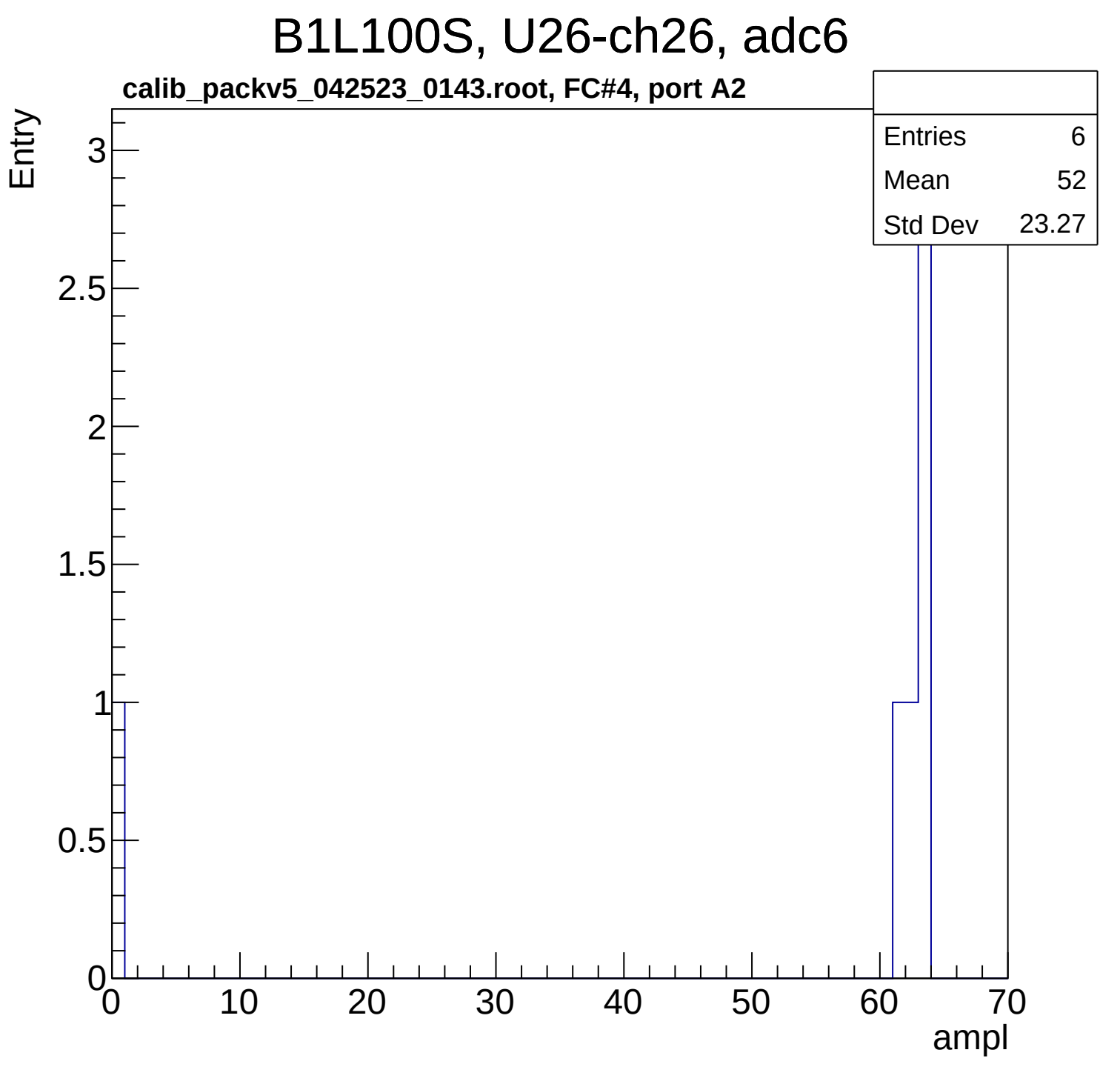
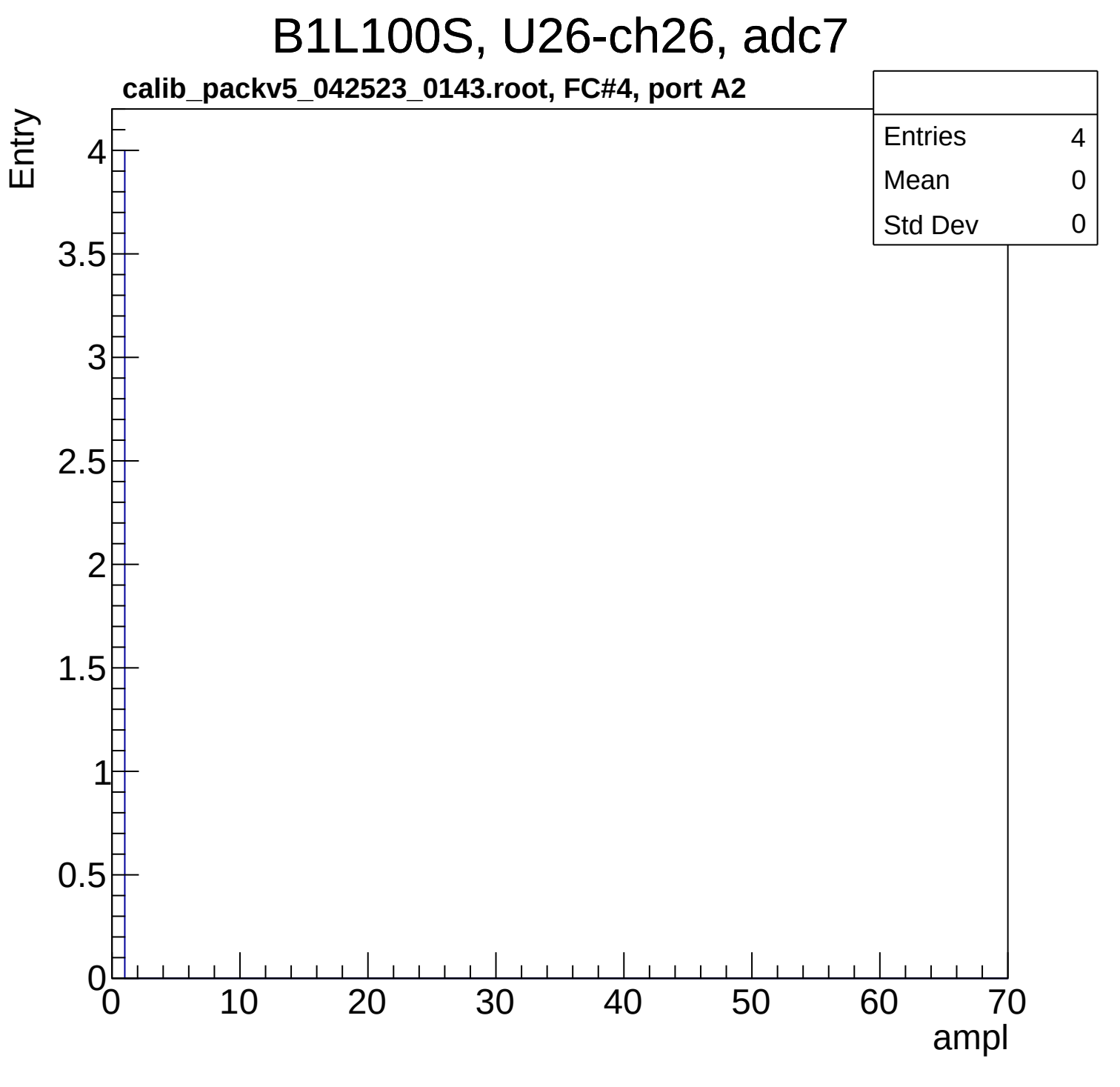
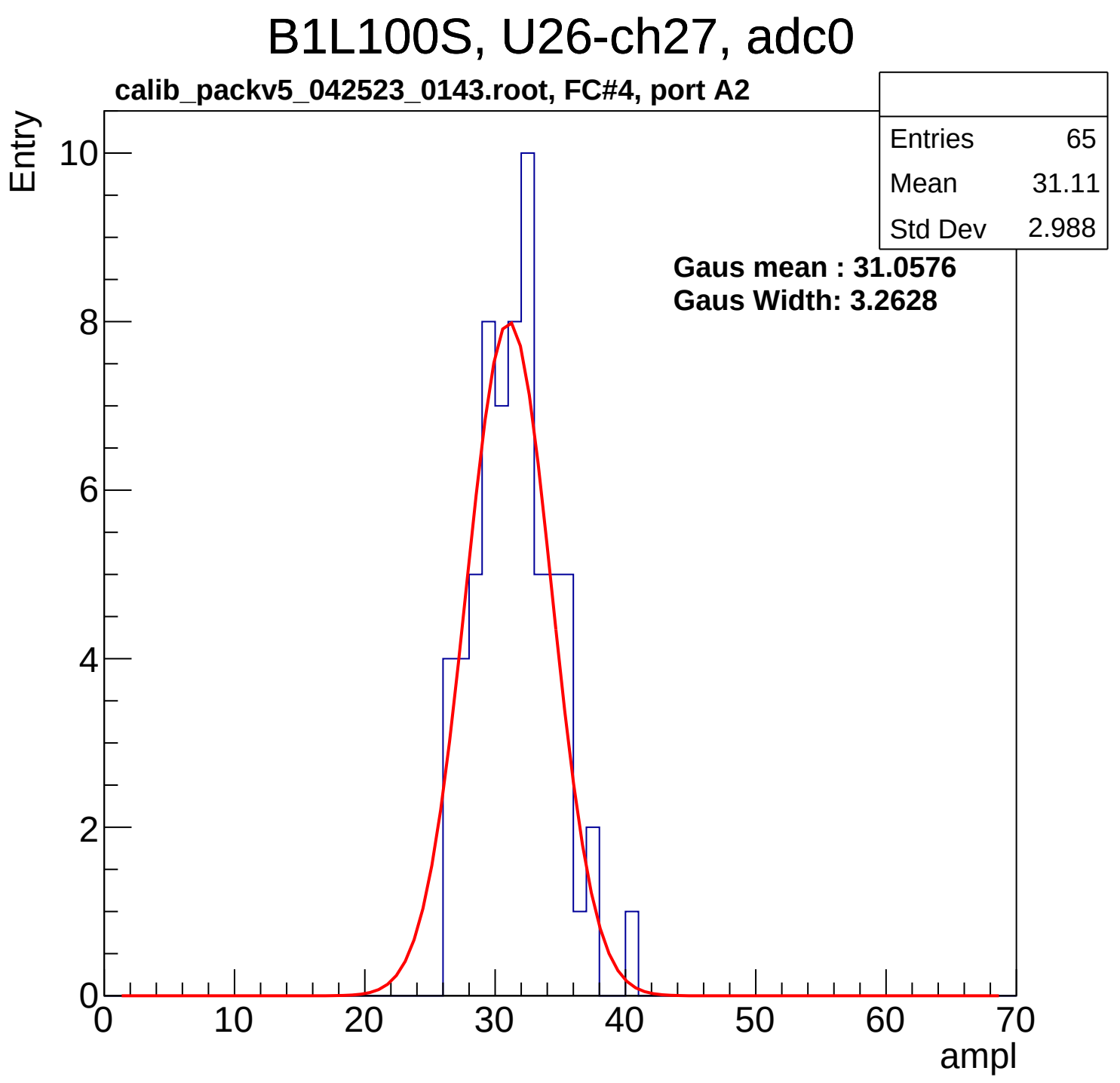
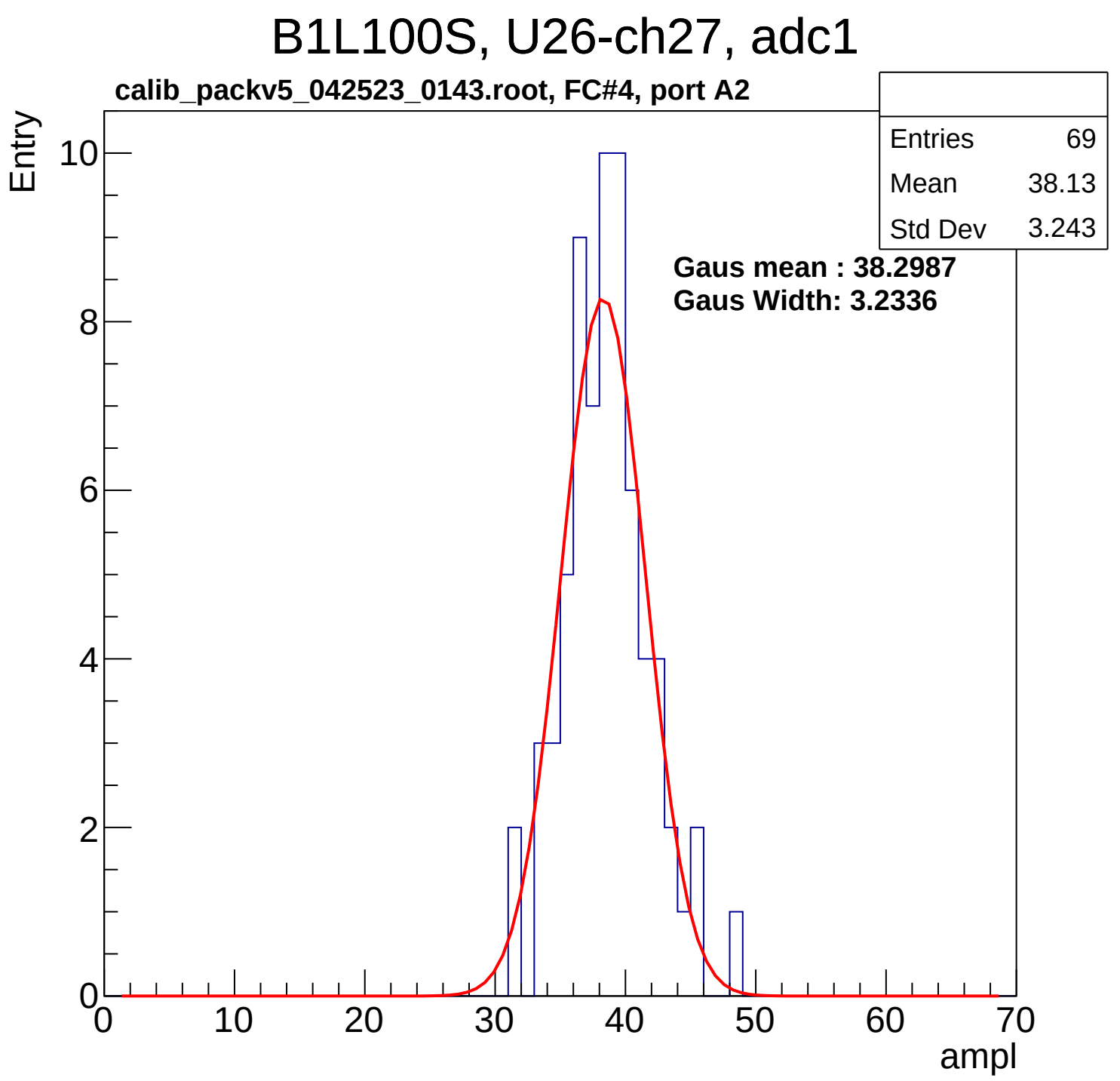

B1L100S, U26-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch26, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 0
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U26-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 31.11
Std Dev 2.988
Gaus mean : 31.0576
Gaus Width: 3.2628
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 38.13
Std Dev 3.243
Gaus mean : 38.2987
Gaus Width: 3.2336
Entry
ampl
0
2
4
6
8
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0
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40
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60
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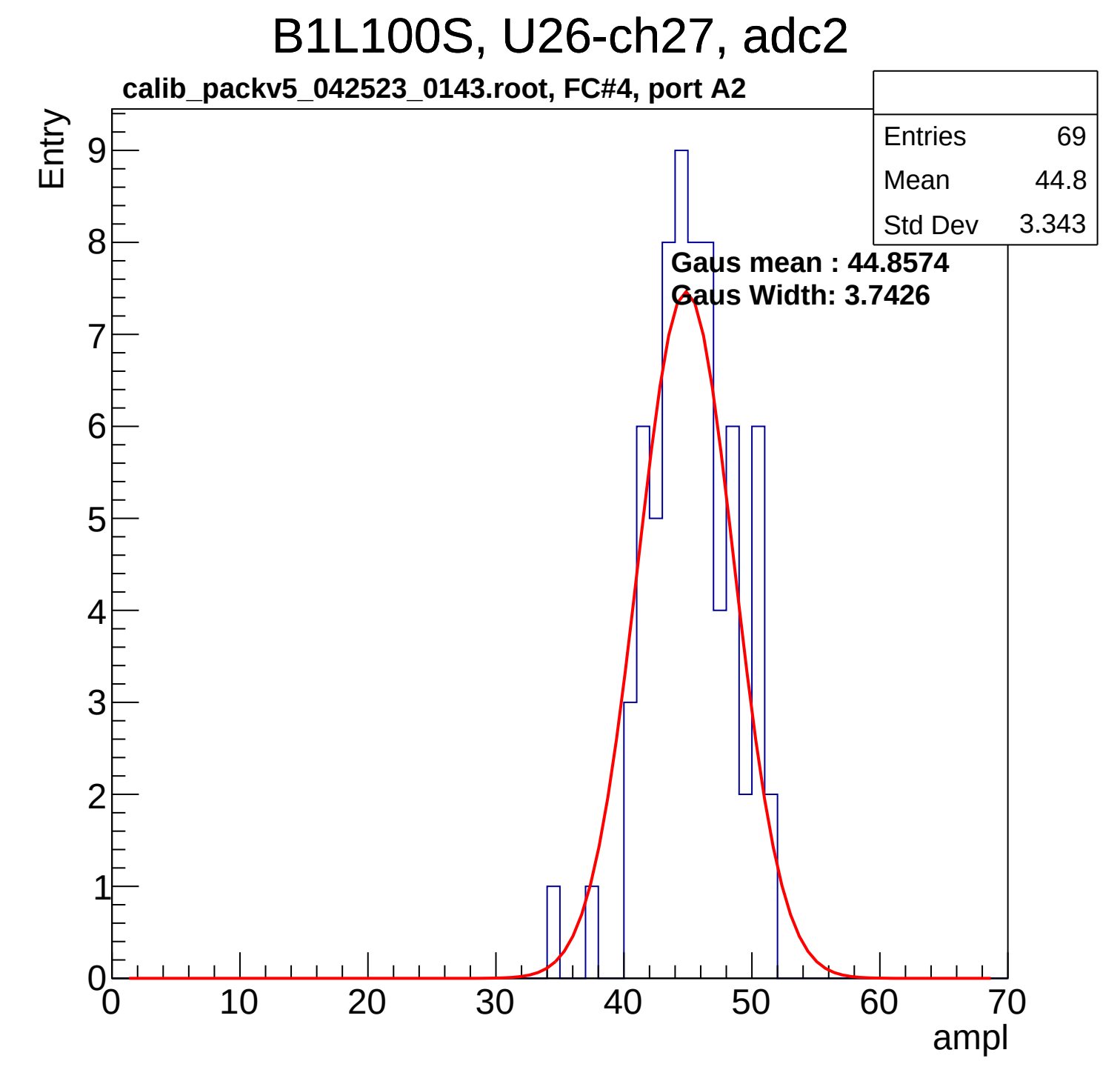
B1L100S, U26-ch27, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.8
Std Dev 3.343
Gaus mean : 44.8574
Gaus Width: 3.7426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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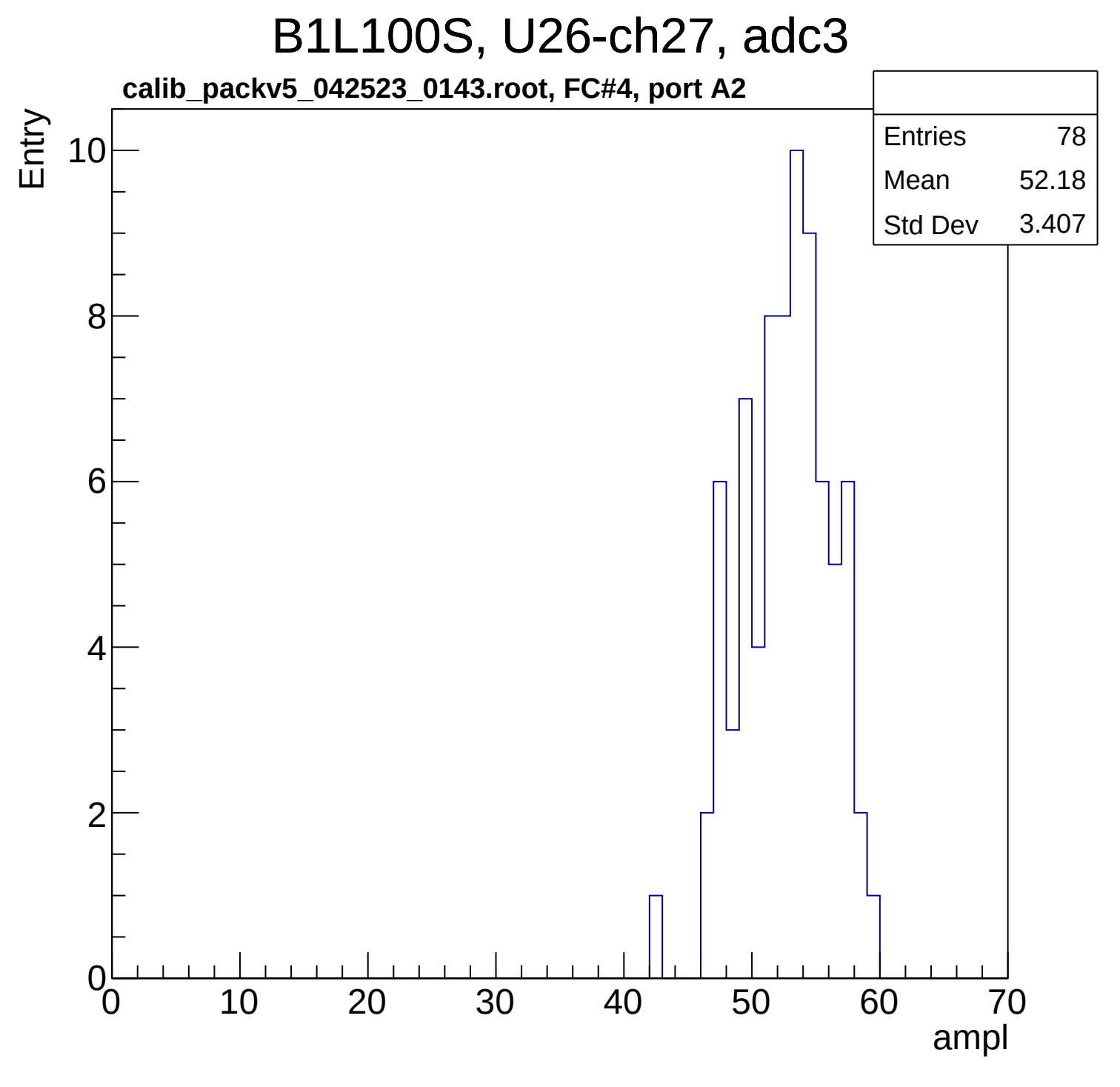
B1L100S, U26-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 52.18
Std Dev 3.407
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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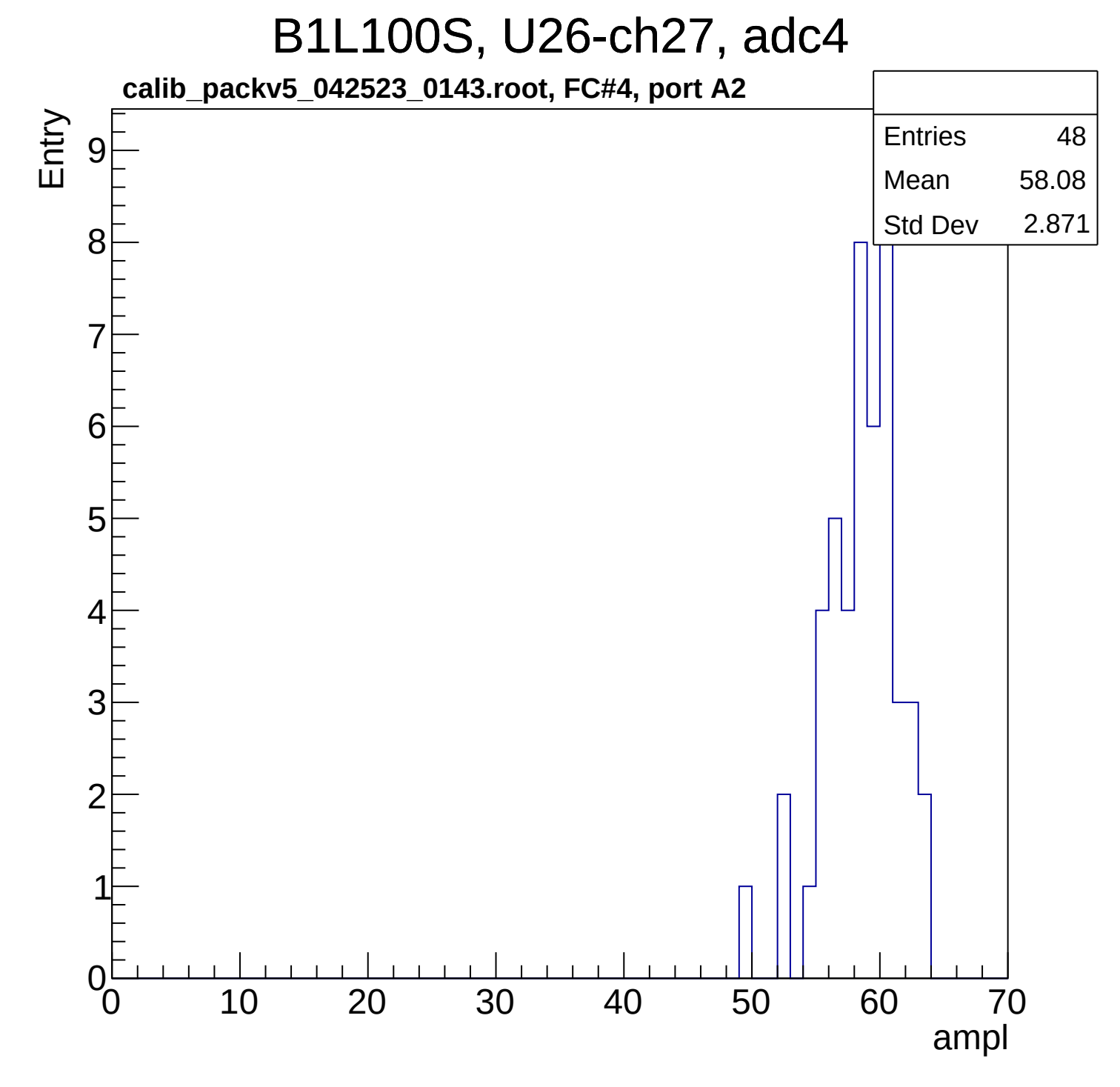
B1L100S, U26-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.08
Std Dev 2.871
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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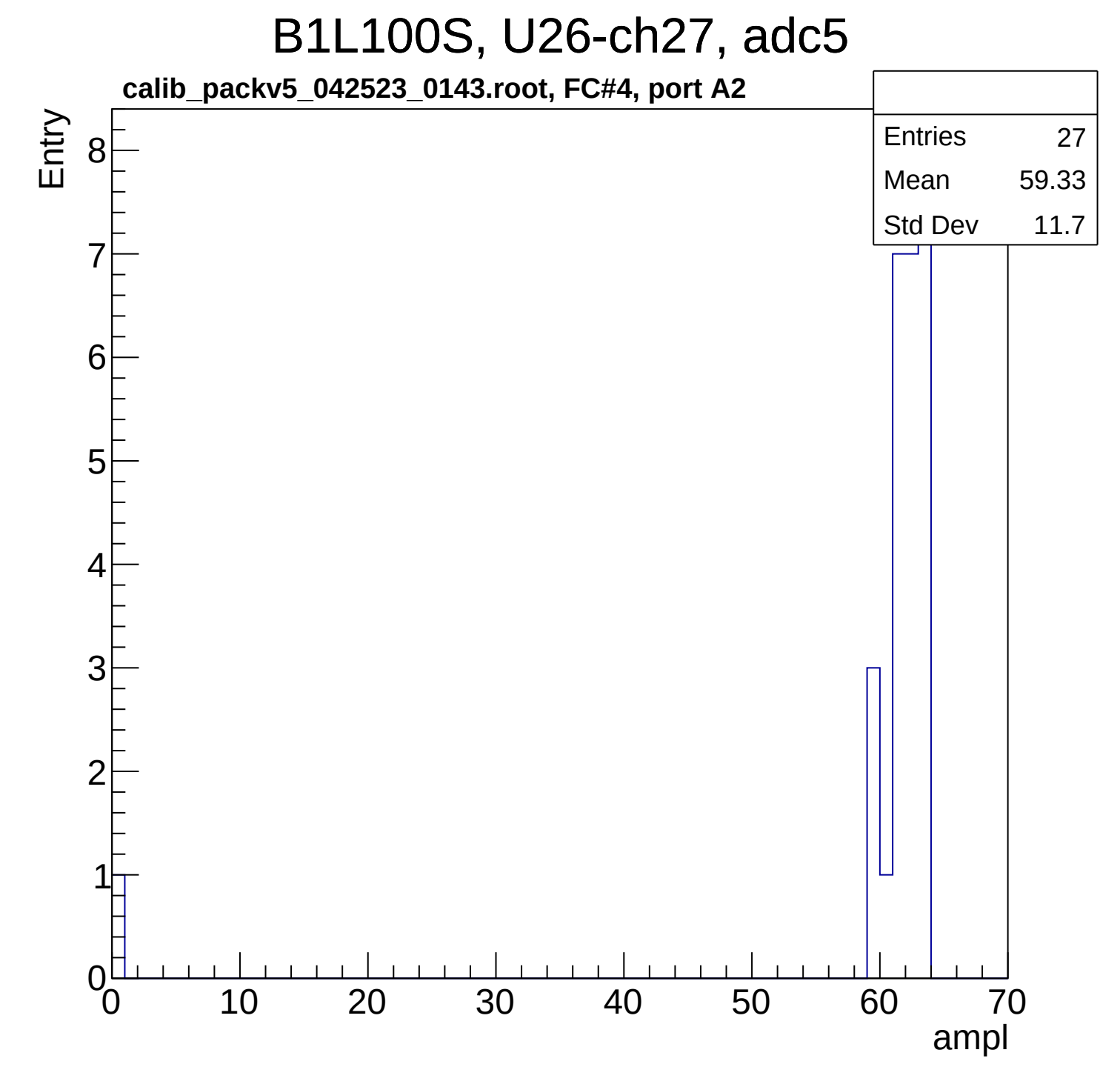
B1L100S, U26-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 59.33
Std Dev 11.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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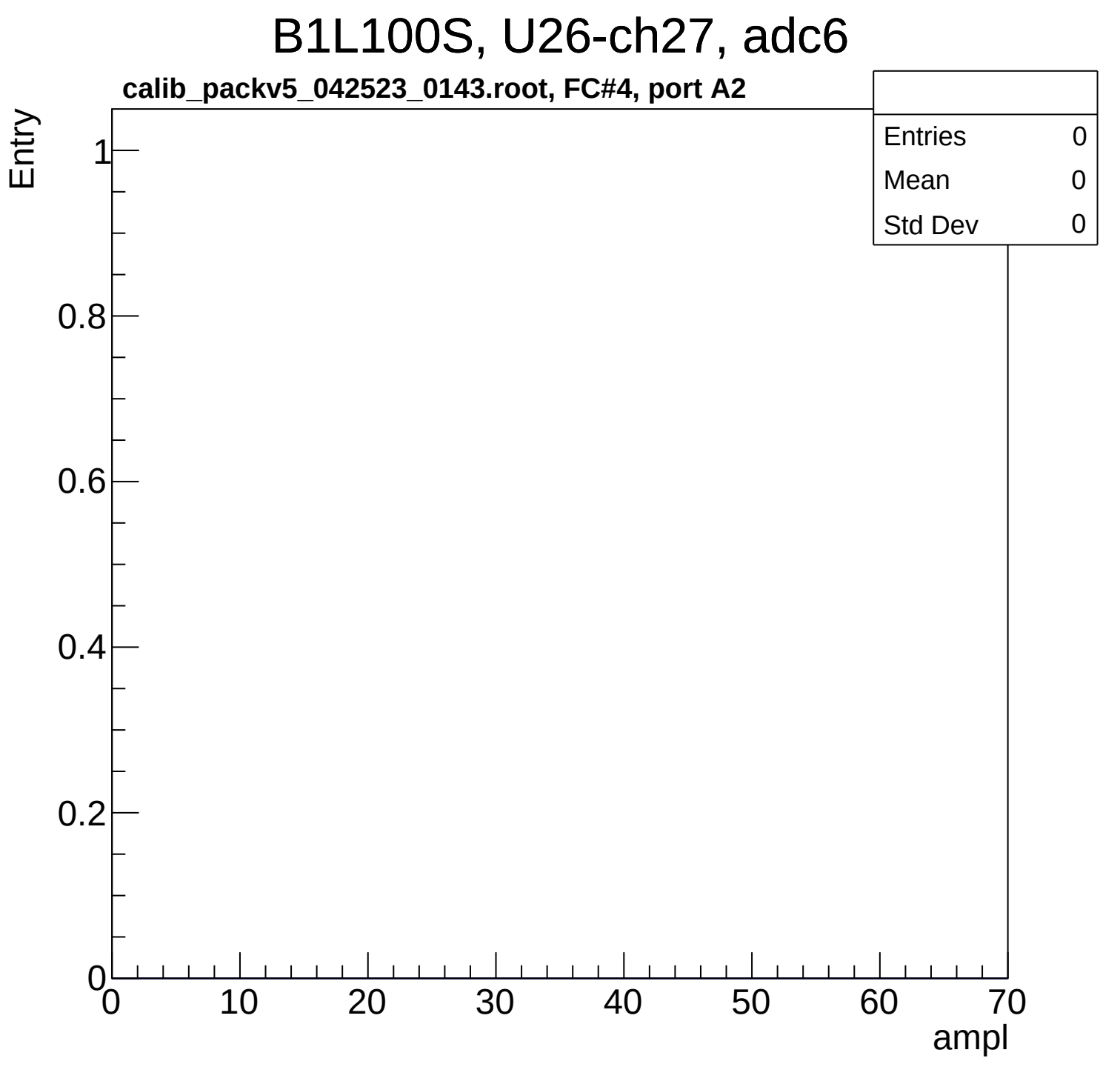
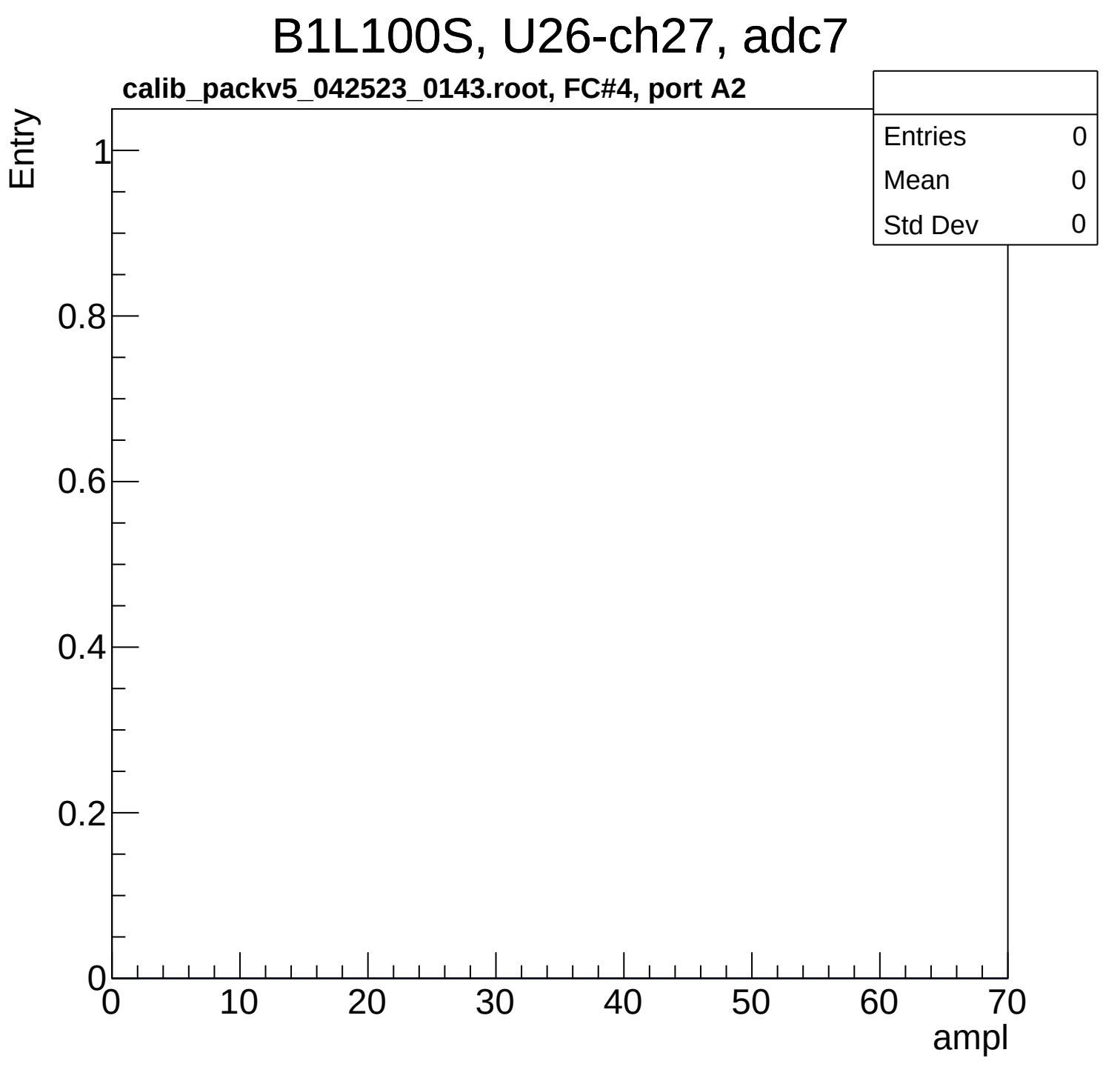
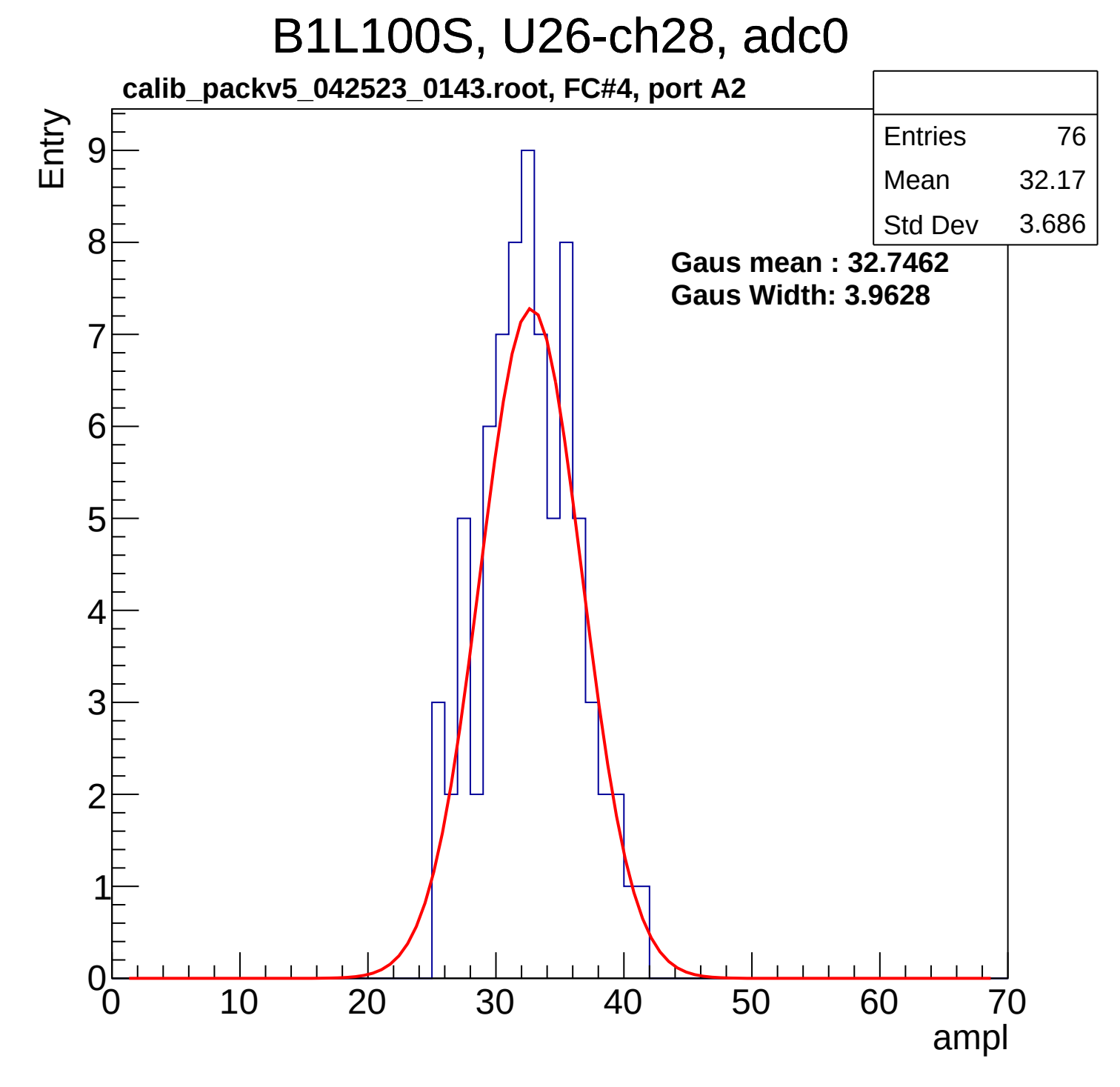

B1L100S, U26-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch27, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 32.17
Std Dev 3.686
Gaus mean : 32.7462
Gaus Width: 3.9628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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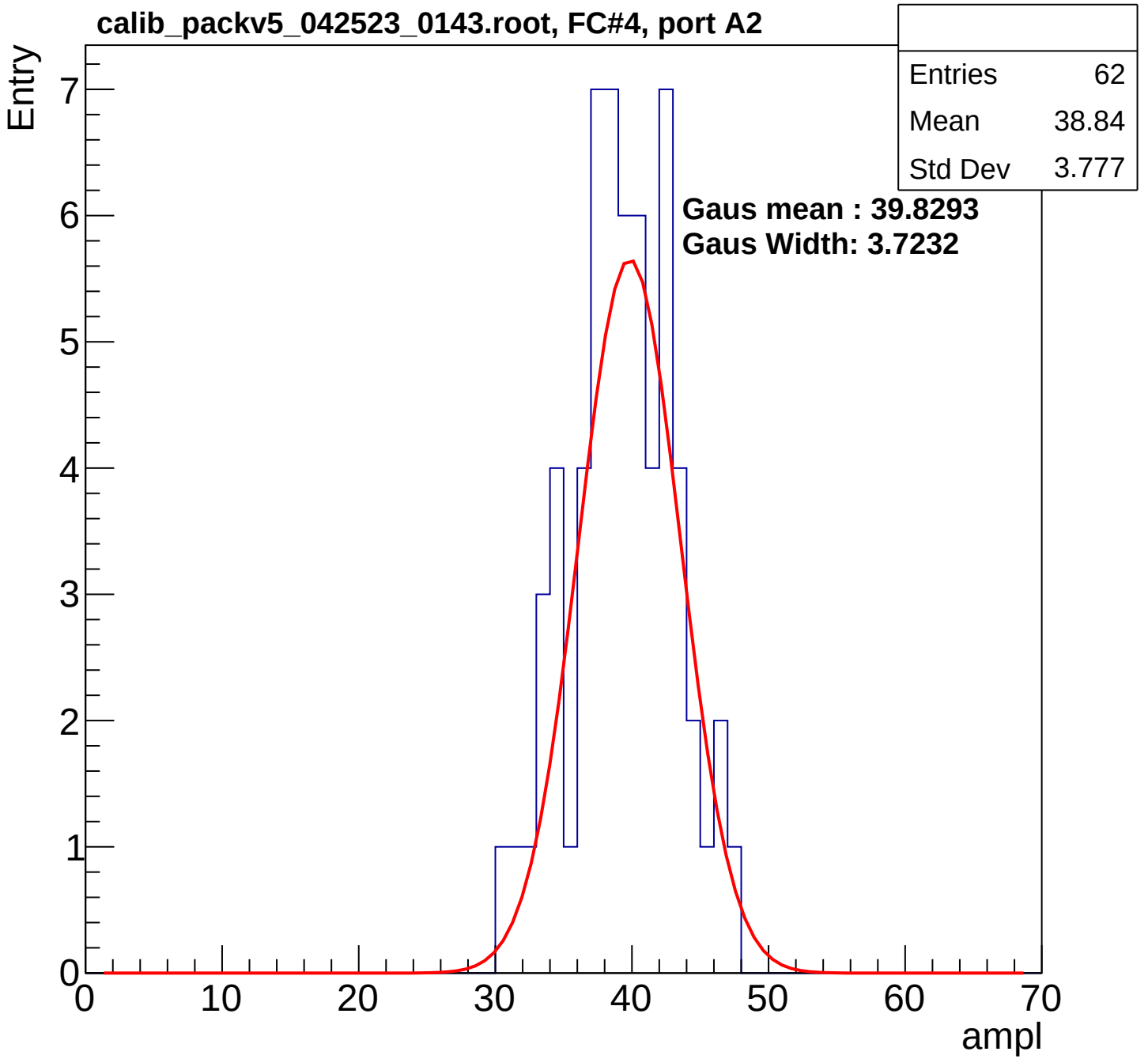
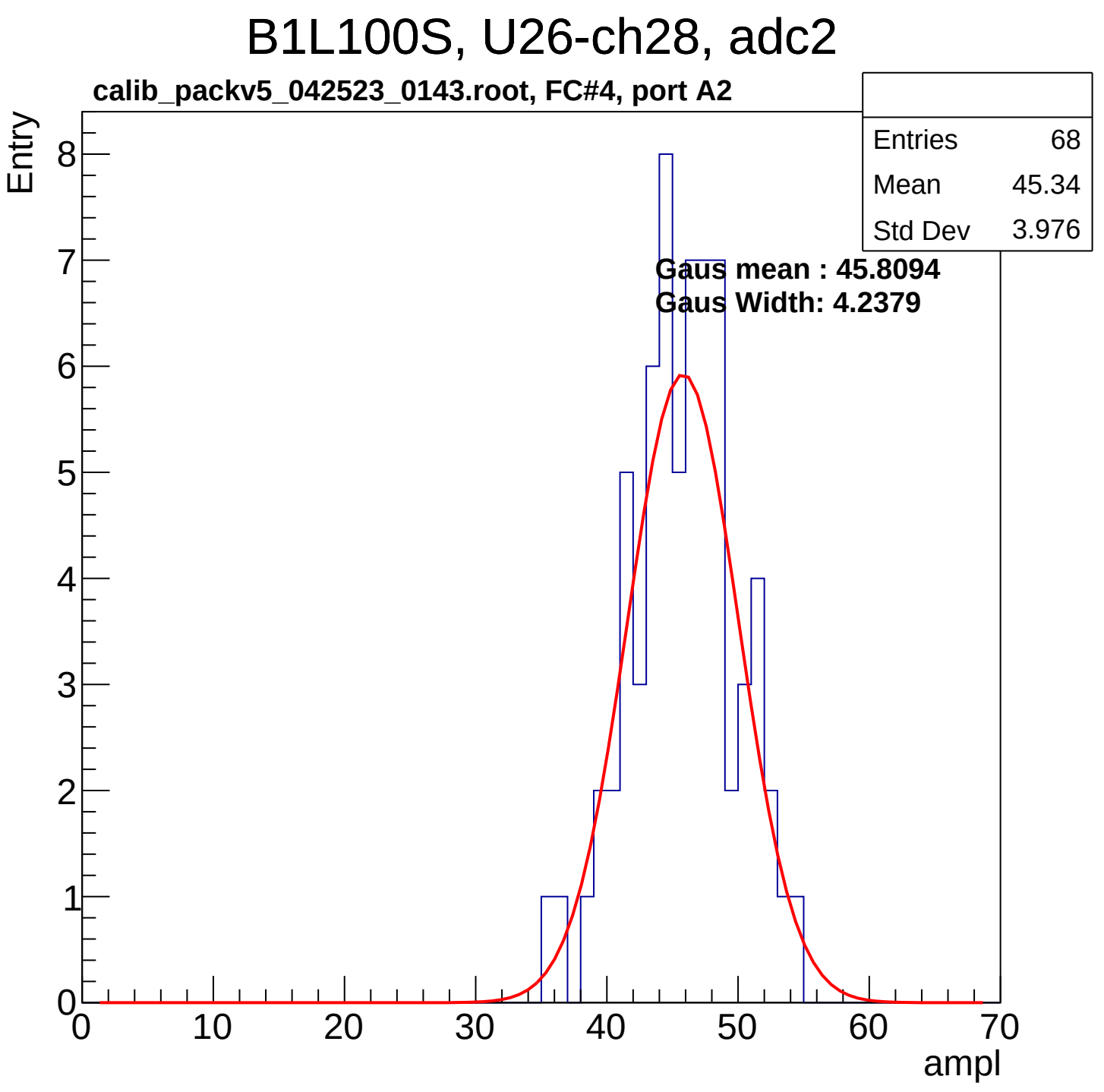
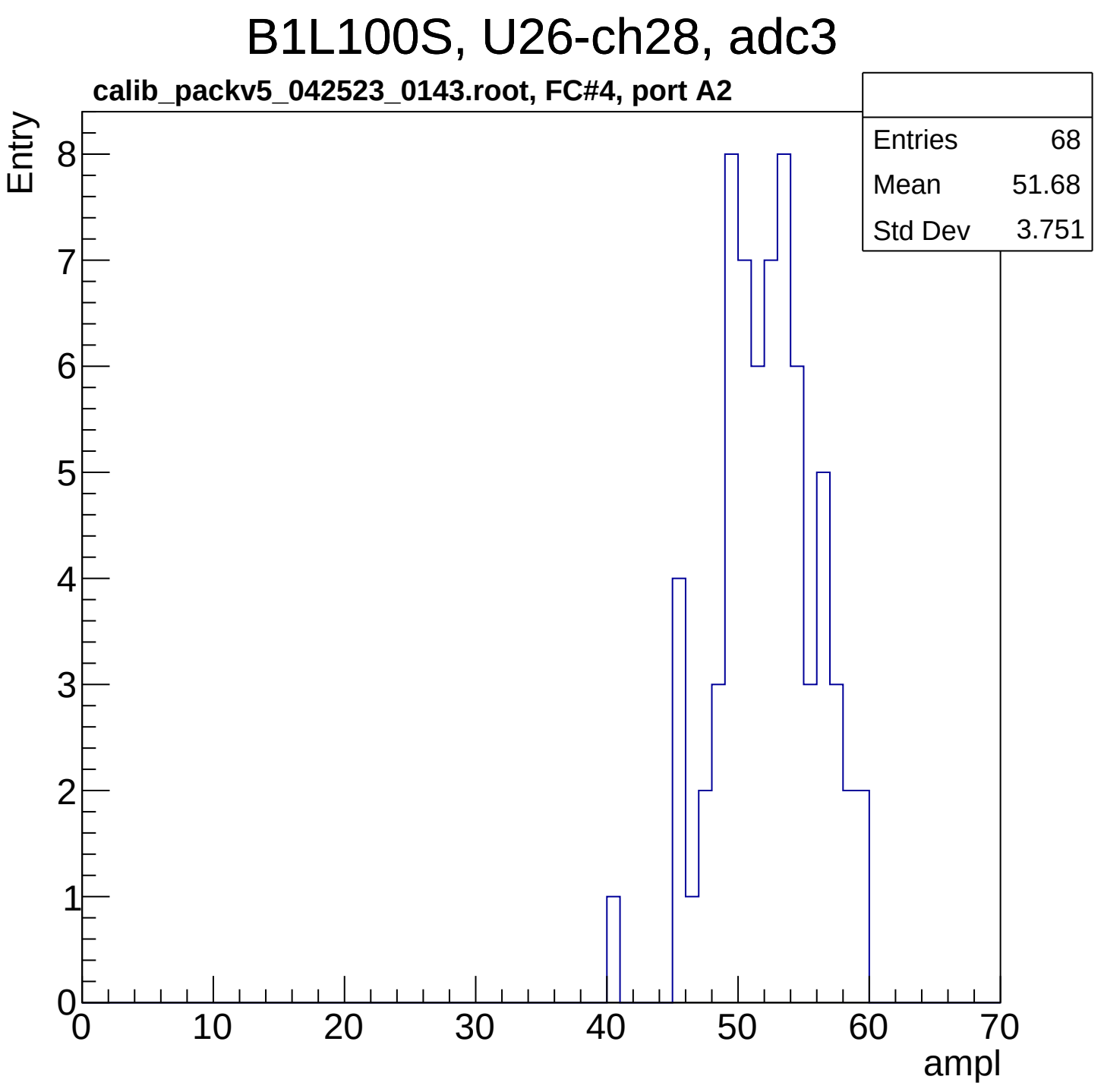
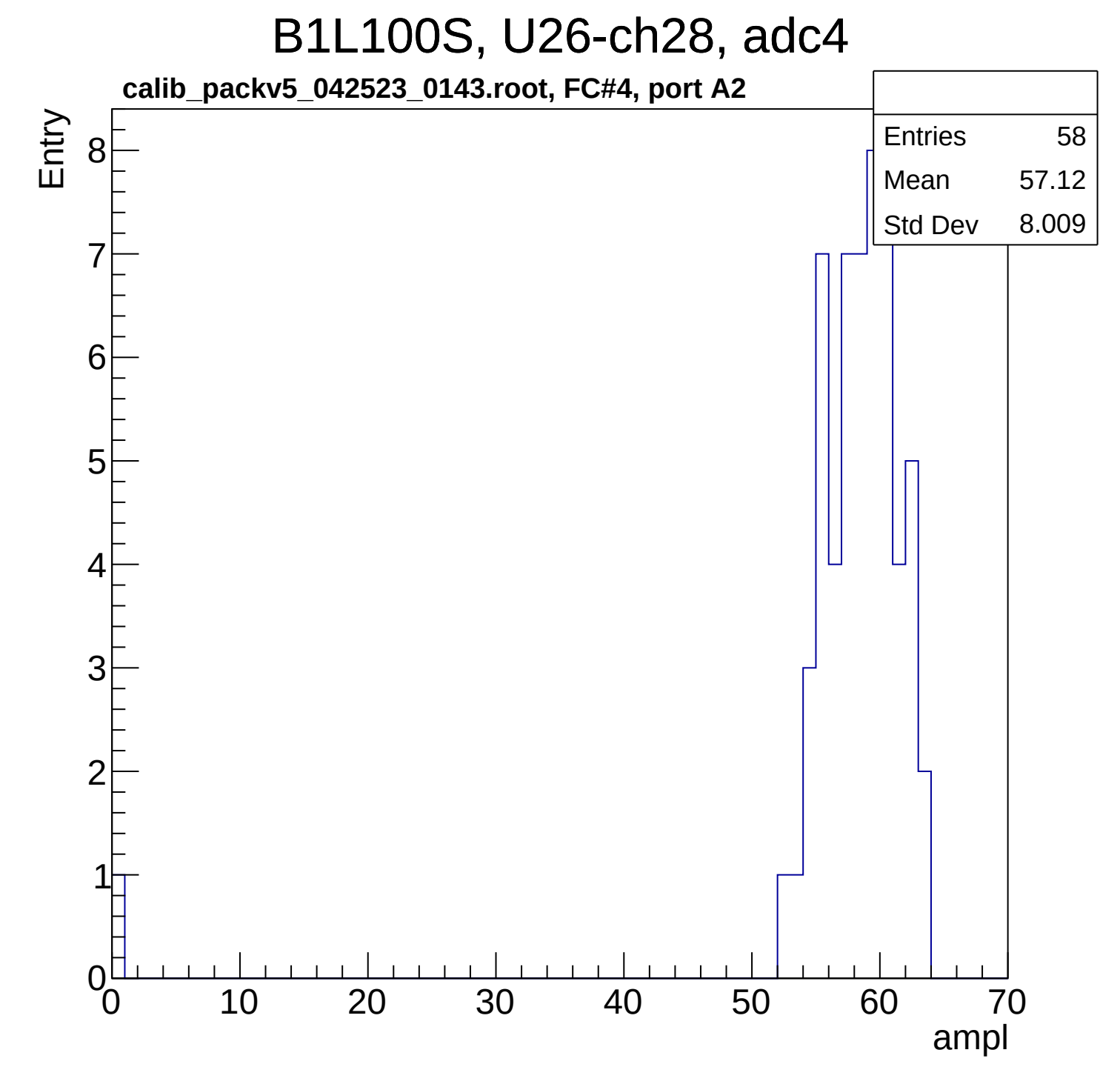
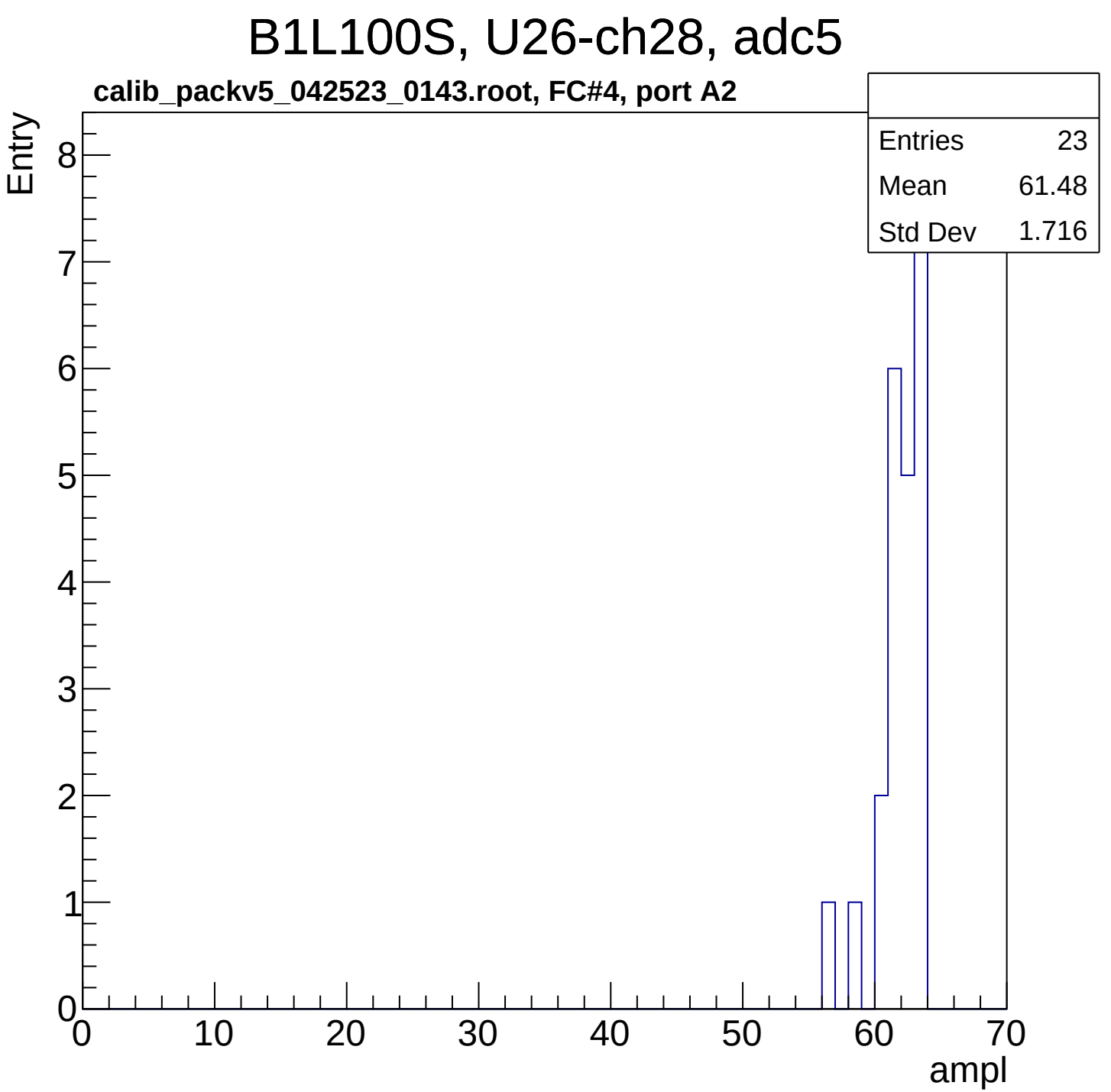
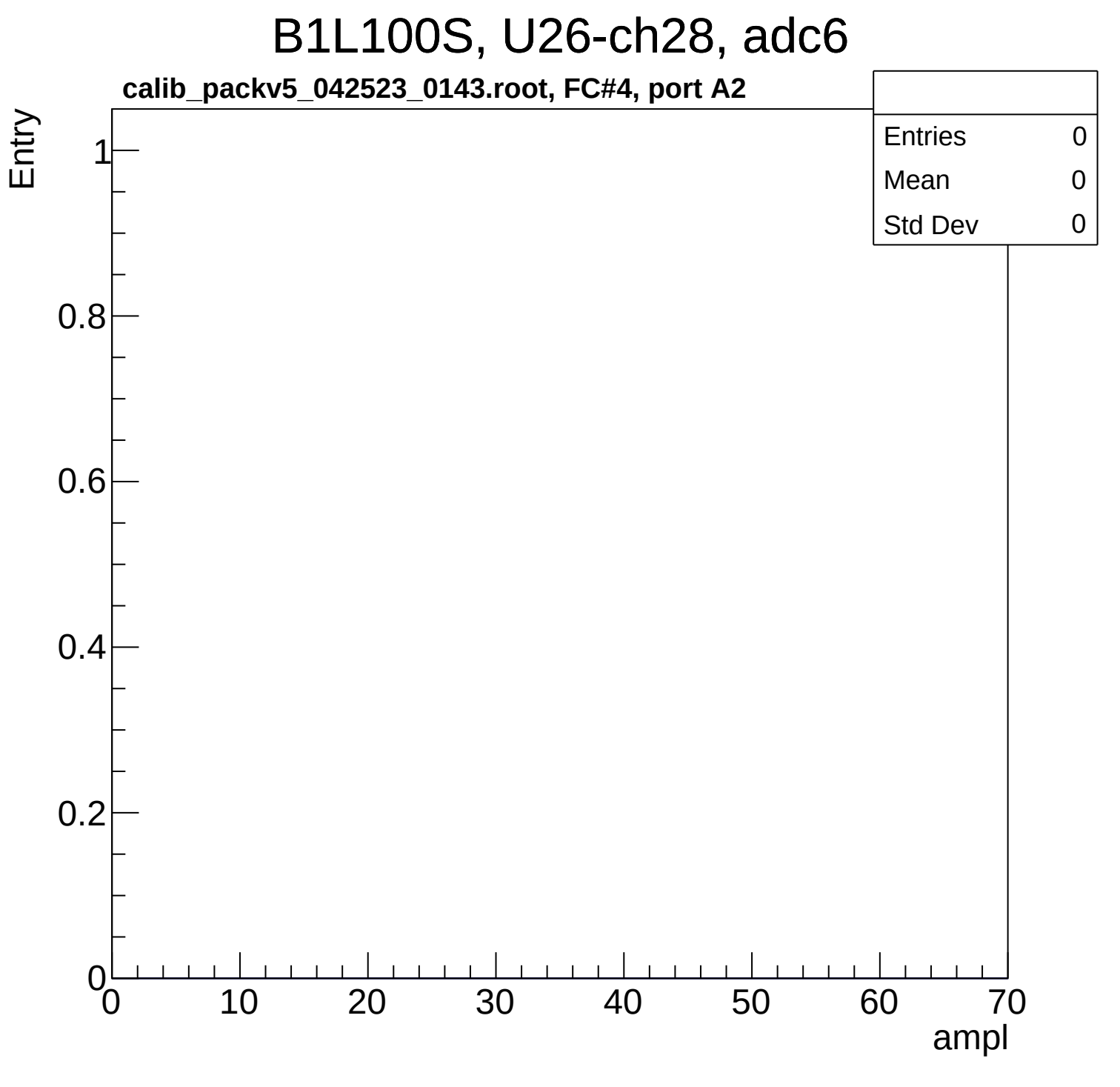

B1L100S, U26-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 38.84
Std Dev 3.777
Gaus mean : 39.8293
Gaus Width: 3.7232
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

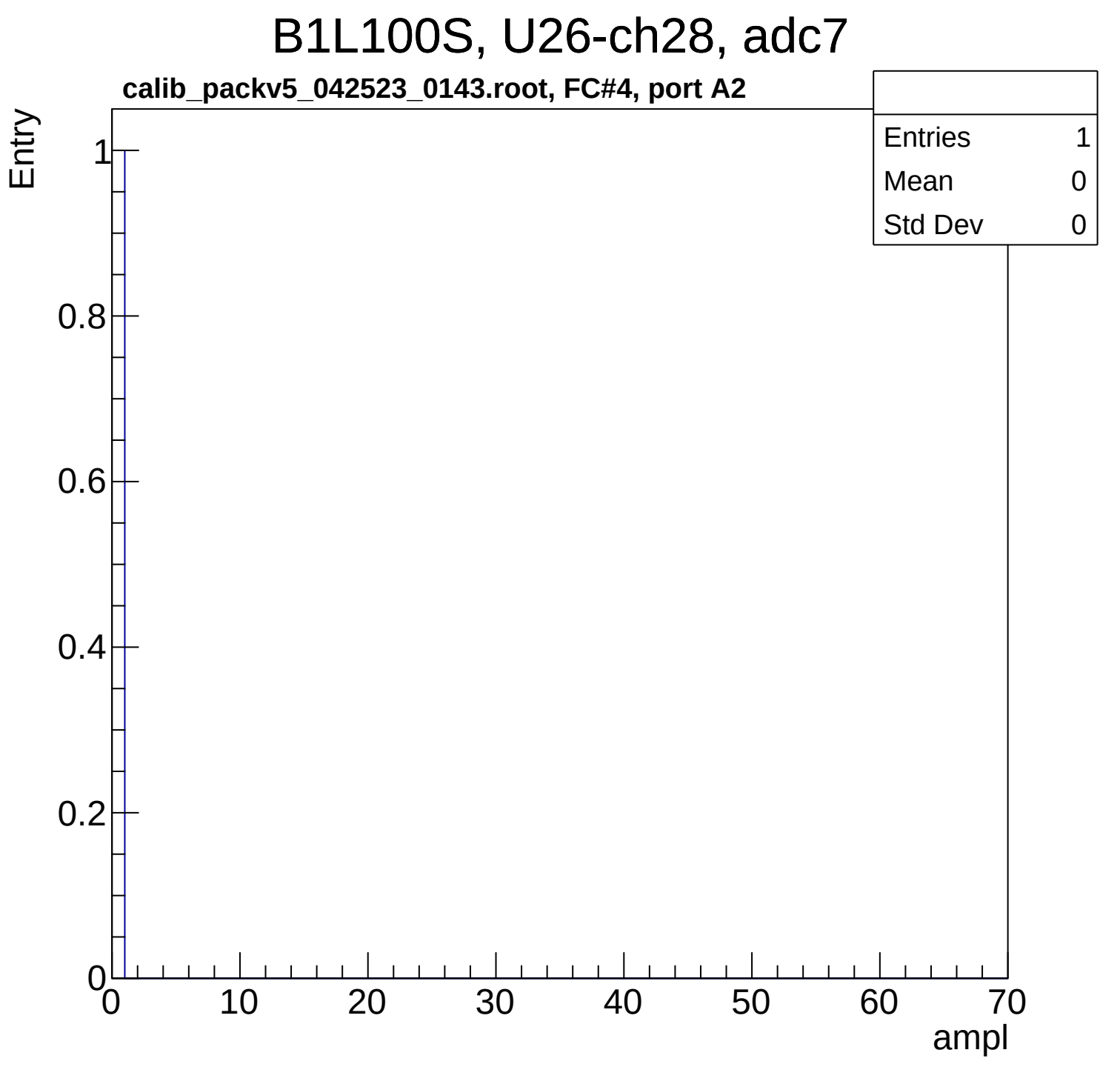

B1L100S, U26-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 45.34
Std Dev 3.976
Gaus mean : 45.8094
Gaus Width: 4.2379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

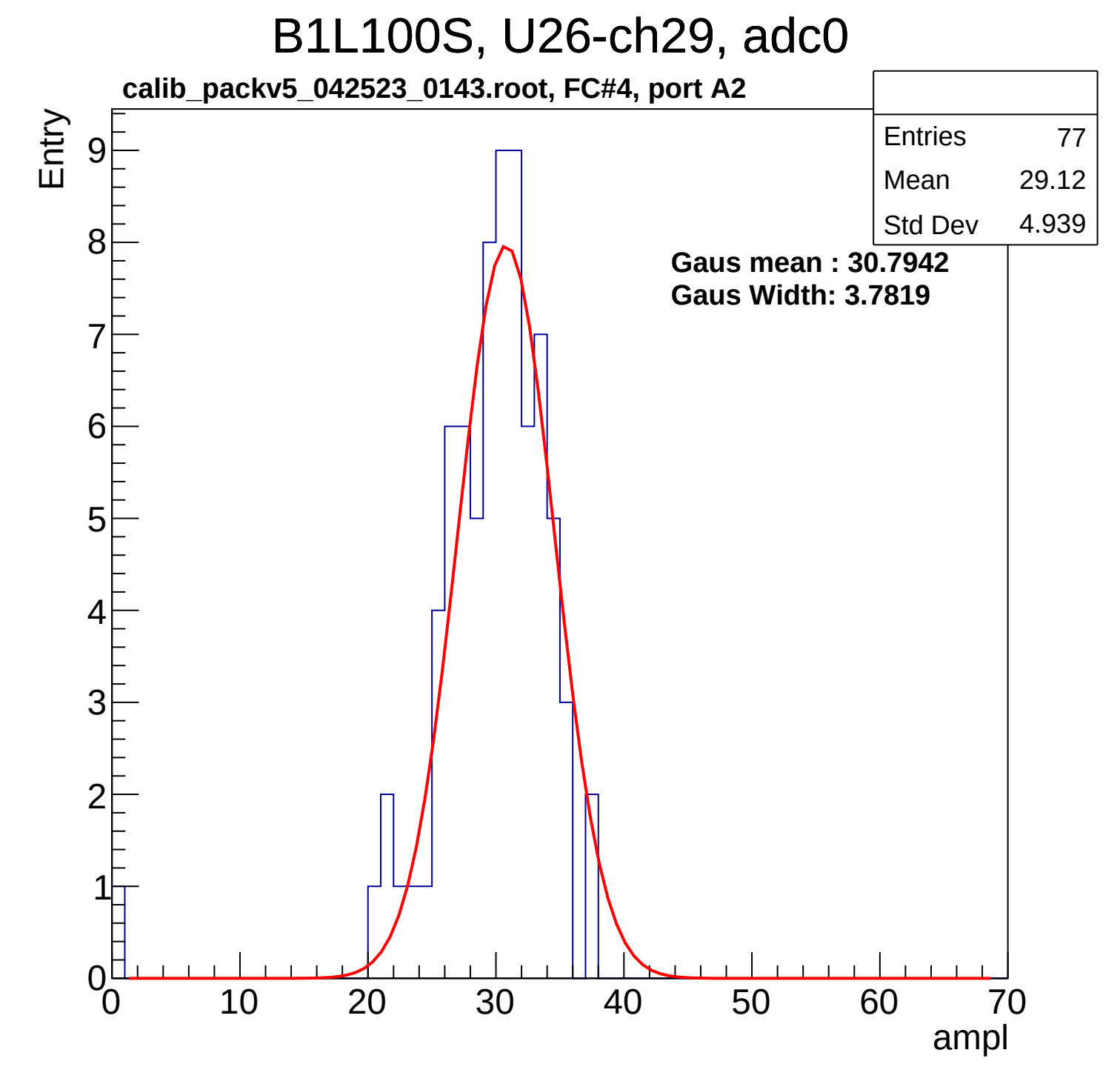

B1L100S, U26-ch28, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 51.68
Std Dev 3.751
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

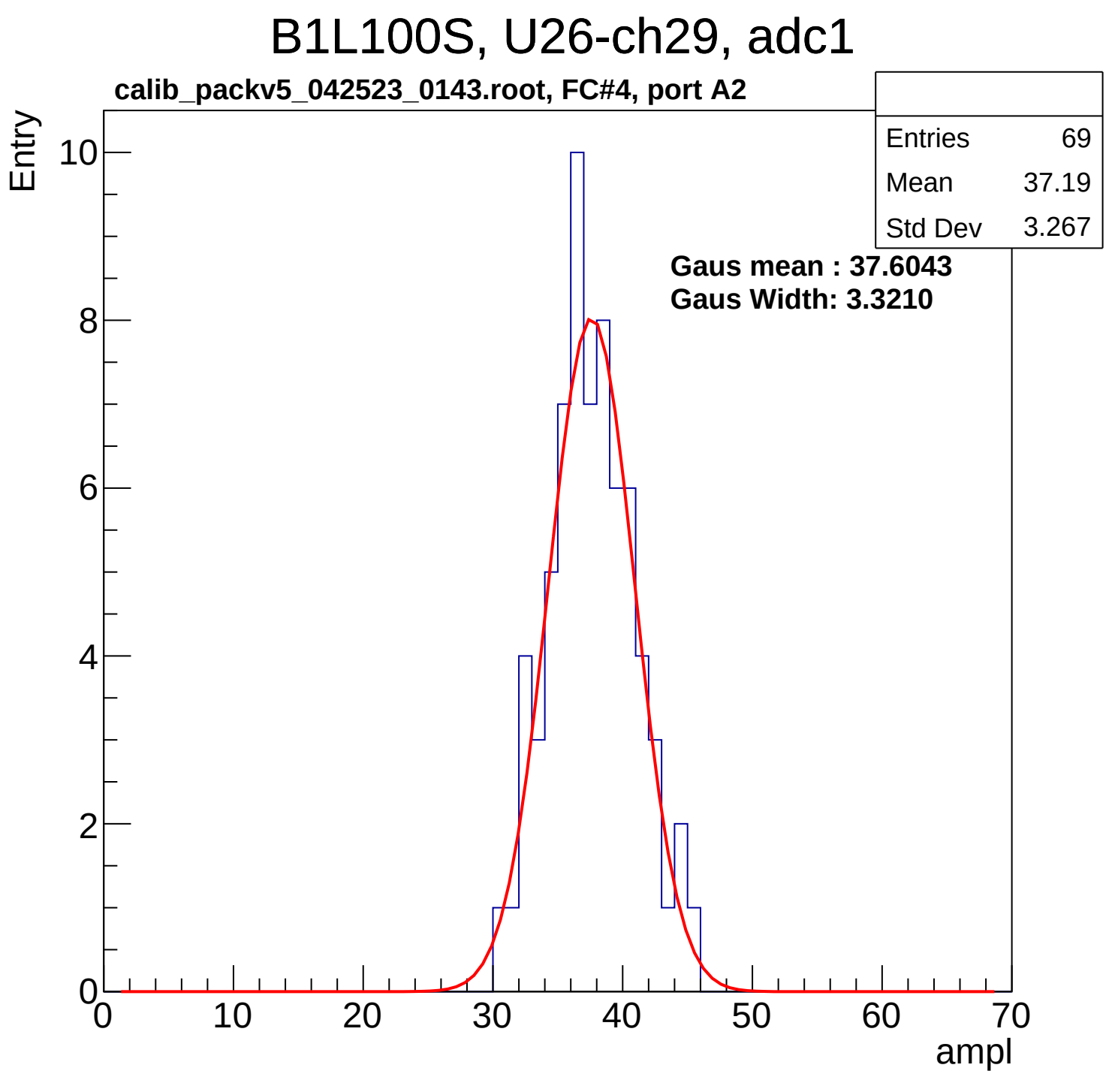

B1L100S, U26-ch28, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.12
Std Dev 8.009
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

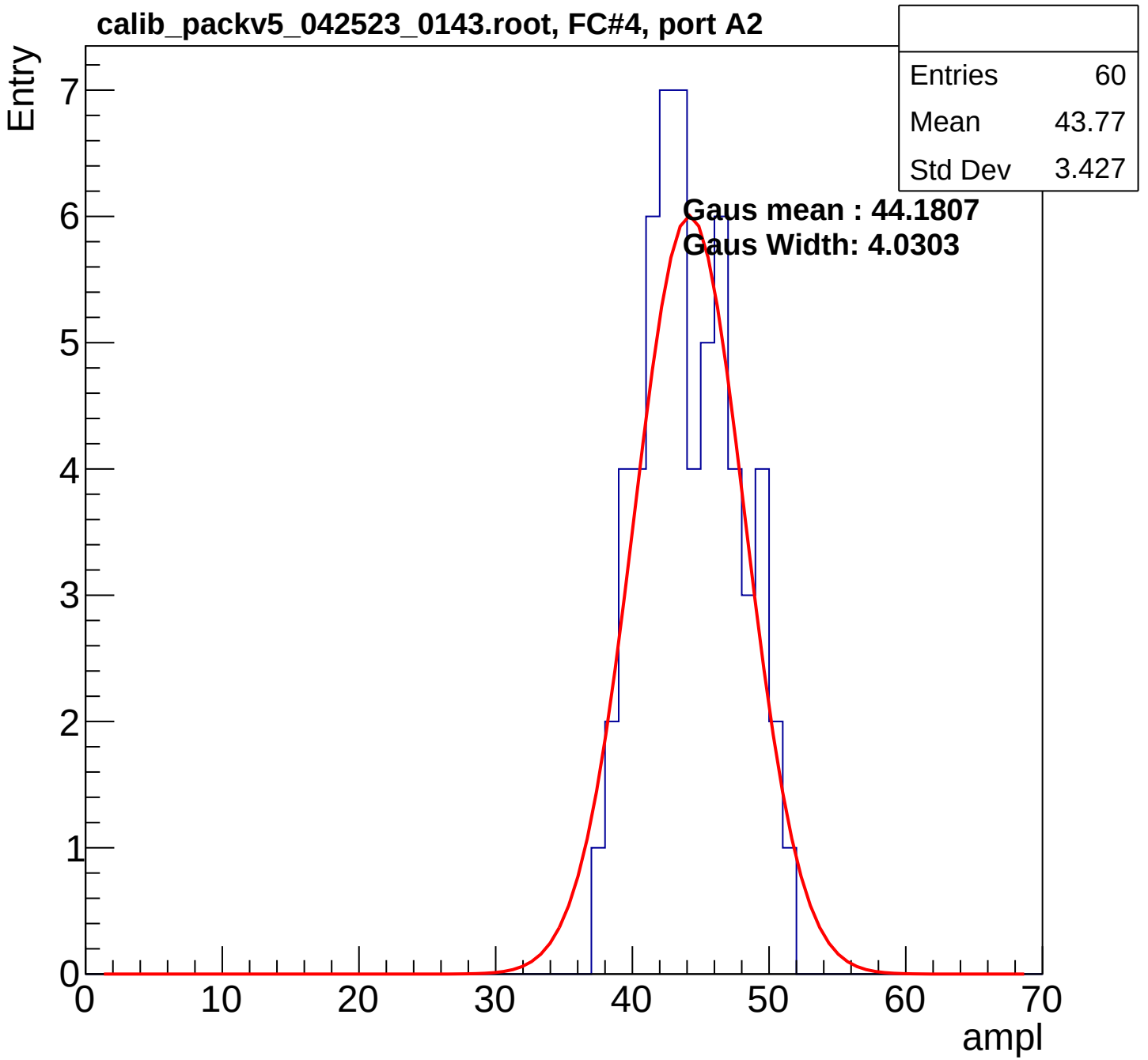

B1L100S, U26-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 61.48
Std Dev 1.716
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

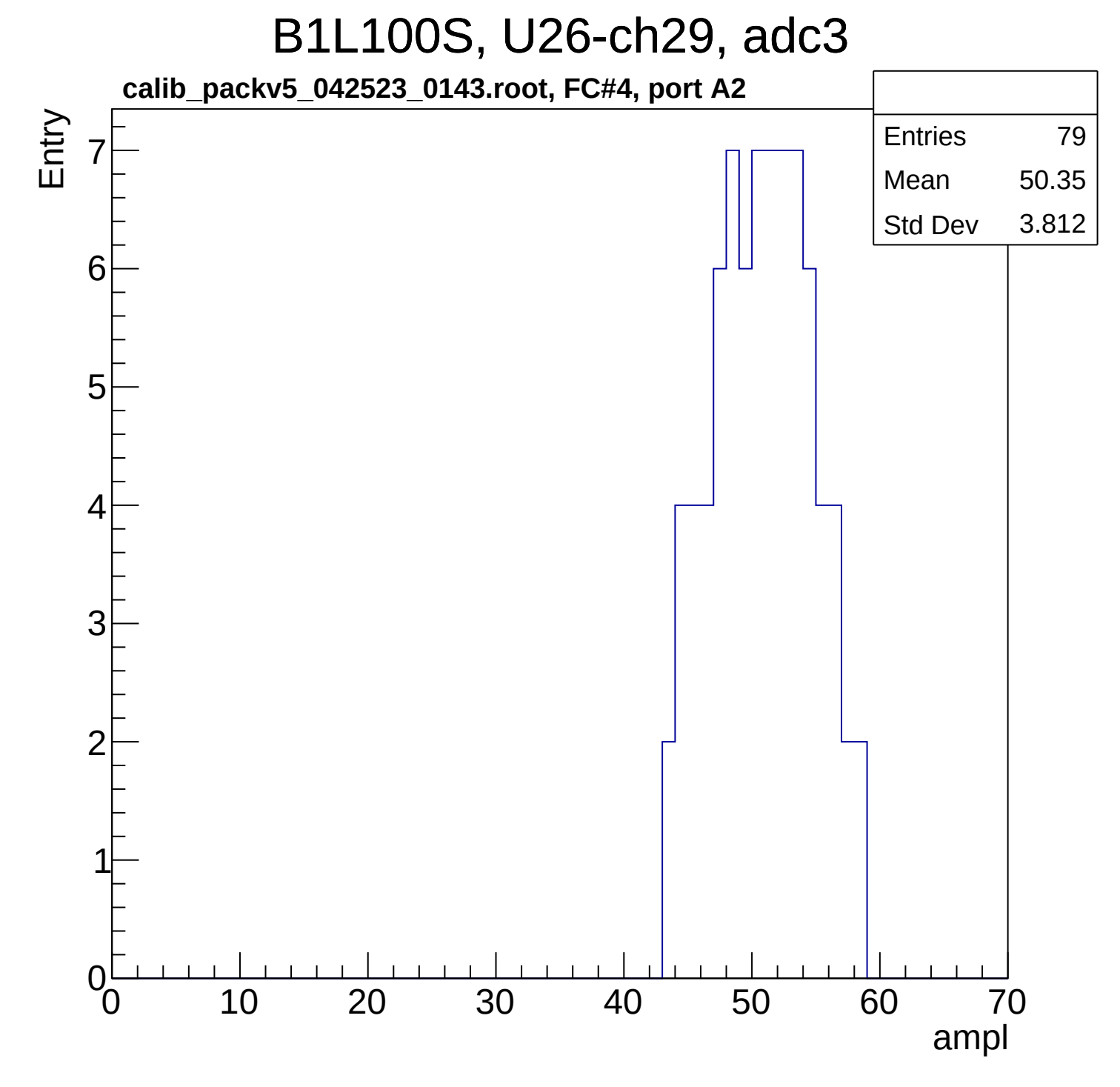

B1L100S, U26-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

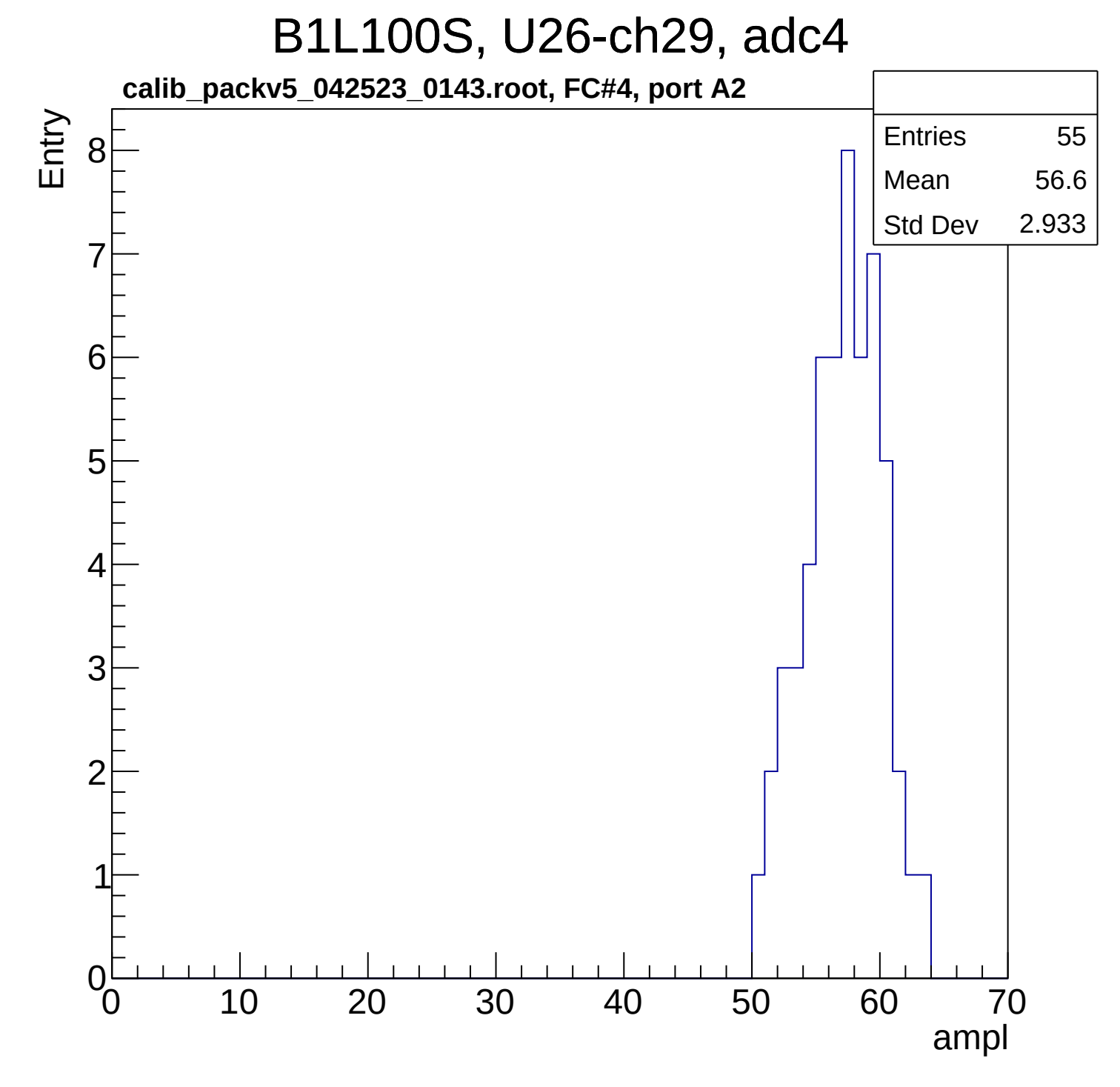

B1L100S, U26-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.12
Std Dev 4.939
Gaus mean : 30.7942
Gaus Width: 3.7819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.19
Std Dev 3.267
Gaus mean : 37.6043
Gaus Width: 3.3210
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.77
Std Dev 3.427
Gaus mean : 44.1807
Gaus Width: 4.0303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch29, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 50.35
Std Dev 3.812
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch29, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.6
Std Dev 2.933
Entry
0
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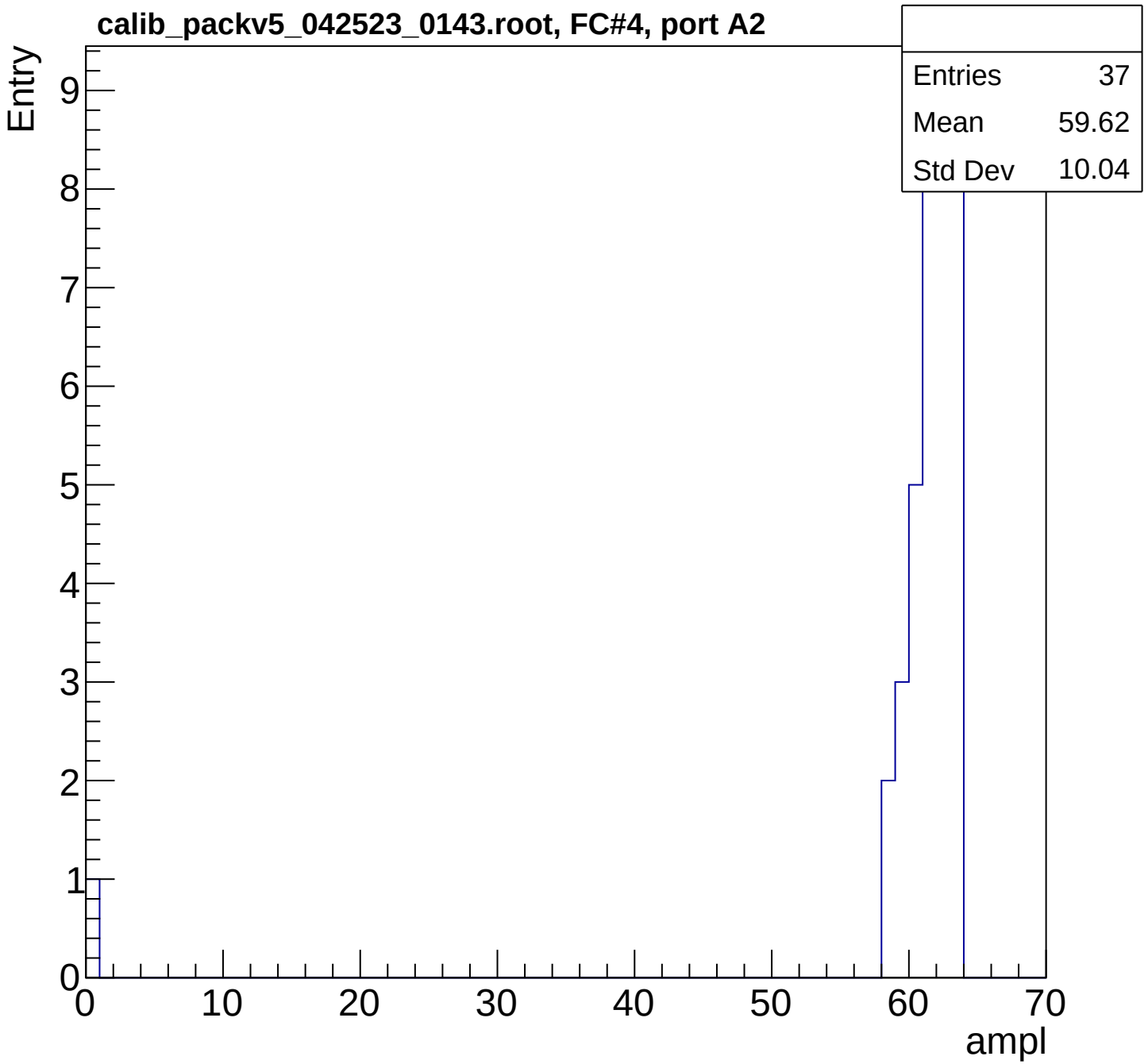
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.62
Std Dev 10.04
Entry
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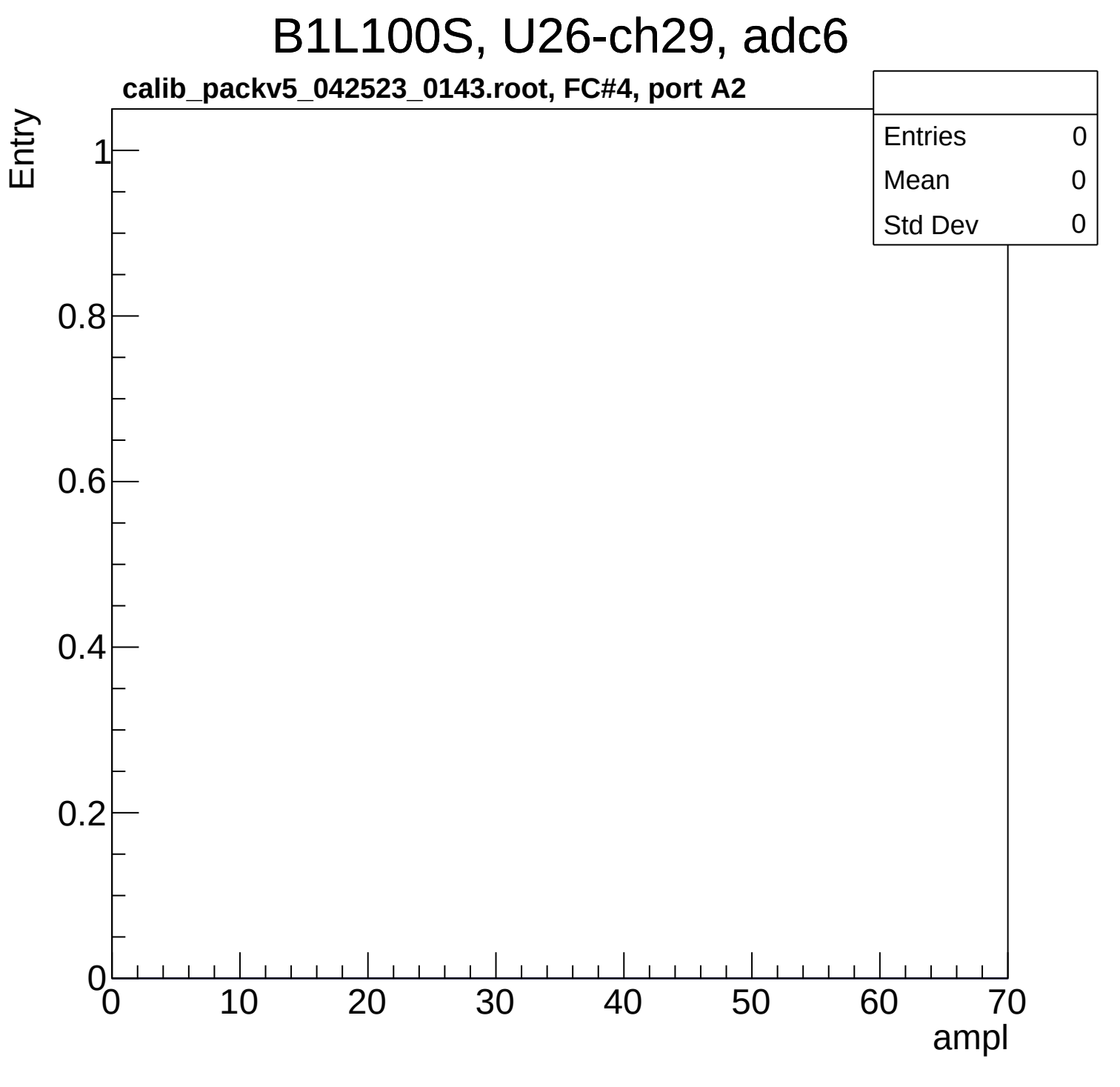
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch29, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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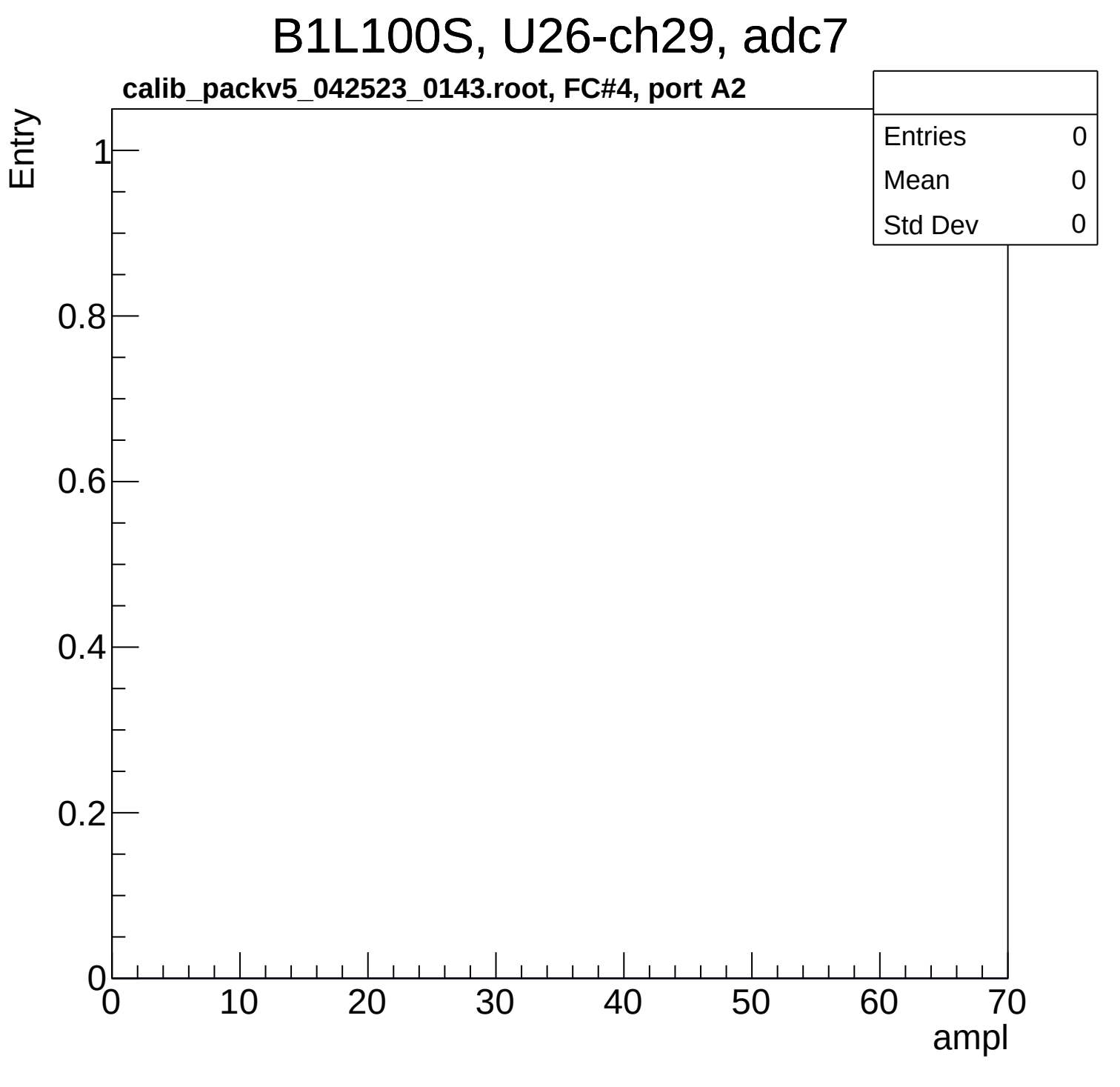
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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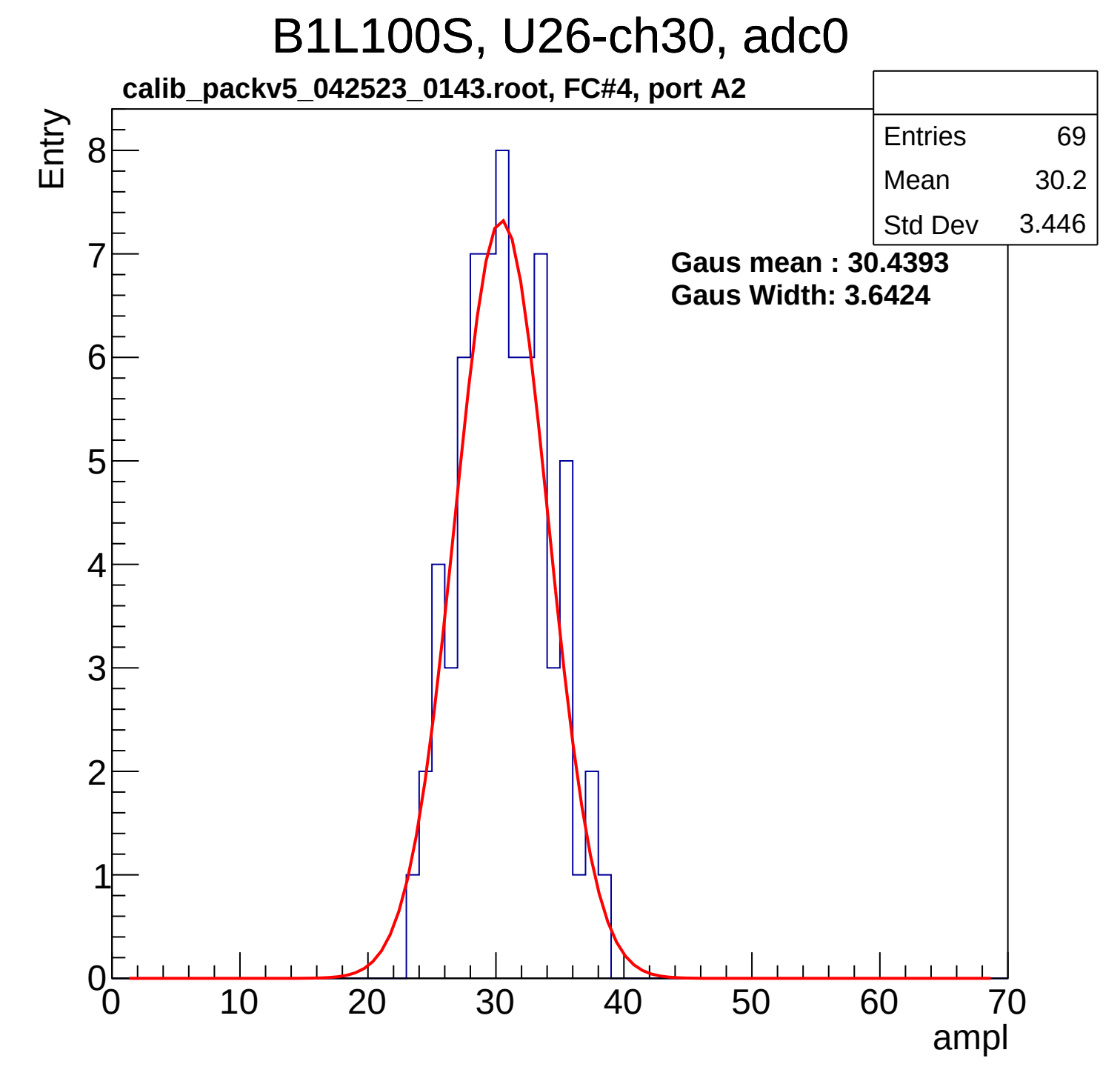
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 30.2
Std Dev 3.446
Gaus mean : 30.4393
Gaus Width: 3.6424
Entry
ampl
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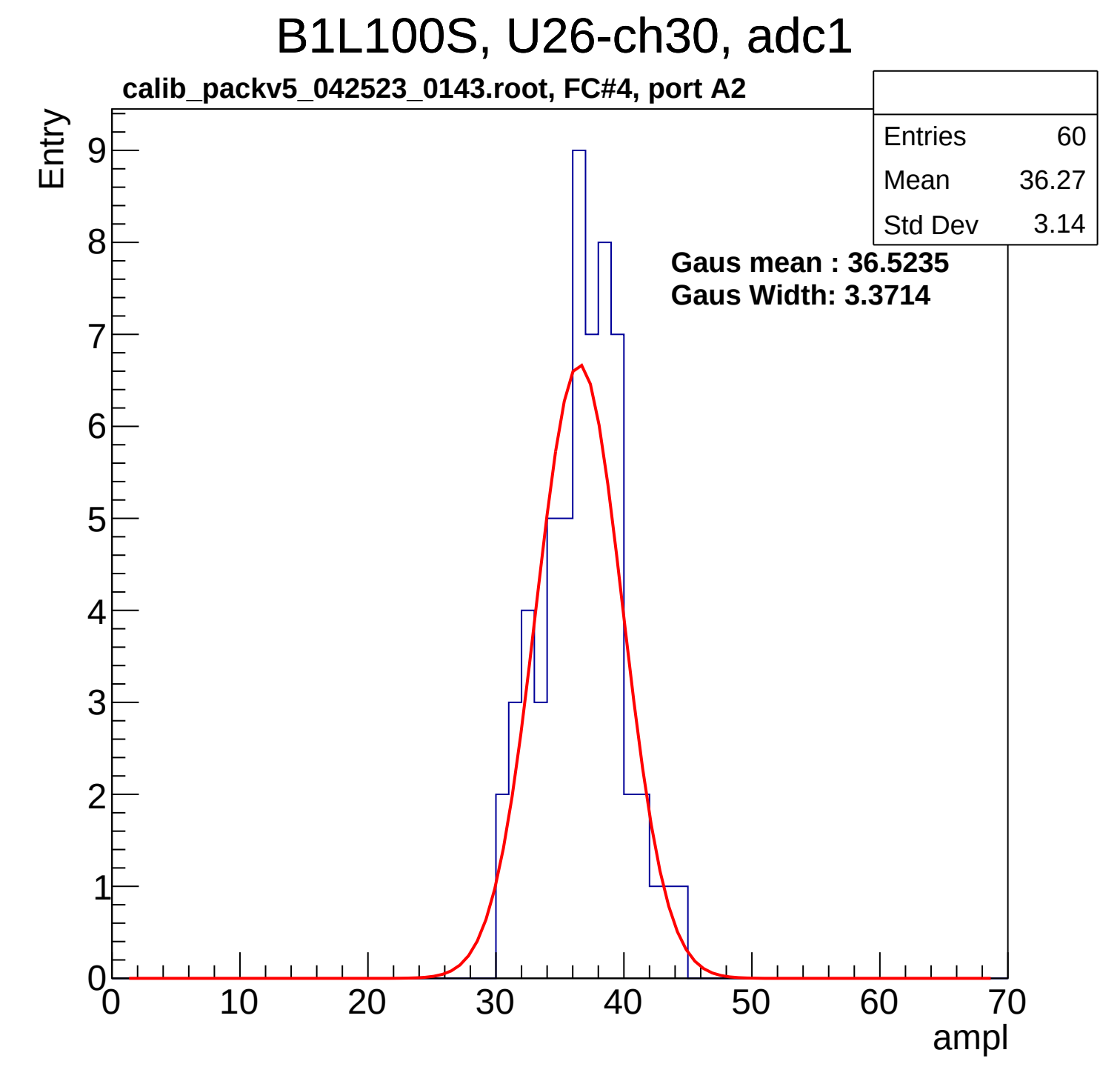
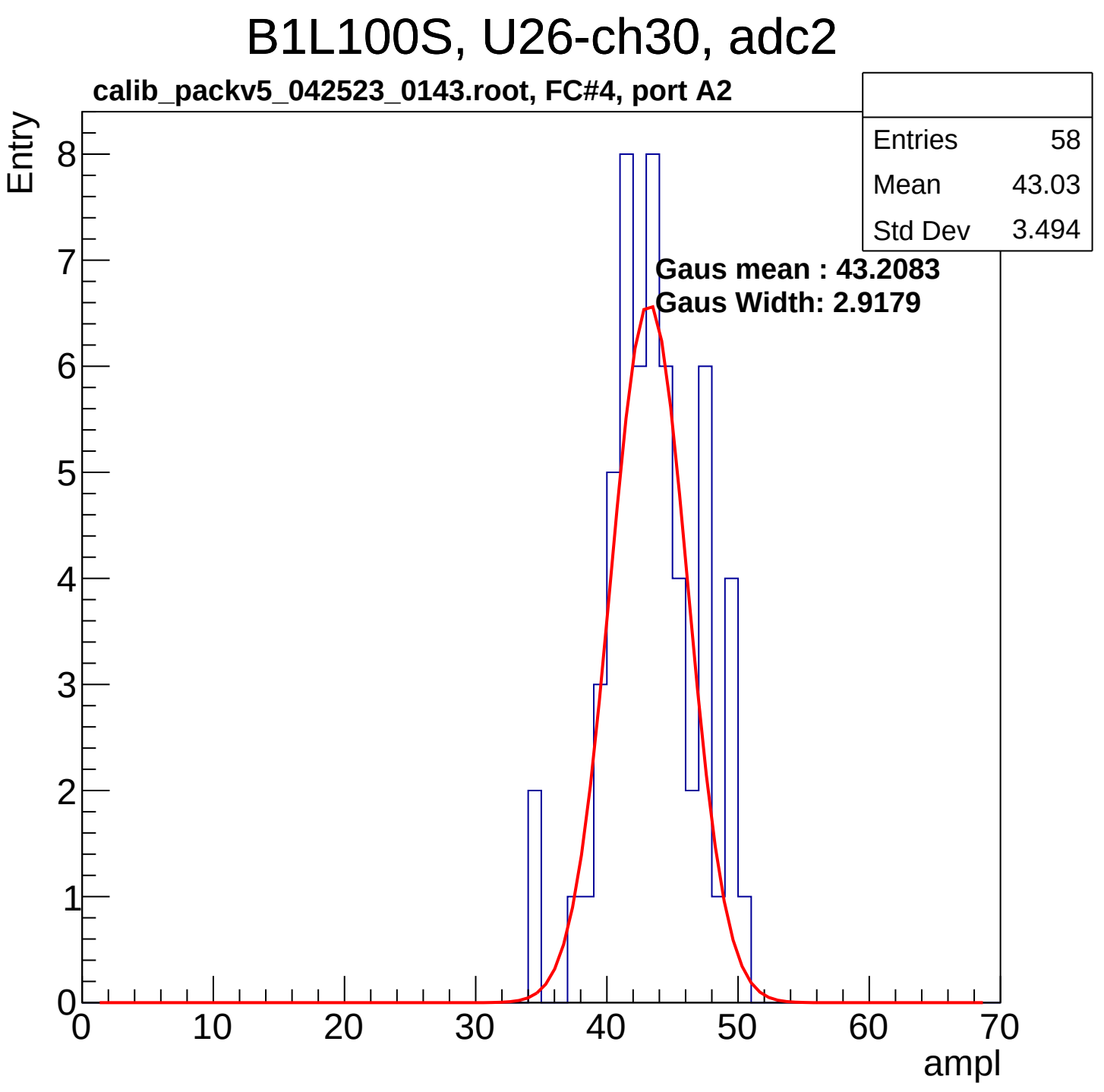
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.27
Std Dev 3.14
Gaus mean : 36.5235
Gaus Width: 3.3714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.03
Std Dev 3.494
Gaus mean : 43.2083
Gaus Width: 2.9179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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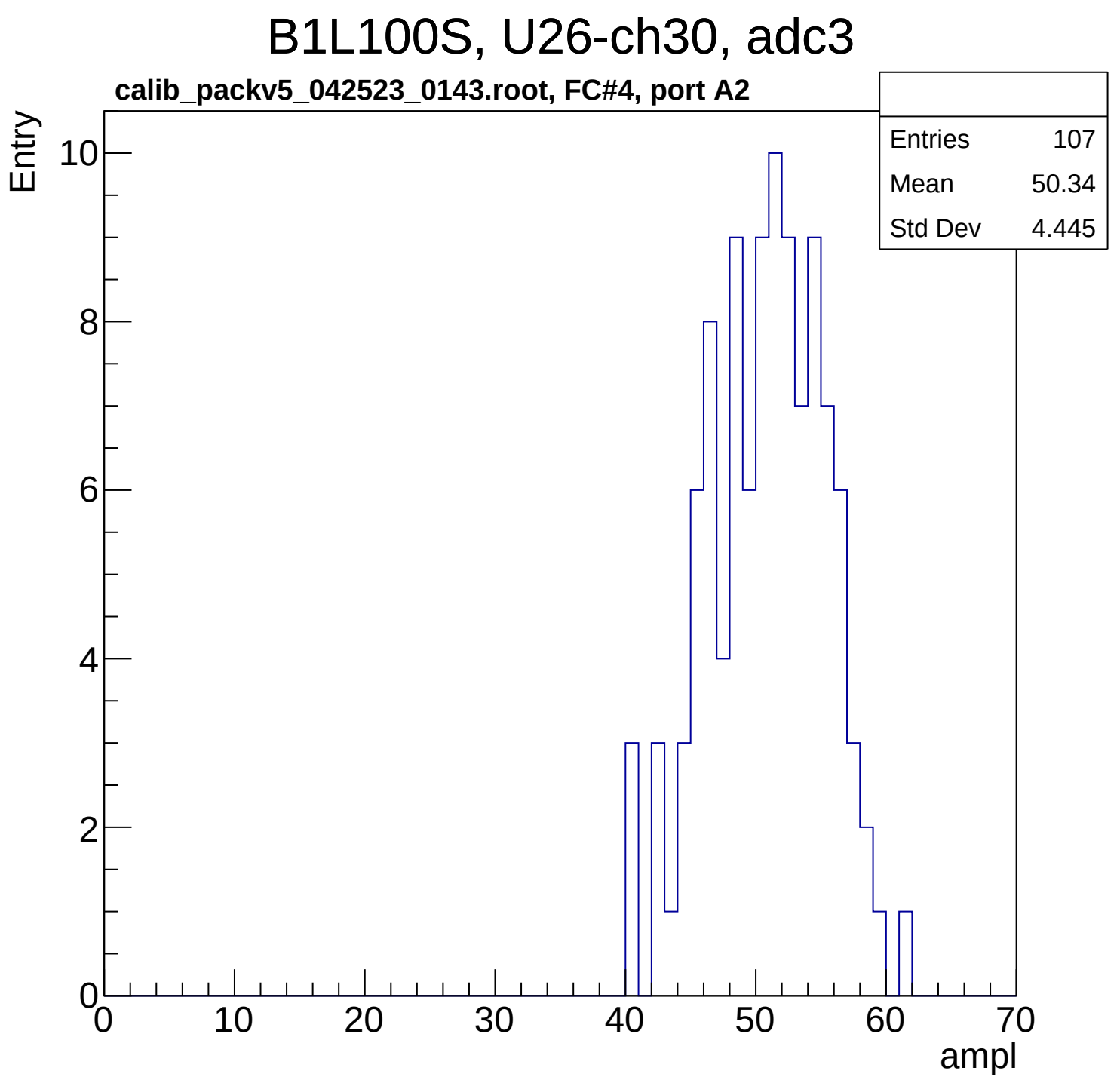
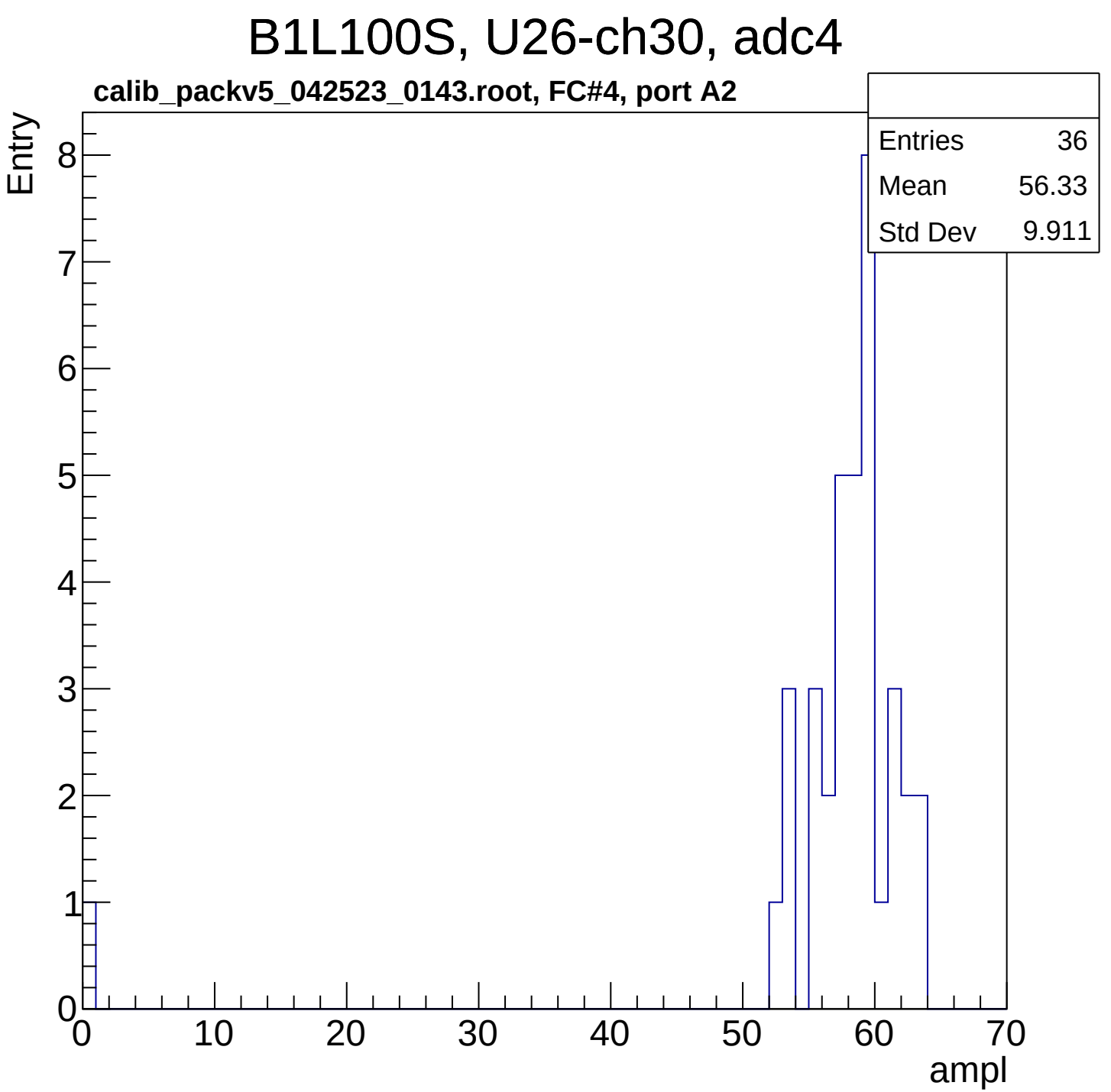
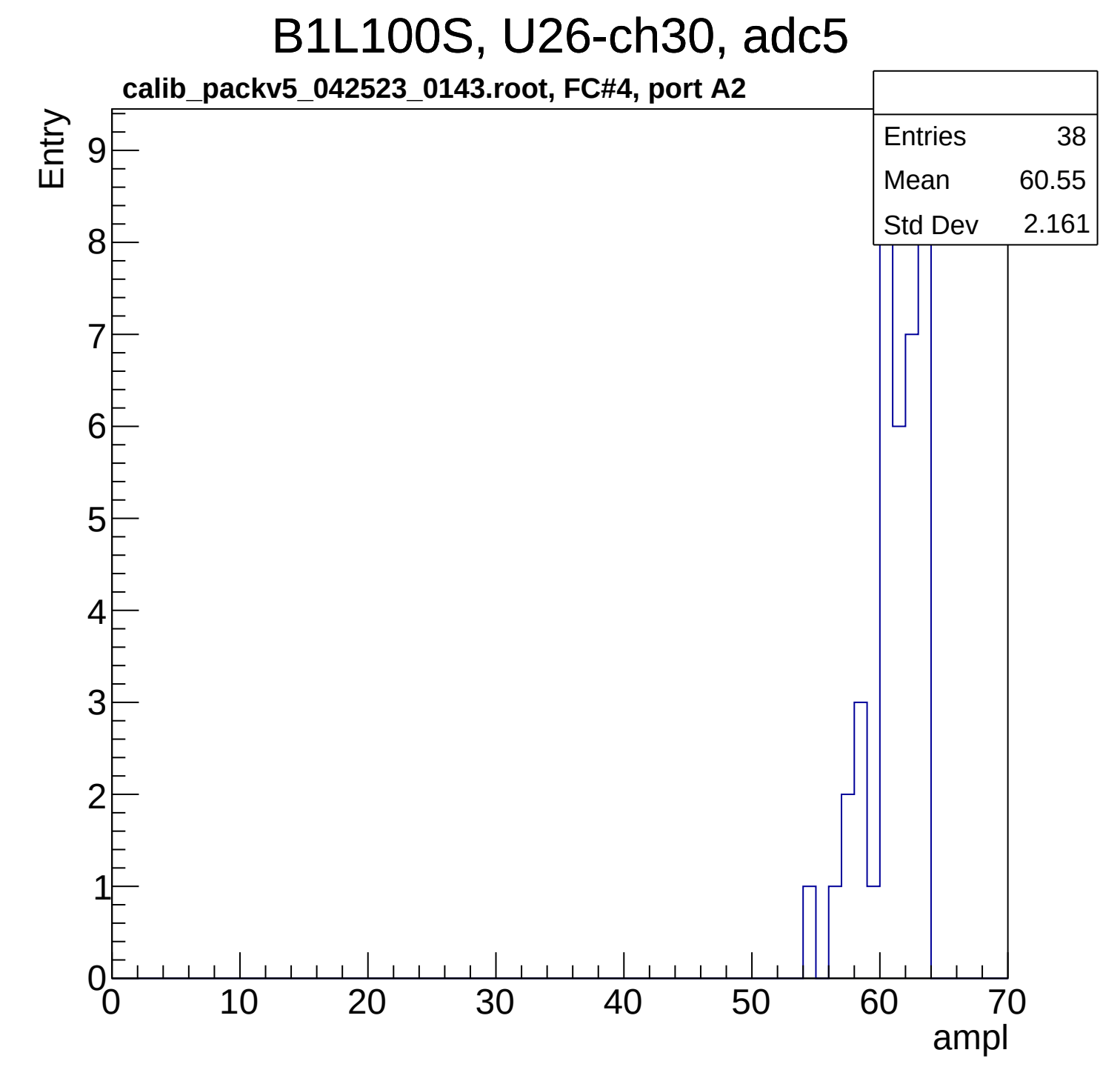
20
30
40
50
60
70


B1L100S, U26-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 107
Mean 50.34
Std Dev 4.445
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 56.33
Std Dev 9.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.55
Std Dev 2.161
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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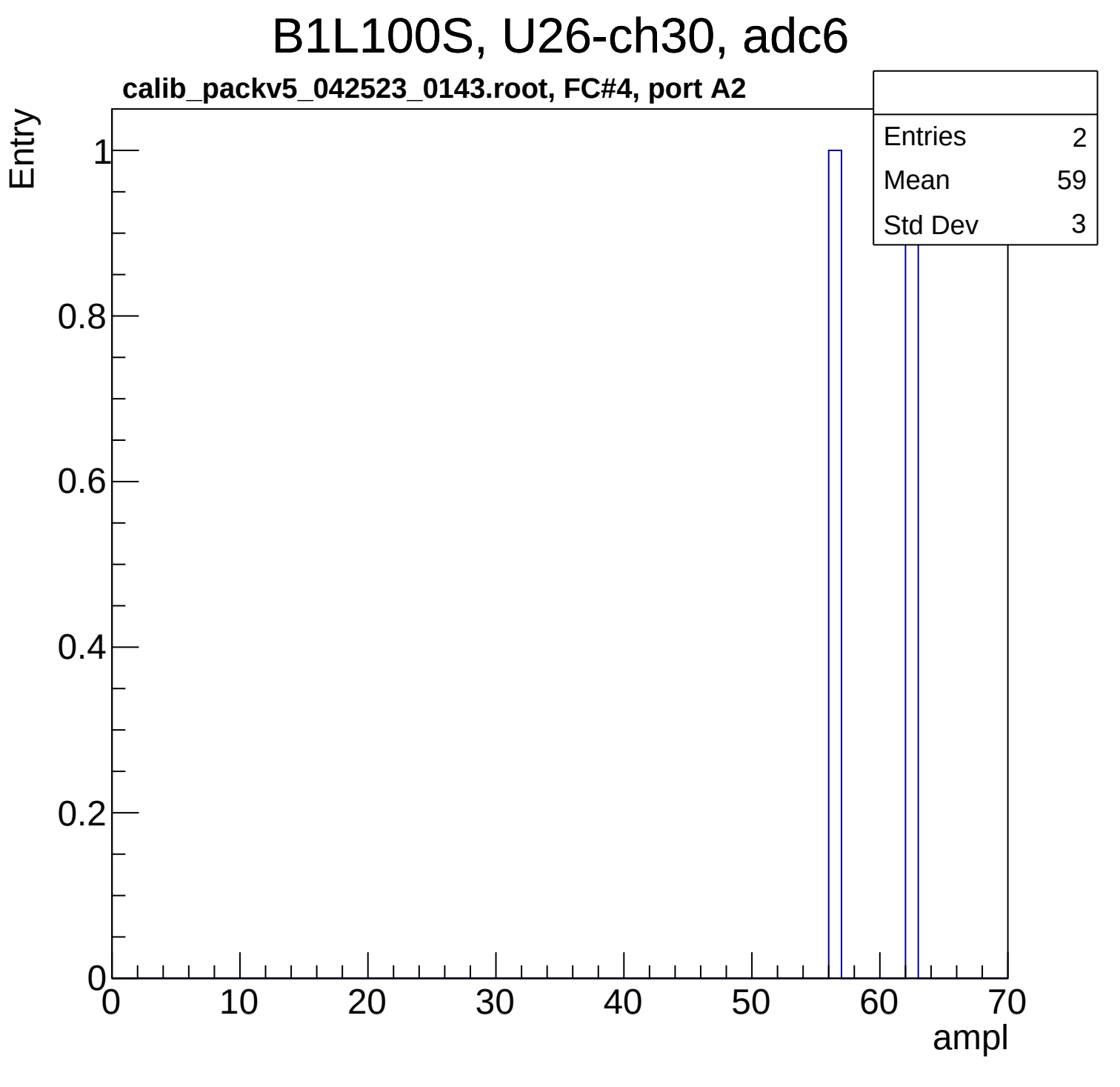
B1L100S, U26-ch30, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 59
Std Dev 3
Entry
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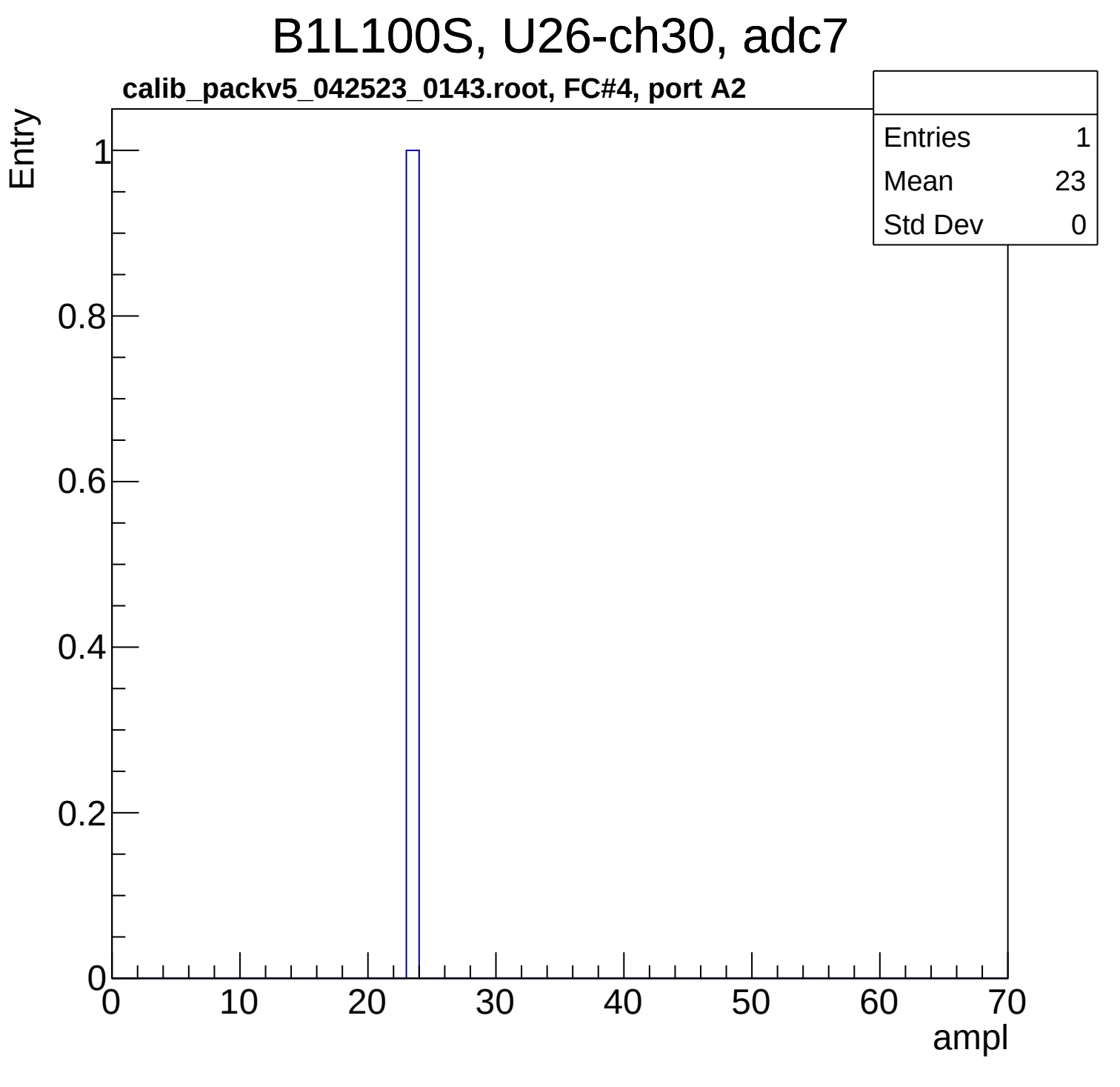
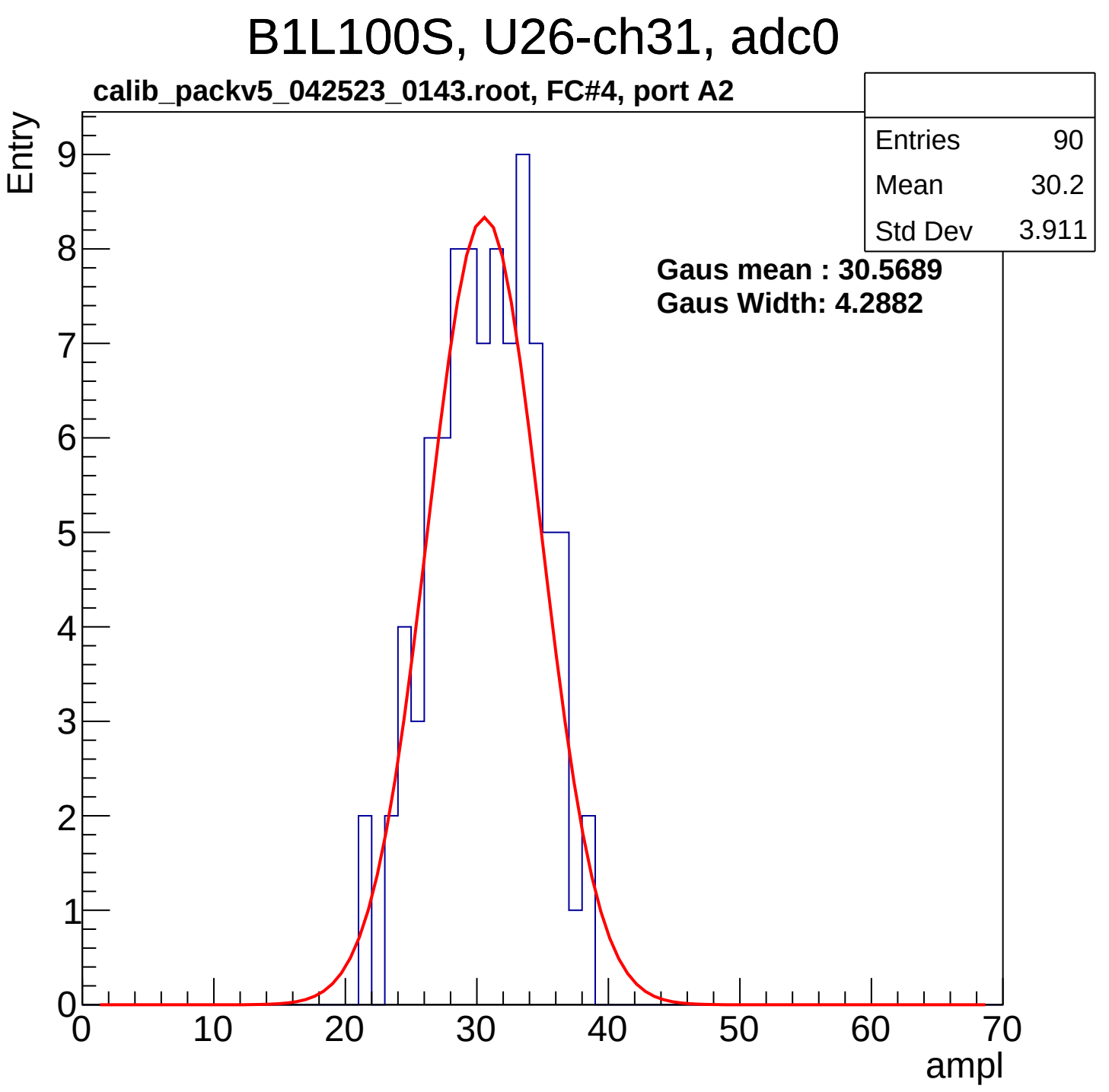
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 30.2
Std Dev 3.911
Gaus mean : 30.5689
Gaus Width: 4.2882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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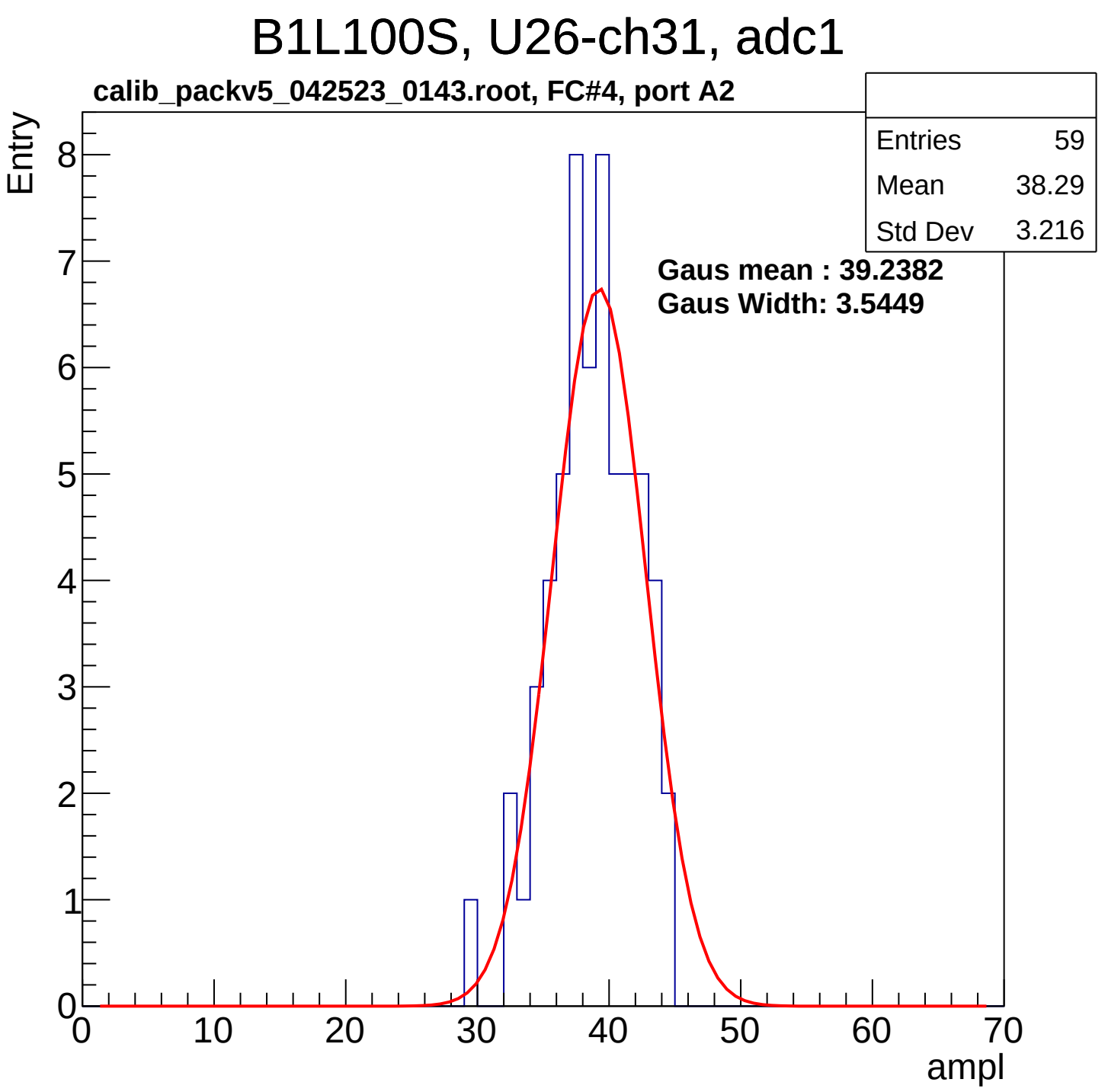
B1L100S, U26-ch31, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 38.29
Std Dev 3.216
Gaus mean : 39.2382
Gaus Width: 3.5449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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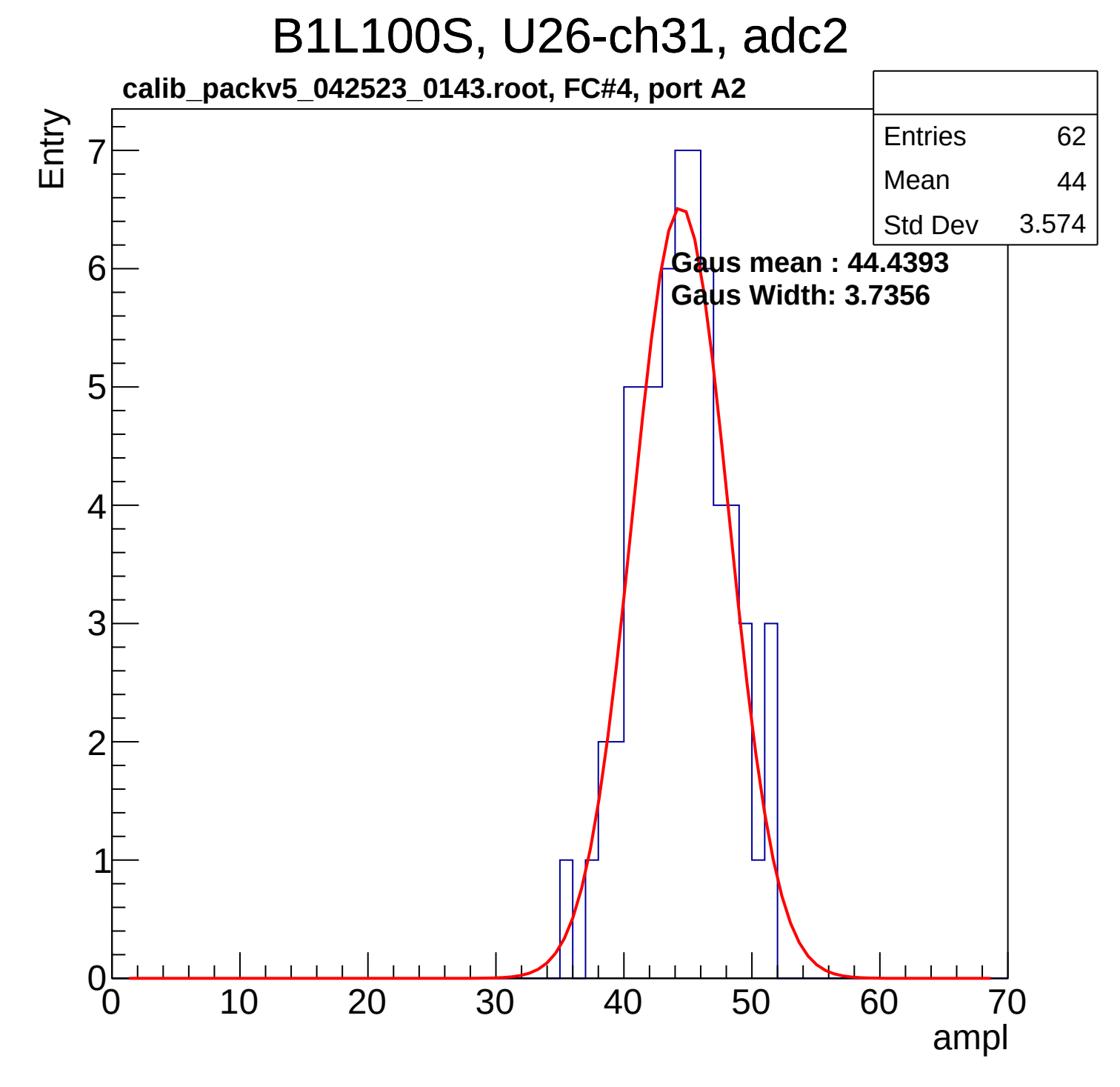
B1L100S, U26-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44
Std Dev 3.574
Gaus mean : 44.4393
Gaus Width: 3.7356
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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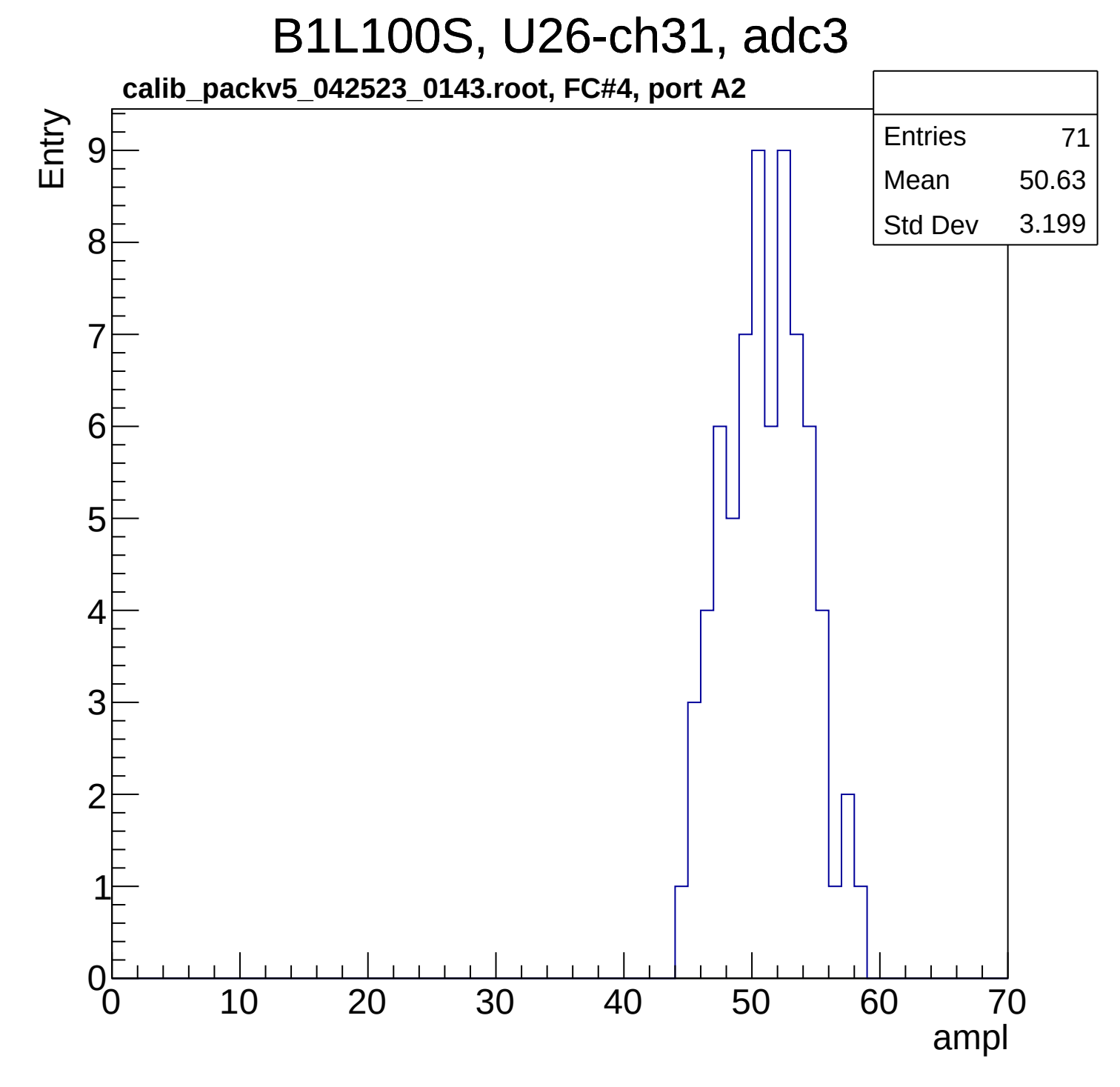
B1L100S, U26-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.63
Std Dev 3.199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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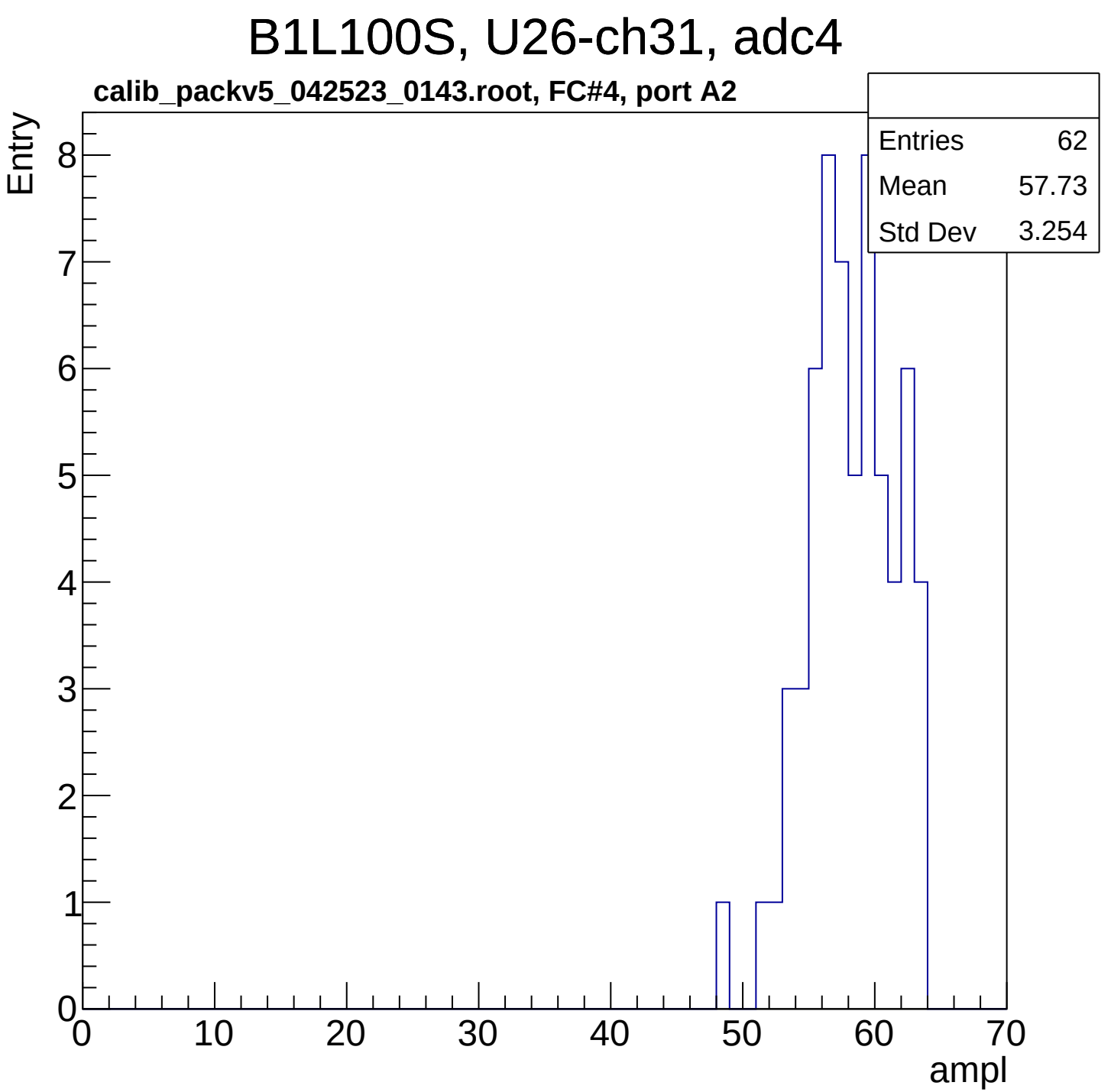
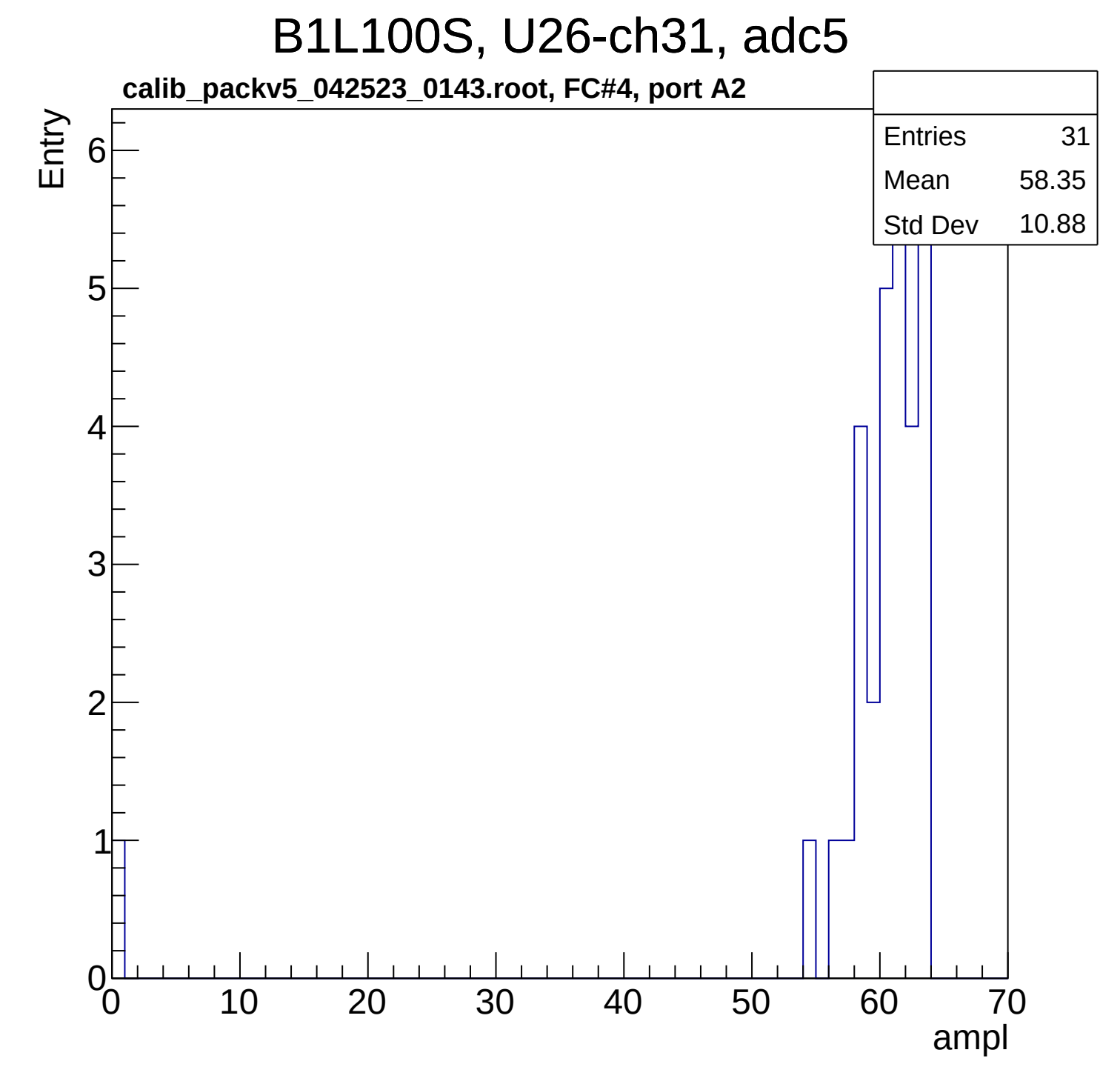

B1L100S, U26-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.73
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.35
Std Dev 10.88
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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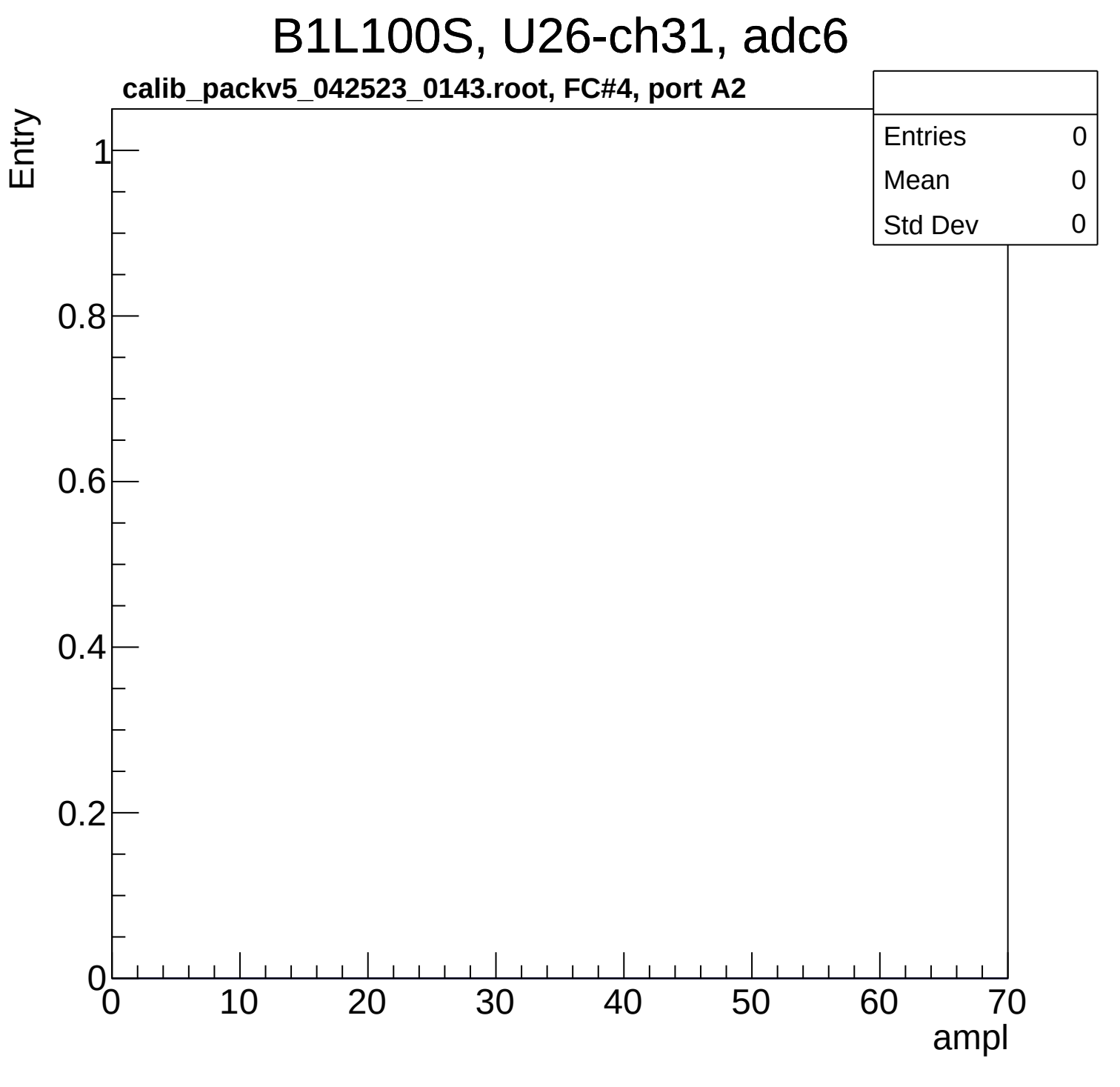
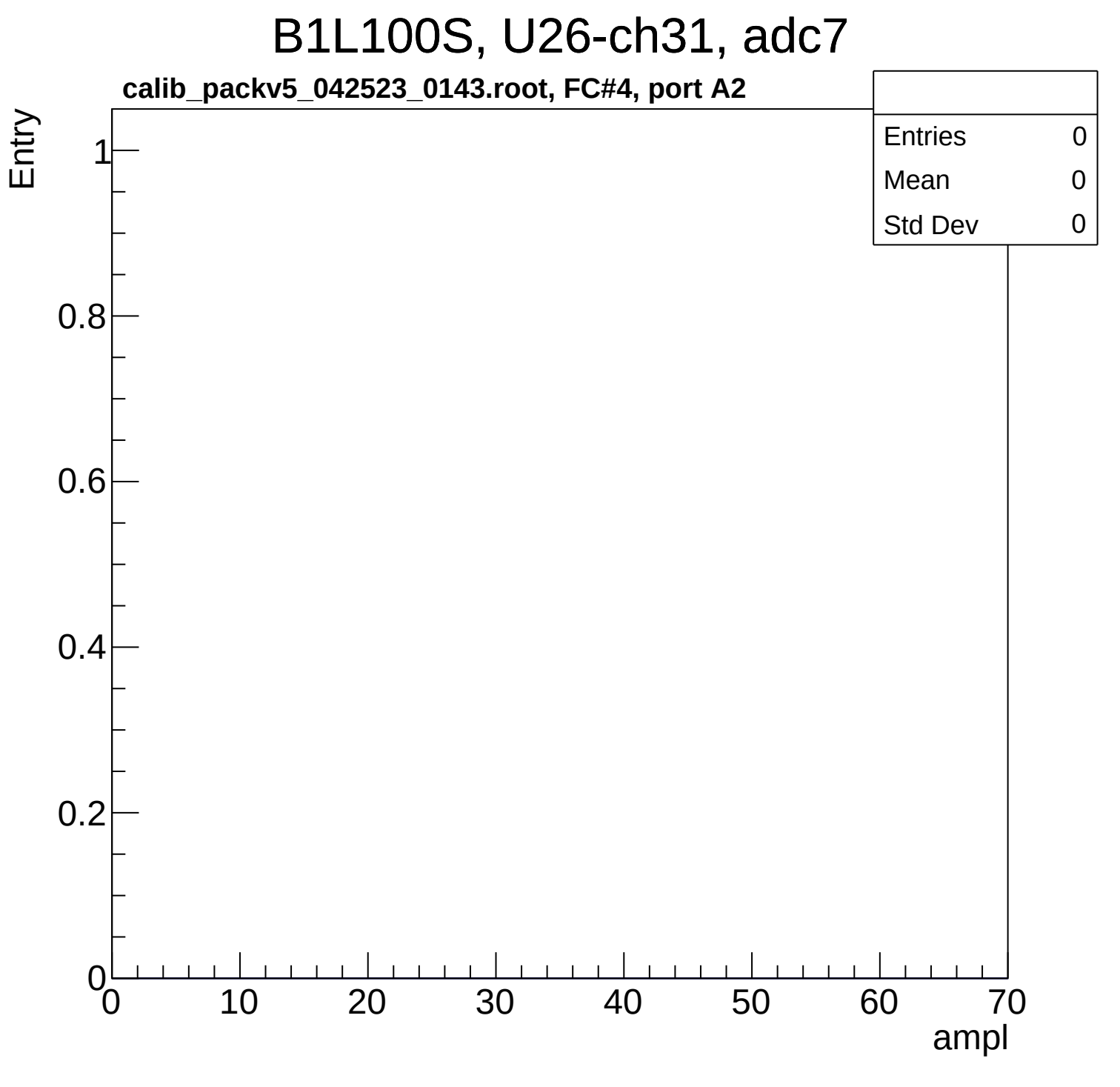
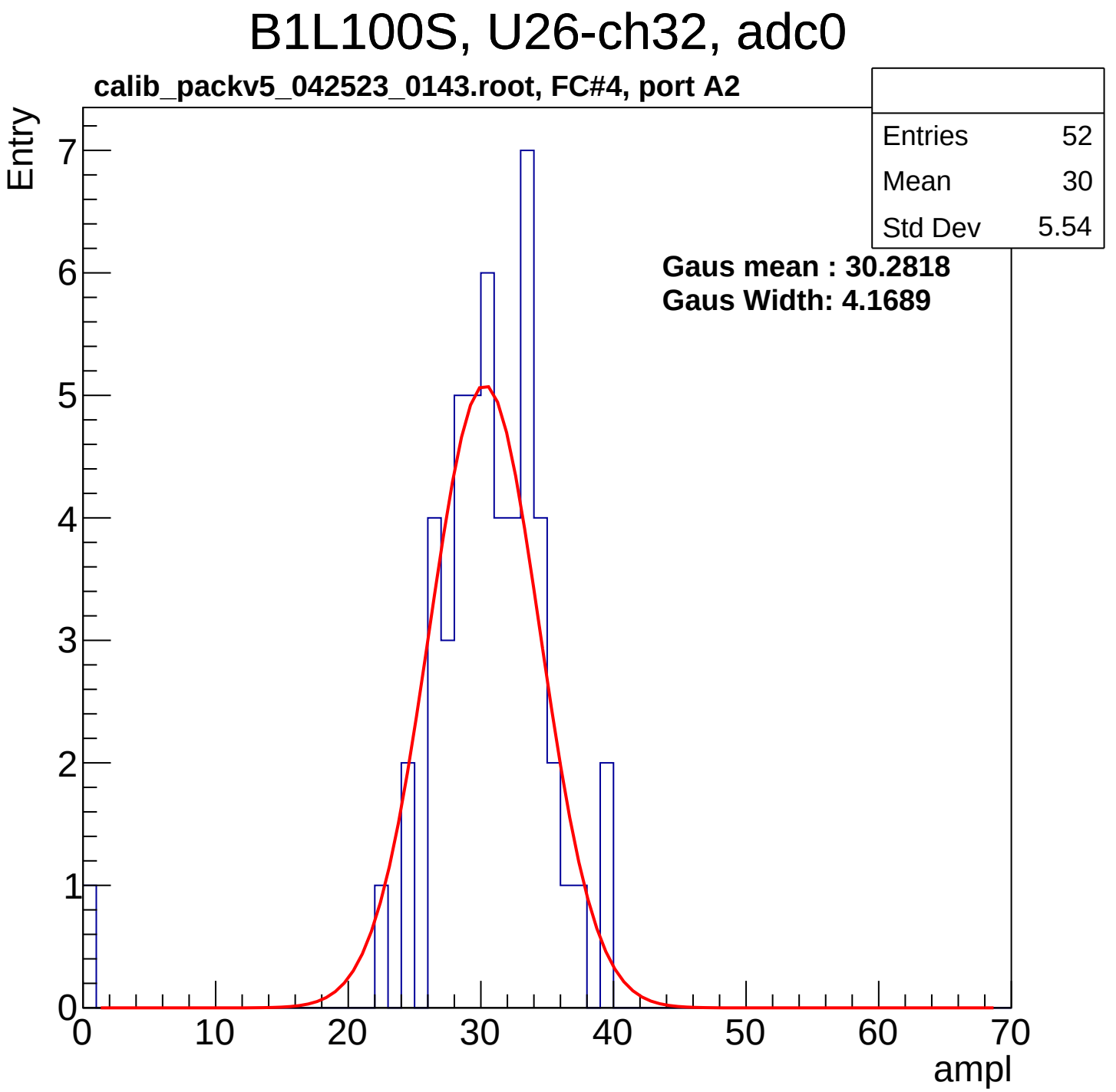
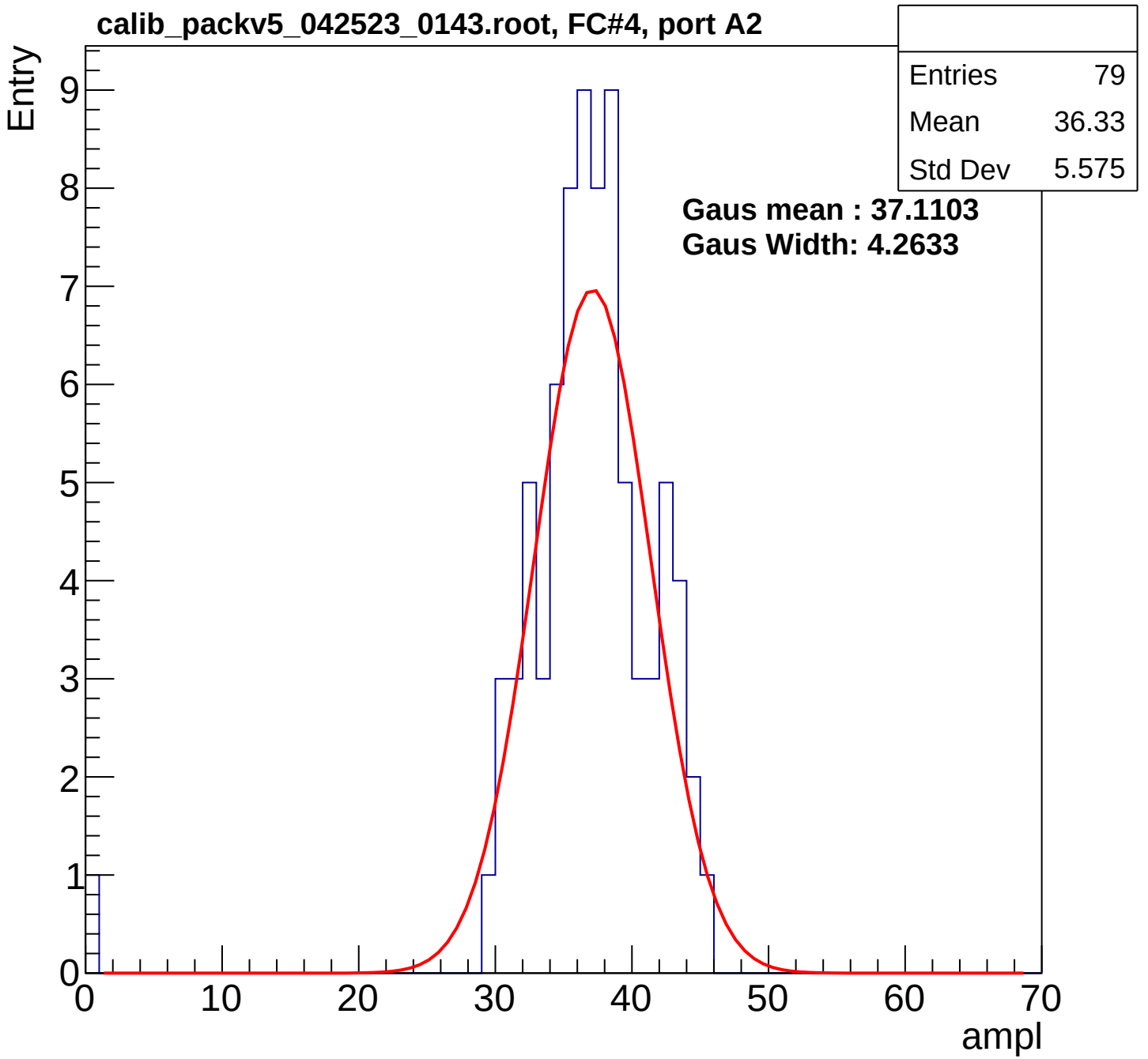

B1L100S, U26-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 30
Std Dev 5.54
Gaus mean : 30.2818
Gaus Width: 4.1689
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch32, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 36.33
Std Dev 5.575
Gaus mean : 37.1103
Gaus Width: 4.2633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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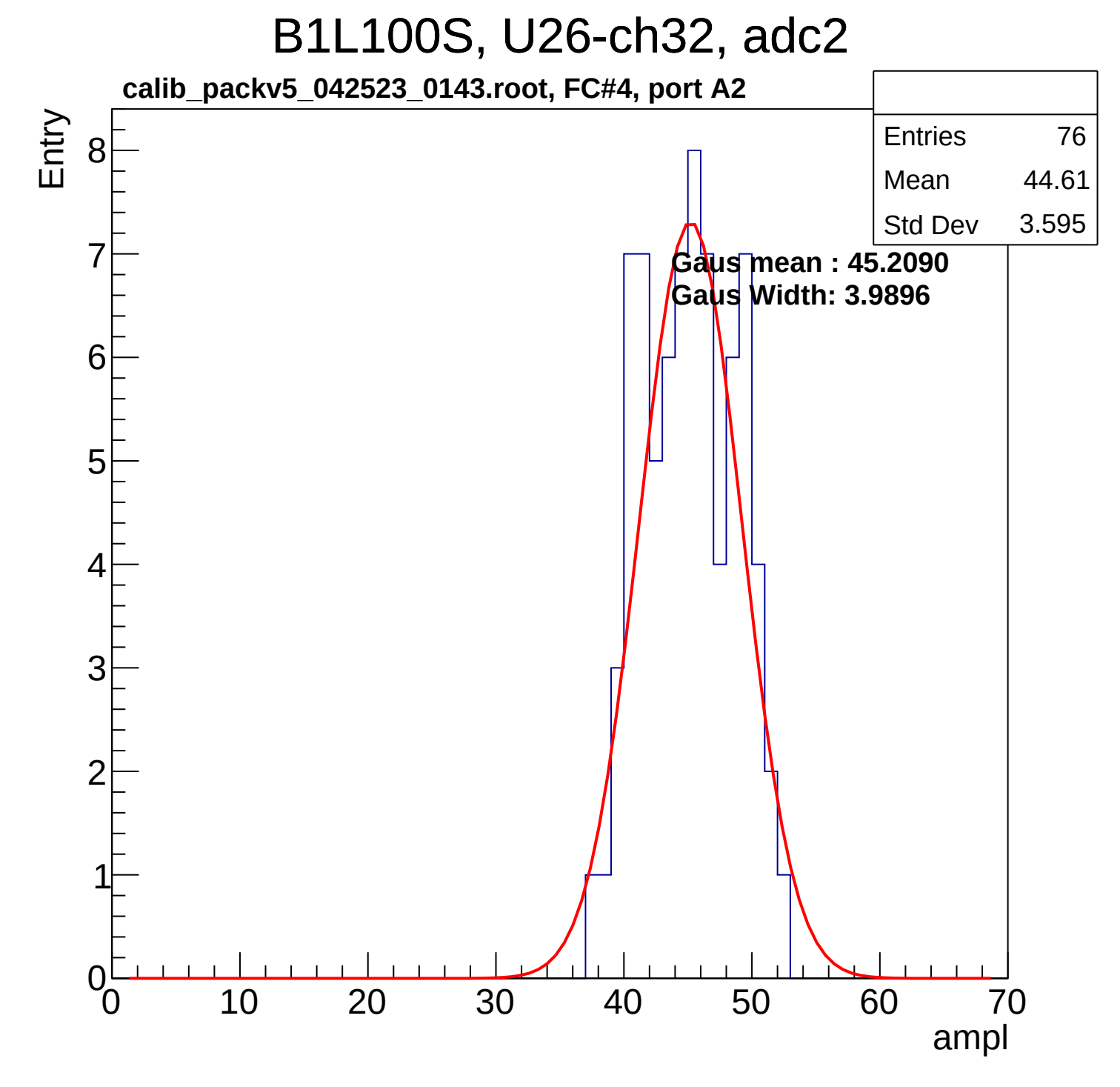
B1L100S, U26-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.61
Std Dev 3.595
Gaus mean : 45.2090
Gaus Width: 3.9896
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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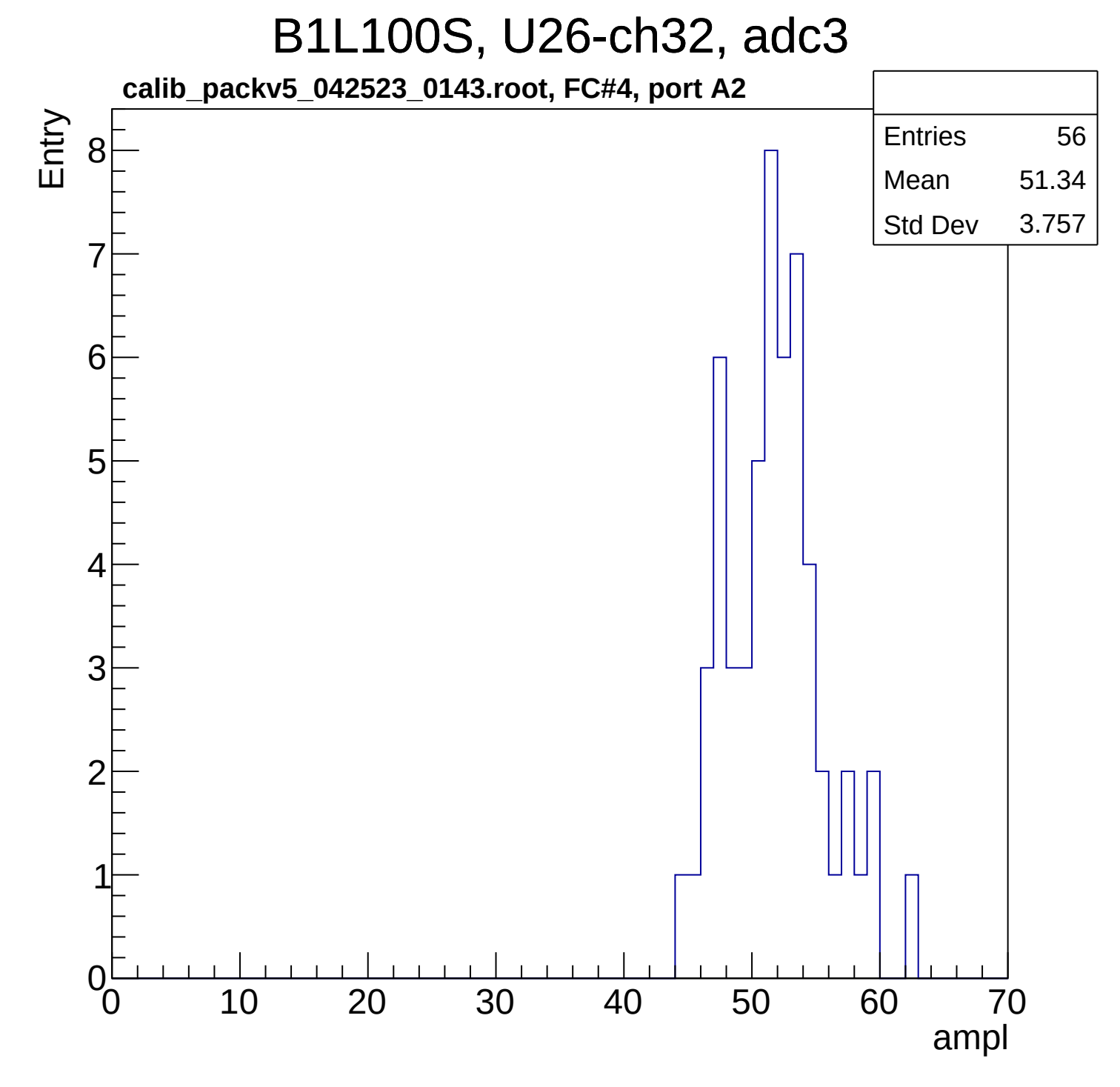
B1L100S, U26-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 51.34
Std Dev 3.757
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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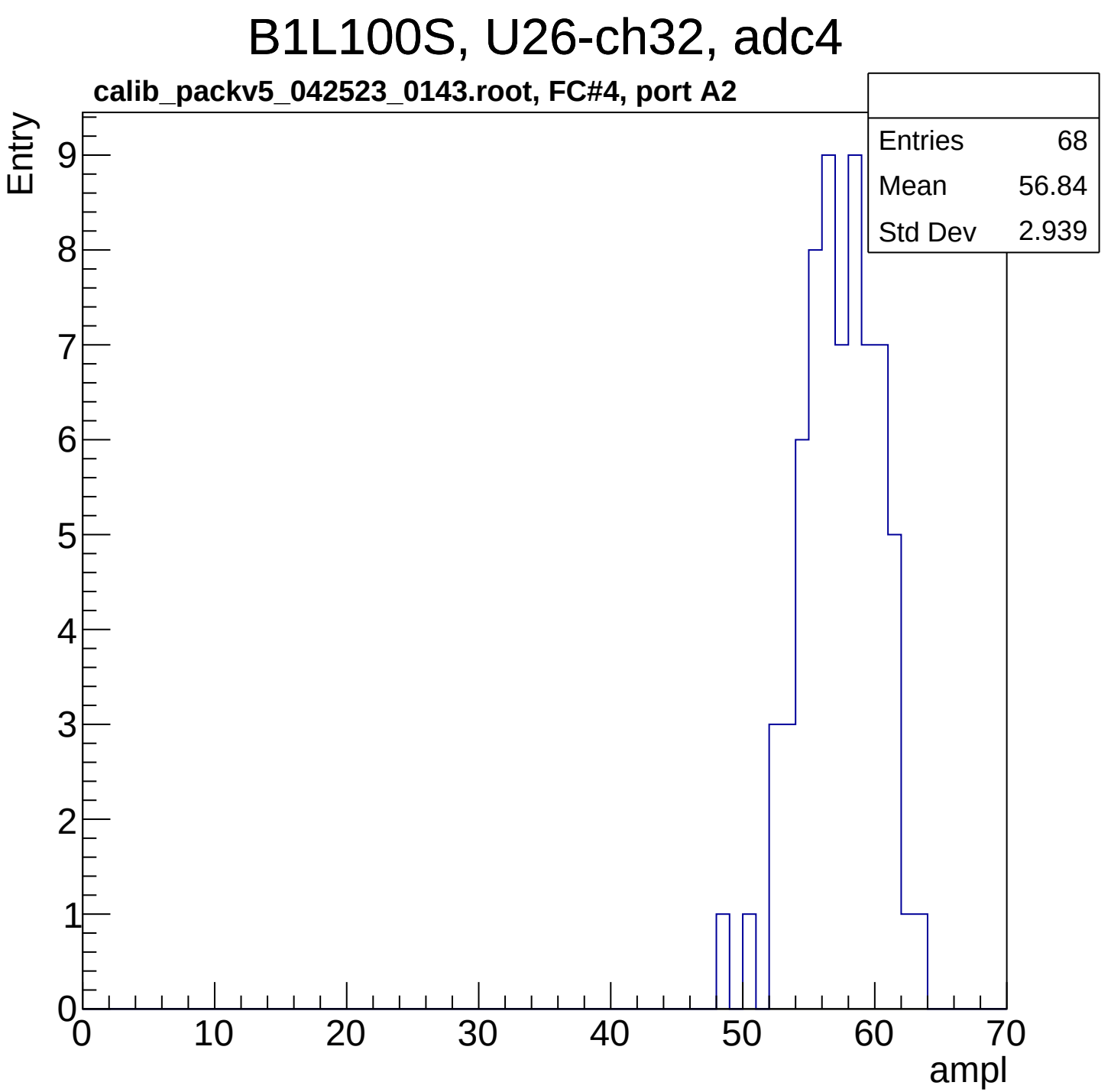
B1L100S, U26-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.84
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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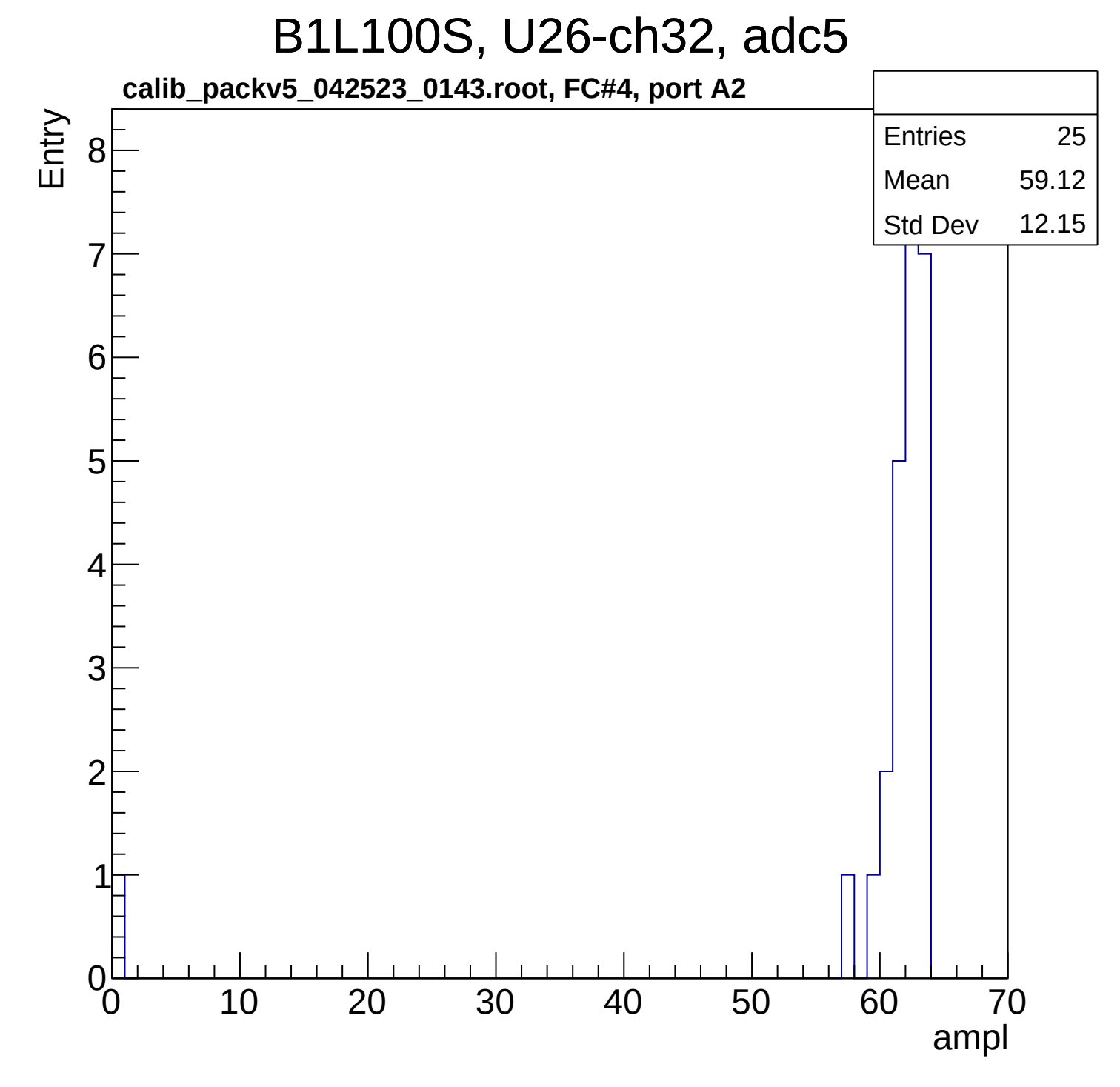
B1L100S, U26-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 59.12
Std Dev 12.15
Entry
ampl
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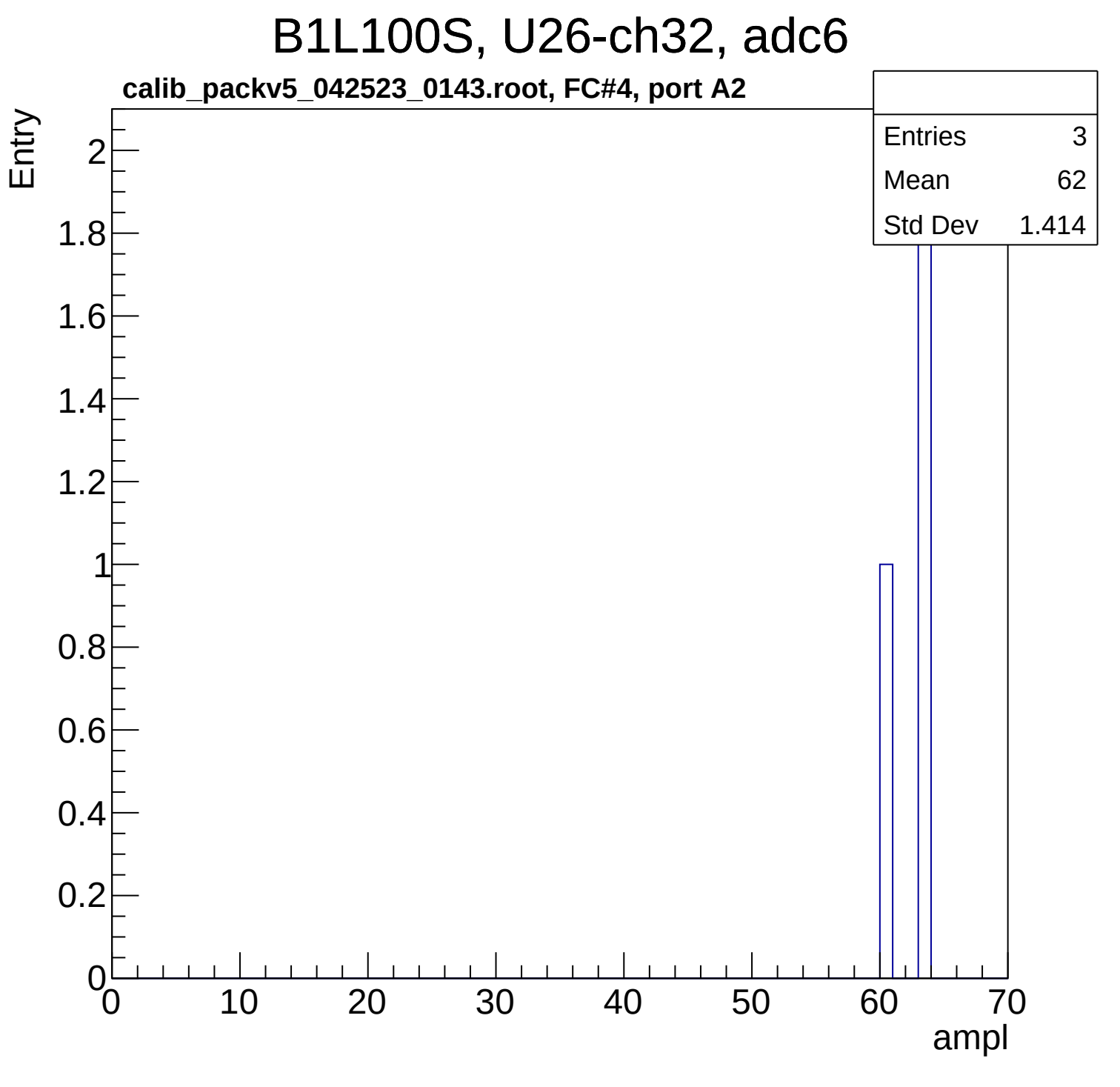
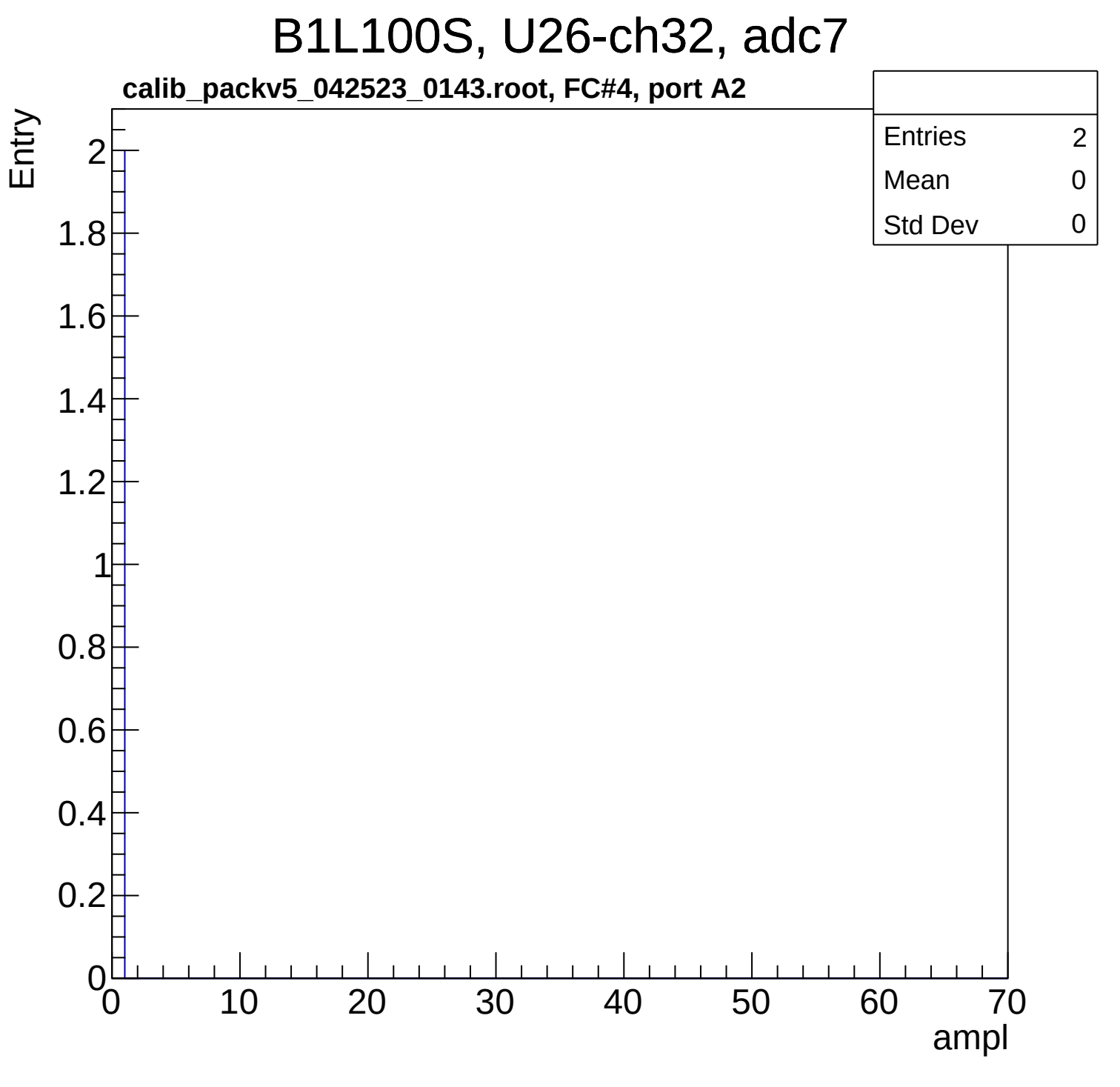
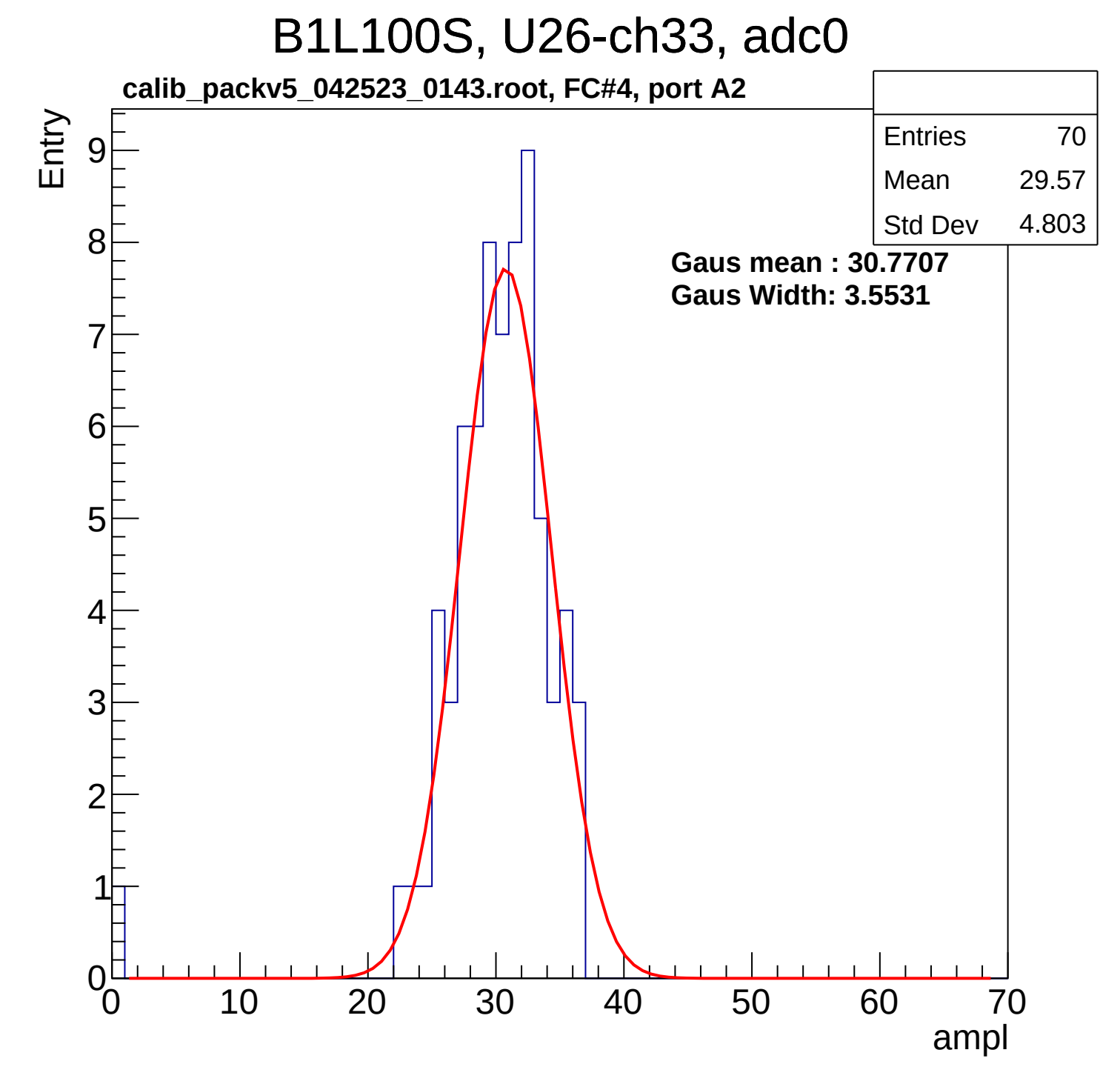
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 29.57
Std Dev 4.803
Gaus mean : 30.7707
Gaus Width: 3.5531
Entry
ampl
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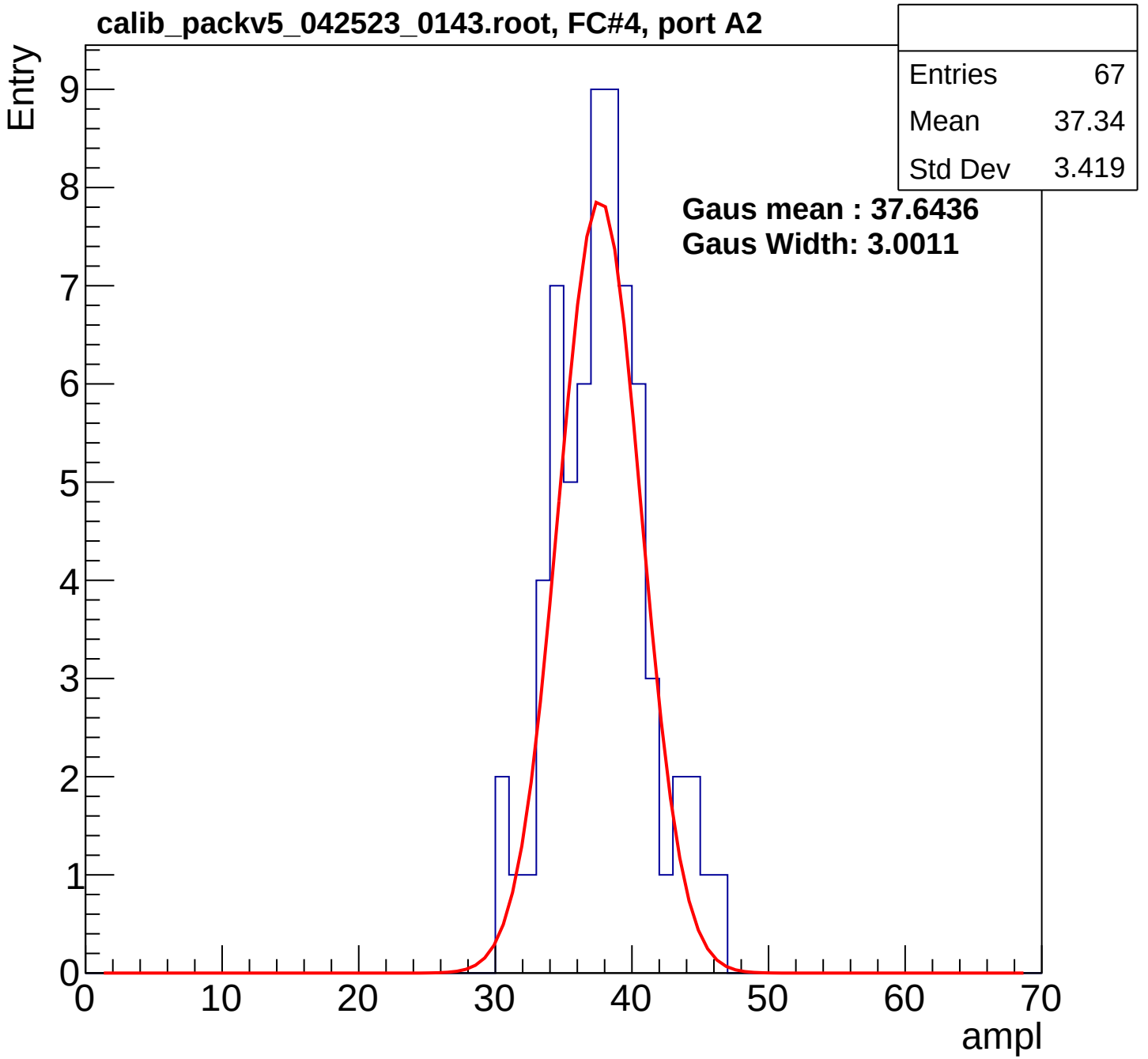
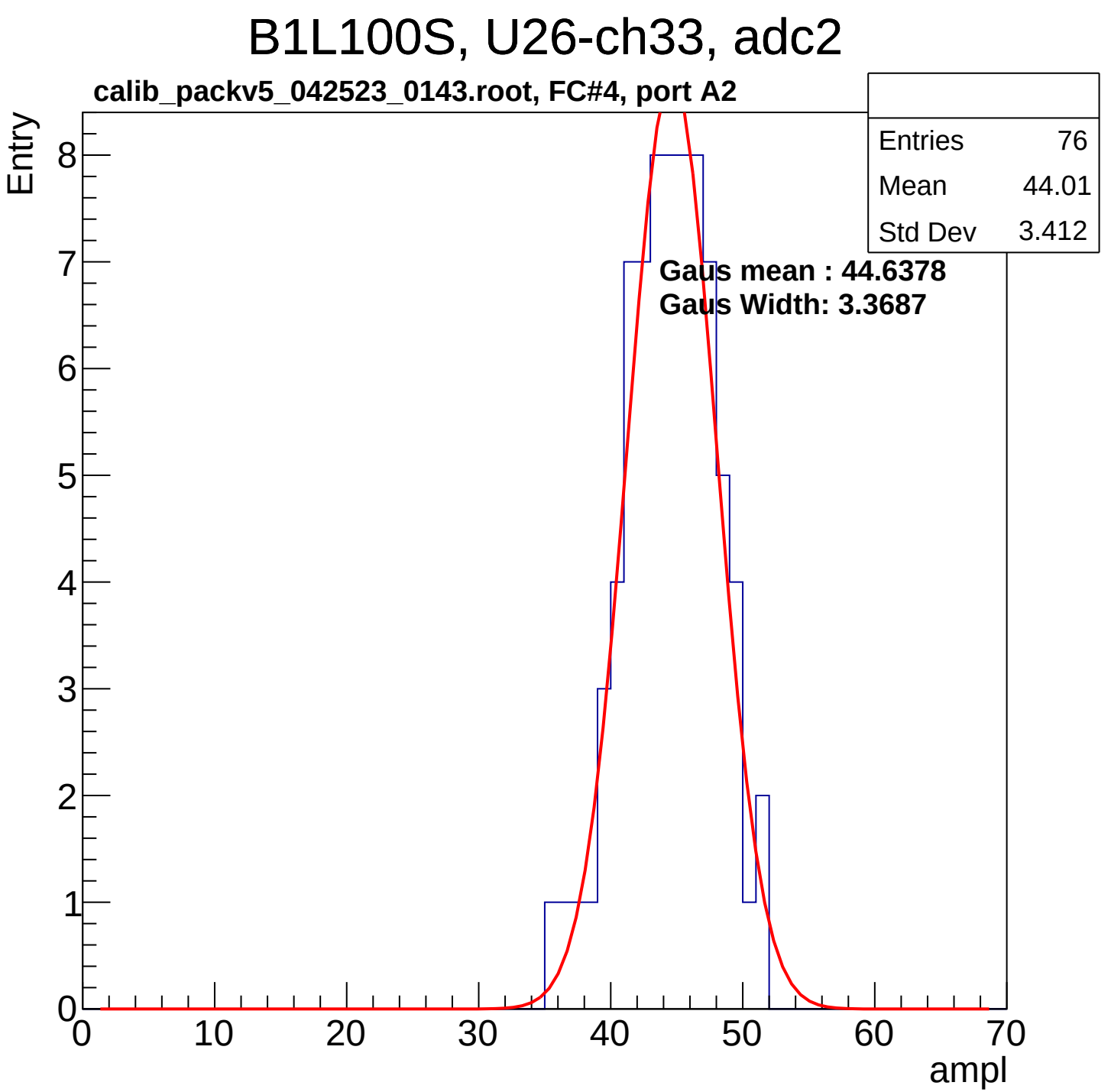
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch33, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.34
Std Dev 3.419
Gaus mean : 37.6436
Gaus Width: 3.0011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.01
Std Dev 3.412
Gaus mean : 44.6378
Gaus Width: 3.3687
Entry
ampl
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2
3
4
5
6
7
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0
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60
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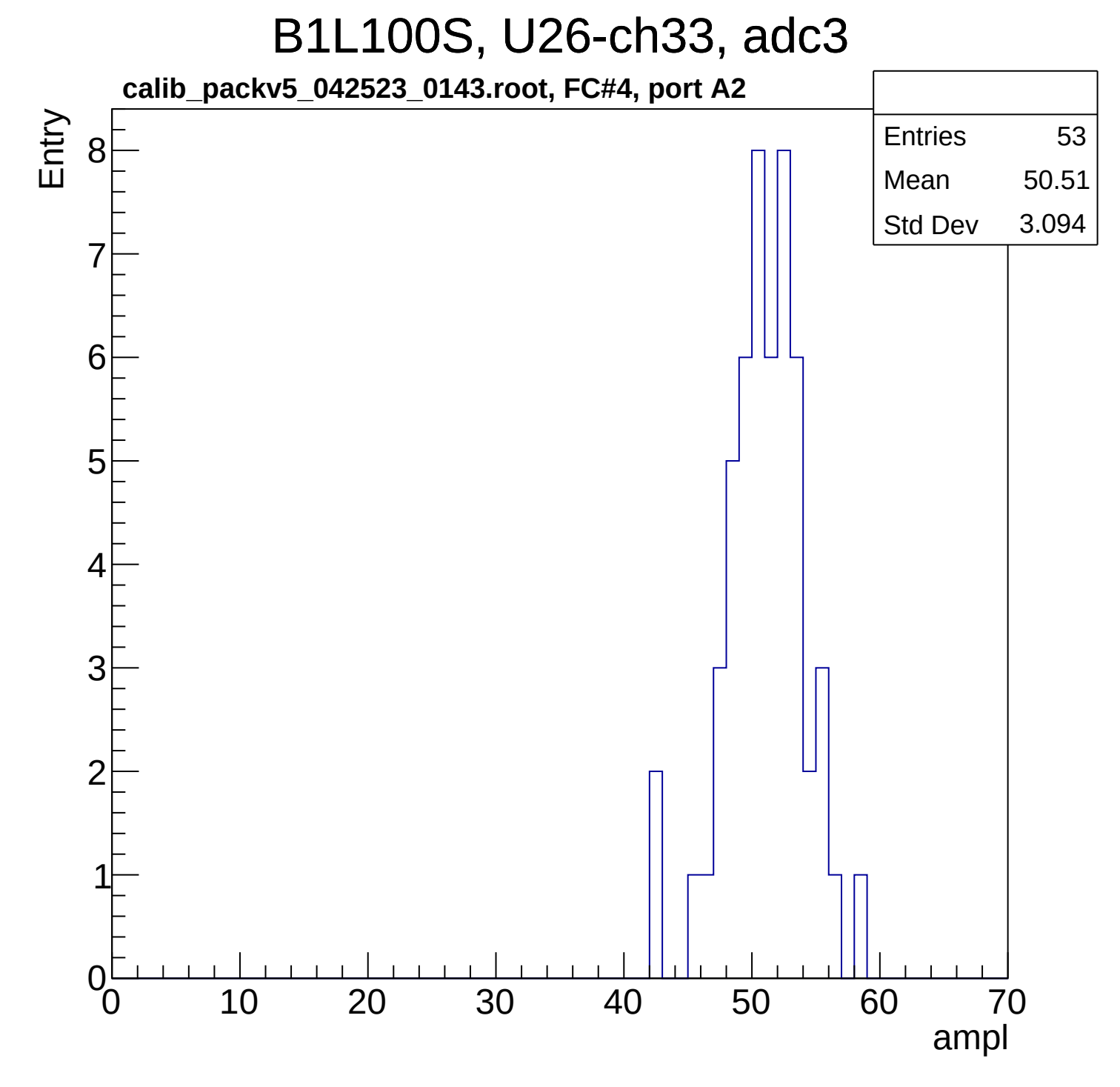
B1L100S, U26-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 50.51
Std Dev 3.094
Entry
ampl
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5
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7
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0
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20
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40
50
60
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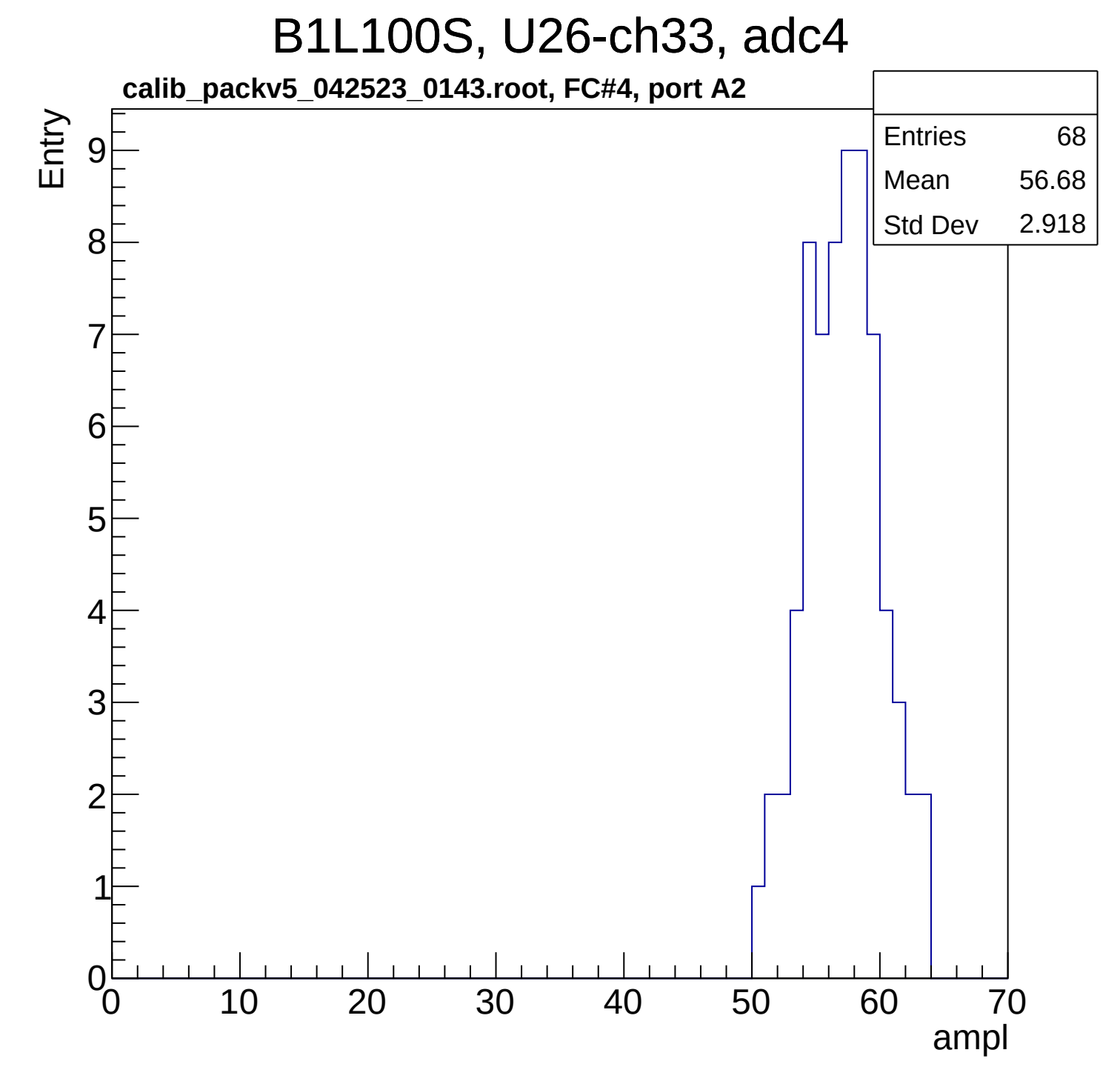
B1L100S, U26-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.68
Std Dev 2.918
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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20
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40
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60
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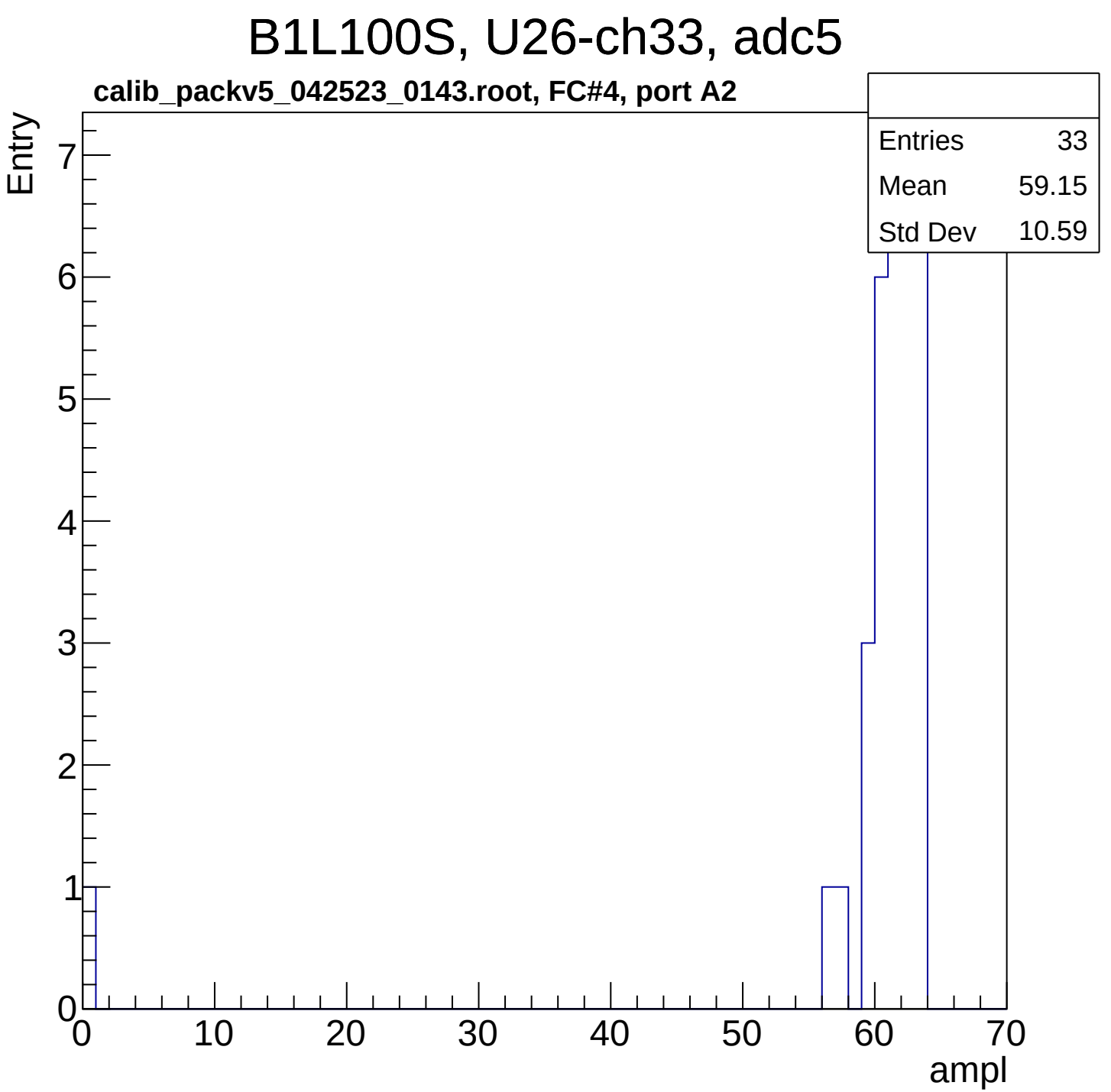
B1L100S, U26-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.15
Std Dev 10.59
Entry
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1
2
3
4
5
6
7
0
10
20
30
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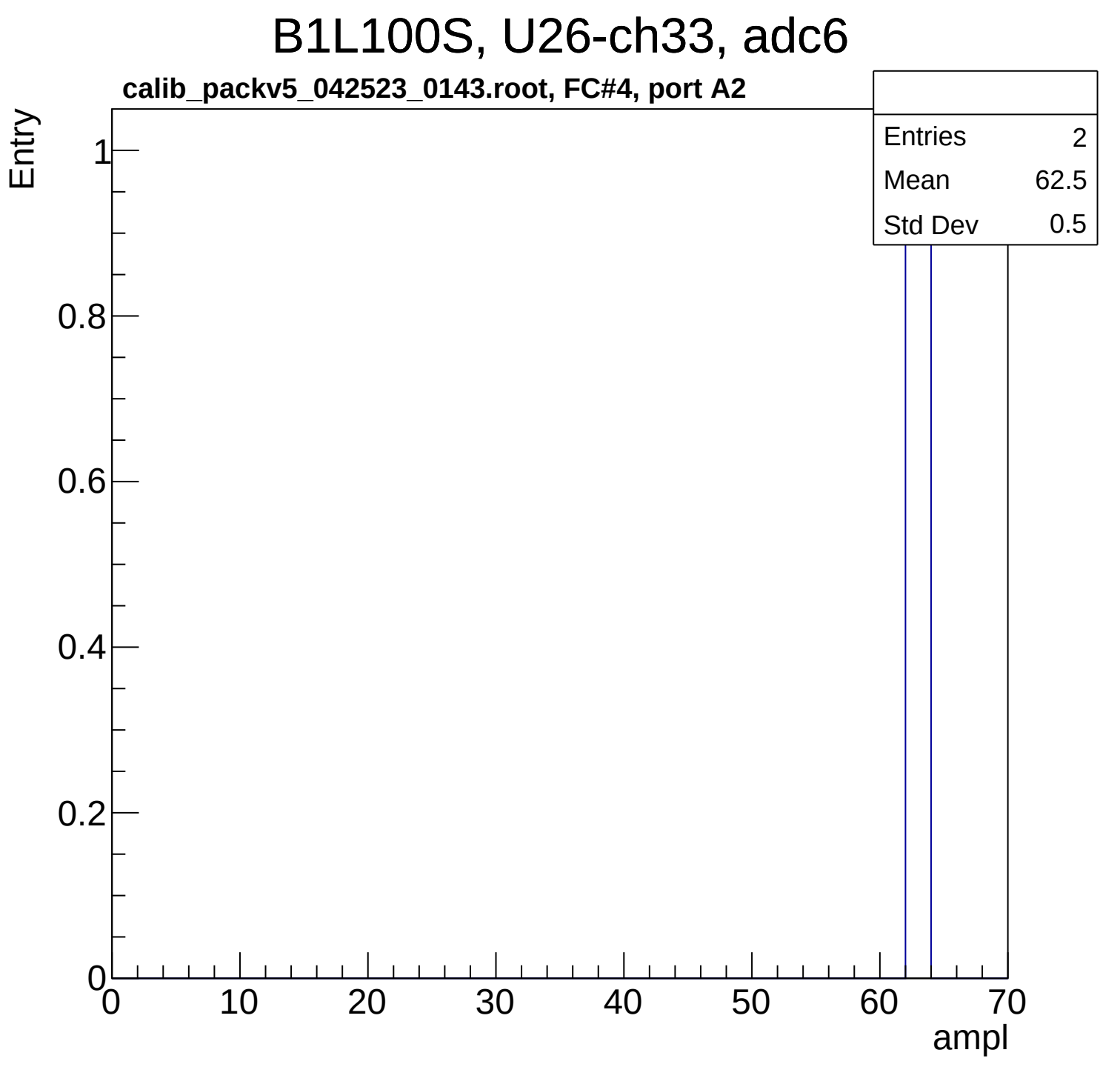
50
60
70
ampl


B1L100S, U26-ch33, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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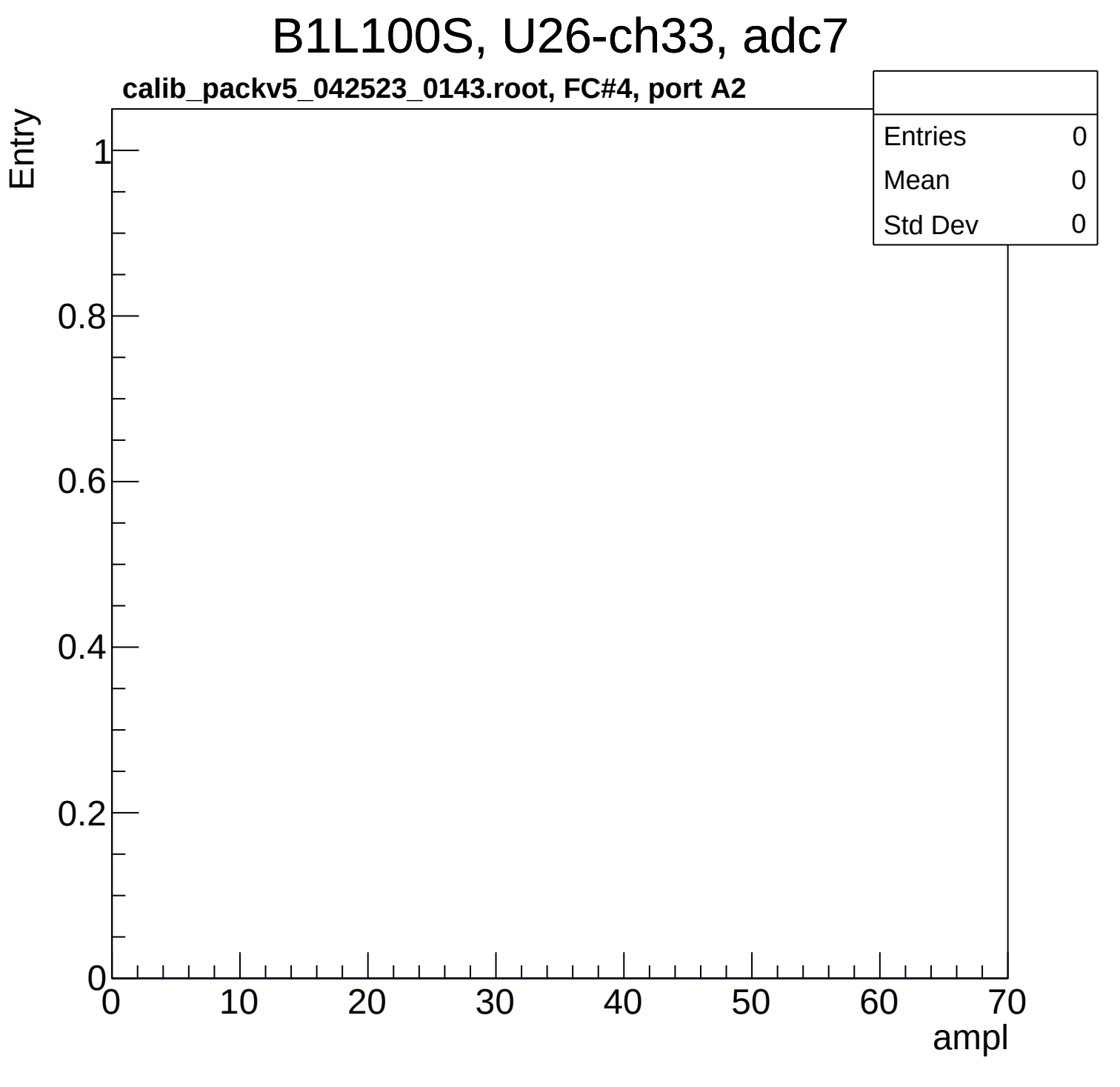
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch33, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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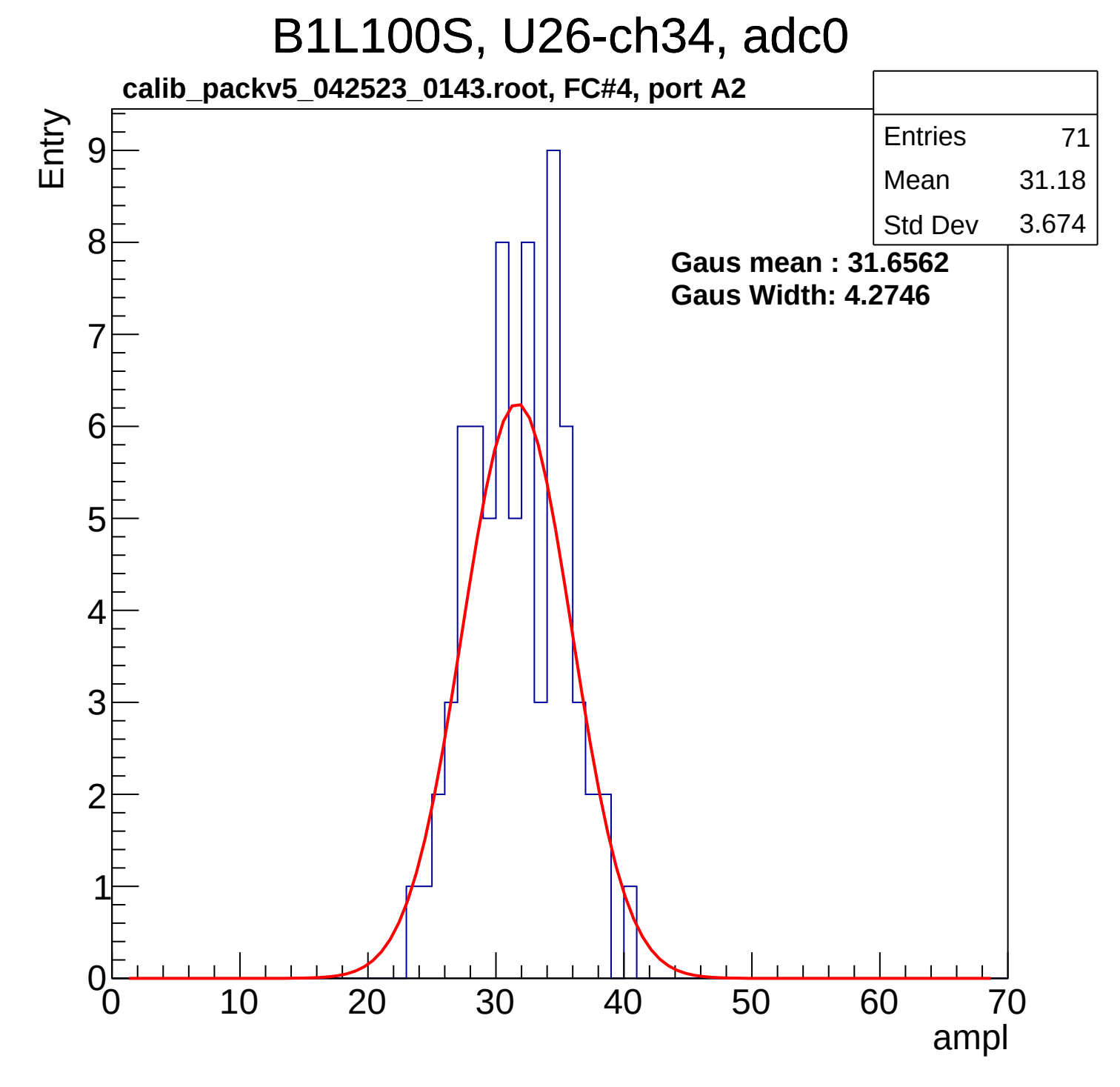
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 31.18
Std Dev 3.674
Gaus mean : 31.6562
Gaus Width: 4.2746
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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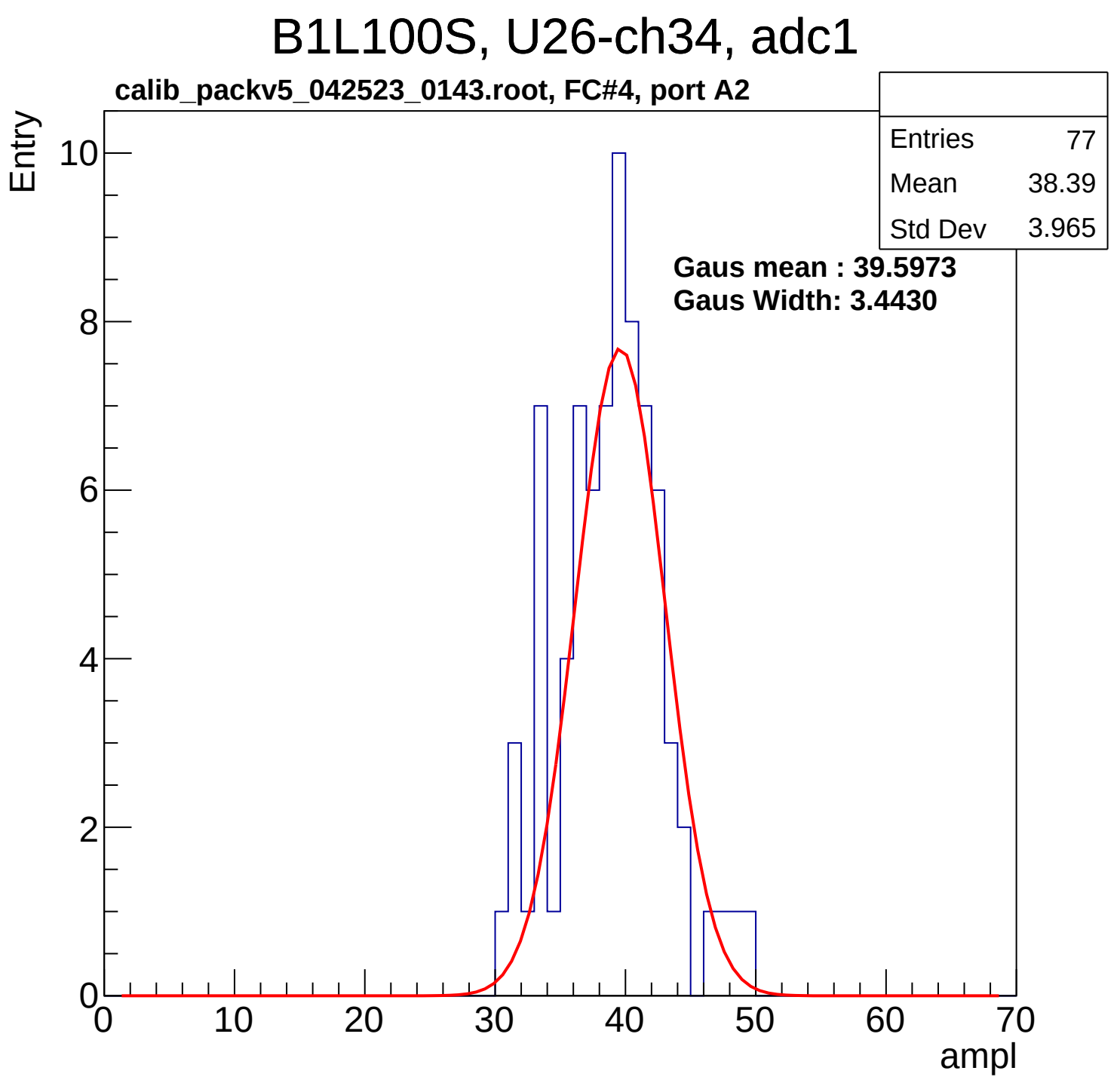
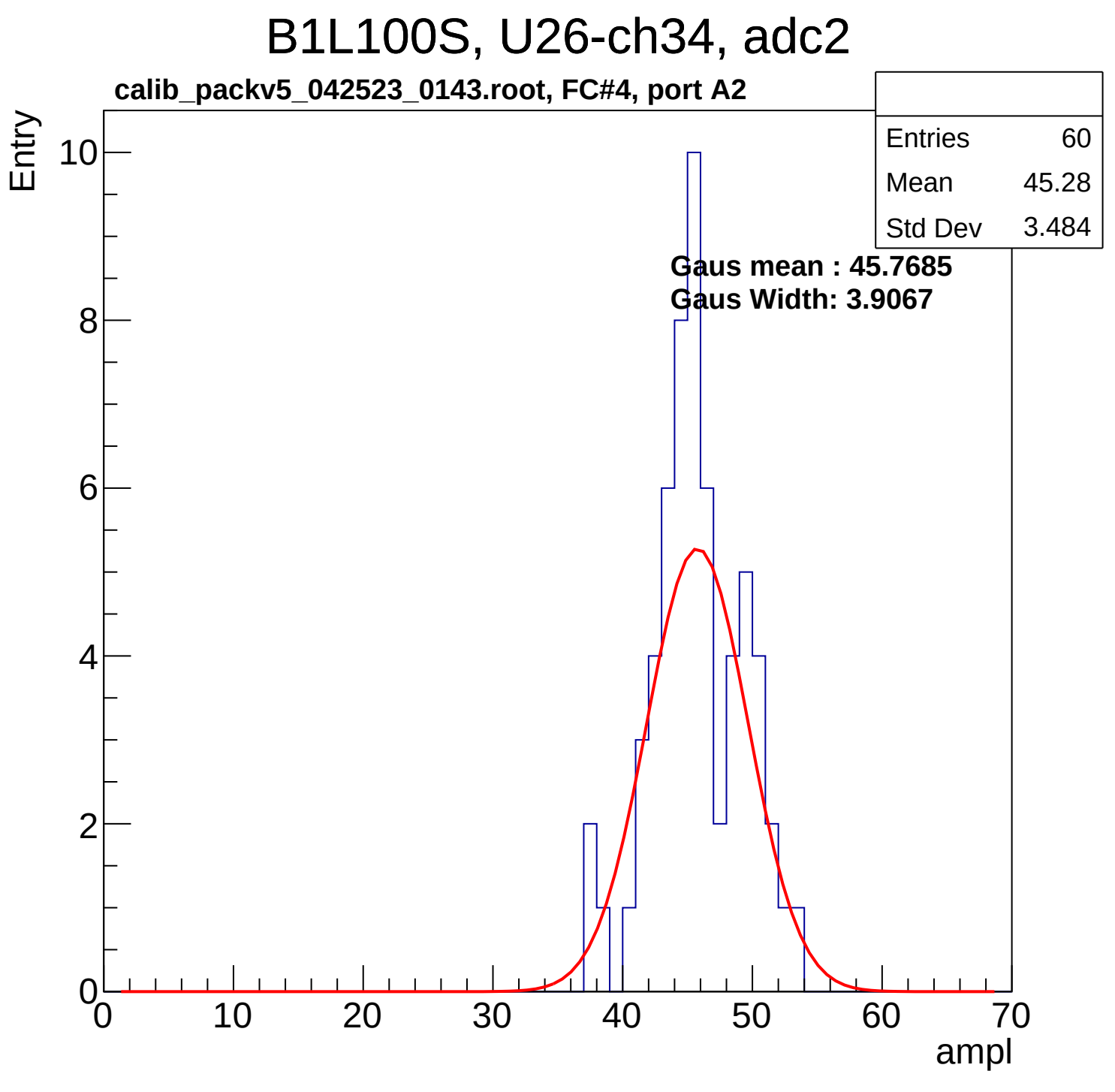
40
50
60
70


B1L100S, U26-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 38.39
Std Dev 3.965
Gaus mean : 39.5973
Gaus Width: 3.4430
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 45.28
Std Dev 3.484
Gaus mean : 45.7685
Gaus Width: 3.9067
Entry
ampl
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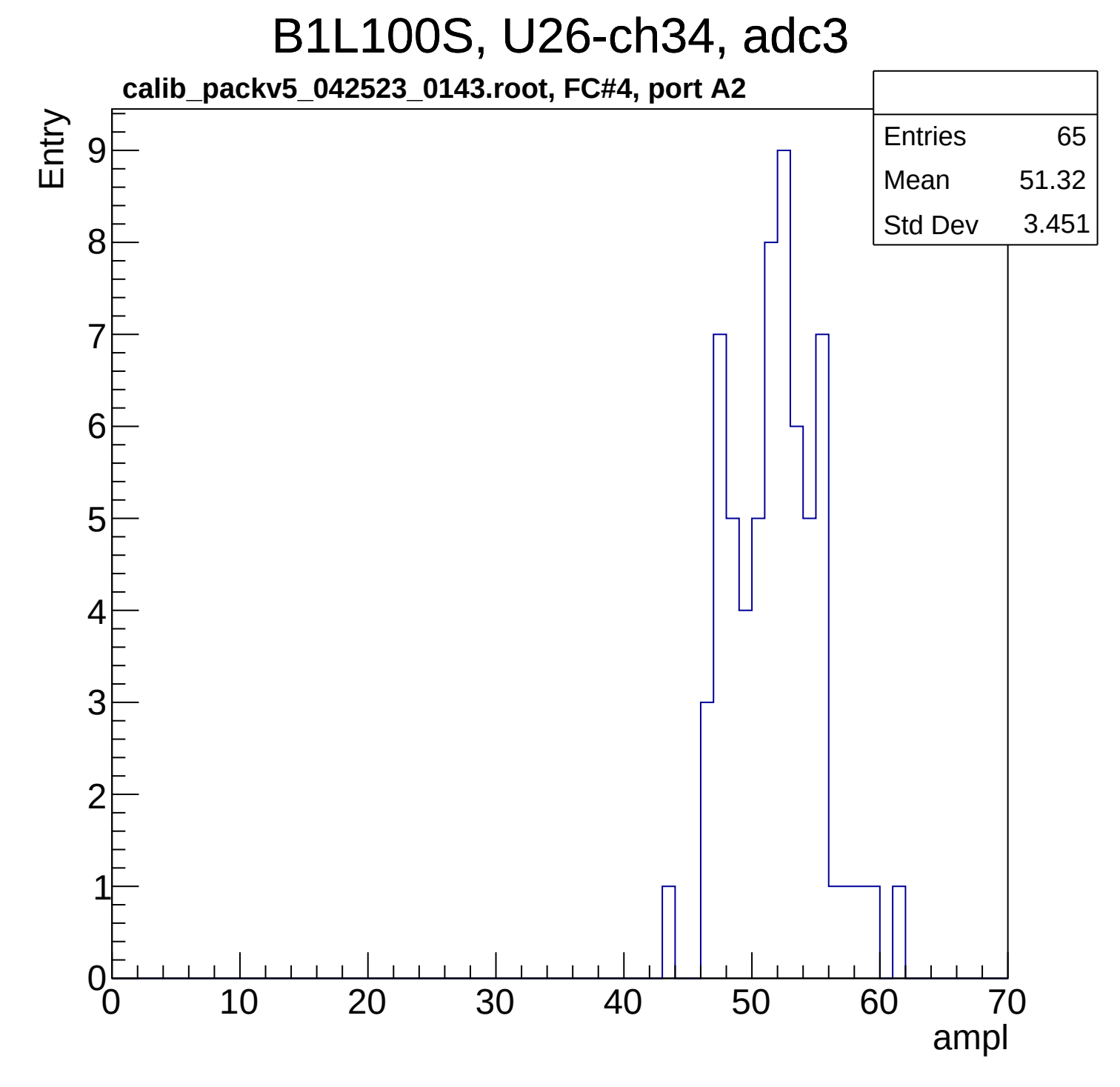
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 51.32
Std Dev 3.451
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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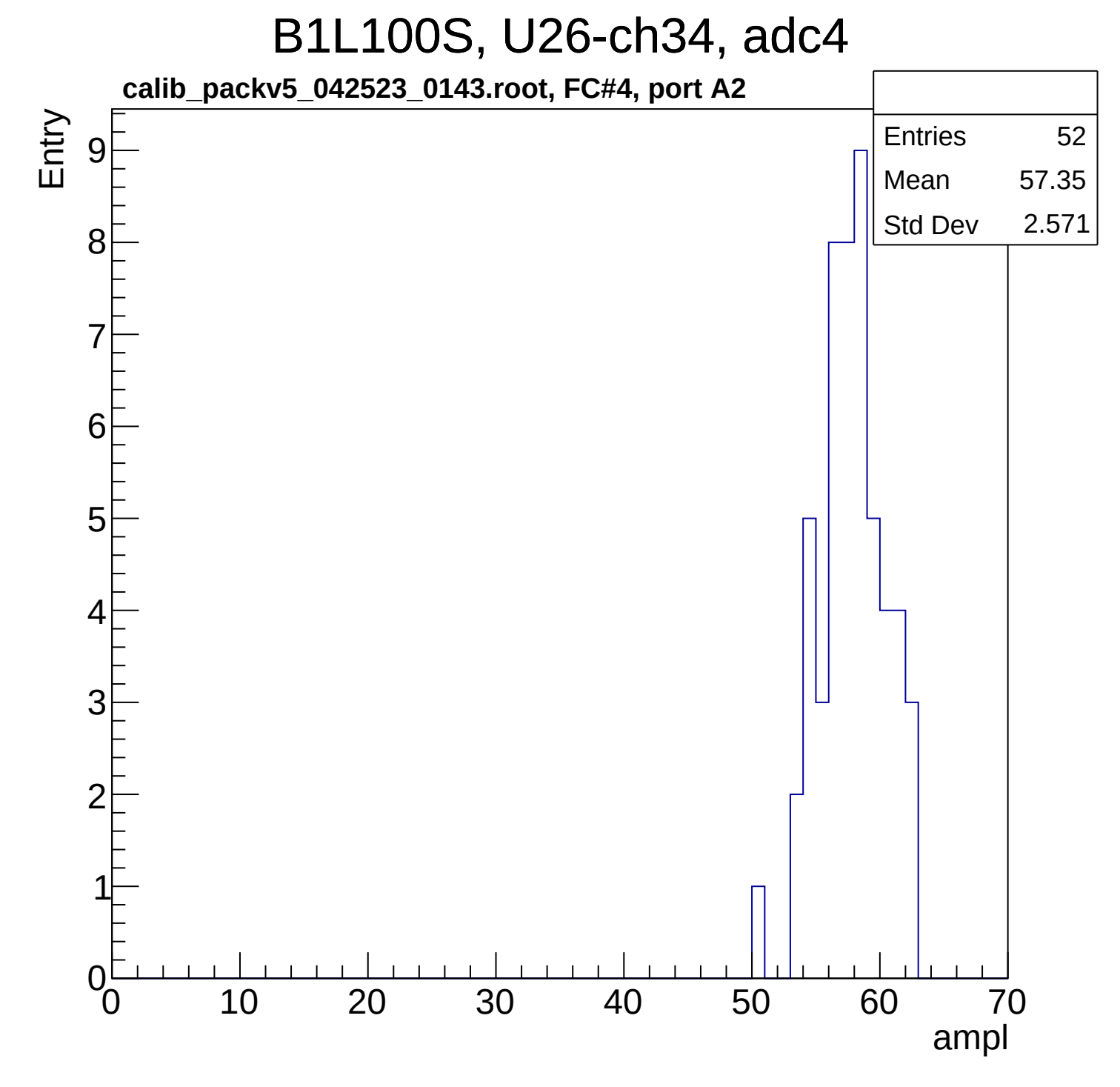
B1L100S, U26-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 57.35
Std Dev 2.571
Entry
ampl
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2
3
4
5
6
7
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0
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60
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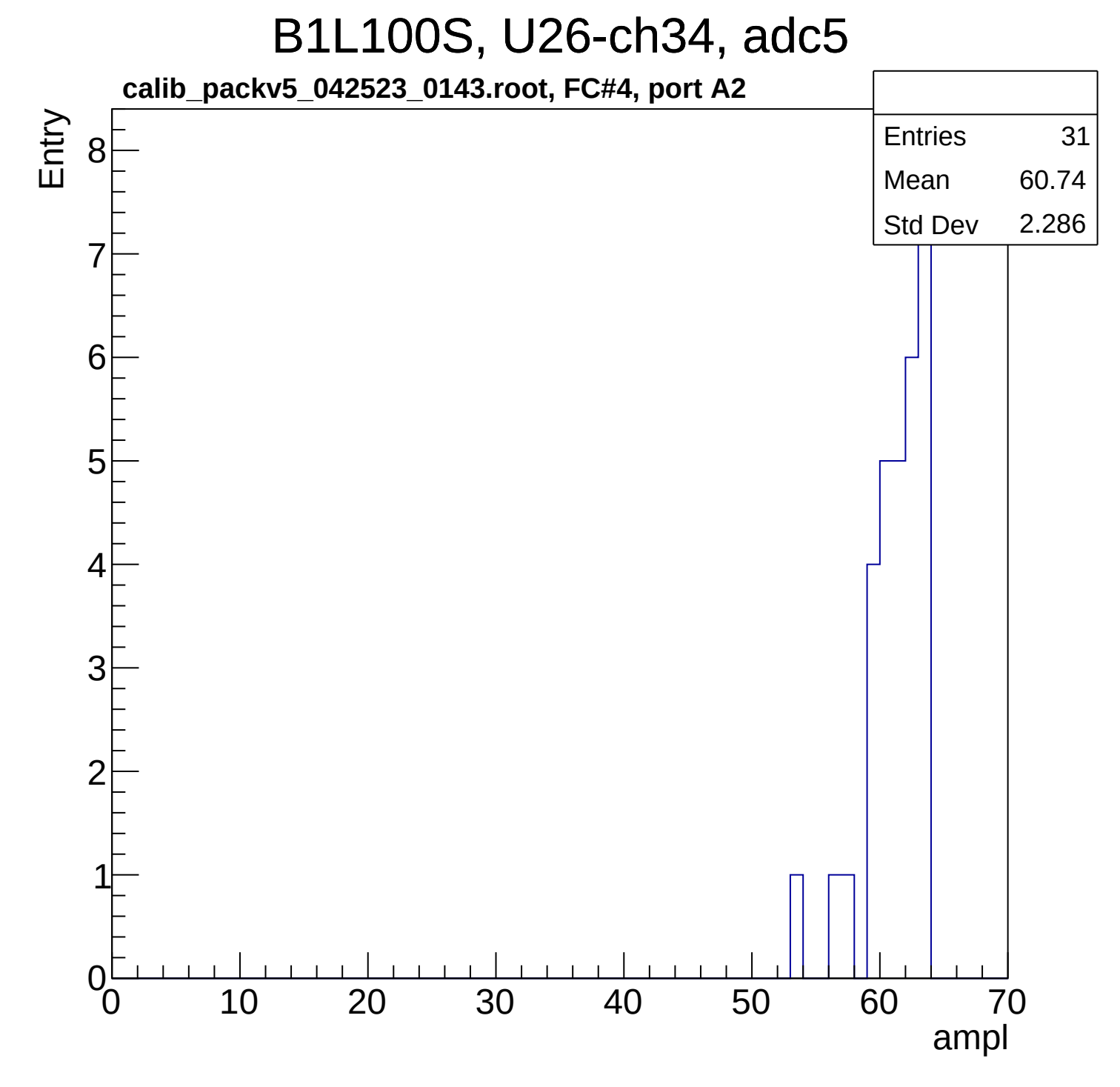
B1L100S, U26-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.74
Std Dev 2.286
Entry
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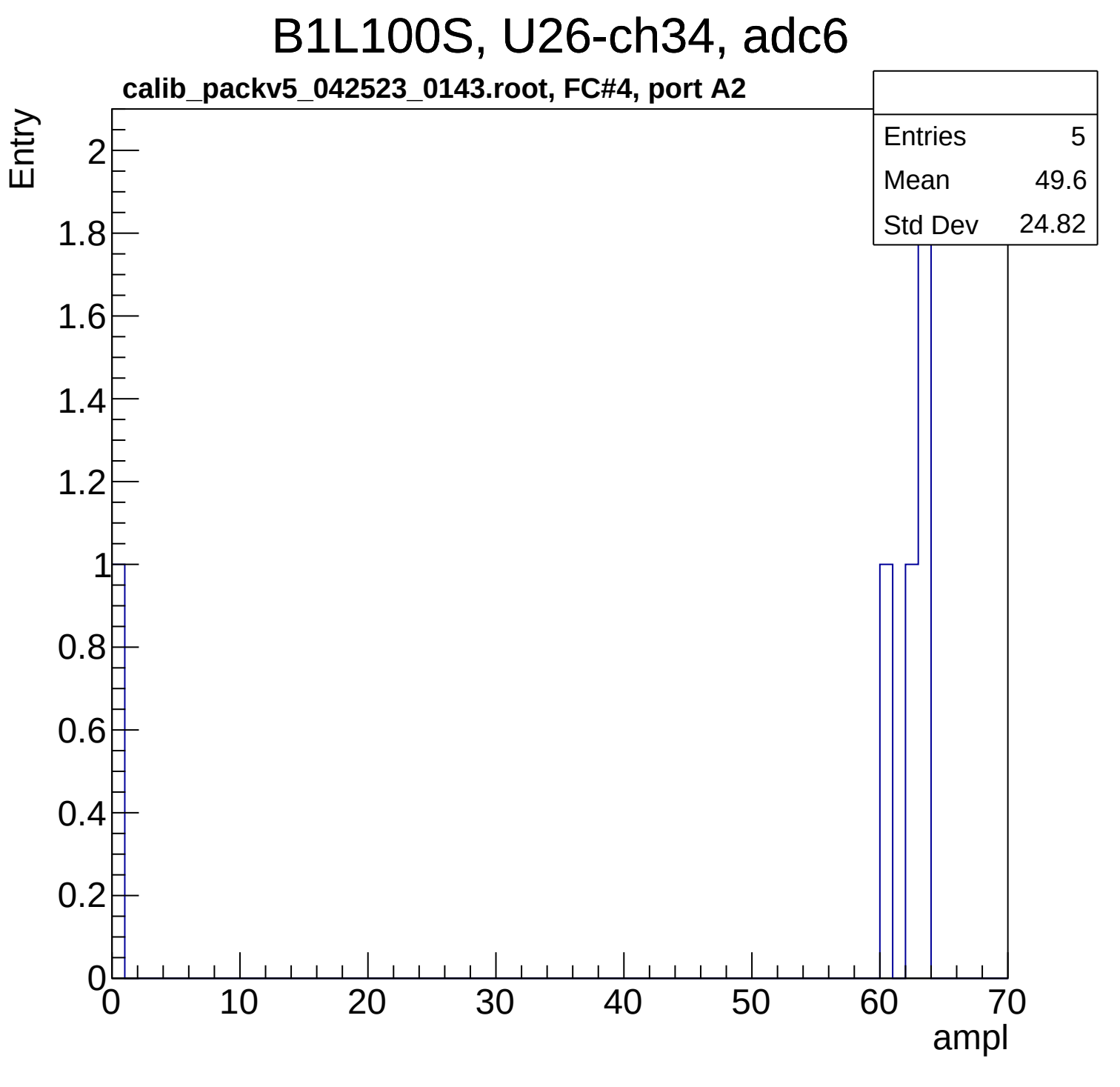
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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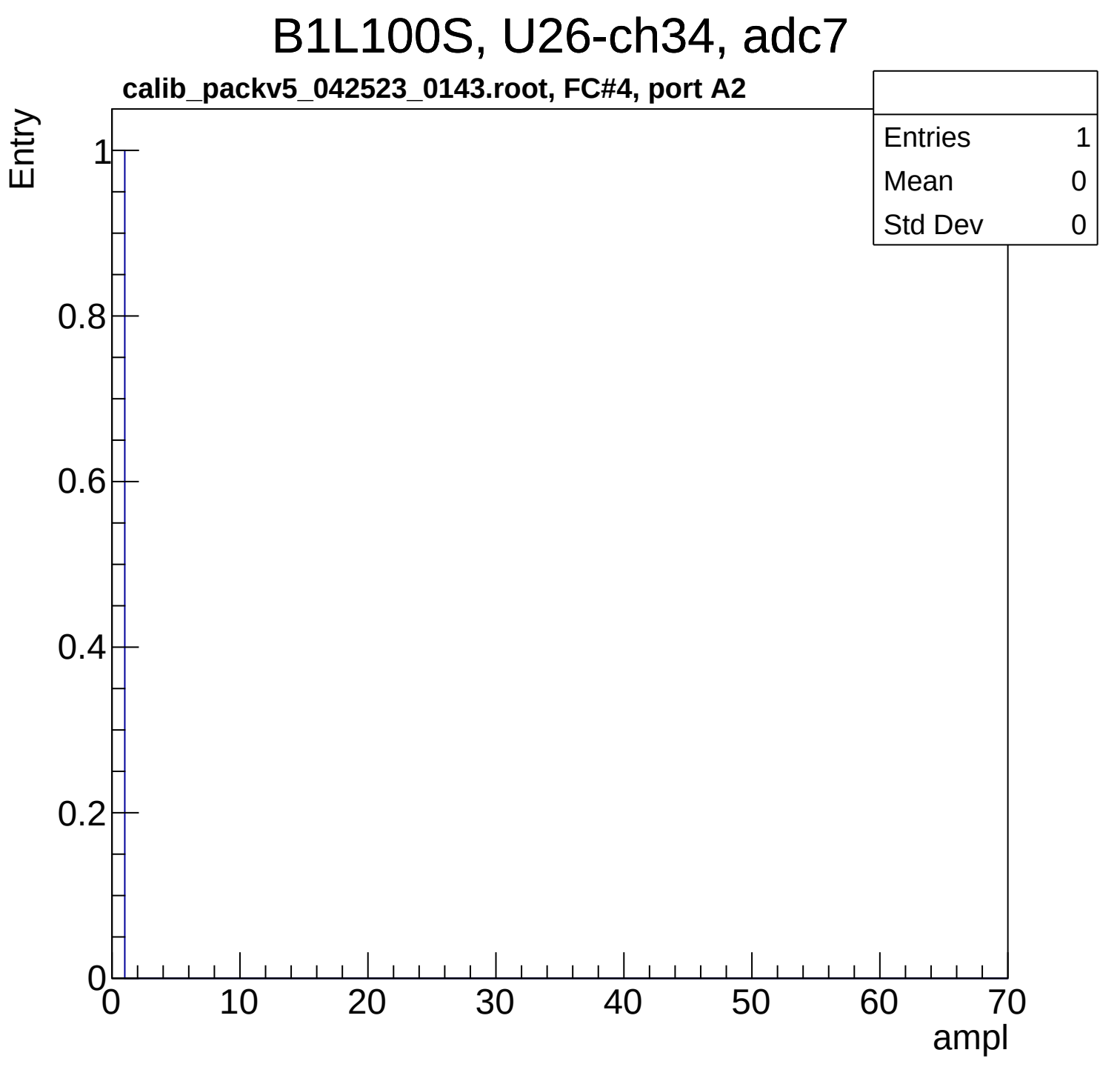
40
50
60
70


B1L100S, U26-ch34, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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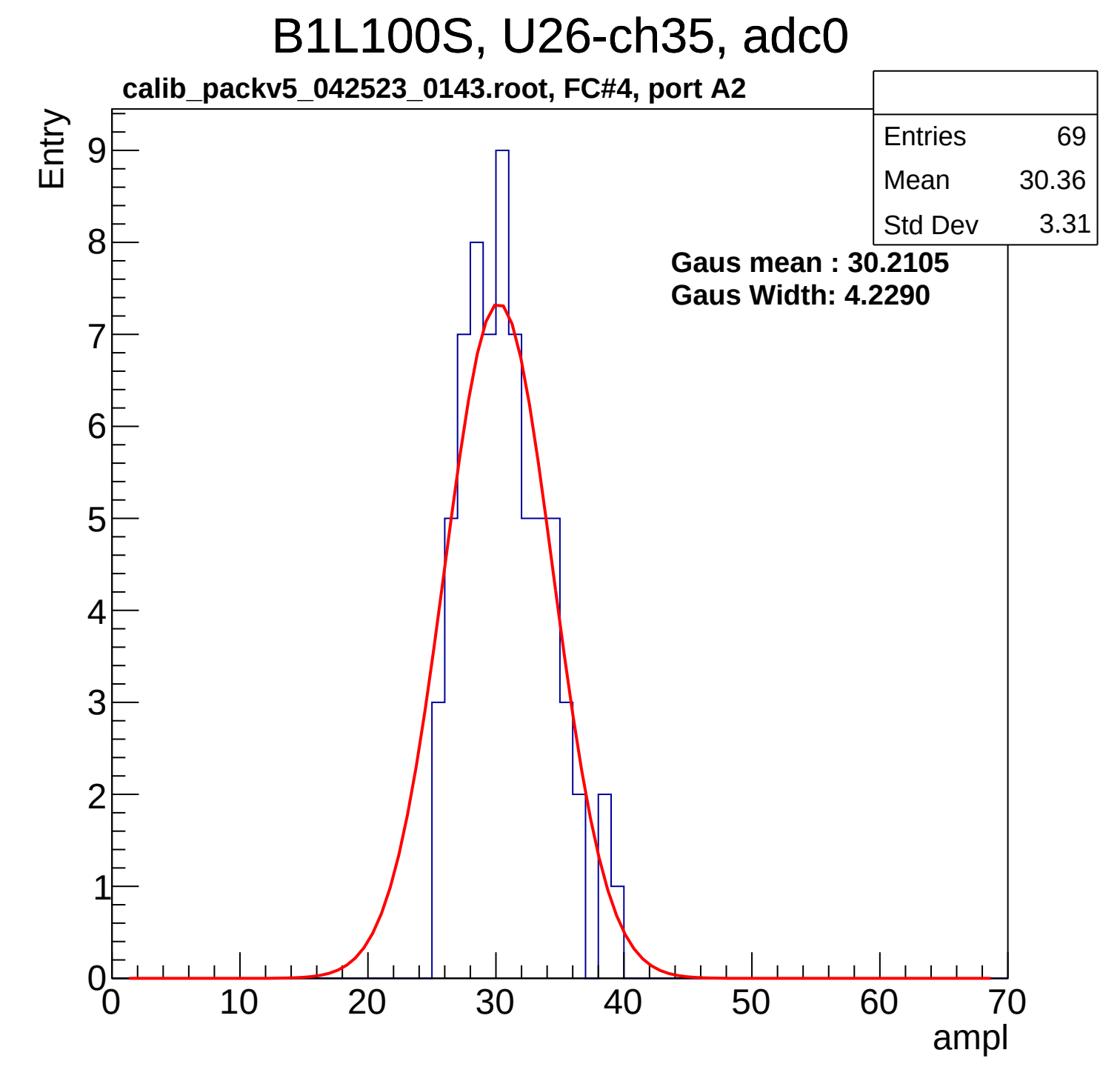
50
60
70
ampl


B1L100S, U26-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 30.36
Std Dev 3.31
Gaus mean : 30.2105
Gaus Width: 4.2290
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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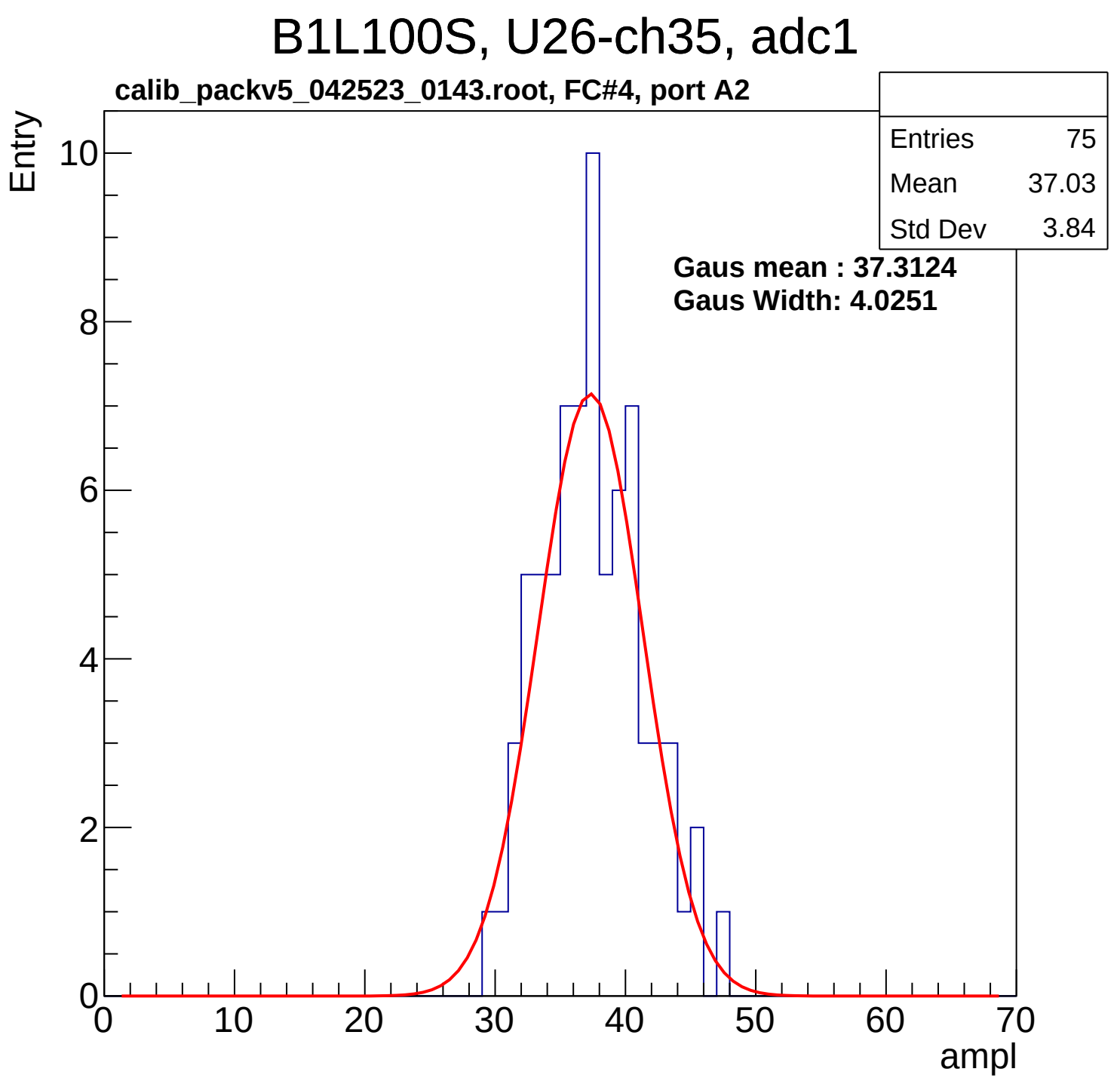
B1L100S, U26-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.03
Std Dev 3.84
Gaus mean : 37.3124
Gaus Width: 4.0251
Entry
ampl
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8
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0
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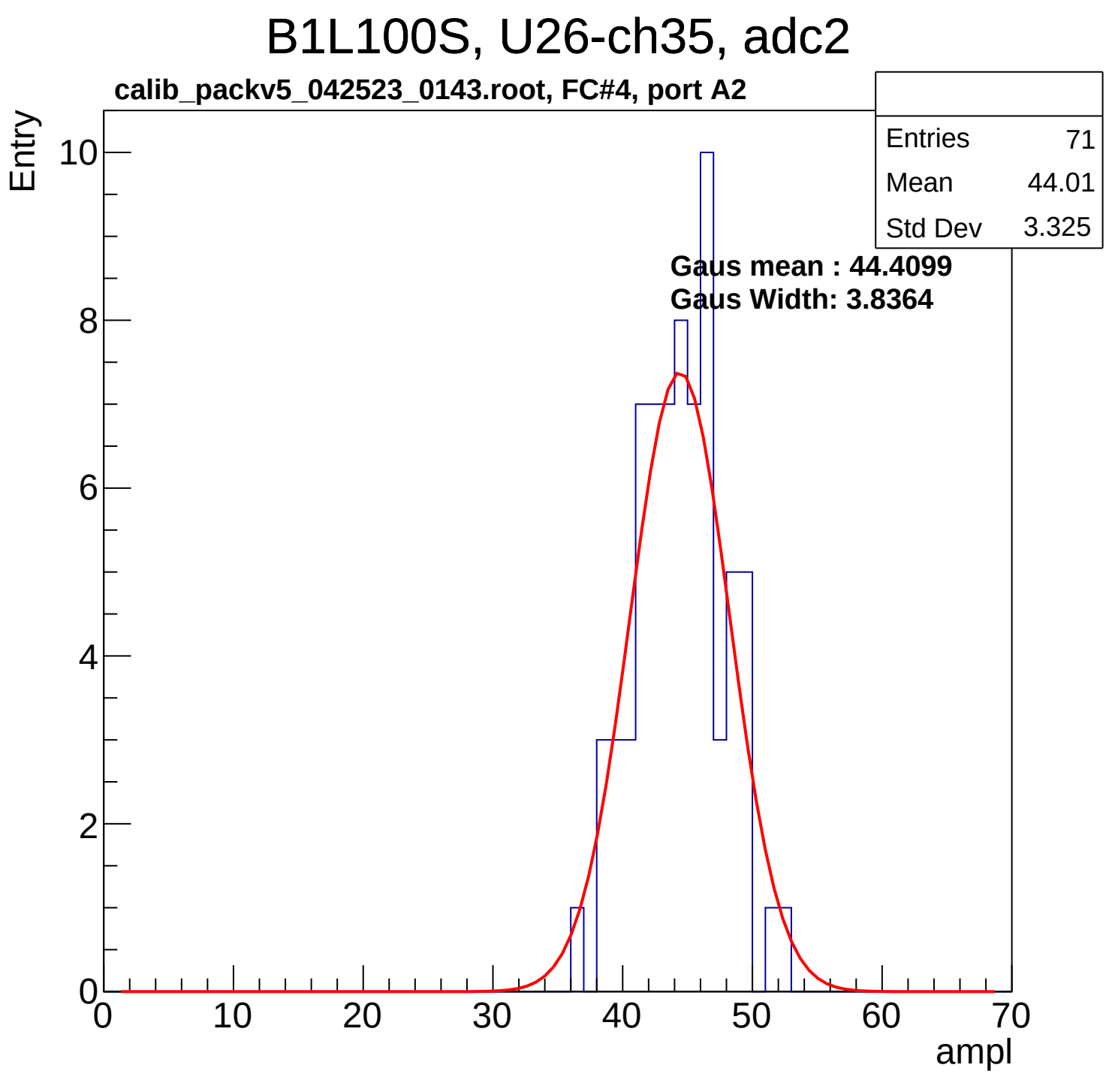
B1L100S, U26-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 44.01
Std Dev 3.325
Gaus mean : 44.4099
Gaus Width: 3.8364
Entry
ampl
0
2
4
6
8
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0
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40
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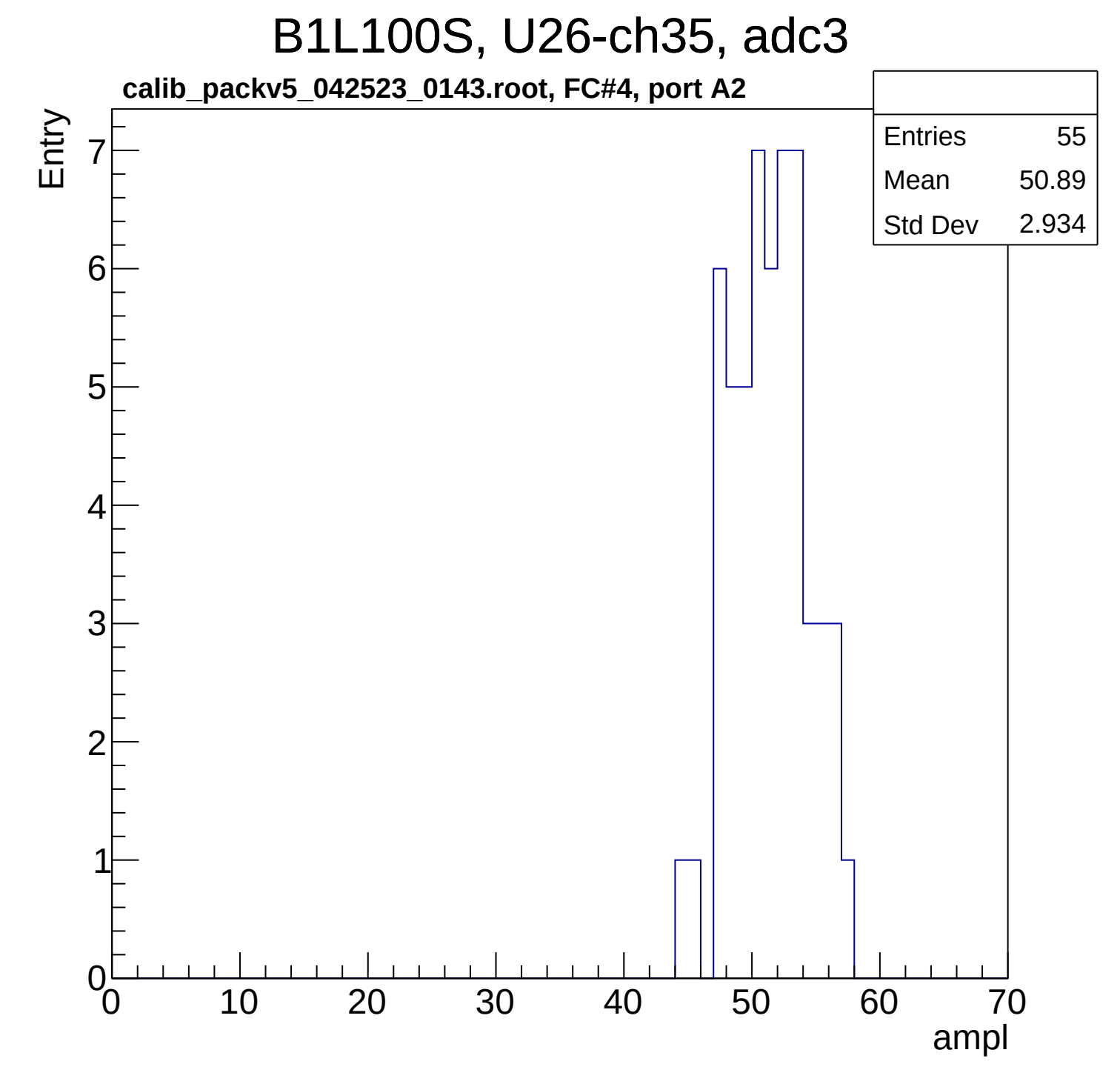
B1L100S, U26-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.89
Std Dev 2.934
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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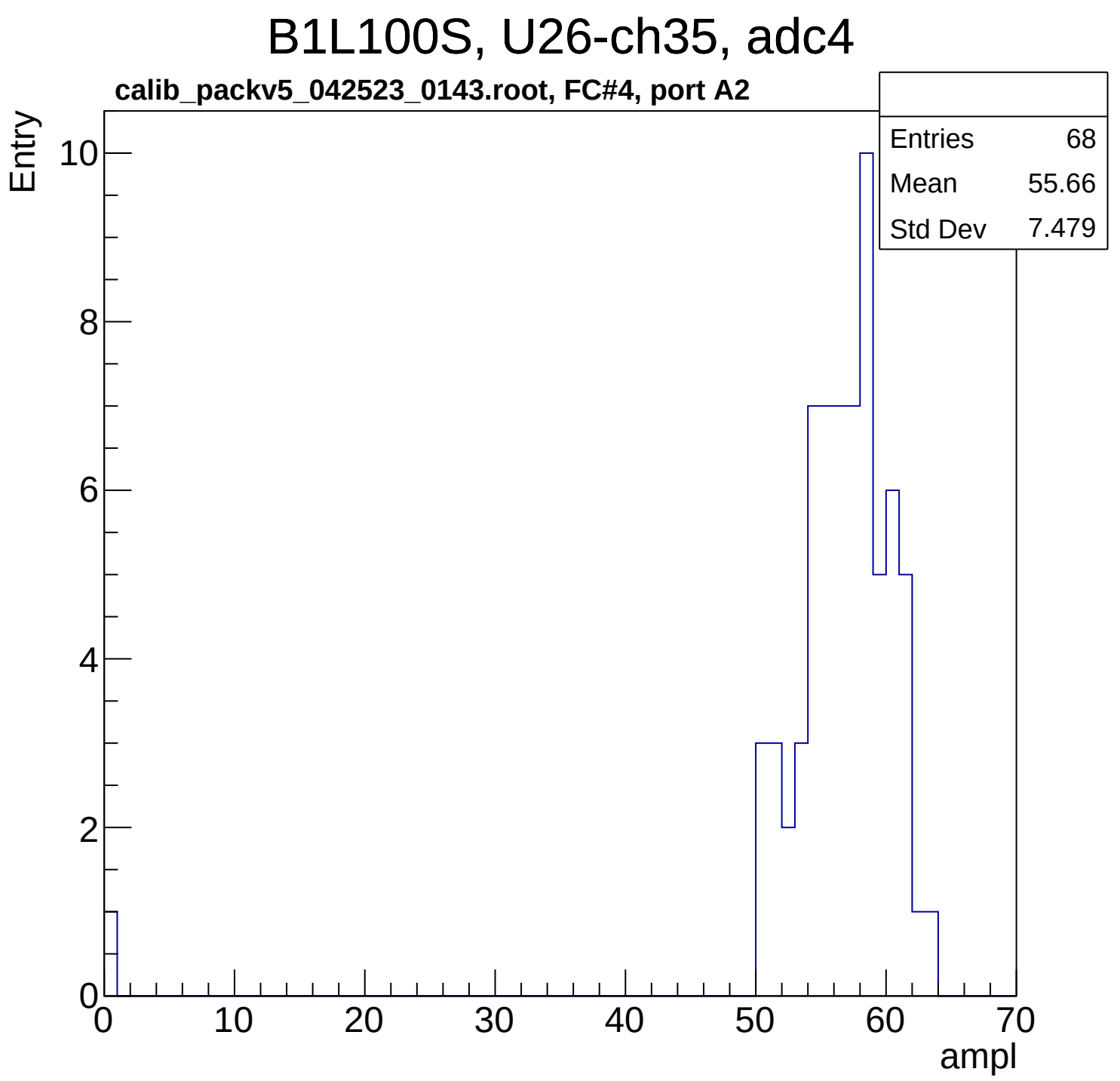
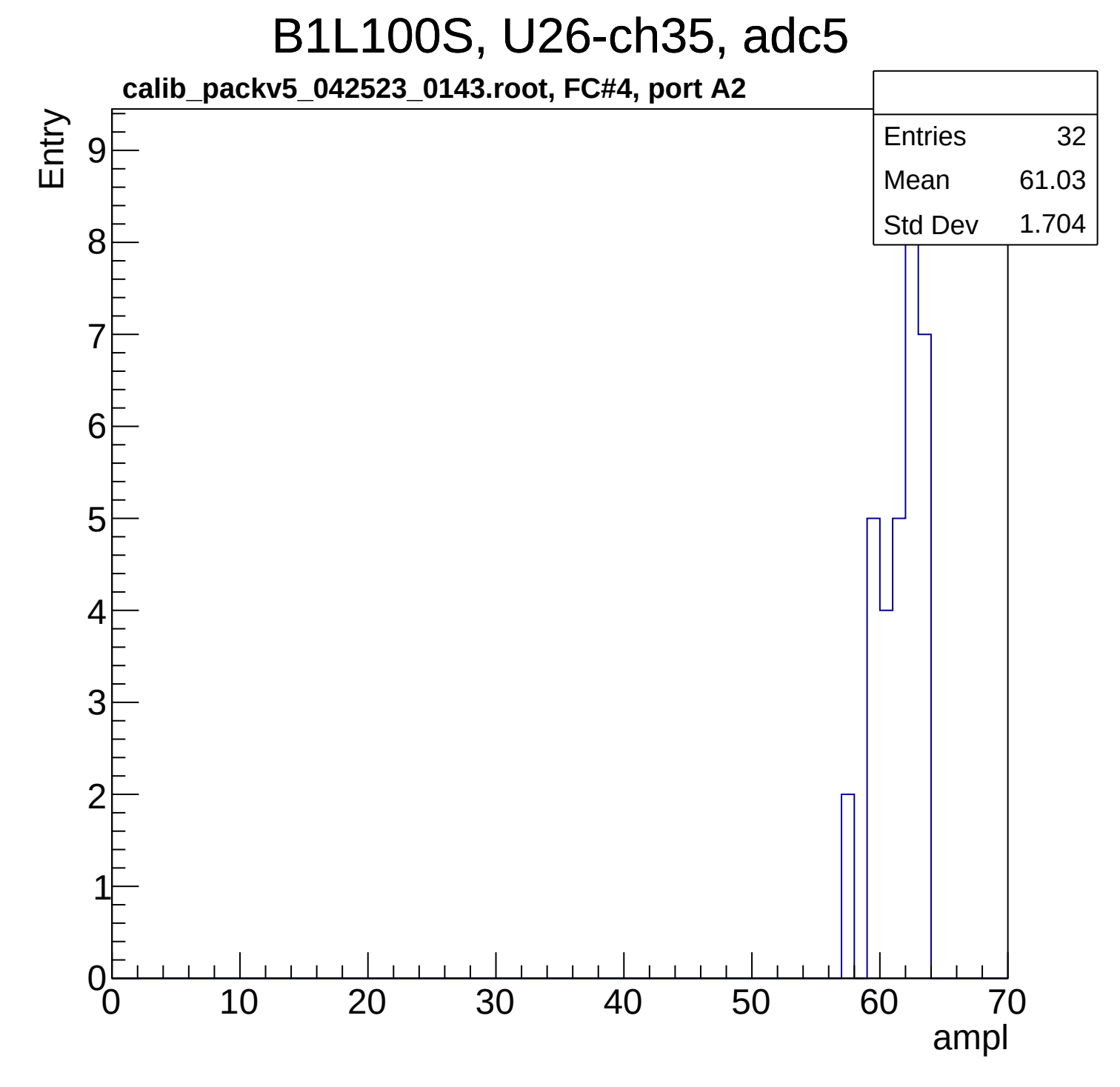

B1L100S, U26-ch35, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 55.66
Std Dev 7.479
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 61.03
Std Dev 1.704
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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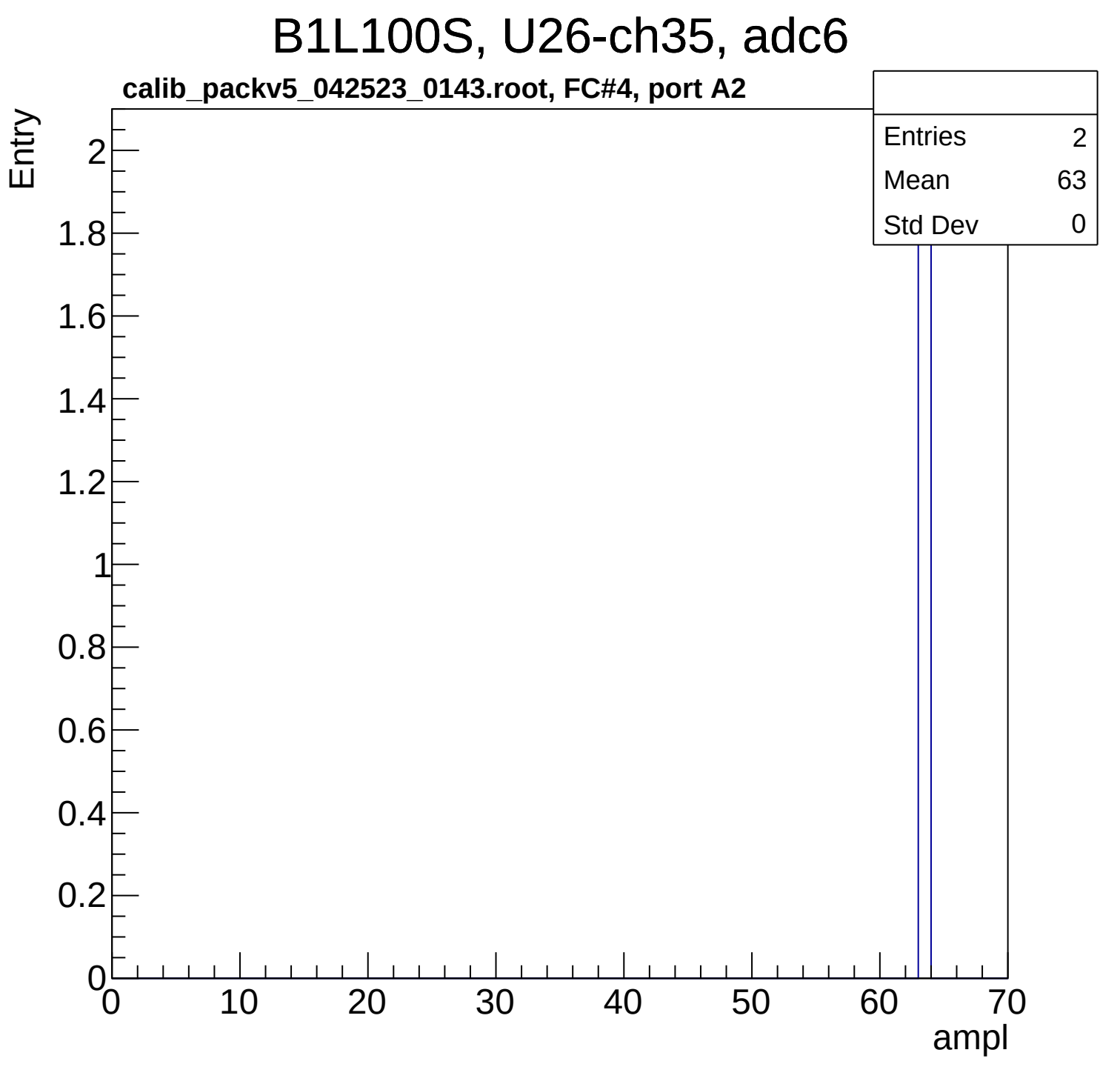
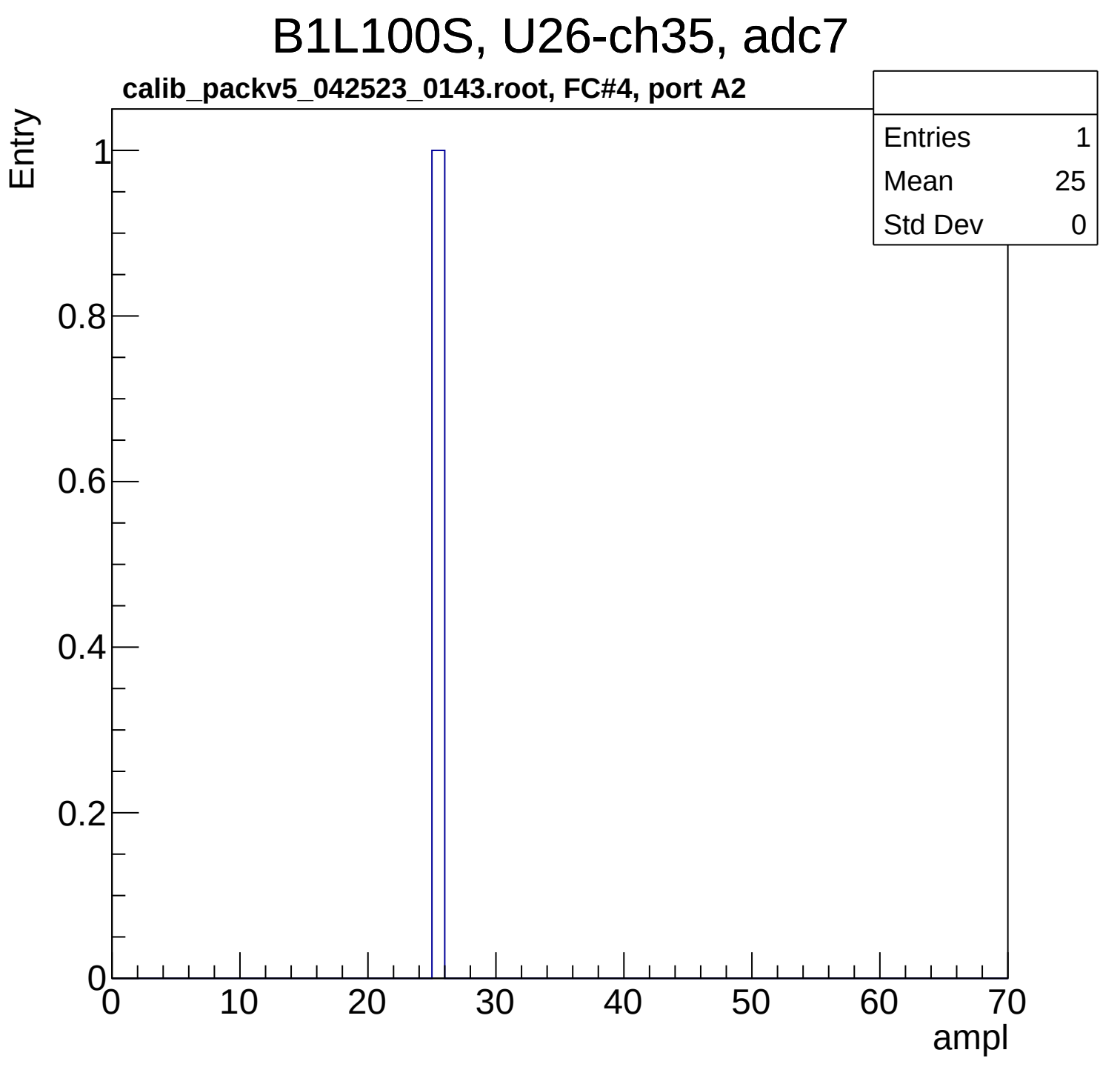
40
50
60
70


B1L100S, U26-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch35, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 25
Std Dev 0
Entry
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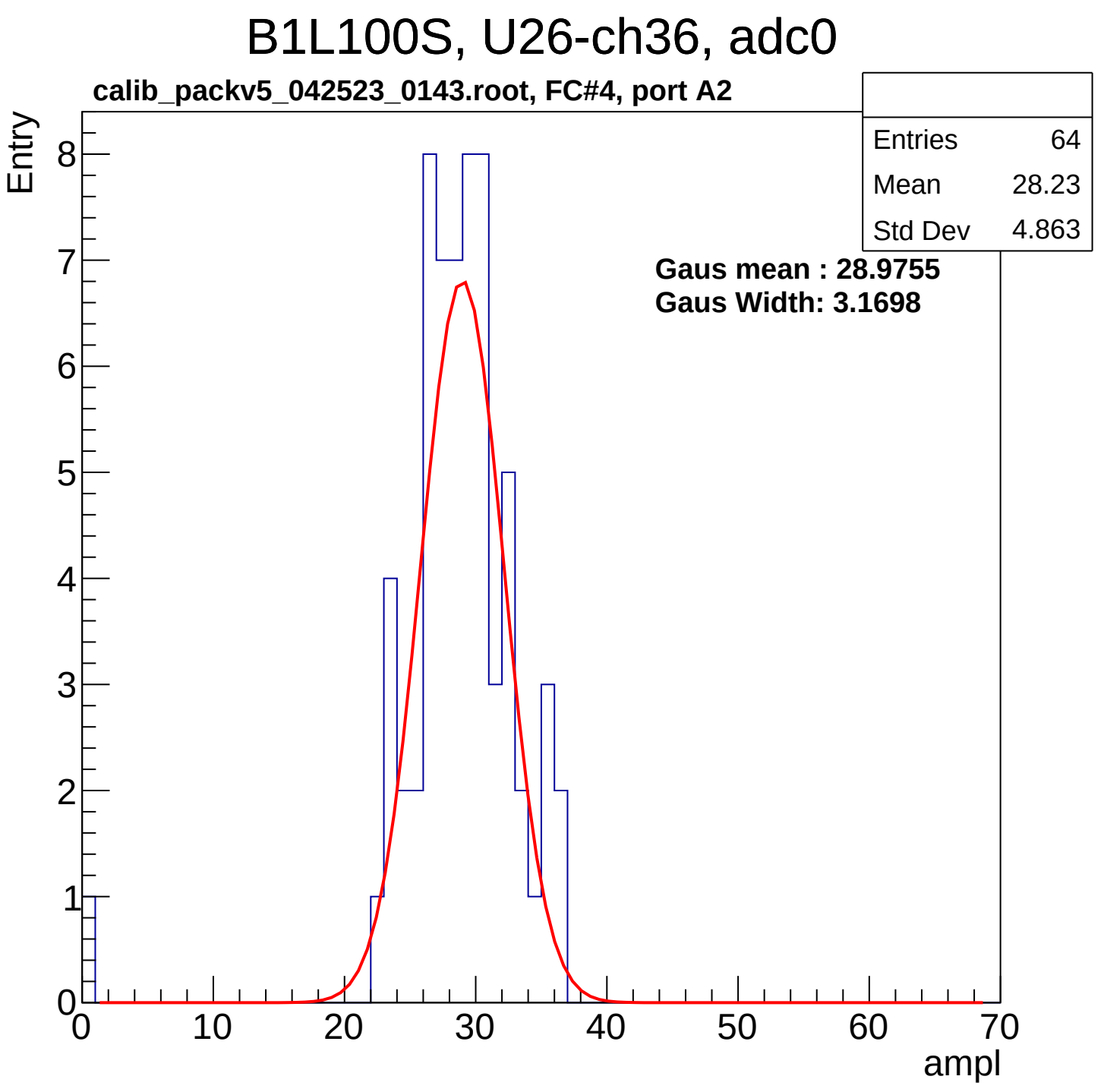
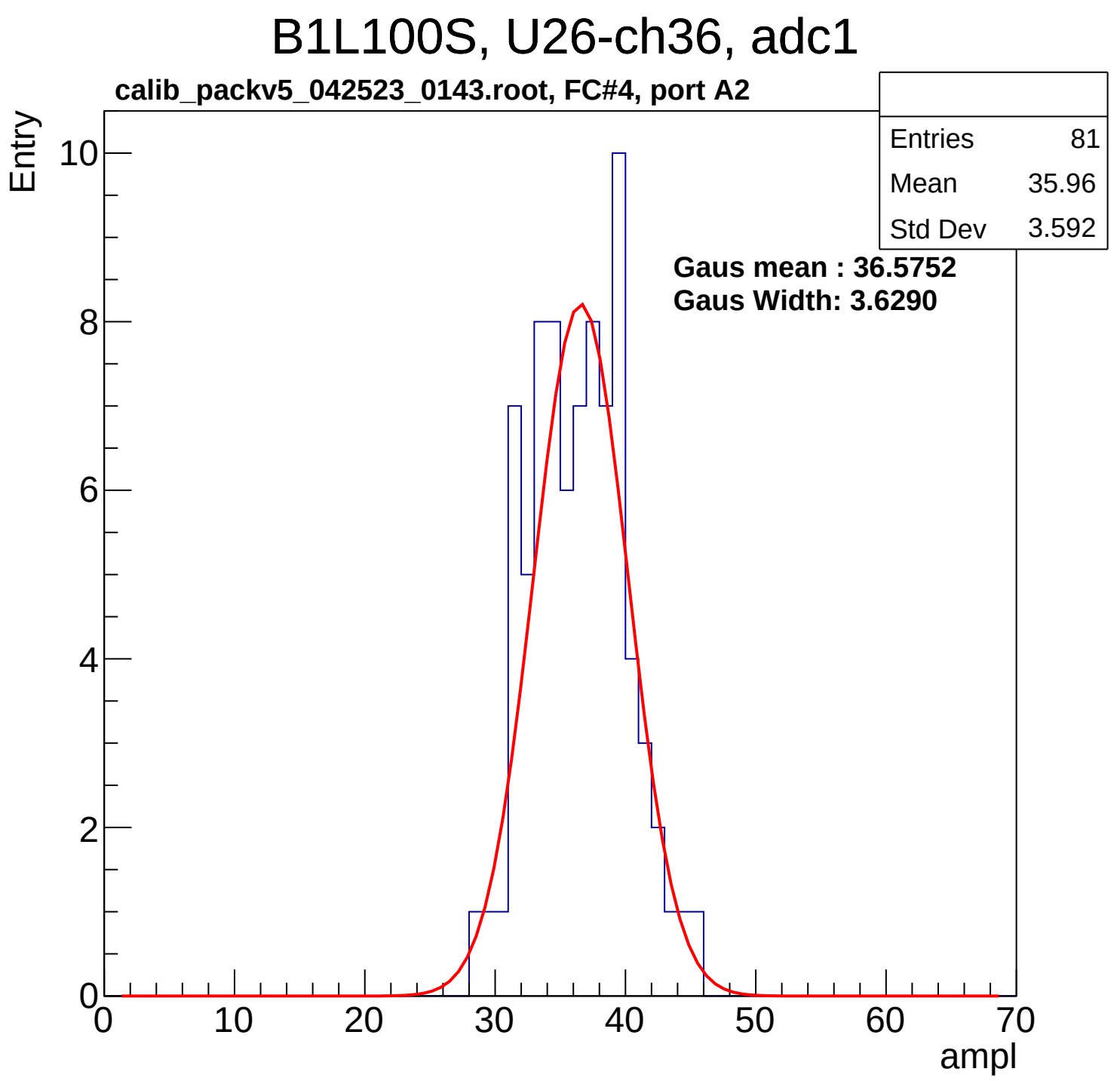
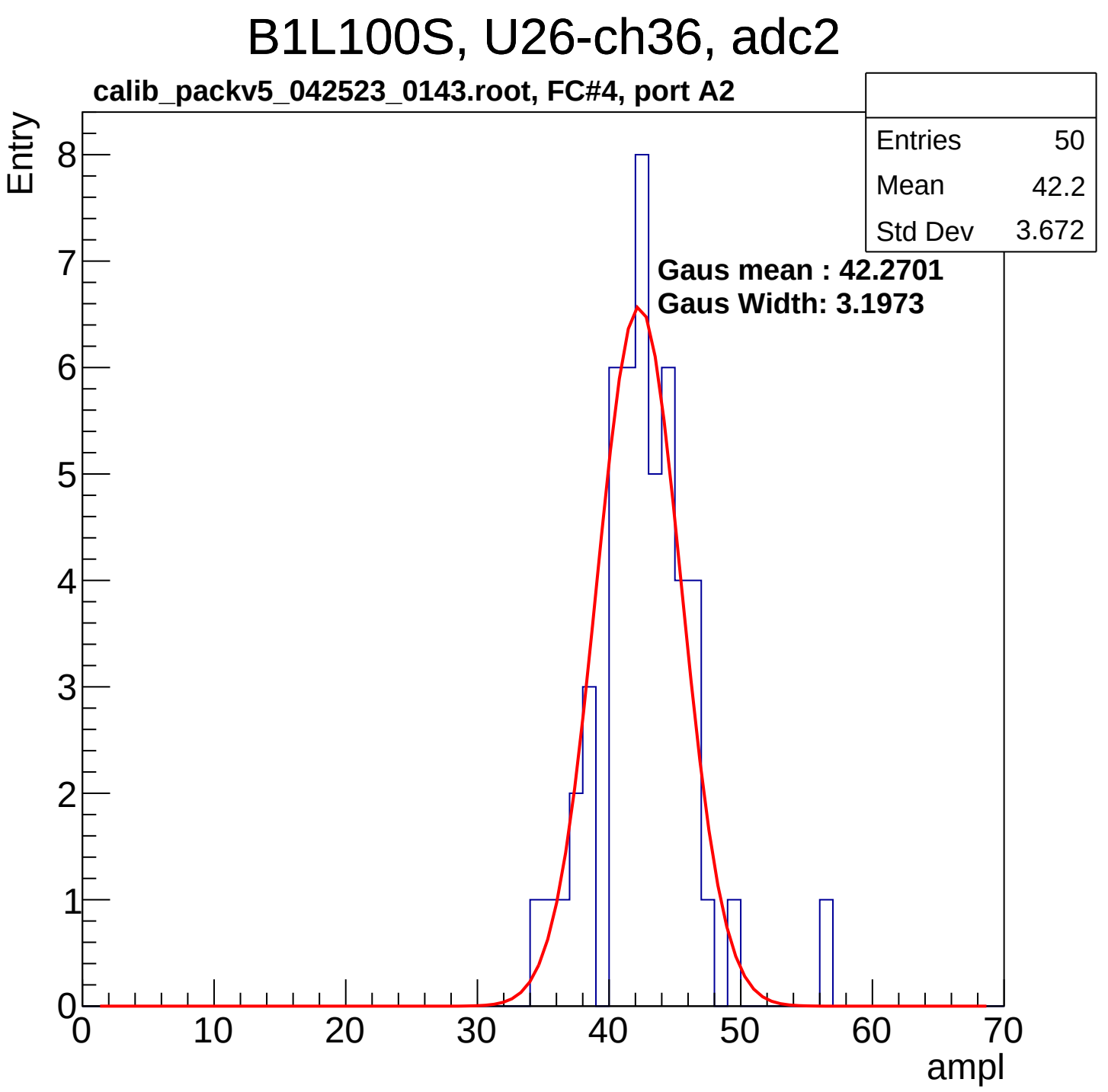
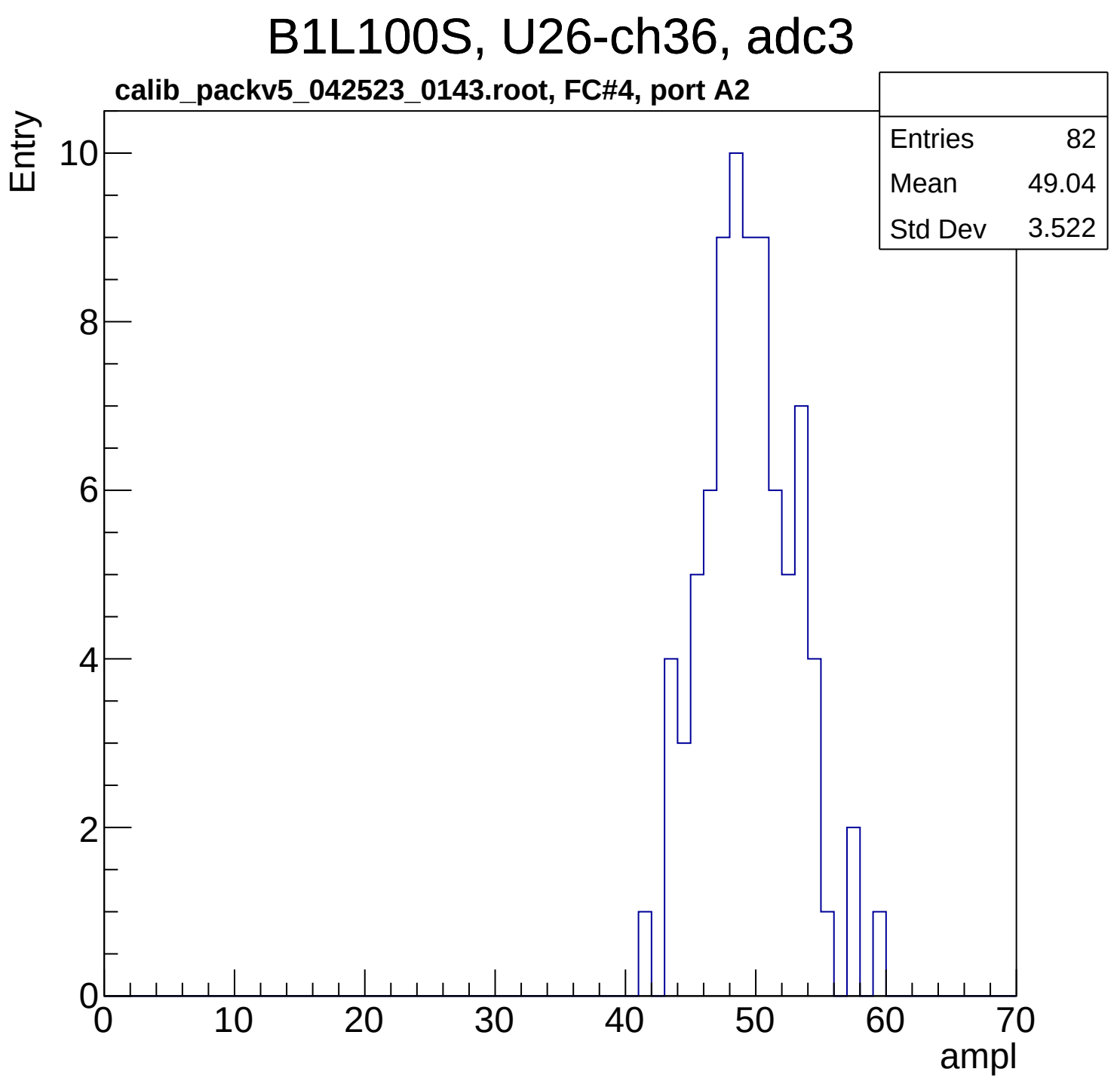
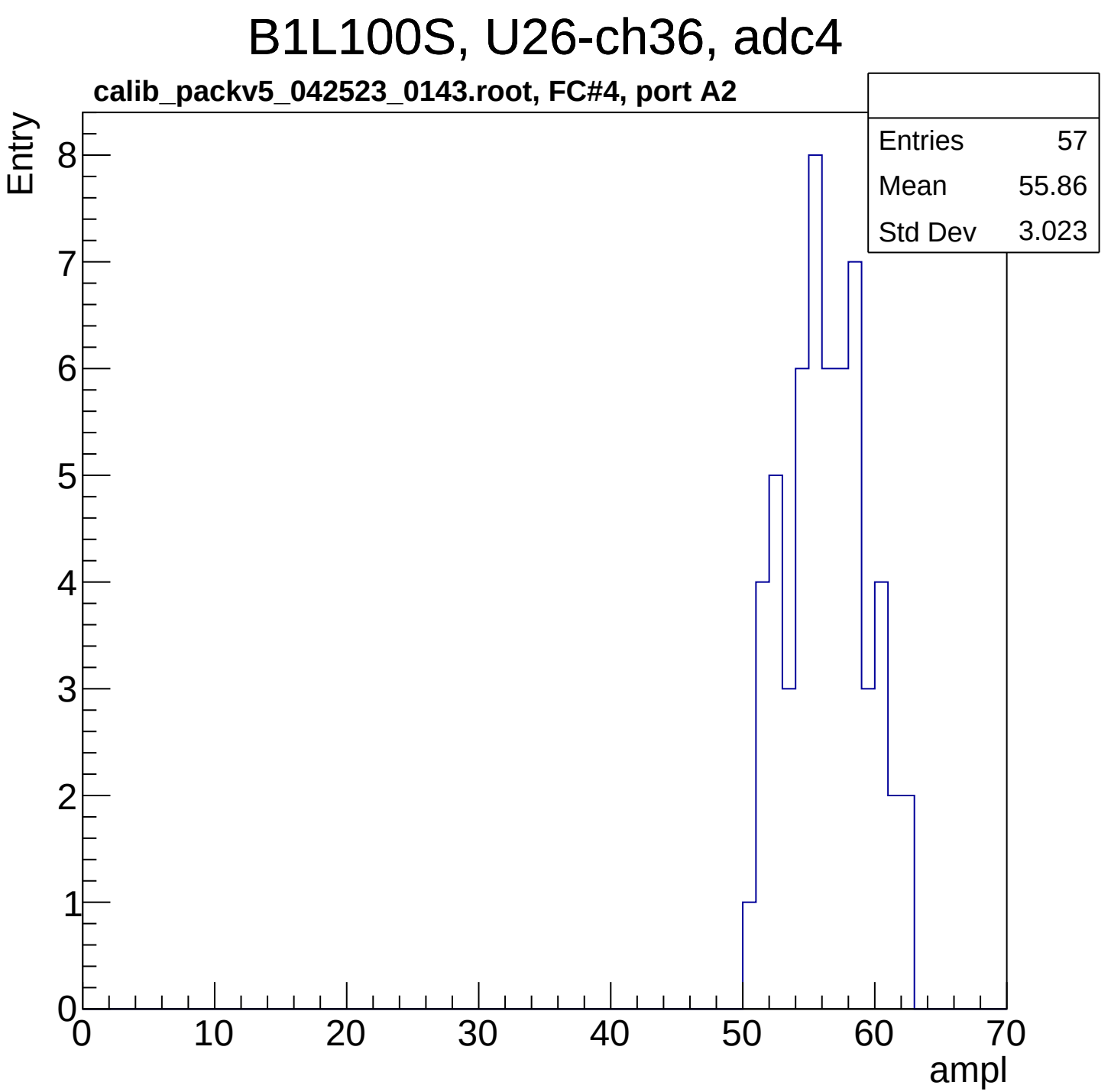
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

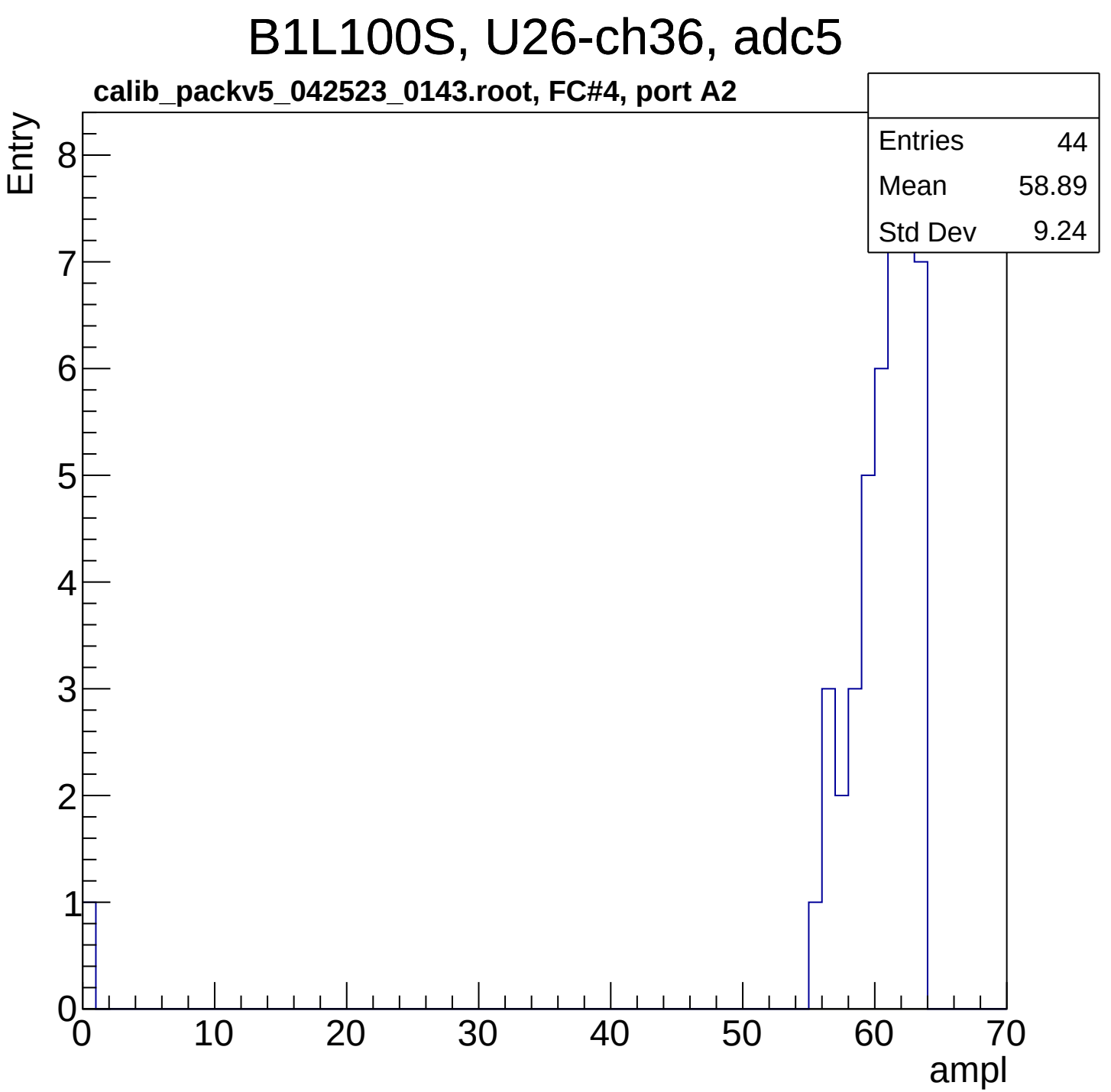

B1L100S, U26-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 28.23
Std Dev 4.863
Gaus mean : 28.9755
Gaus Width: 3.1698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

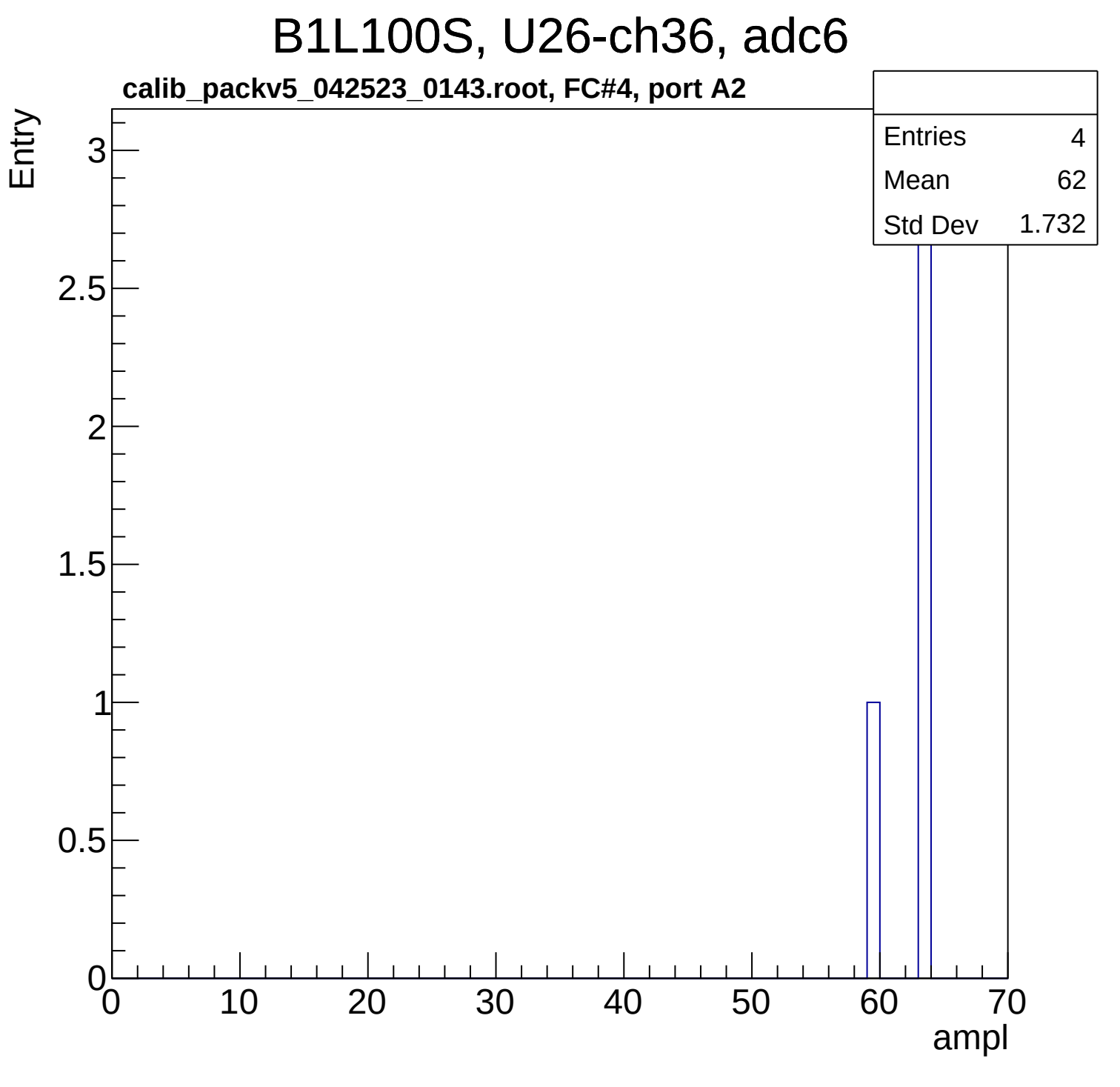

B1L100S, U26-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.96
Std Dev 3.592
Gaus mean : 36.5752
Gaus Width: 3.6290
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

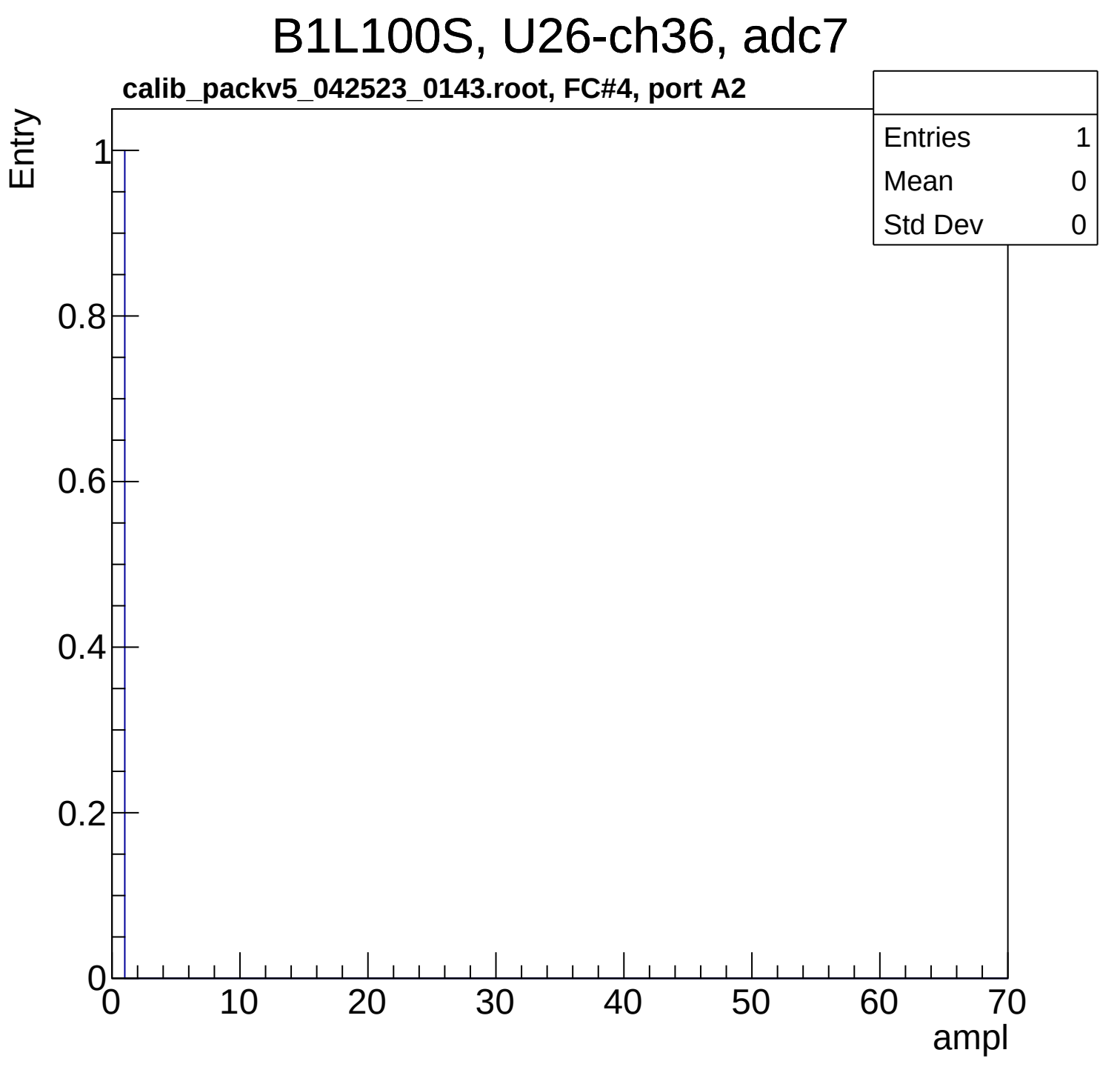

B1L100S, U26-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 42.2
Std Dev 3.672
Gaus mean : 42.2701
Gaus Width: 3.1973
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

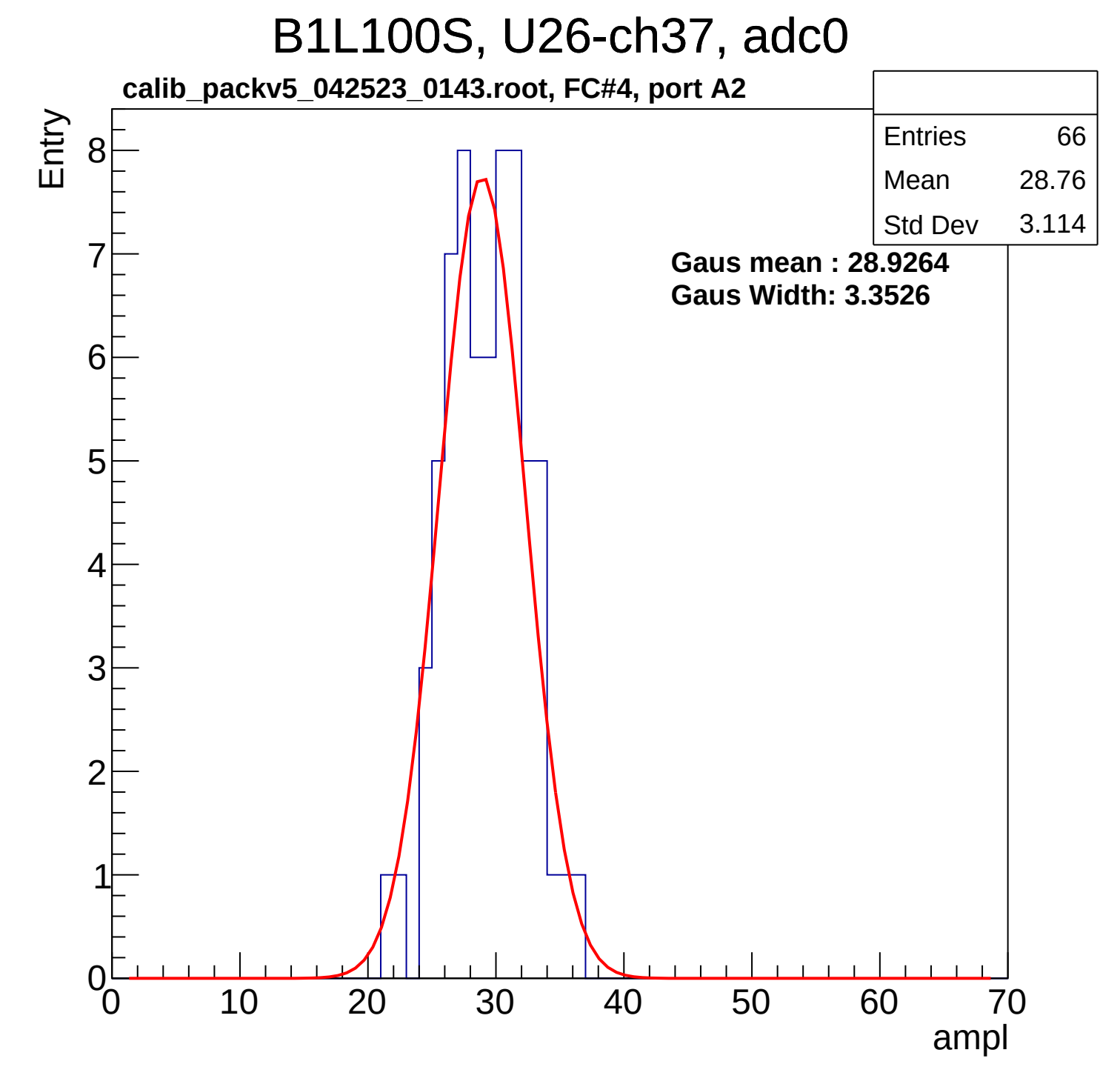

B1L100S, U26-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 49.04
Std Dev 3.522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 55.86
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.89
Std Dev 9.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.76
Std Dev 3.114
Gaus mean : 28.9264
Gaus Width: 3.3526
Entry
ampl
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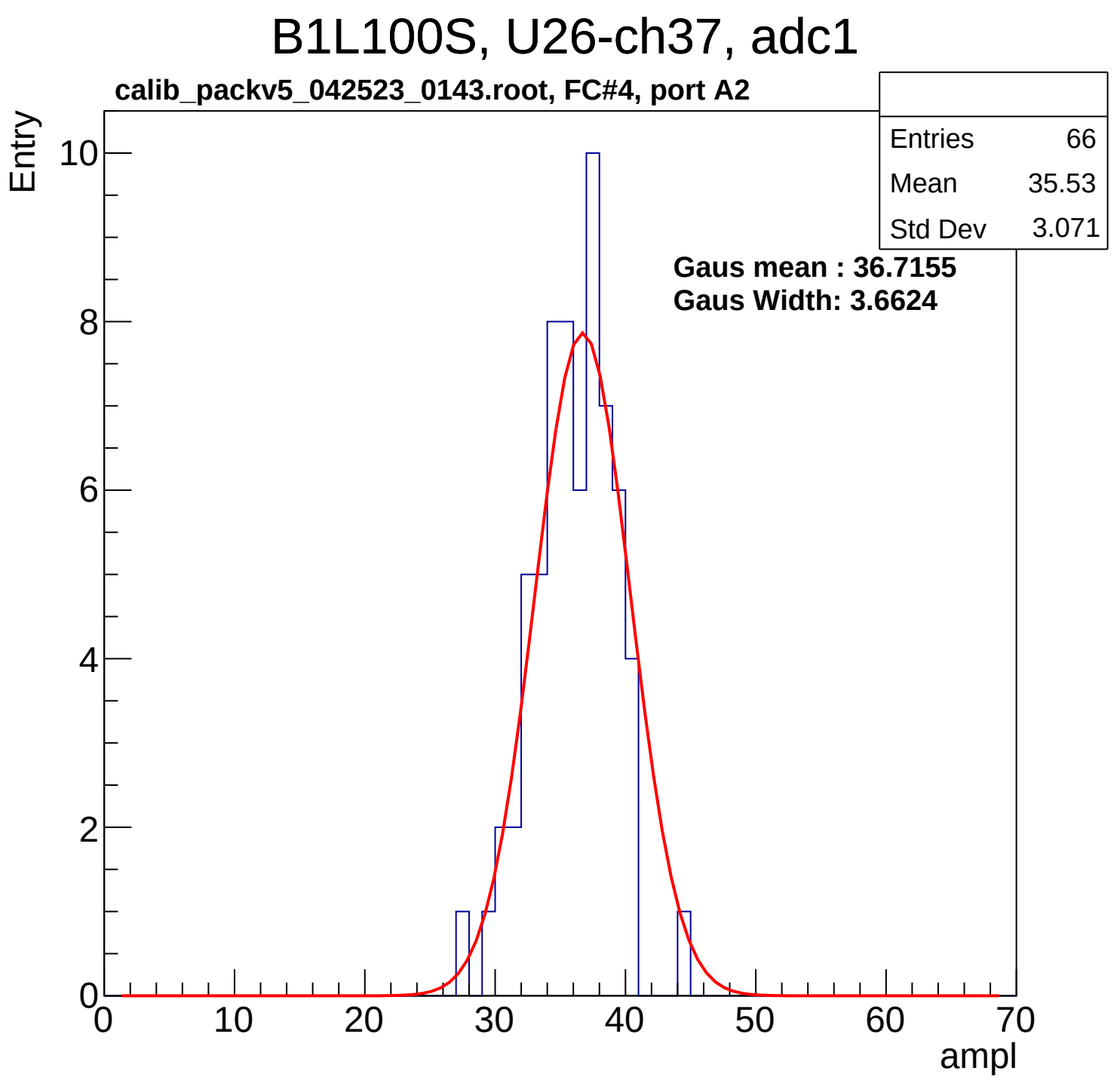
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch37, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 35.53
Std Dev 3.071
Gaus mean : 36.7155
Gaus Width: 3.6624
Entry
ampl
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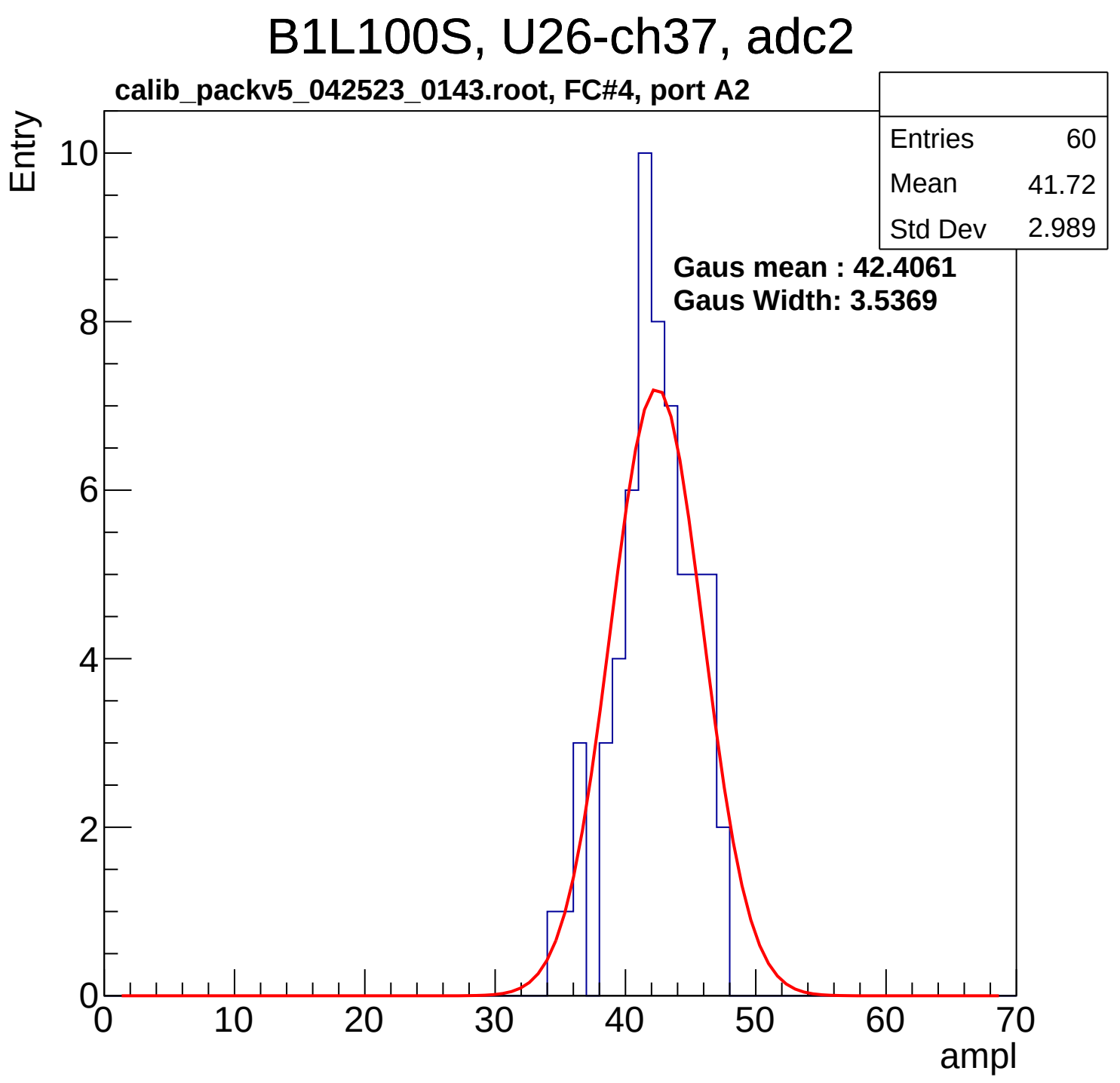
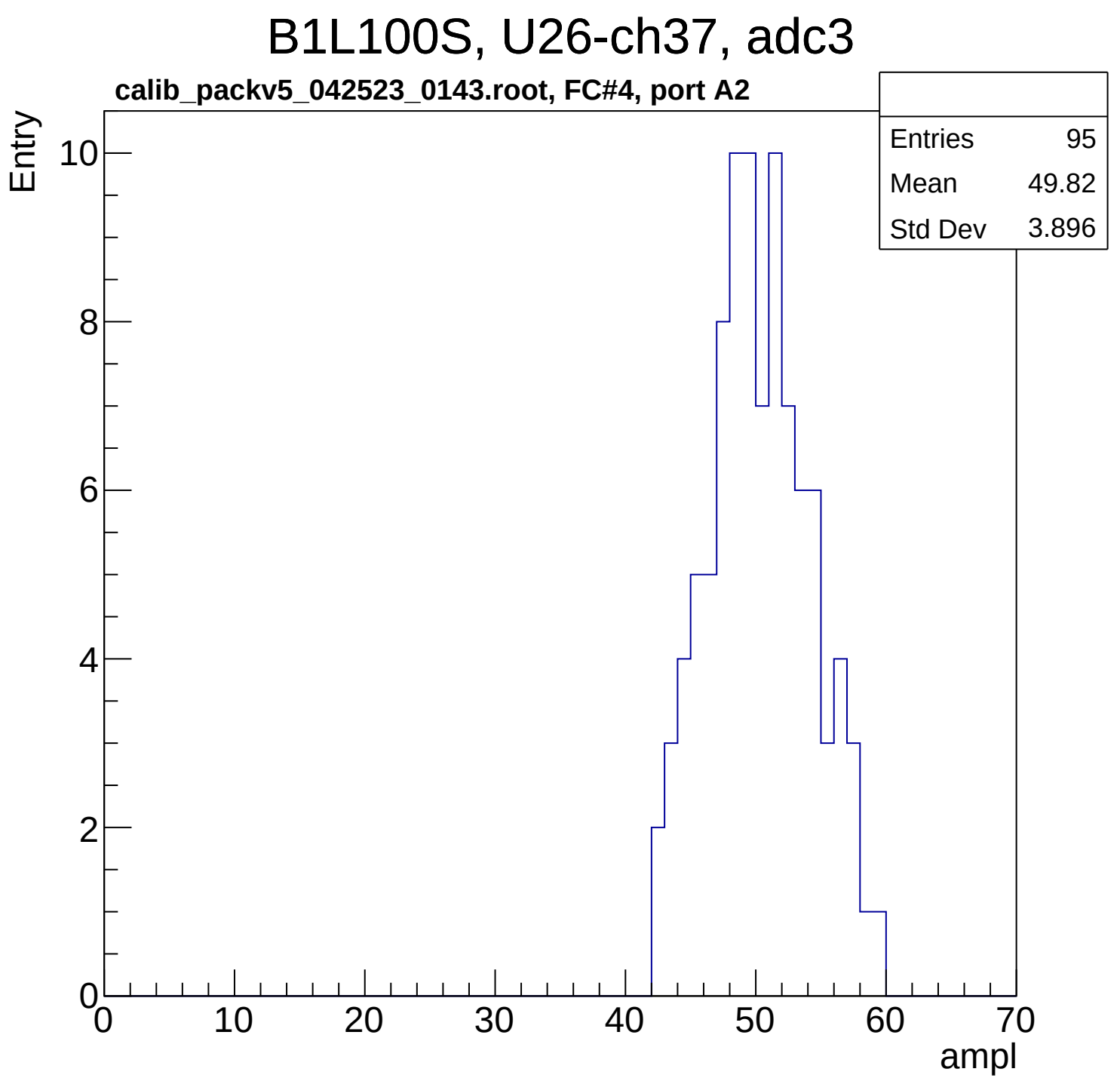
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.72
Std Dev 2.989
Gaus mean : 42.4061
Gaus Width: 3.5369
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 49.82
Std Dev 3.896
Entry
ampl
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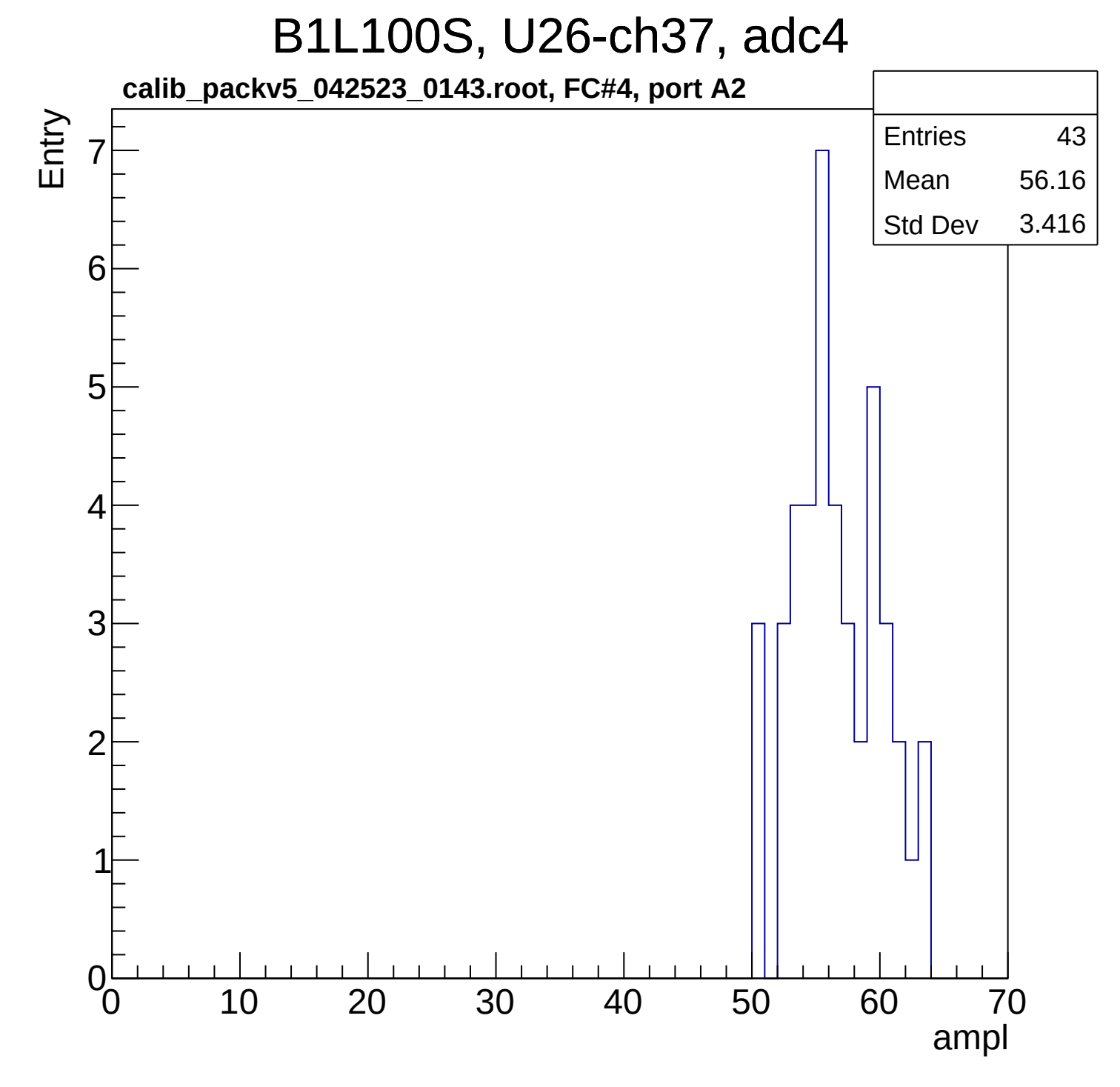
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch37, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 56.16
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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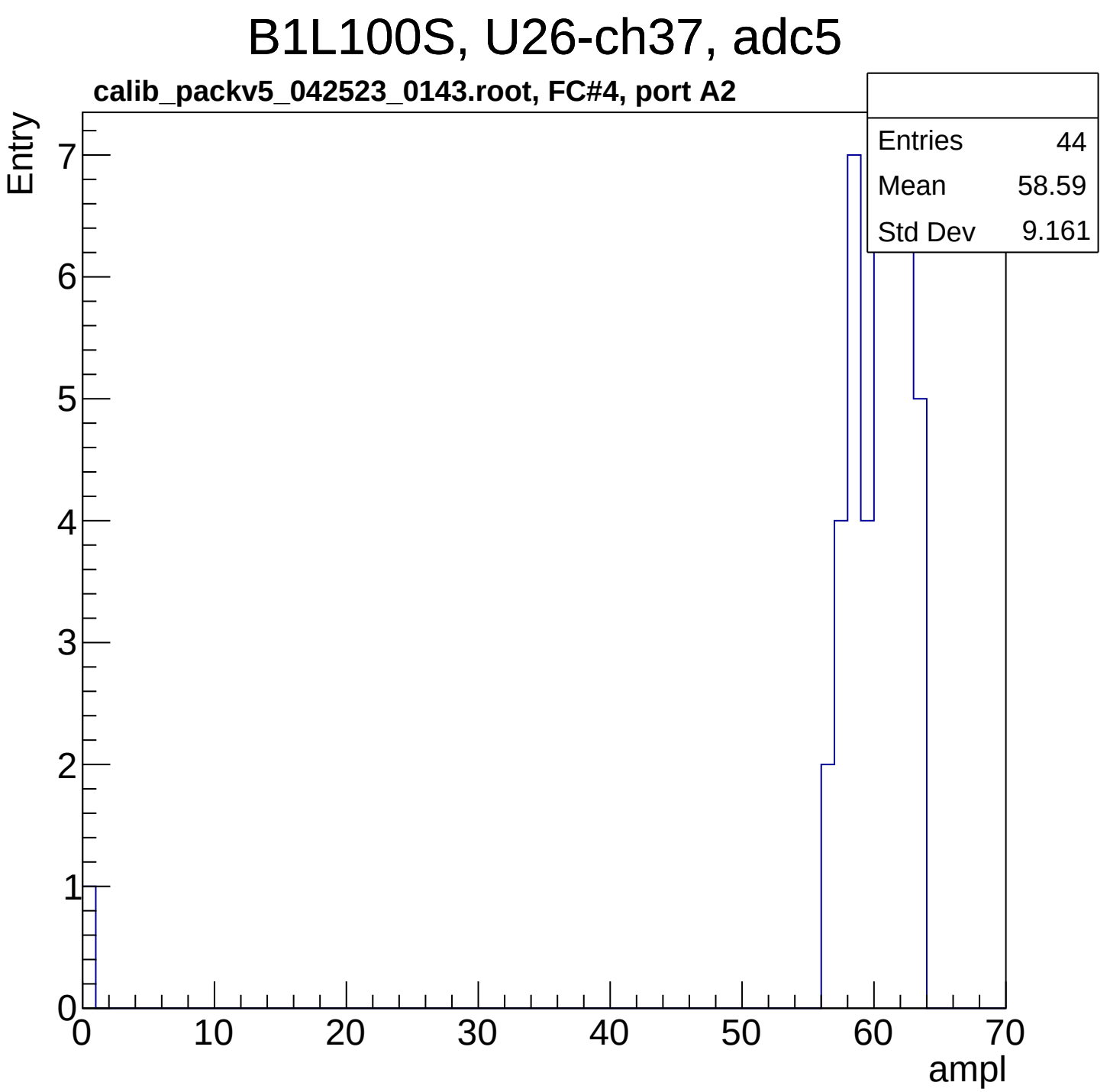
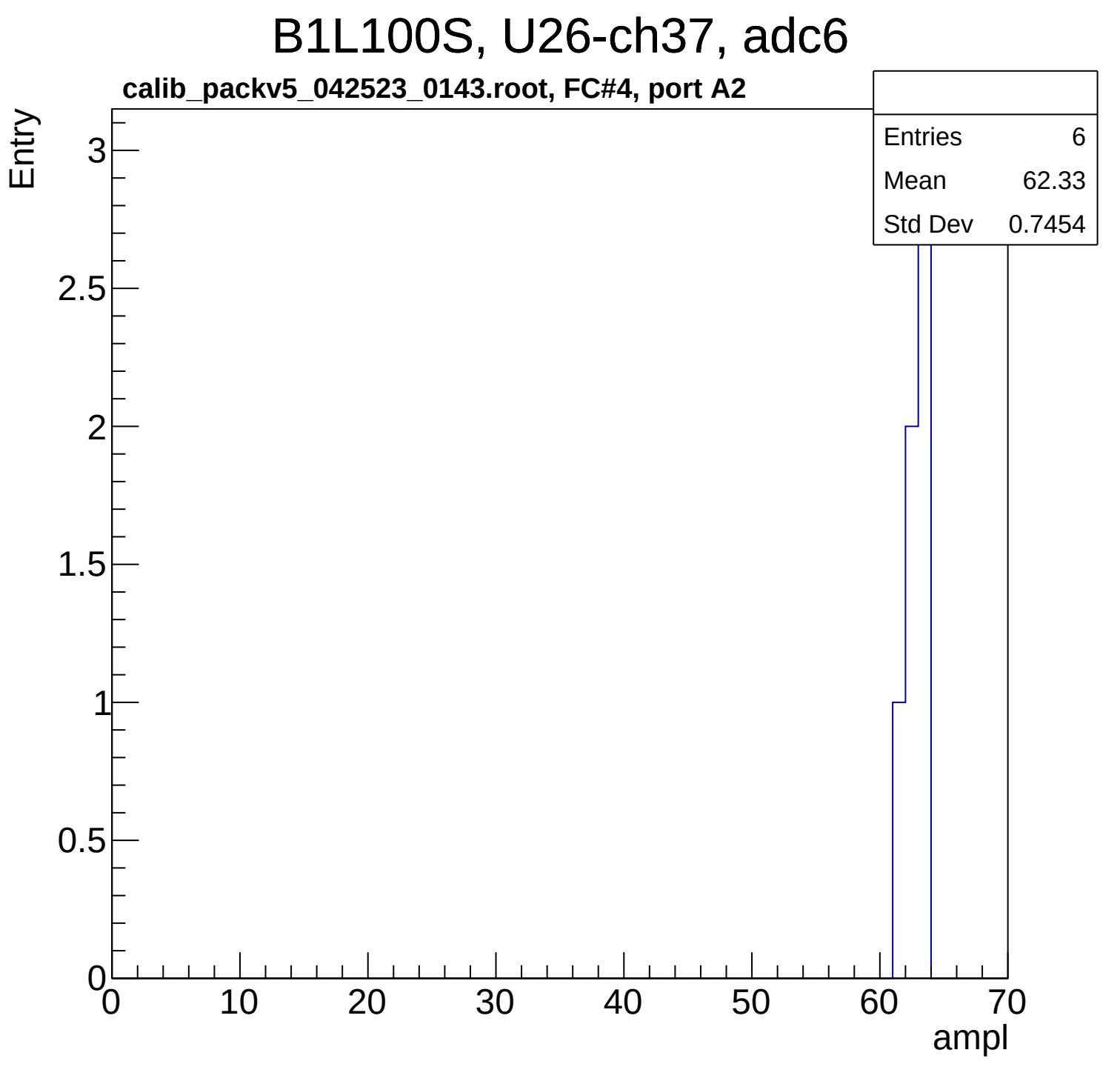

B1L100S, U26-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.59
Std Dev 9.161
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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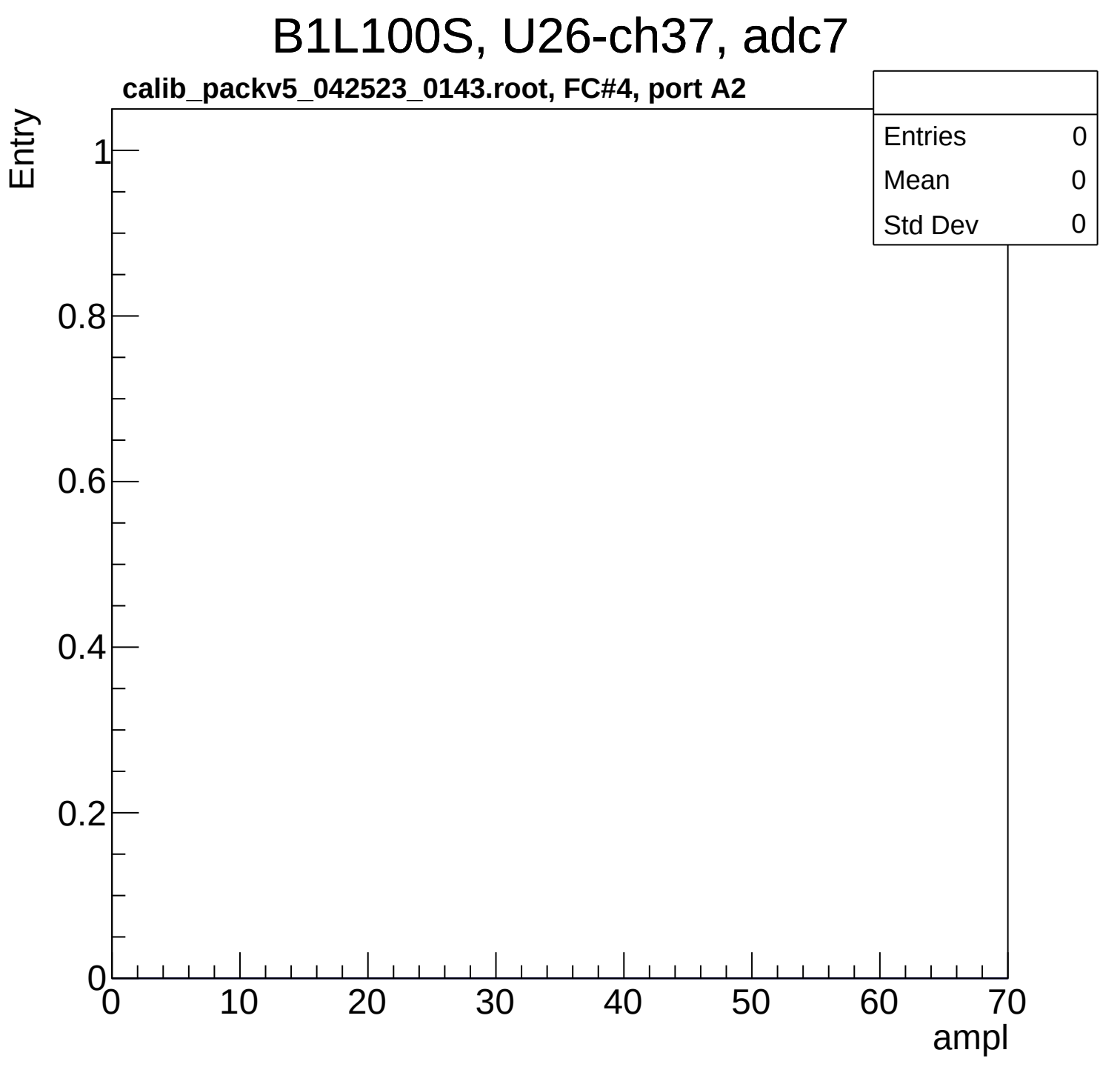
20
30
40
50
60
70


B1L100S, U26-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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30
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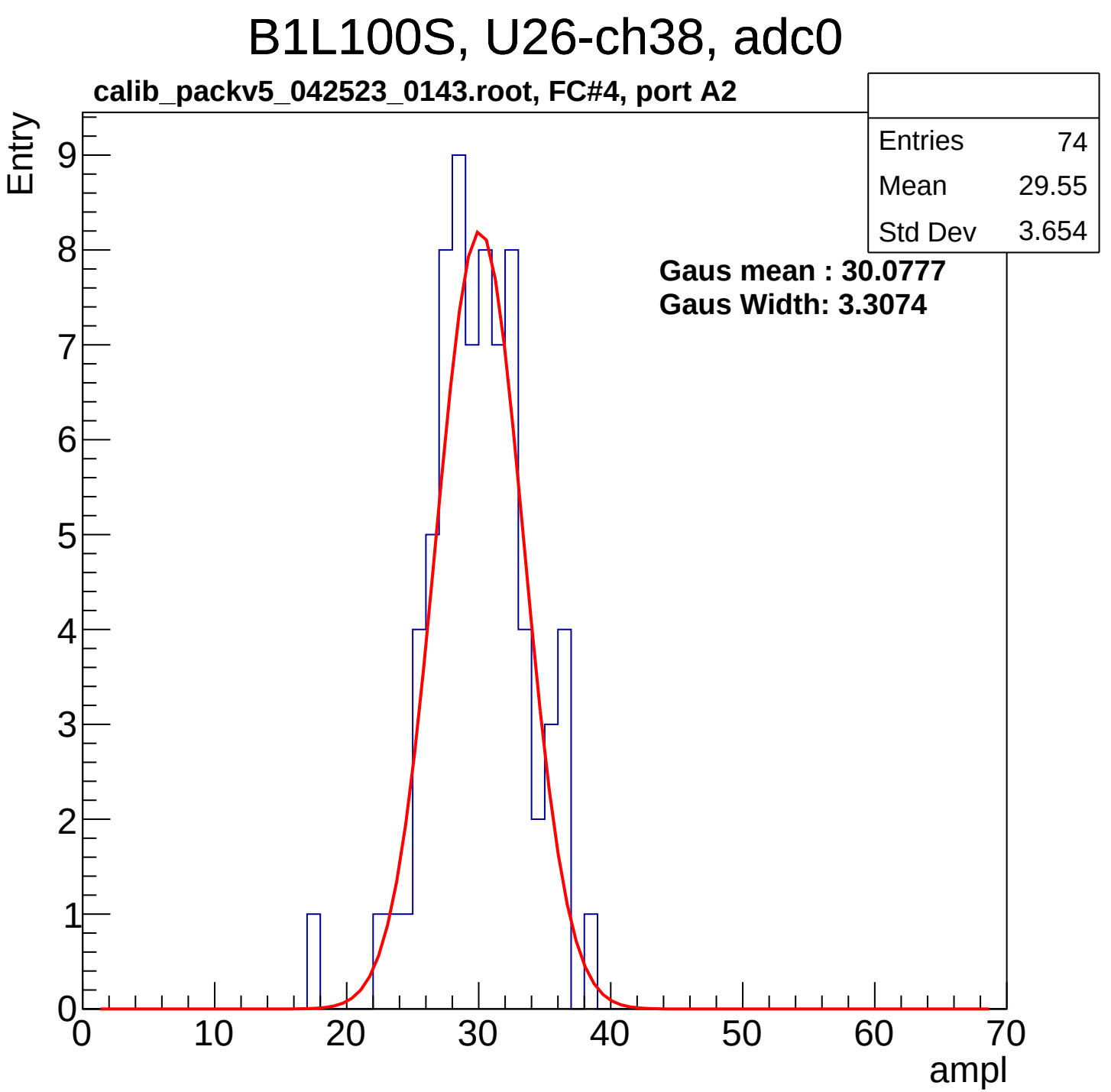
50
60
70
ampl


B1L100S, U26-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.55
Std Dev 3.654
Gaus mean : 30.0777
Gaus Width: 3.3074
Entry
ampl
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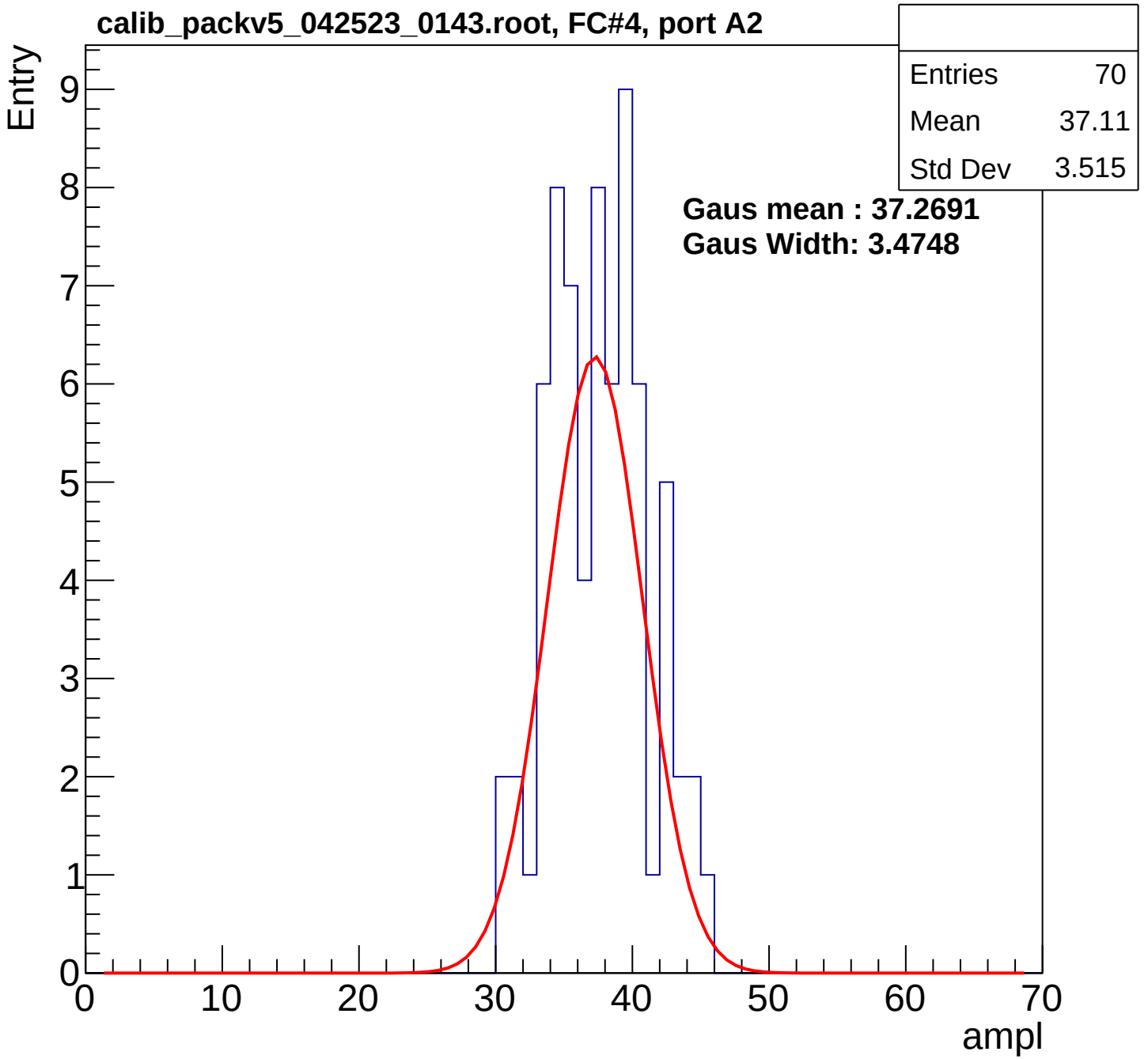
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.11
Std Dev 3.515
Gaus mean : 37.2691
Gaus Width: 3.4748
Entry
ampl
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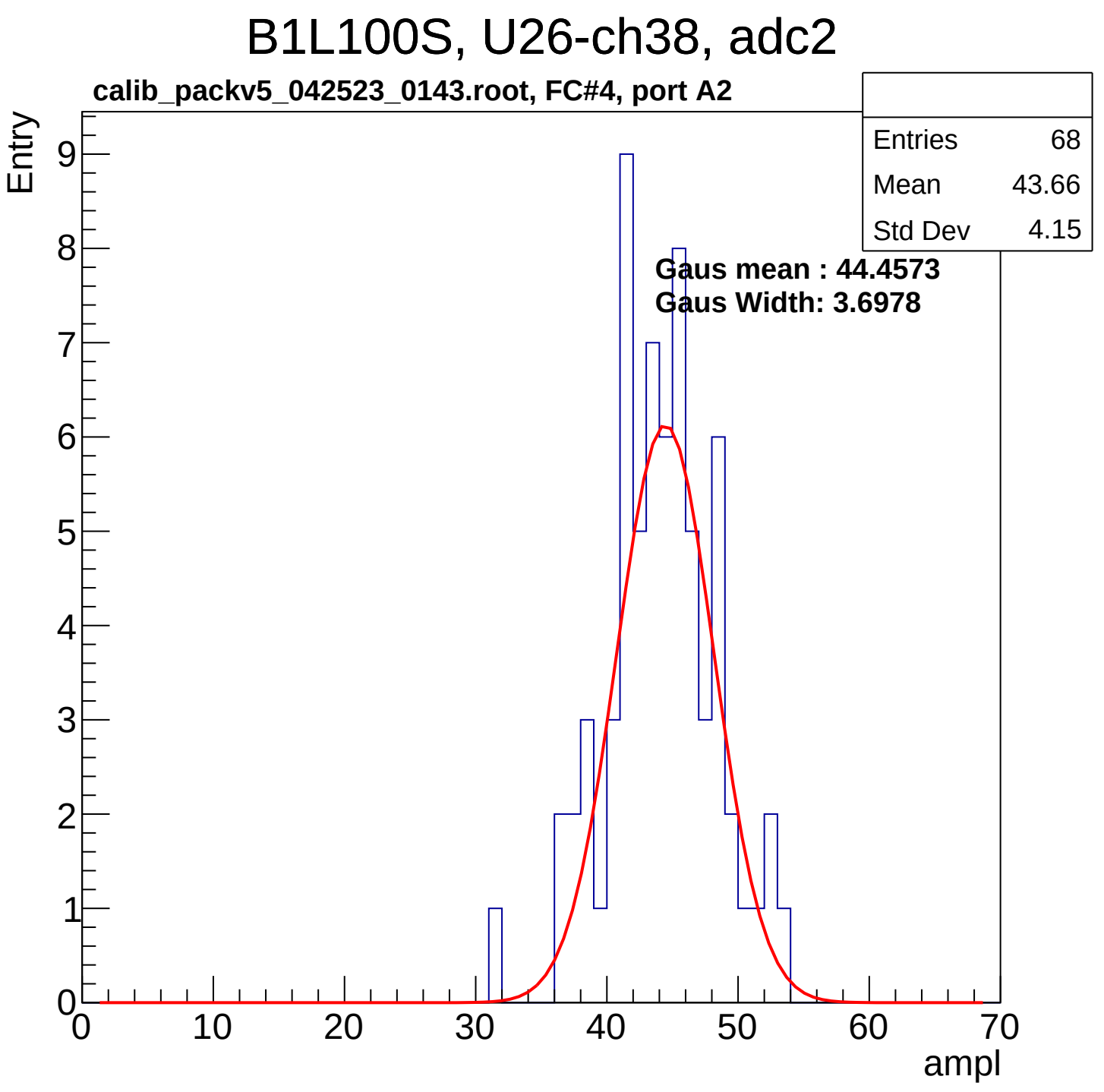
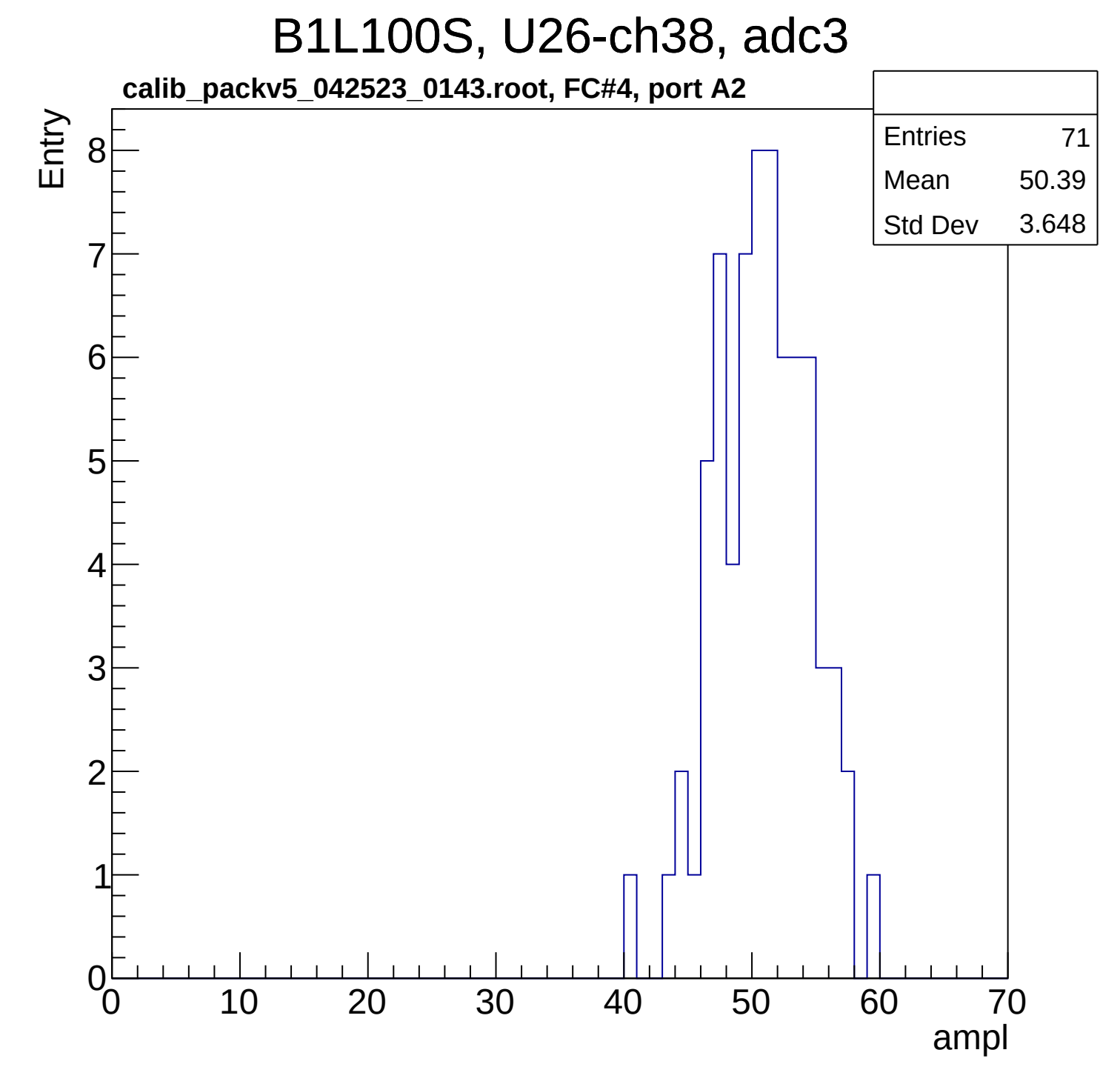
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.66
Std Dev 4.15
Gaus mean : 44.4573
Gaus Width: 3.6978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch38, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.39
Std Dev 3.648
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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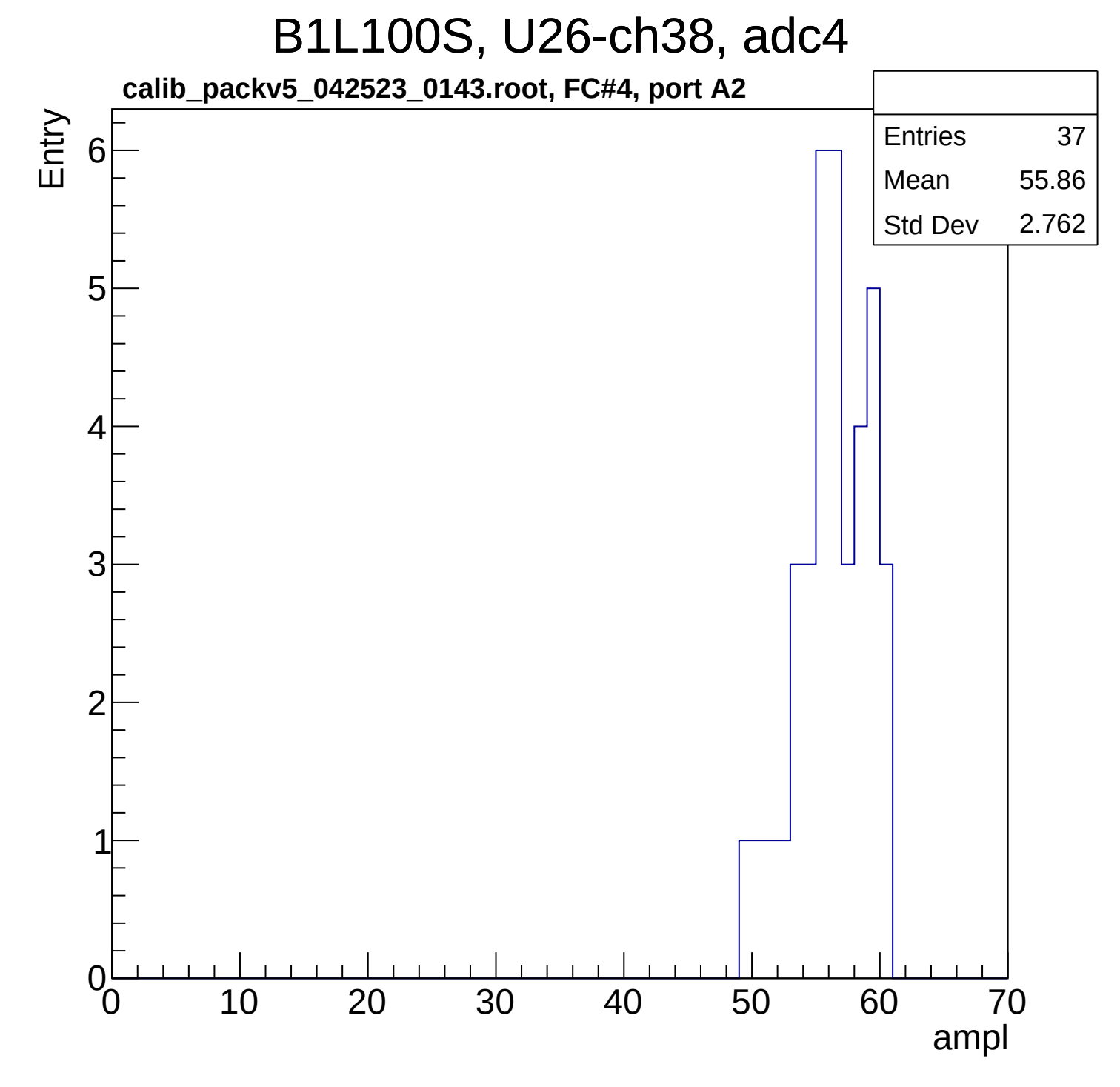
B1L100S, U26-ch38, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 55.86
Std Dev 2.762
Entry
ampl
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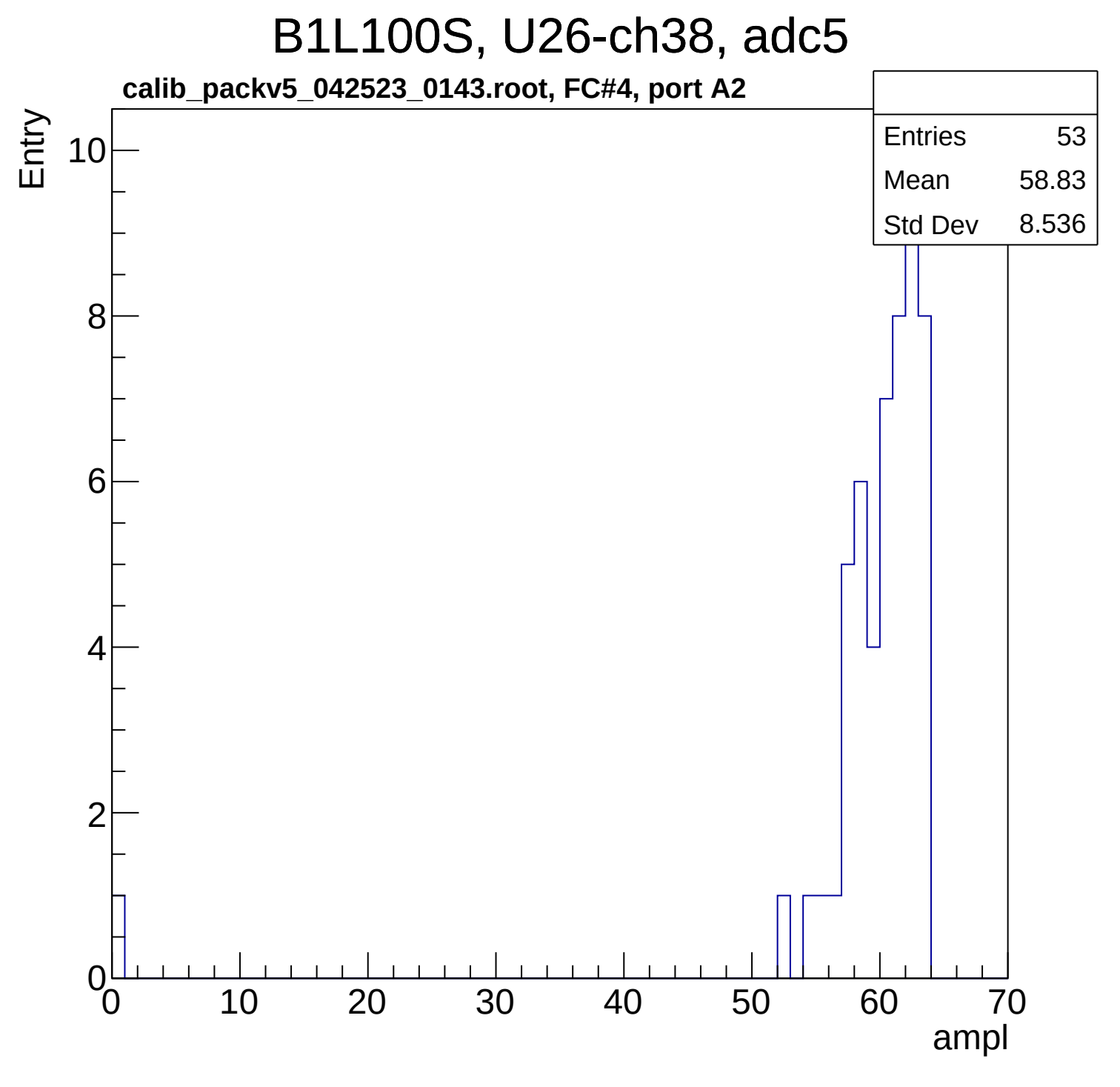
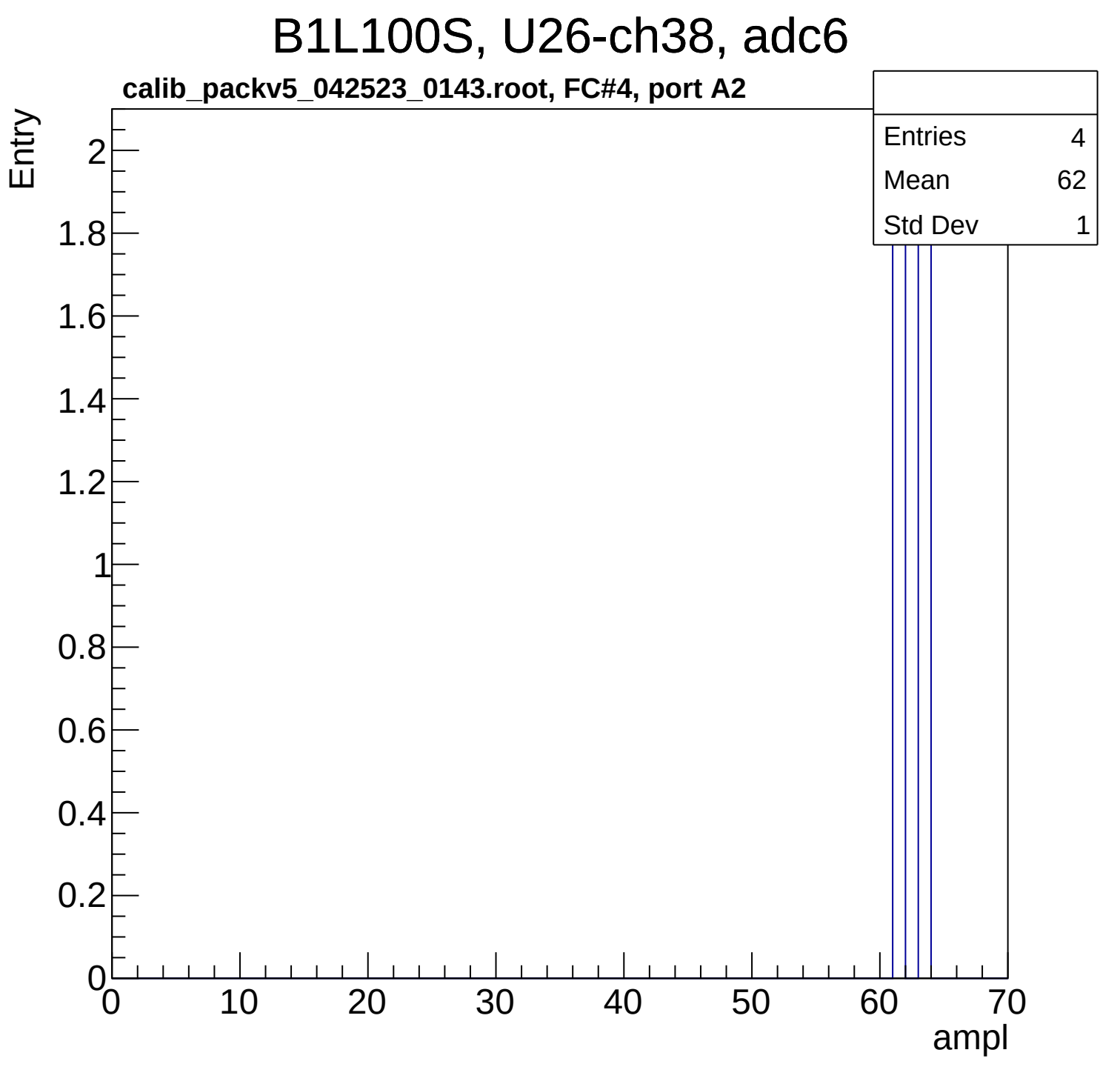
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U26-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.83
Std Dev 8.536
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch38, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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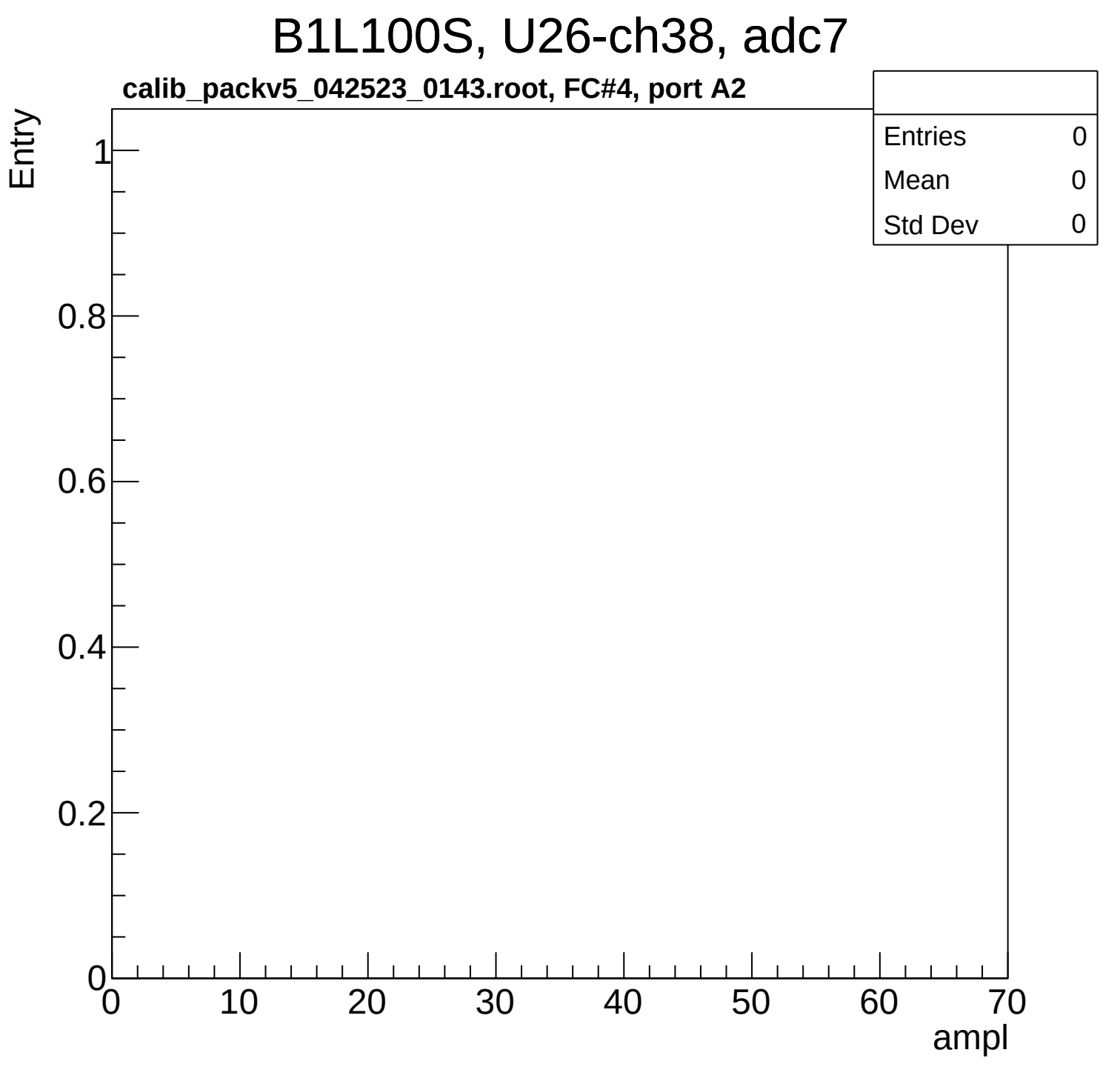
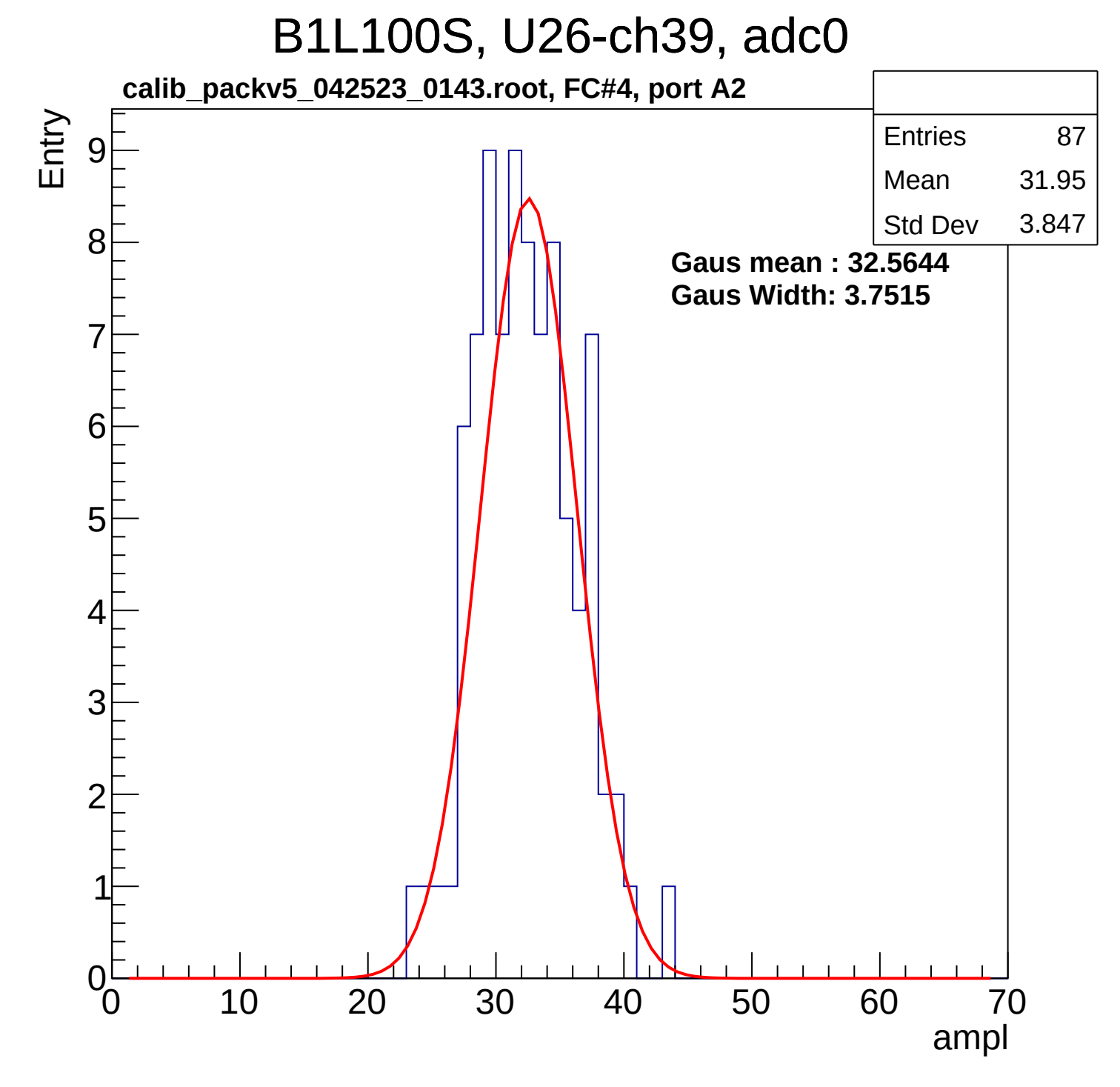

B1L100S, U26-ch38, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 31.95
Std Dev 3.847
Gaus mean : 32.5644
Gaus Width: 3.7515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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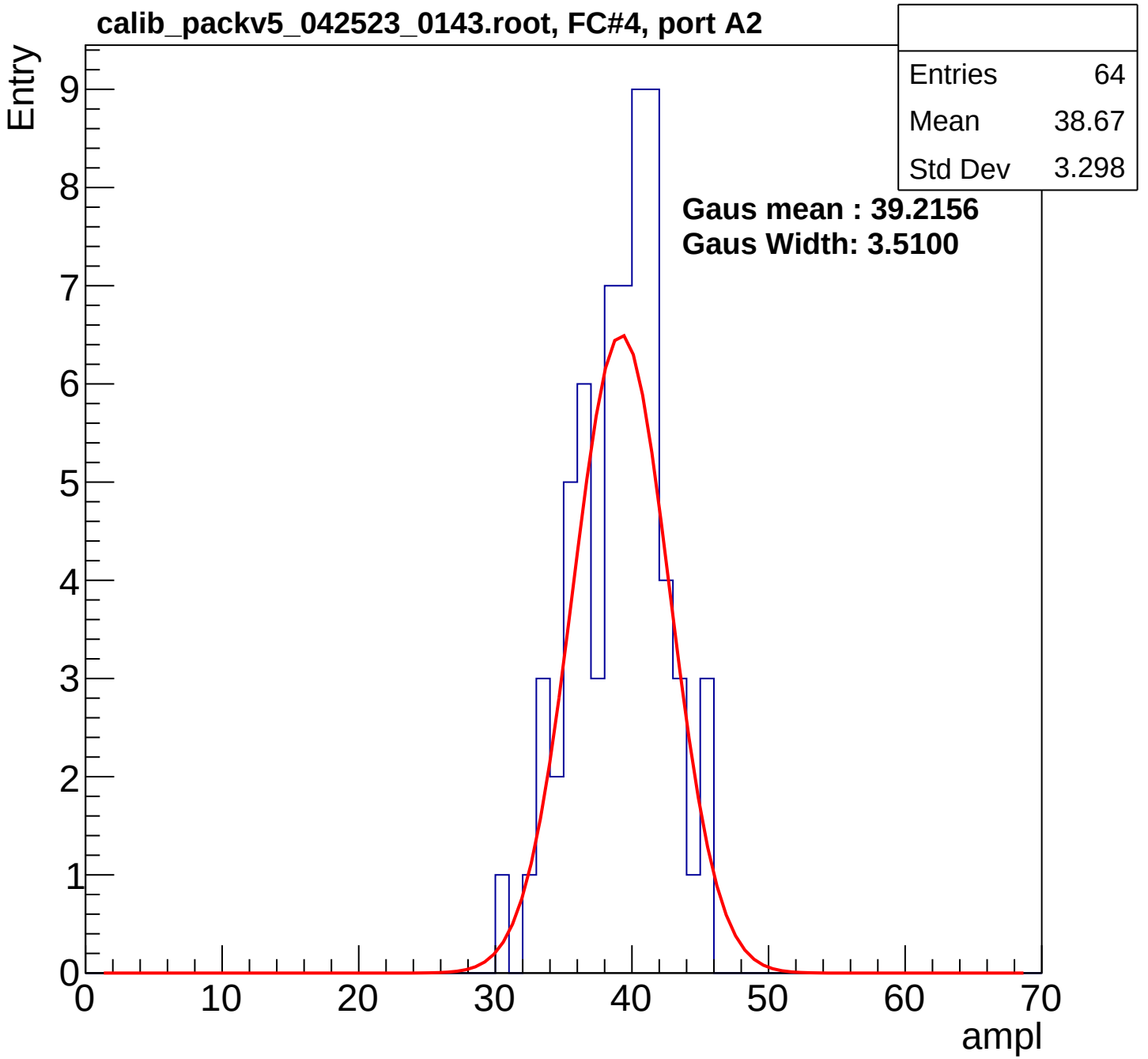
B1L100S, U26-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 38.67
Std Dev 3.298
Gaus mean : 39.2156
Gaus Width: 3.5100
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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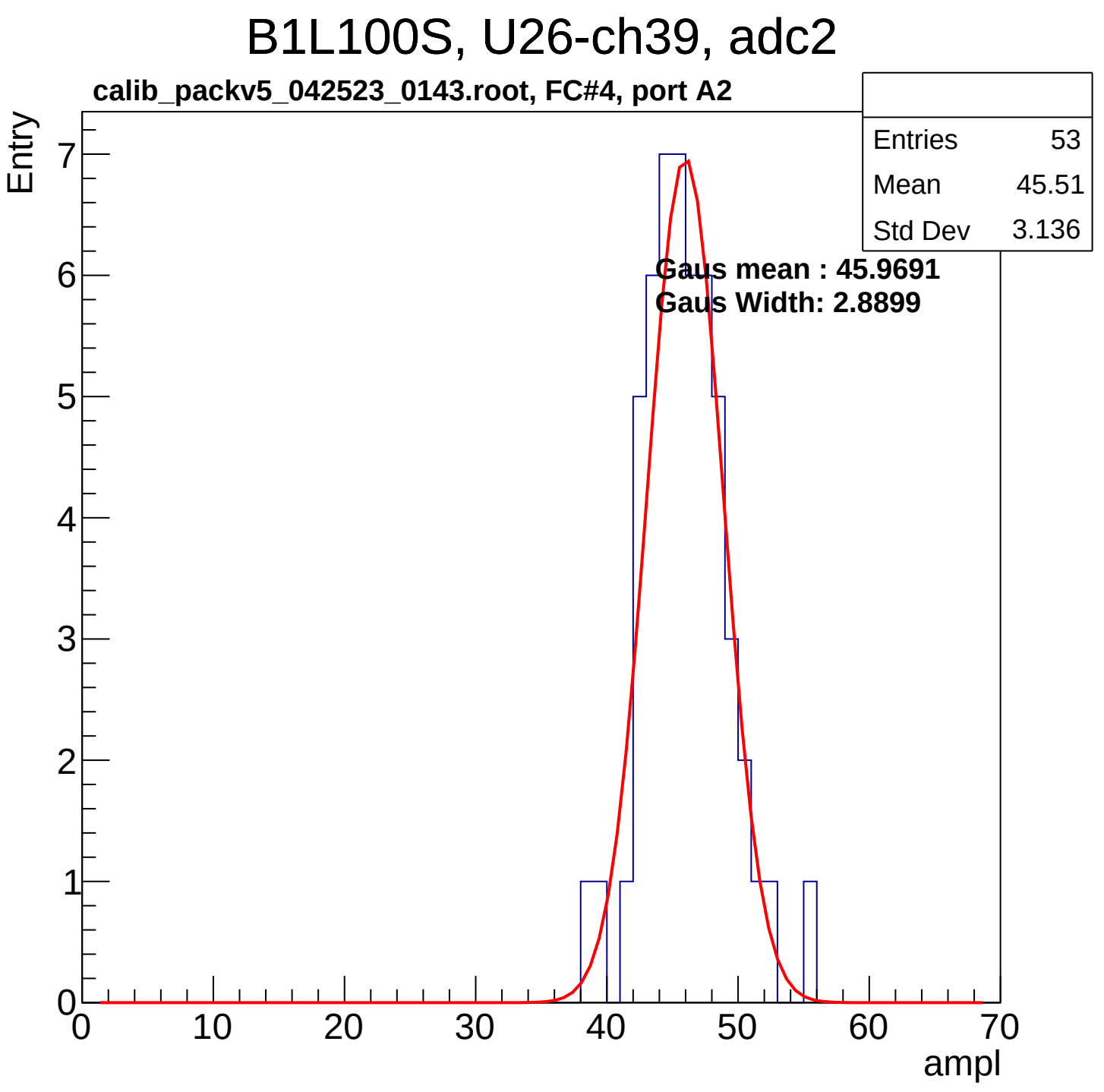
B1L100S, U26-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 45.51
Std Dev 3.136
Gaus mean : 45.9691
Gaus Width: 2.8899
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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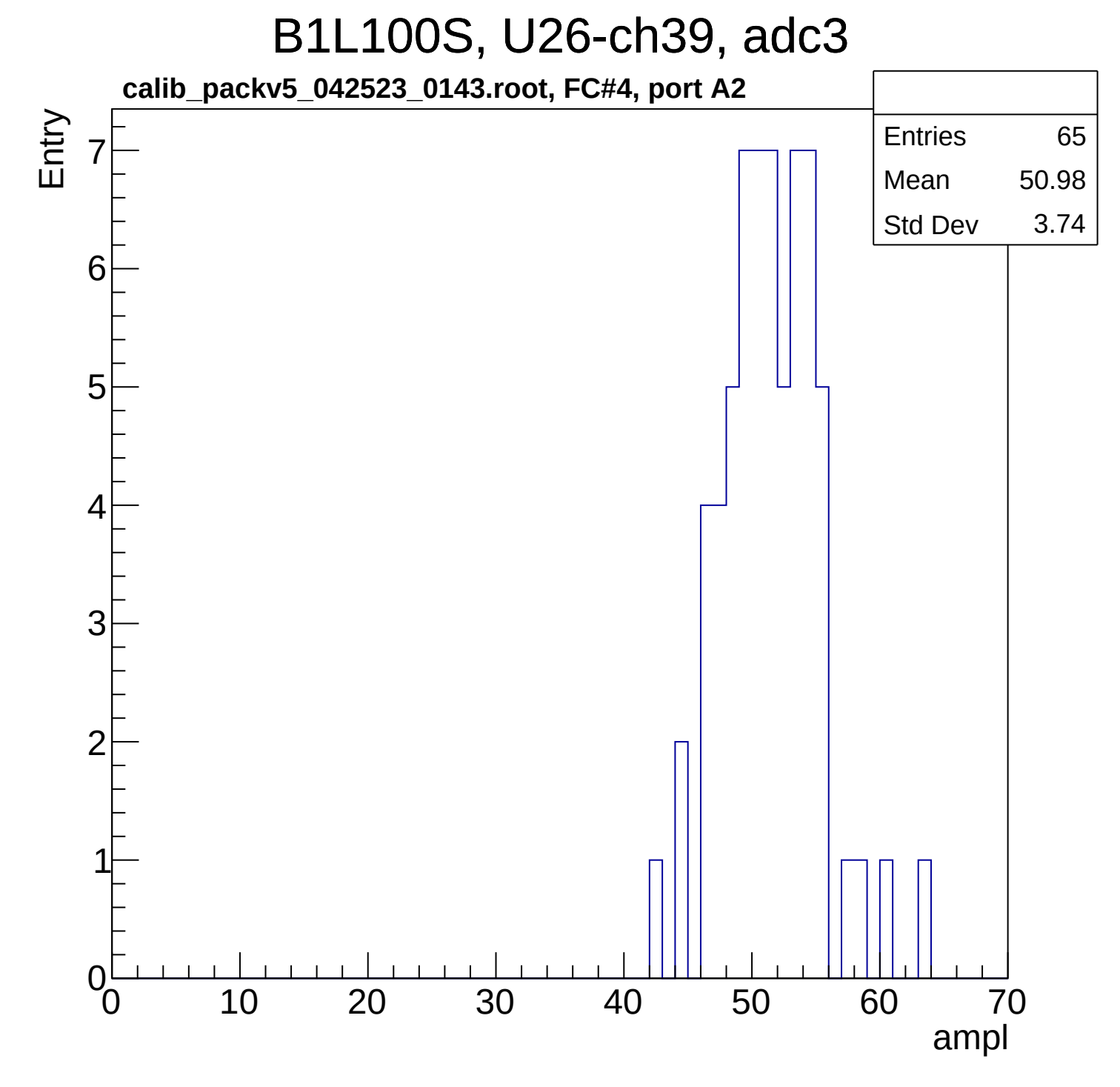
B1L100S, U26-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.98
Std Dev 3.74
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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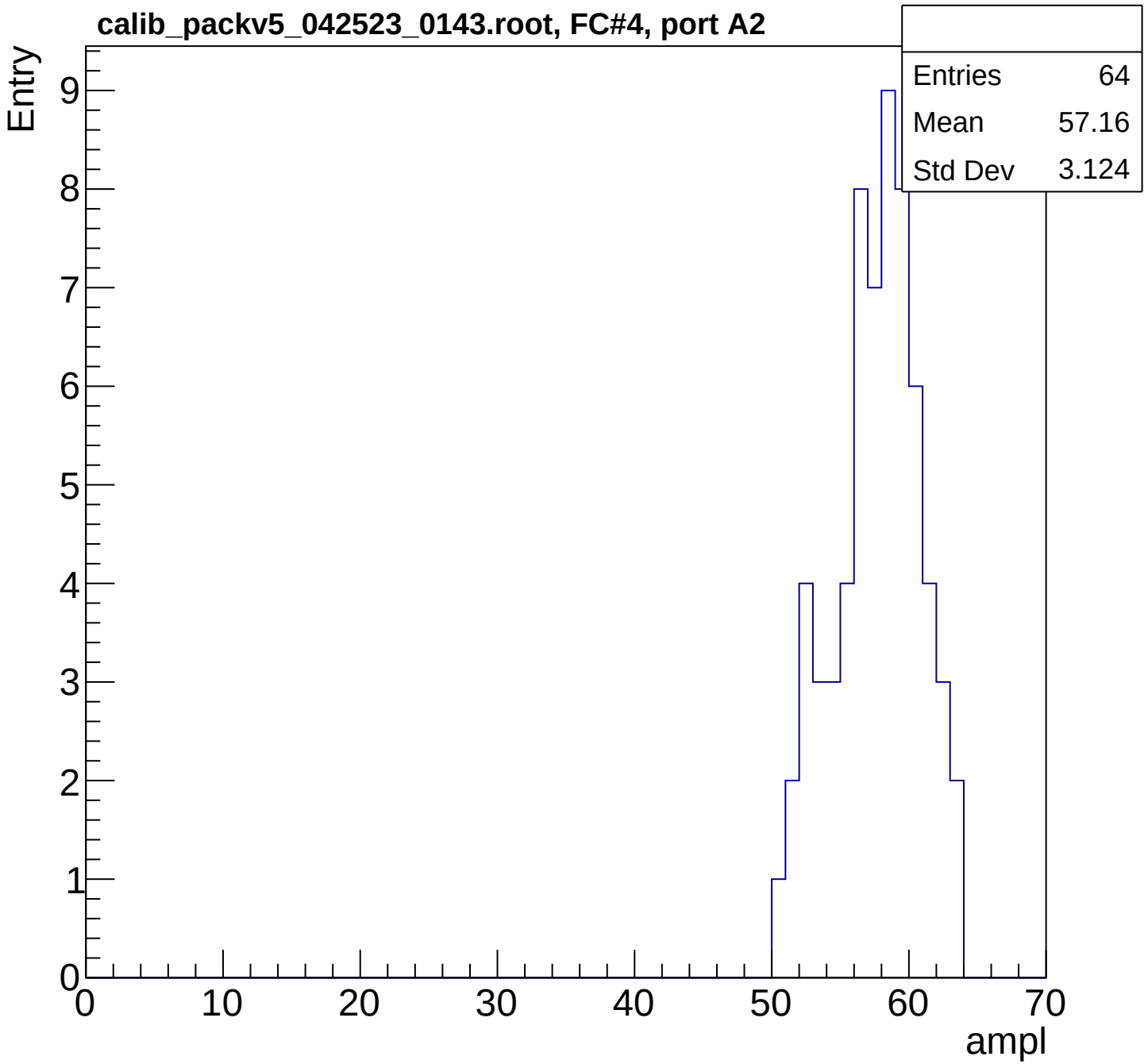
B1L100S, U26-ch39, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.16
Std Dev 3.124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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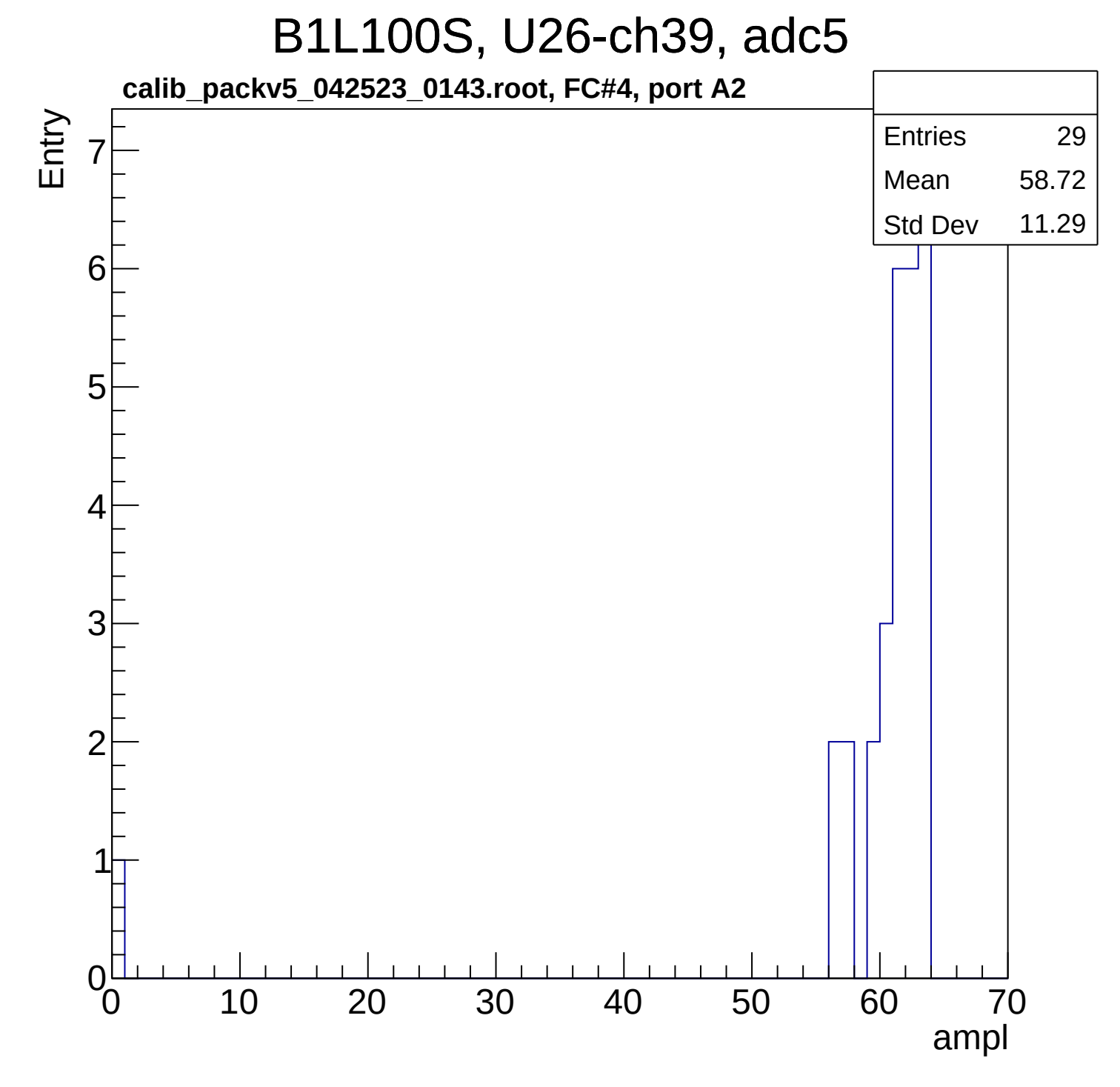
B1L100S, U26-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.72
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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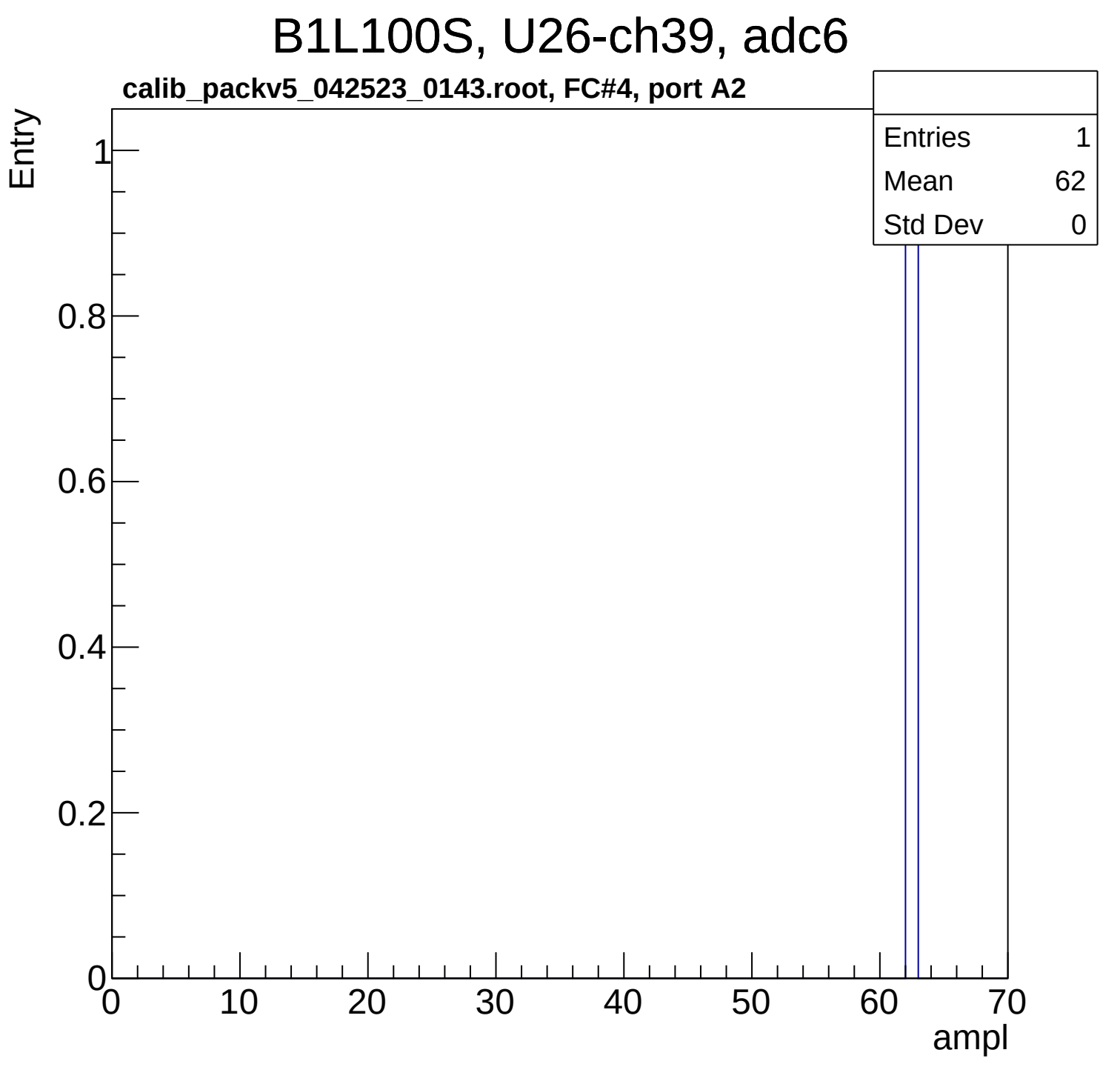
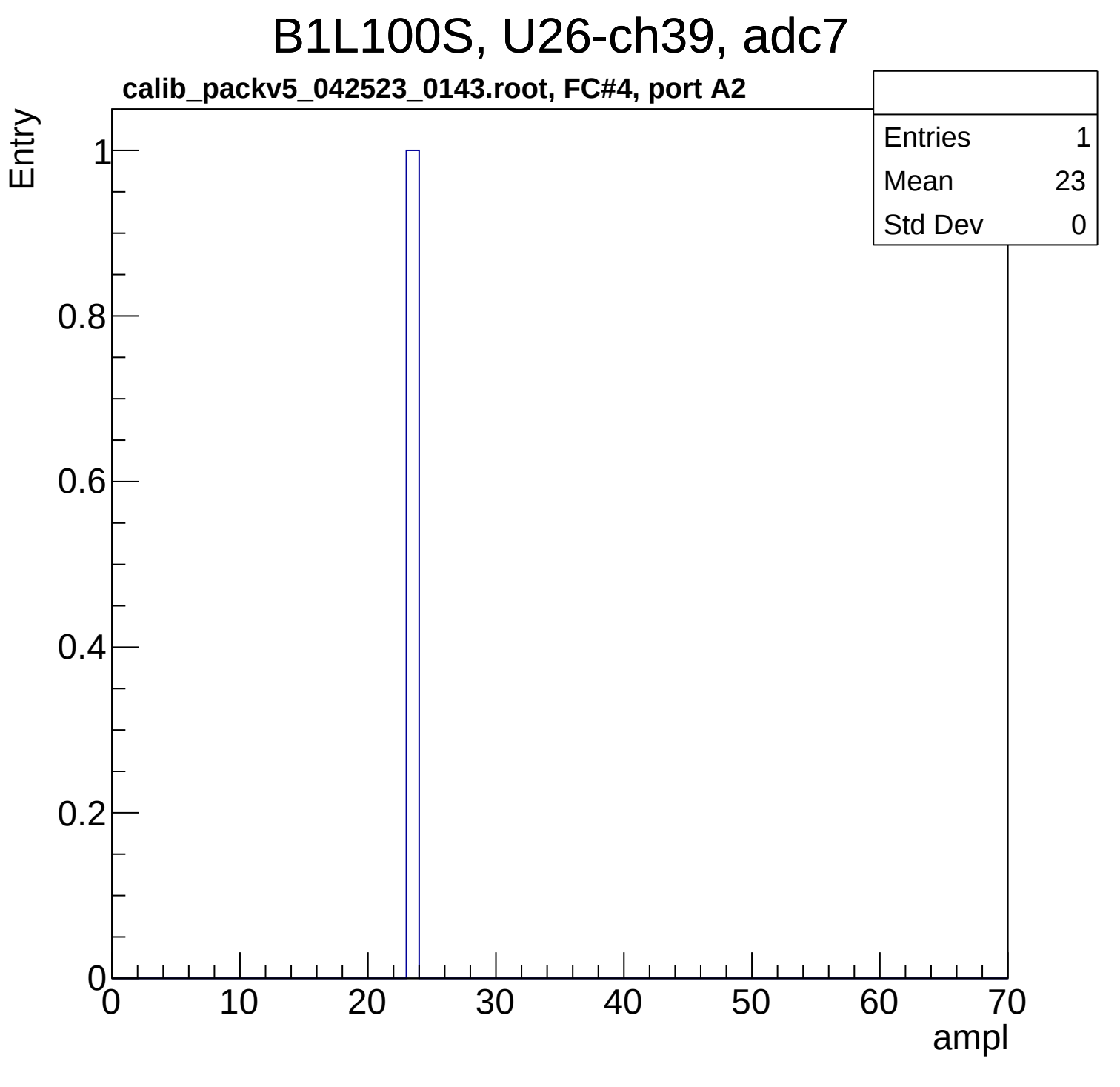

B1L100S, U26-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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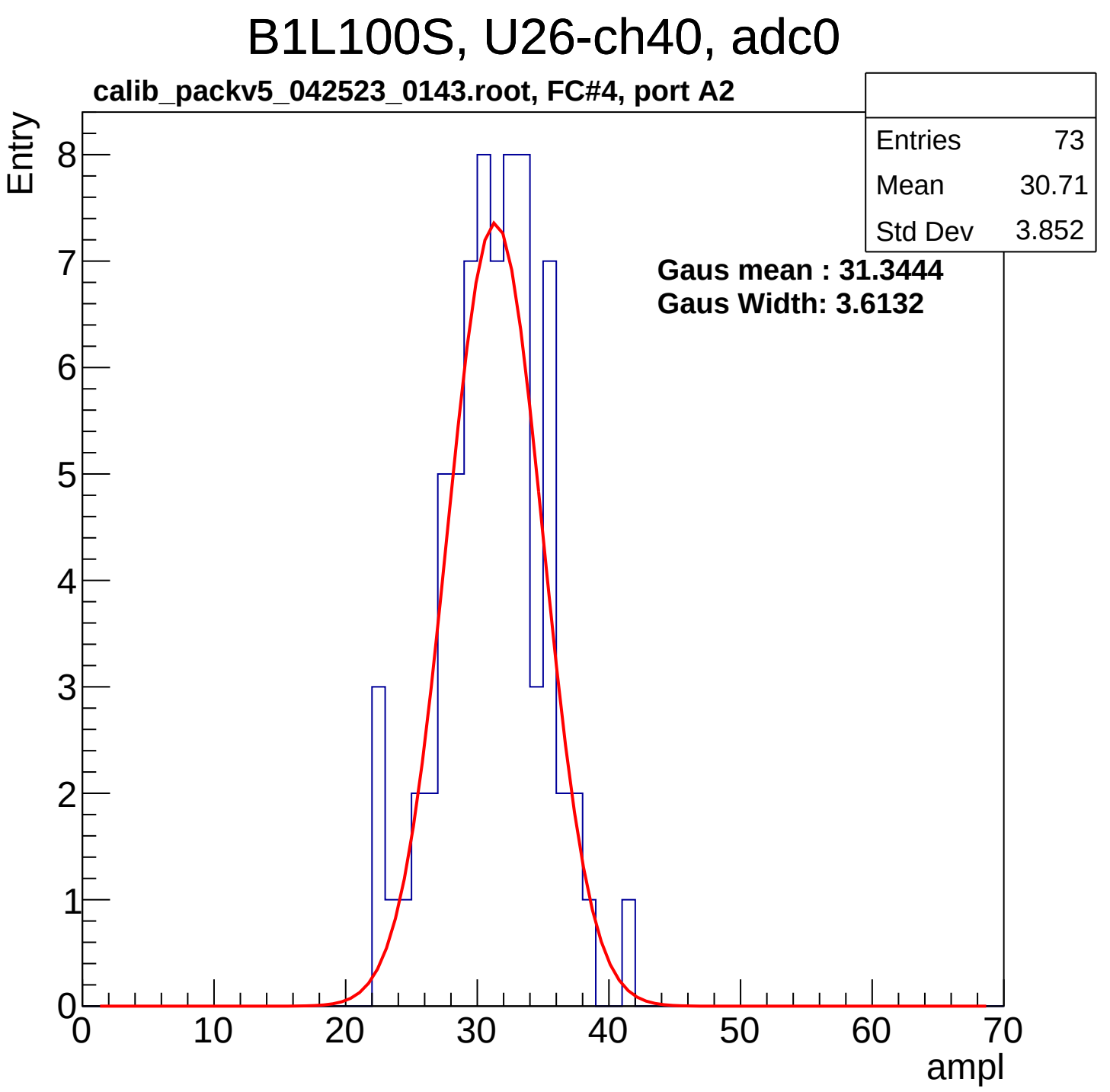
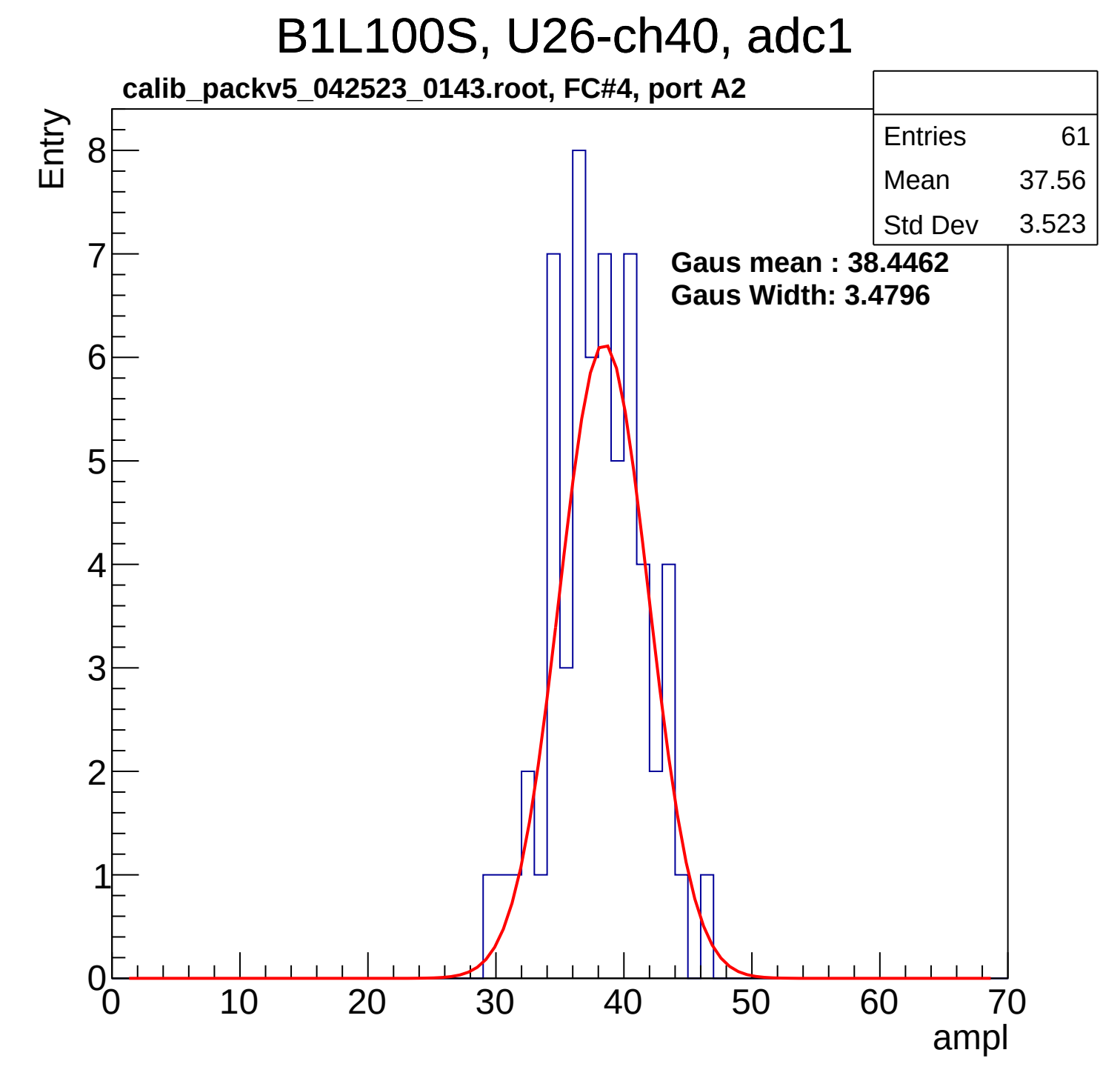
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.71
Std Dev 3.852
Gaus mean : 31.3444
Gaus Width: 3.6132
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.56
Std Dev 3.523
Gaus mean : 38.4462
Gaus Width: 3.4796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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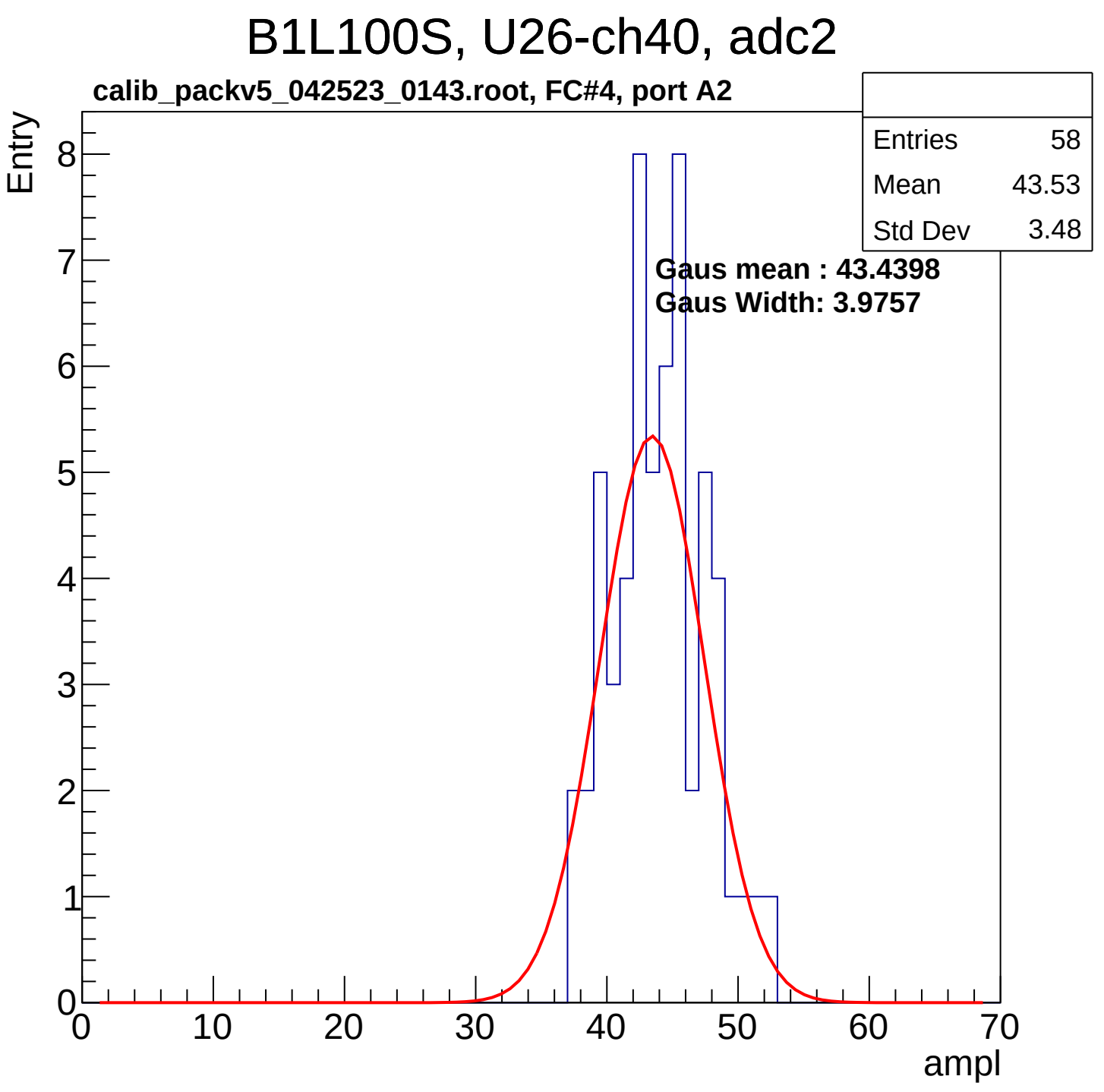
B1L100S, U26-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.53
Std Dev 3.48
Gaus mean : 43.4398
Gaus Width: 3.9757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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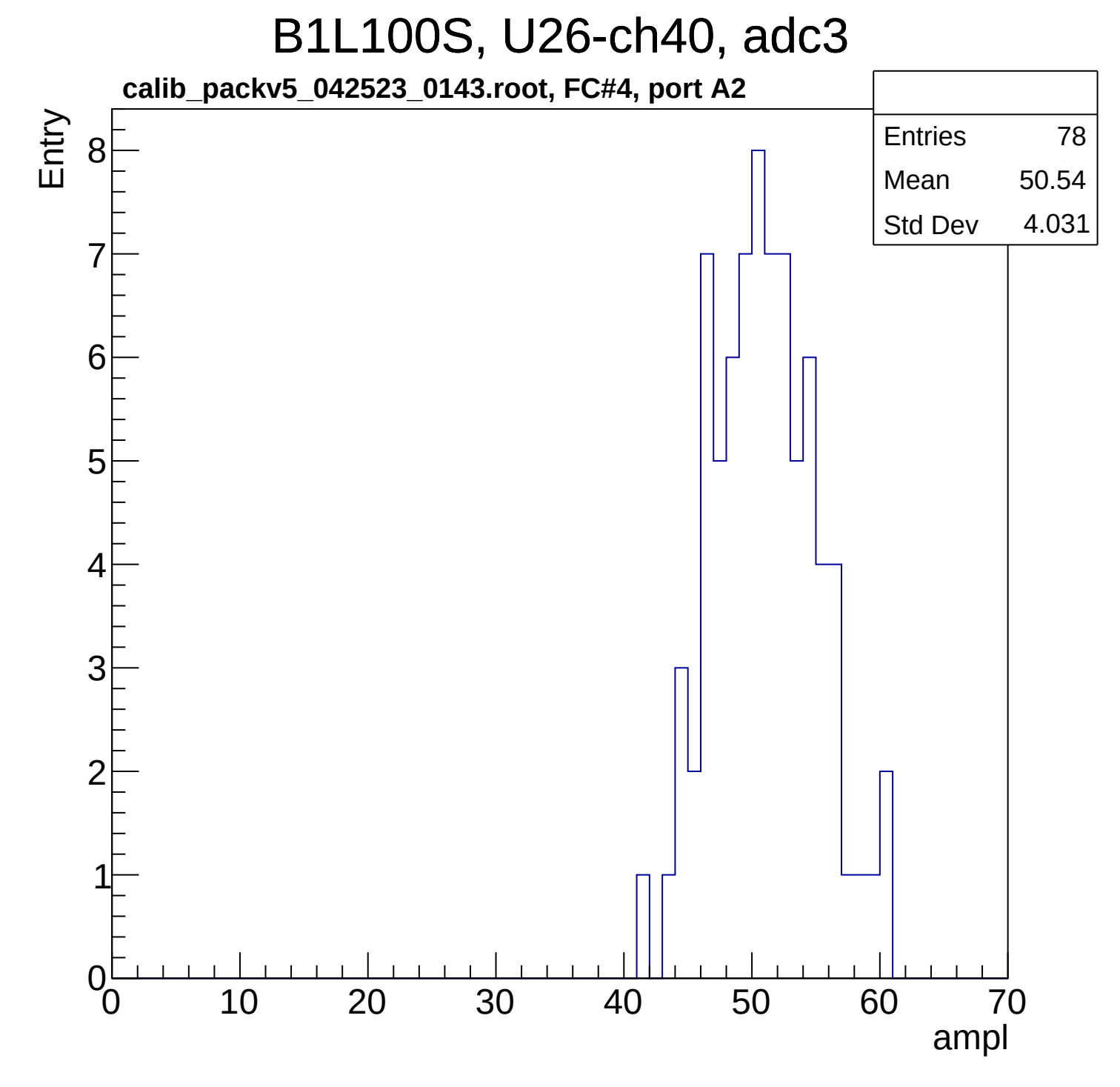
B1L100S, U26-ch40, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 50.54
Std Dev 4.031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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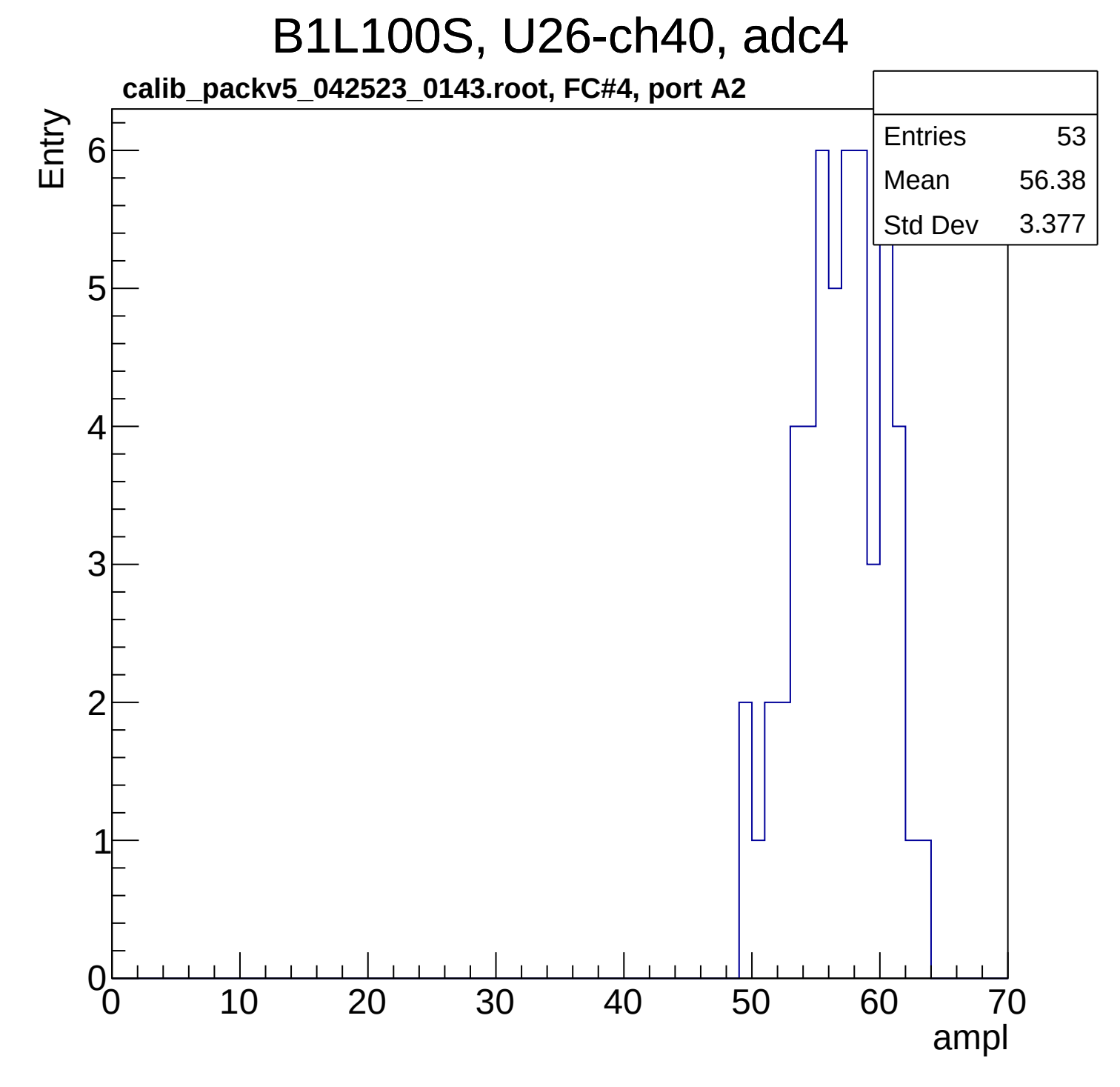
B1L100S, U26-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.38
Std Dev 3.377
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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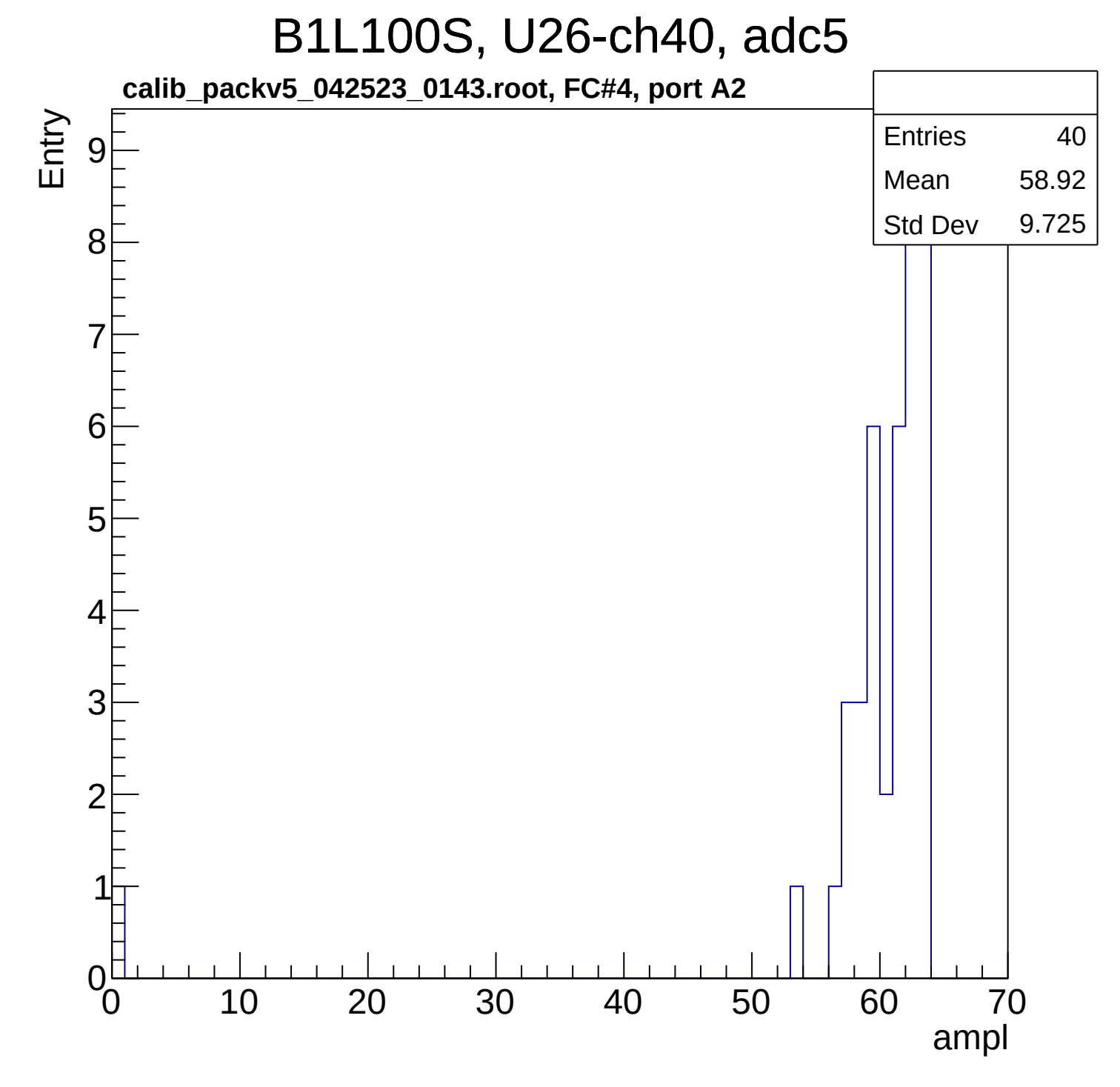
B1L100S, U26-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.92
Std Dev 9.725
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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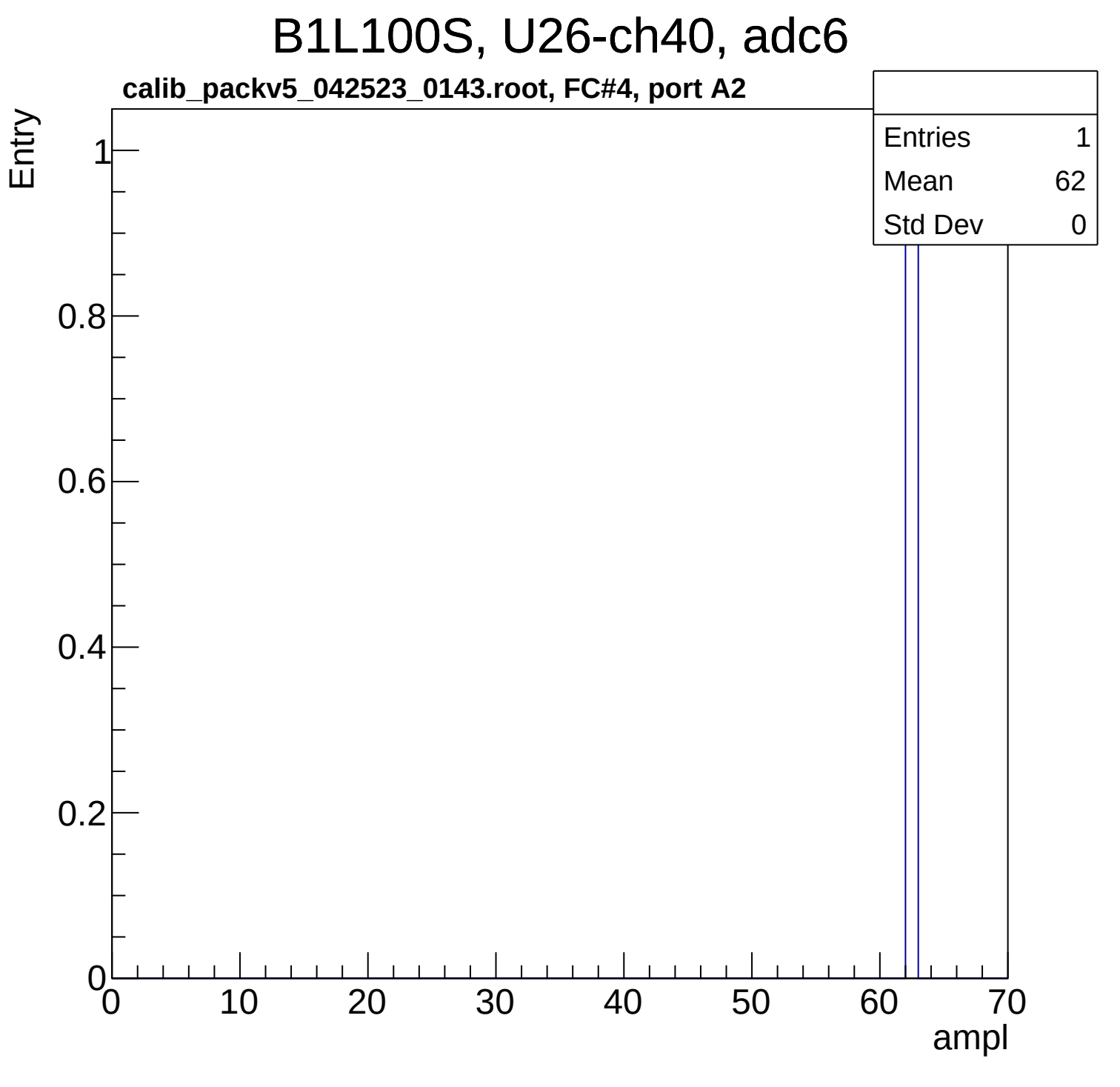
B1L100S, U26-ch40, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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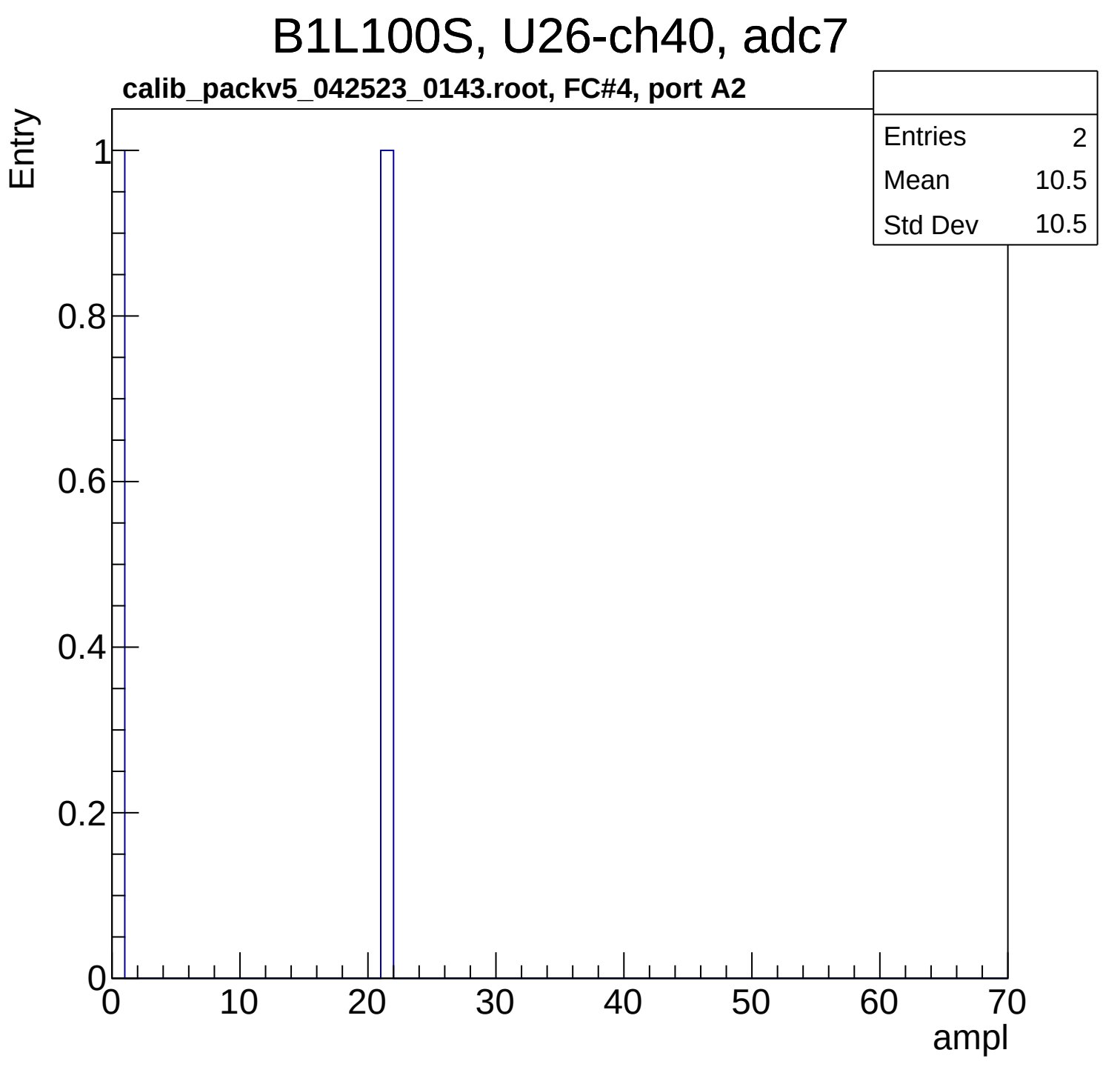
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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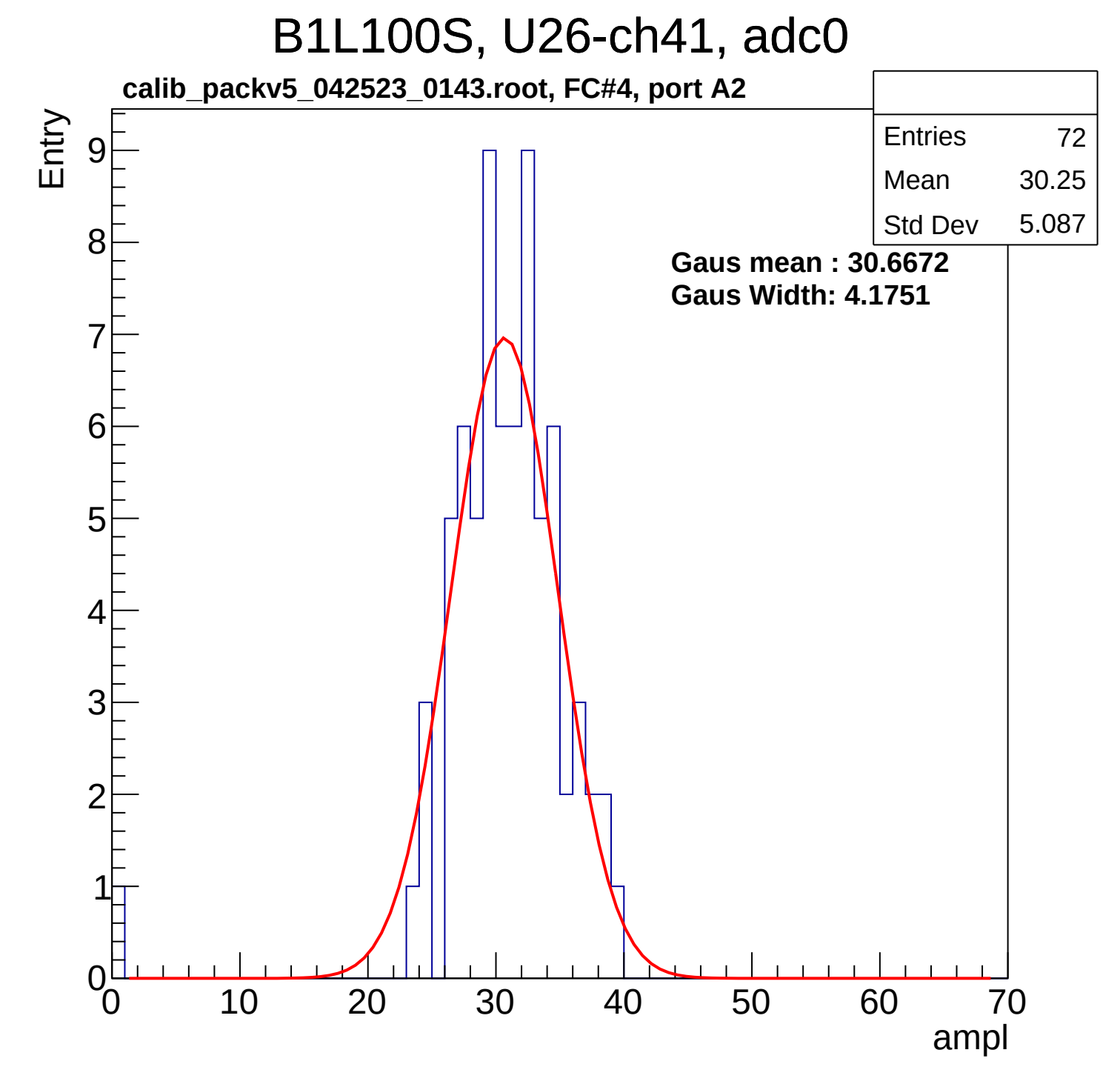
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch41, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.25
Std Dev 5.087
Gaus mean : 30.6672
Gaus Width: 4.1751
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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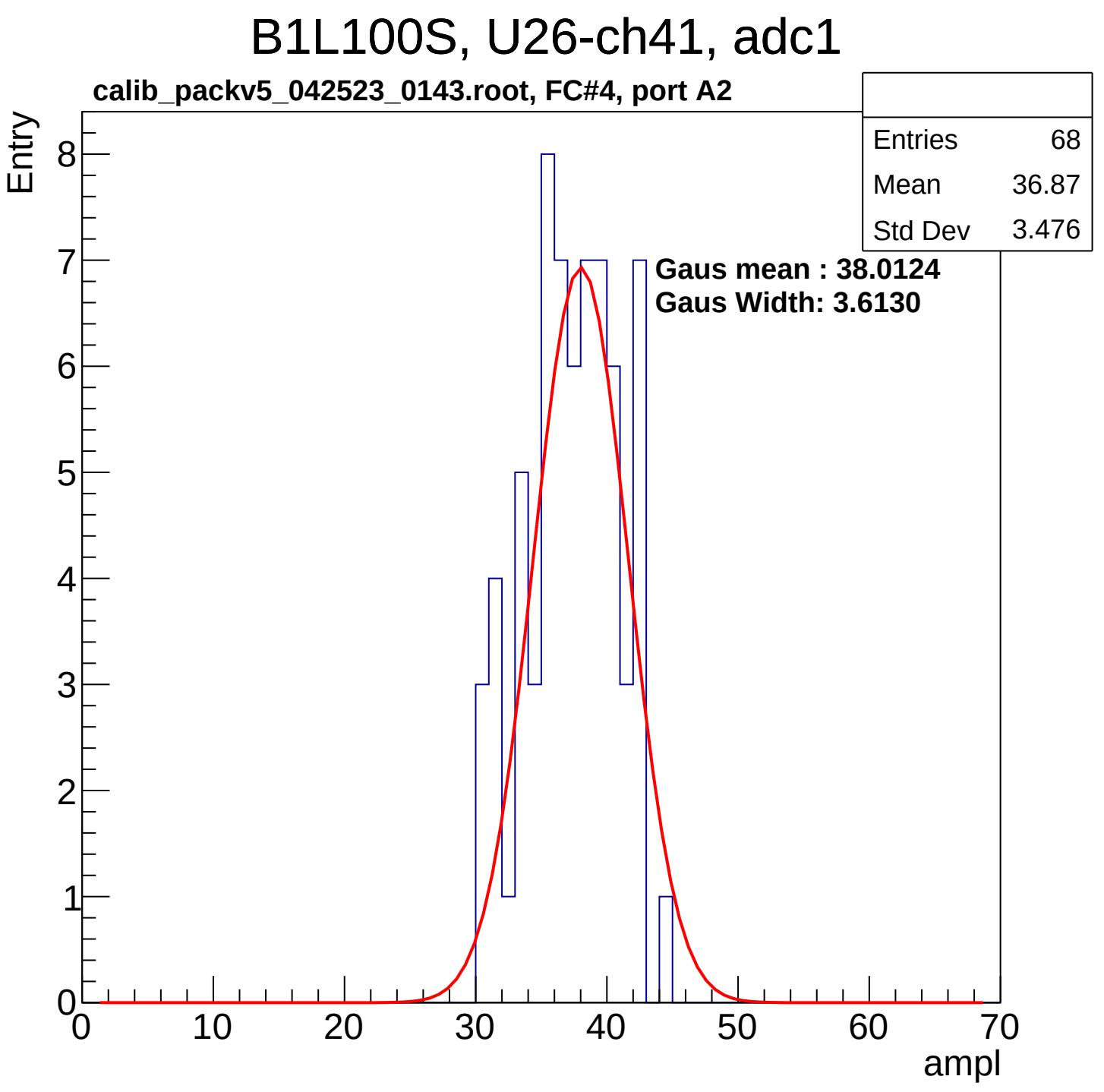
B1L100S, U26-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.87
Std Dev 3.476
Gaus mean : 38.0124
Gaus Width: 3.6130
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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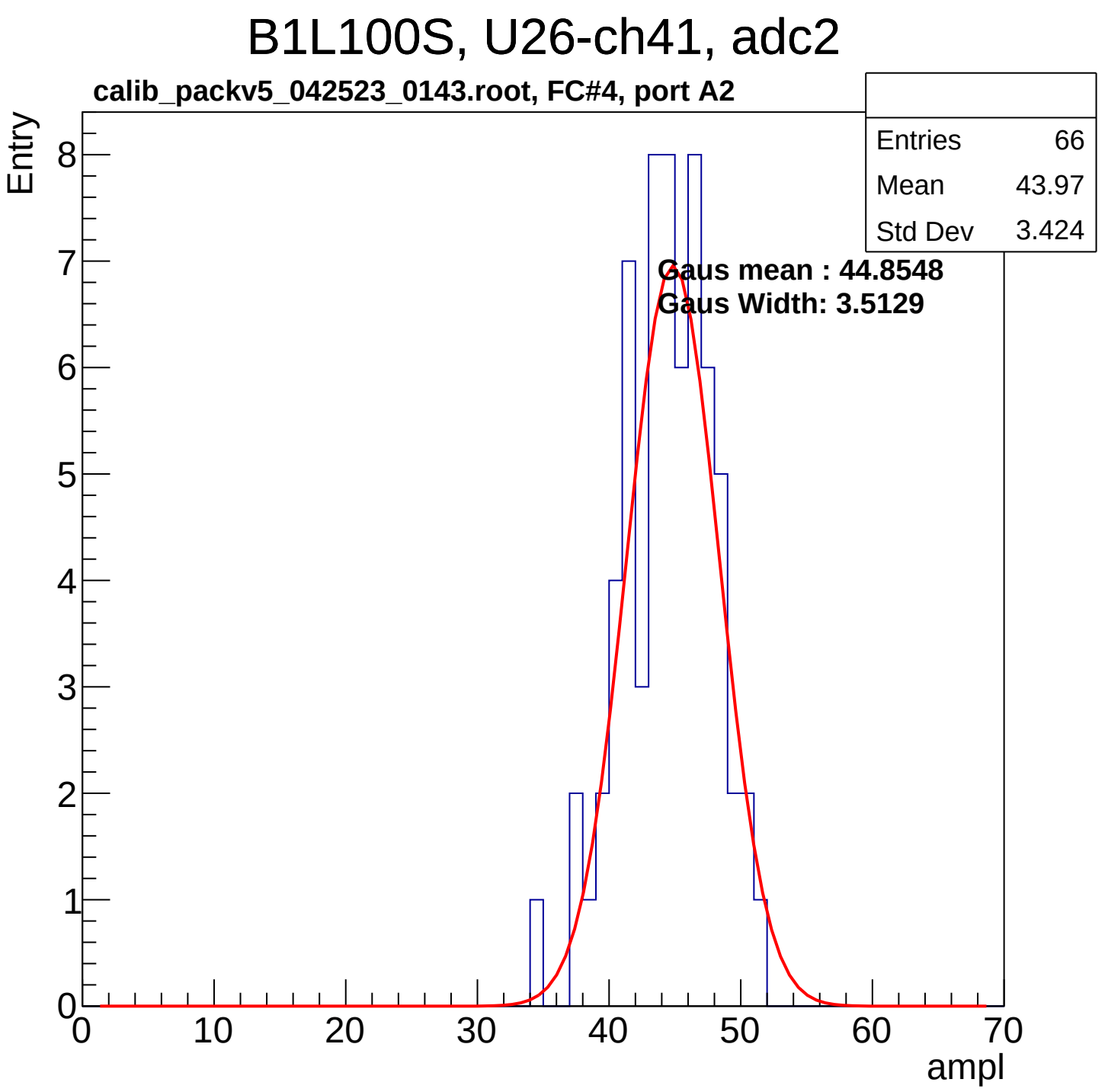
40
50
60
70


B1L100S, U26-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.97
Std Dev 3.424
Gaus mean : 44.8548
Gaus Width: 3.5129
Entry
ampl
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4
5
6
7
8
0
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40
50
60
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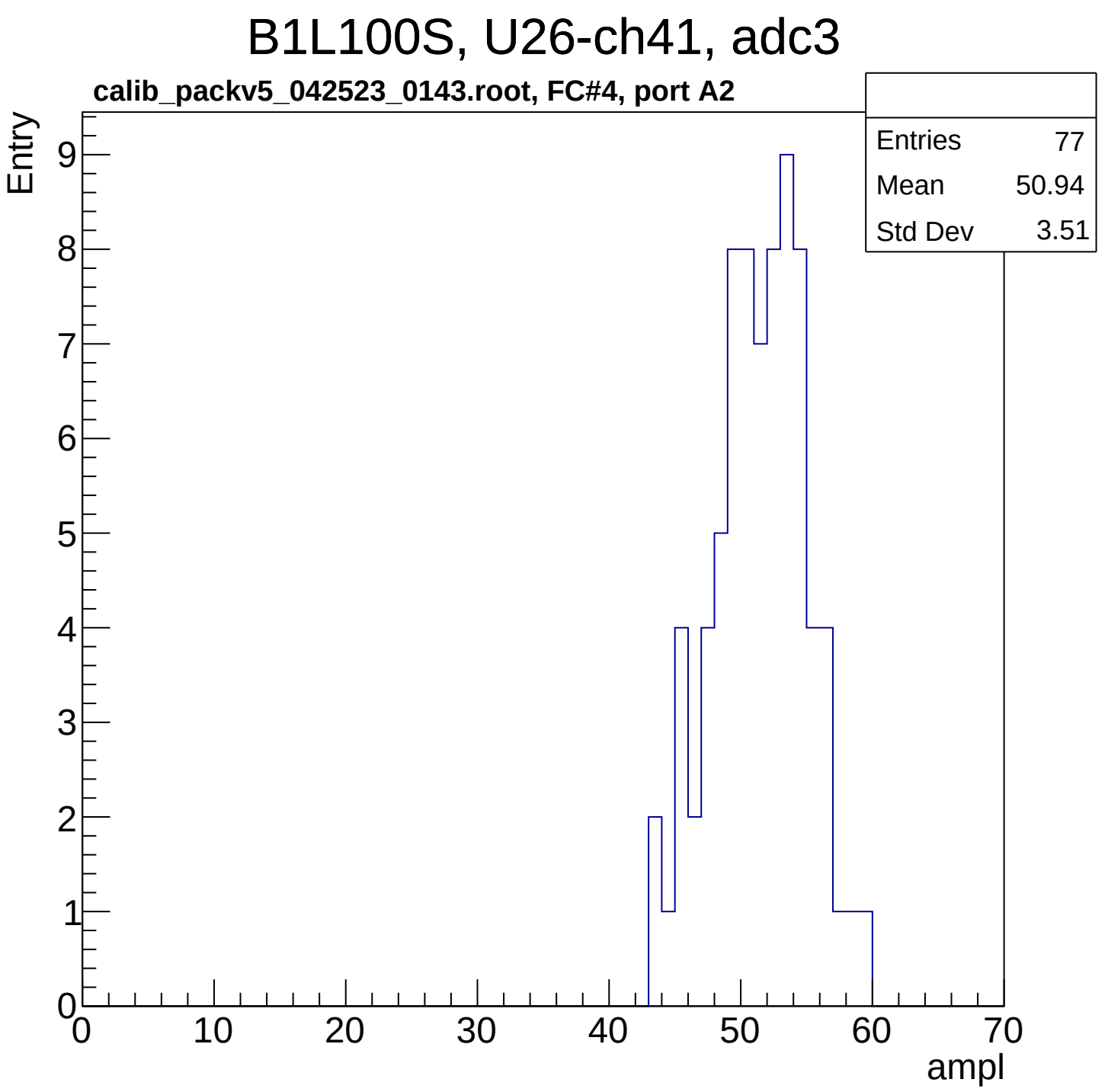
B1L100S, U26-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 50.94
Std Dev 3.51
Entry
ampl
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3
4
5
6
7
8
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0
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60
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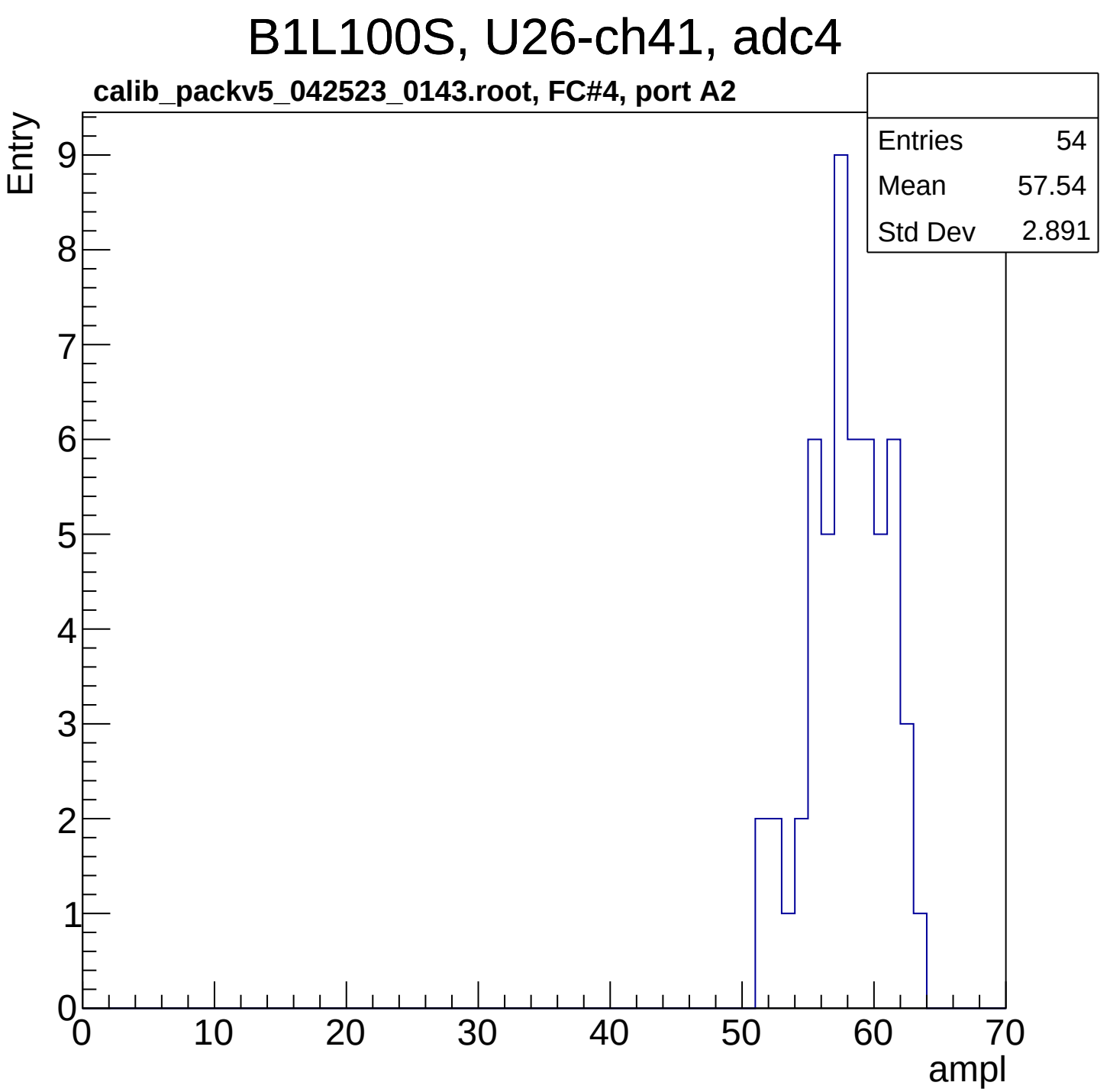
B1L100S, U26-ch41, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 57.54
Std Dev 2.891
Entry
ampl
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6
7
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0
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40
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60
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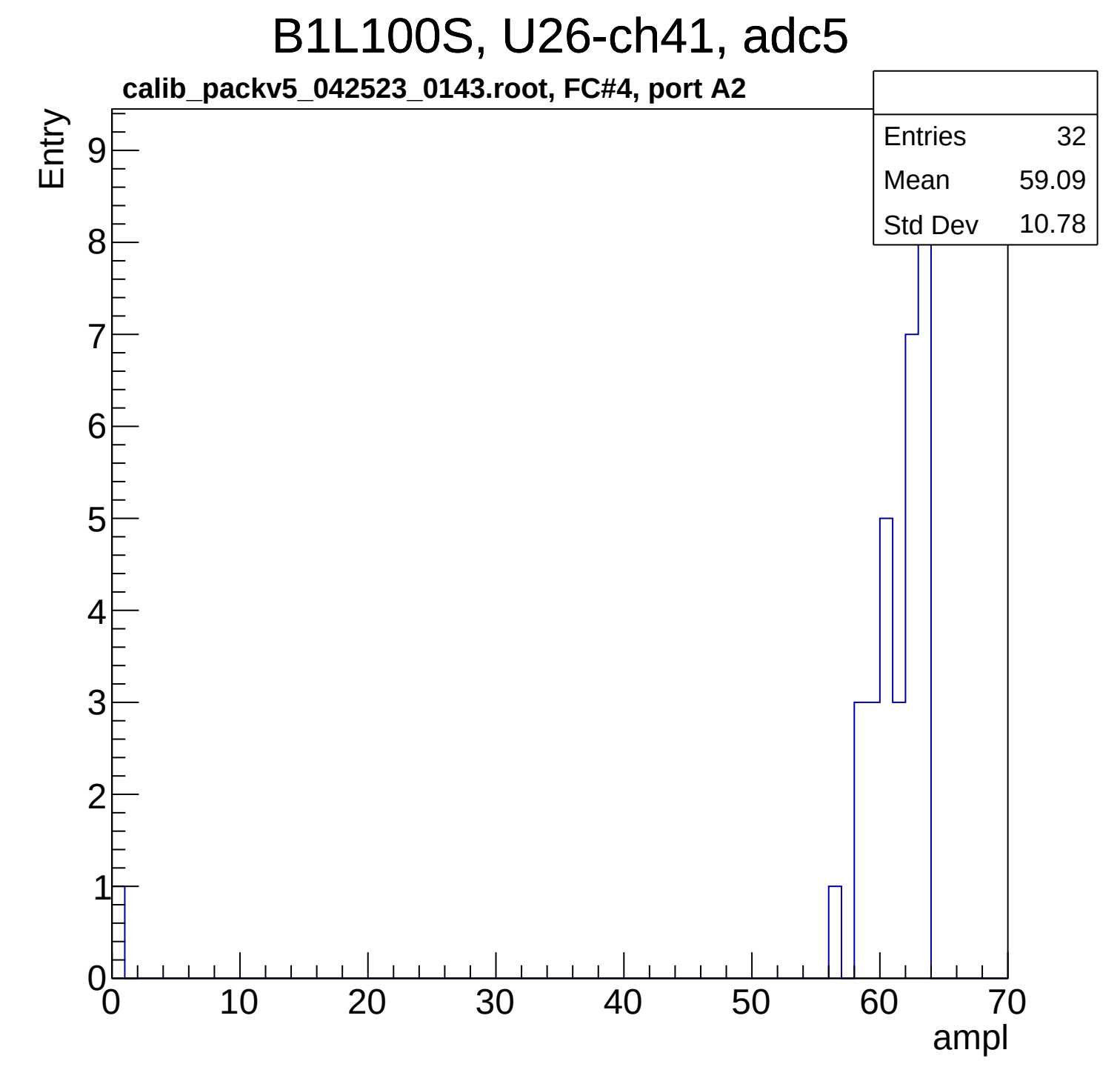
B1L100S, U26-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.09
Std Dev 10.78
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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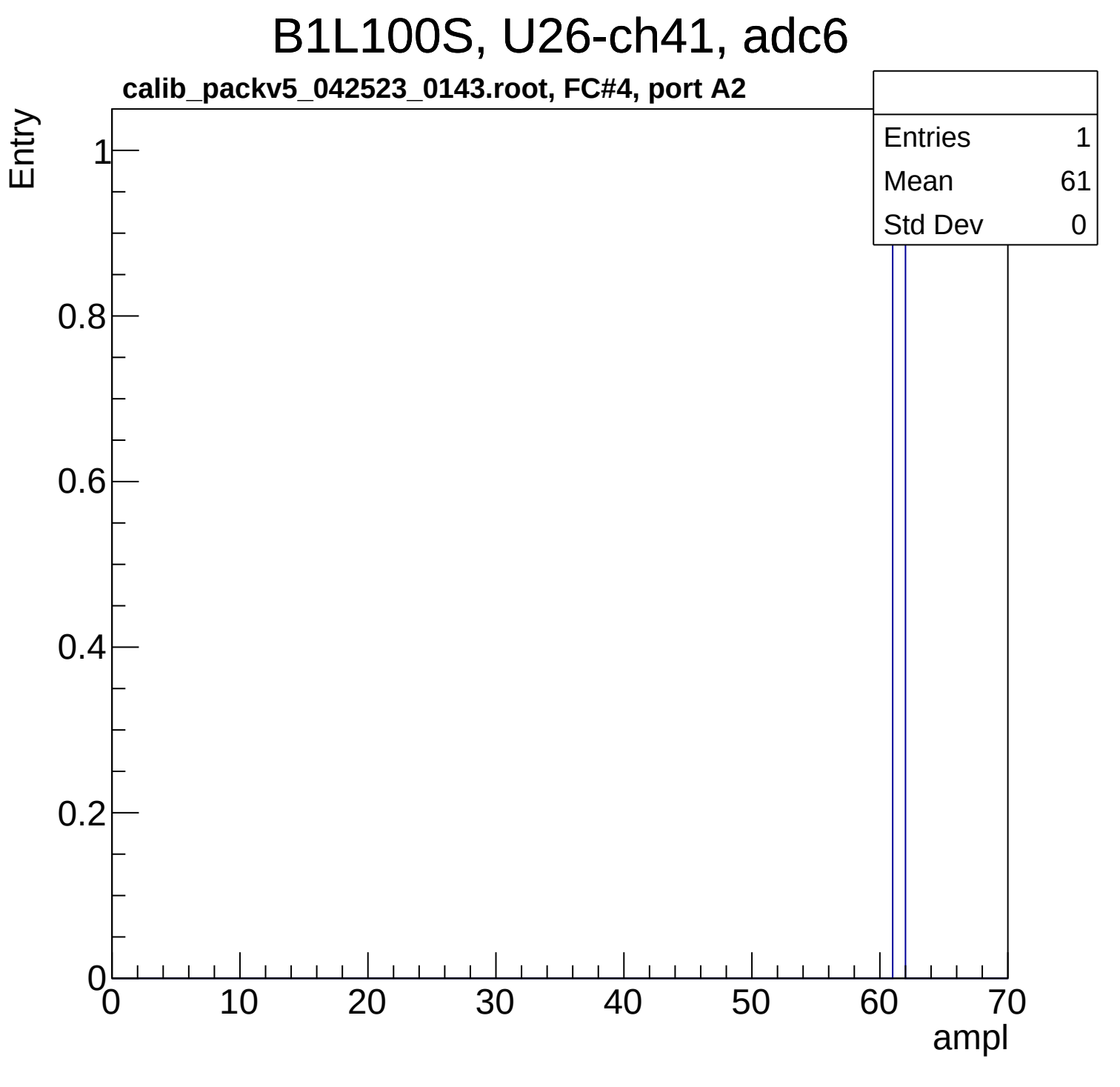
B1L100S, U26-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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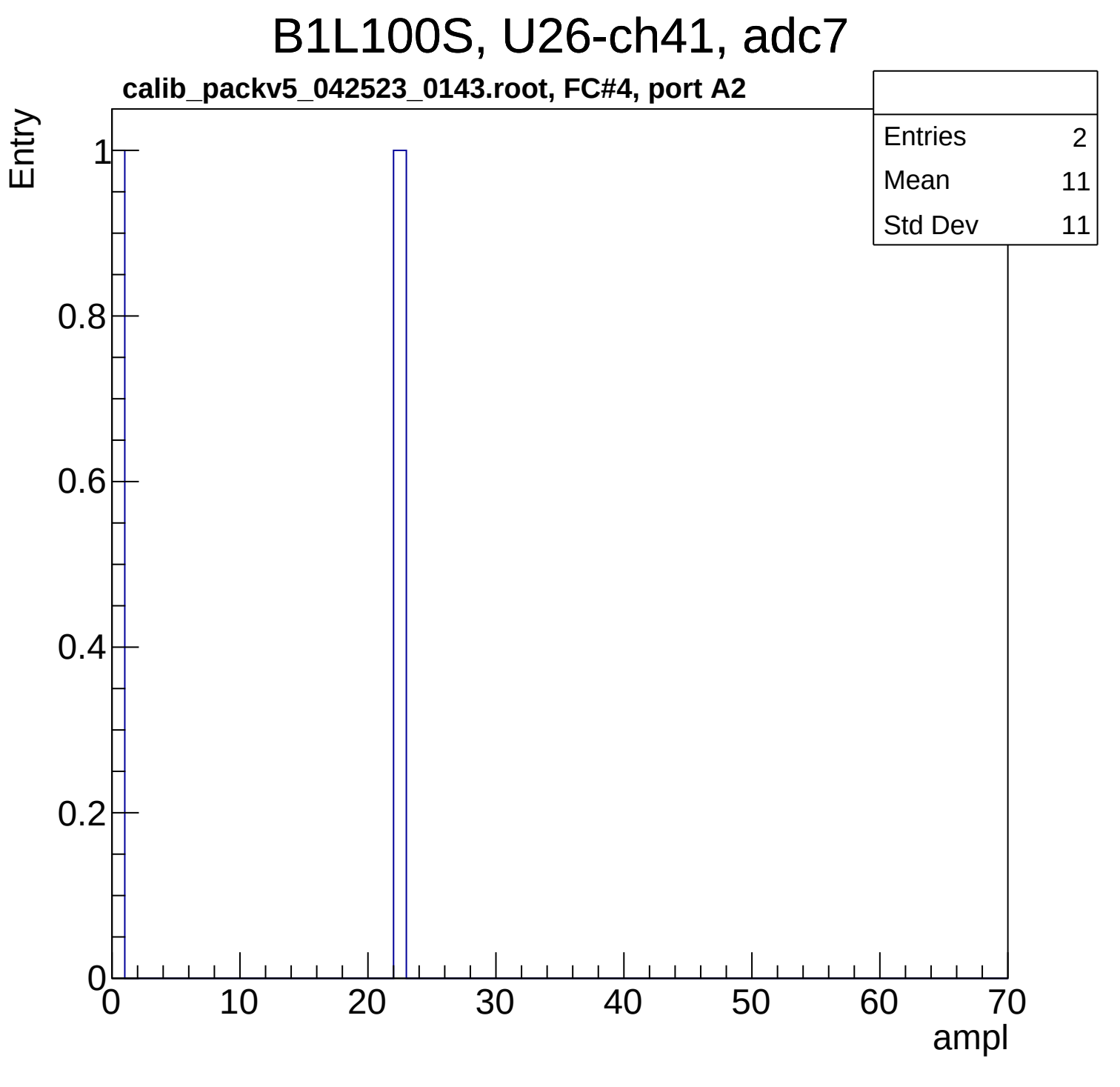
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
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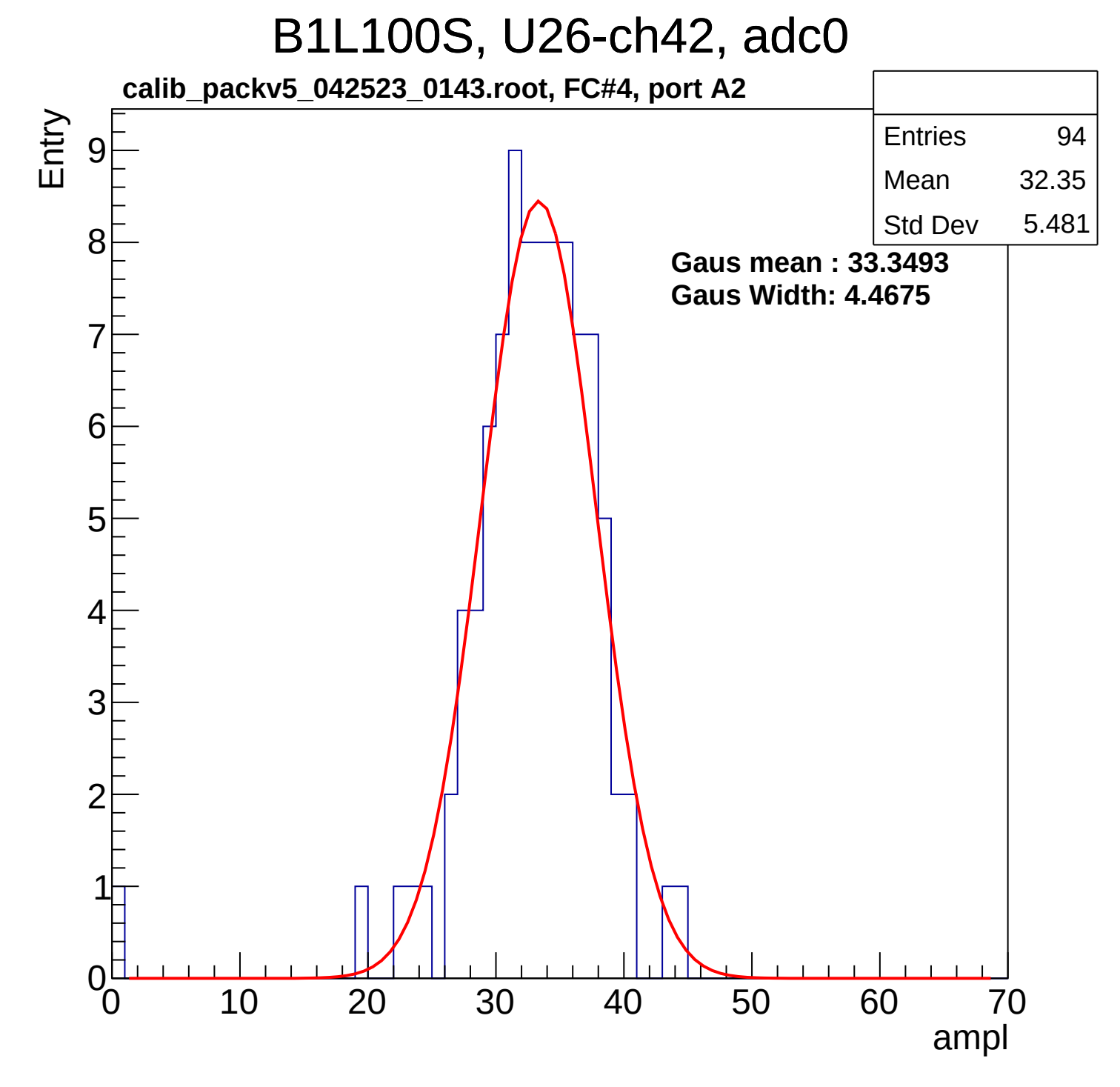
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 32.35
Std Dev 5.481
Gaus mean : 33.3493
Gaus Width: 4.4675
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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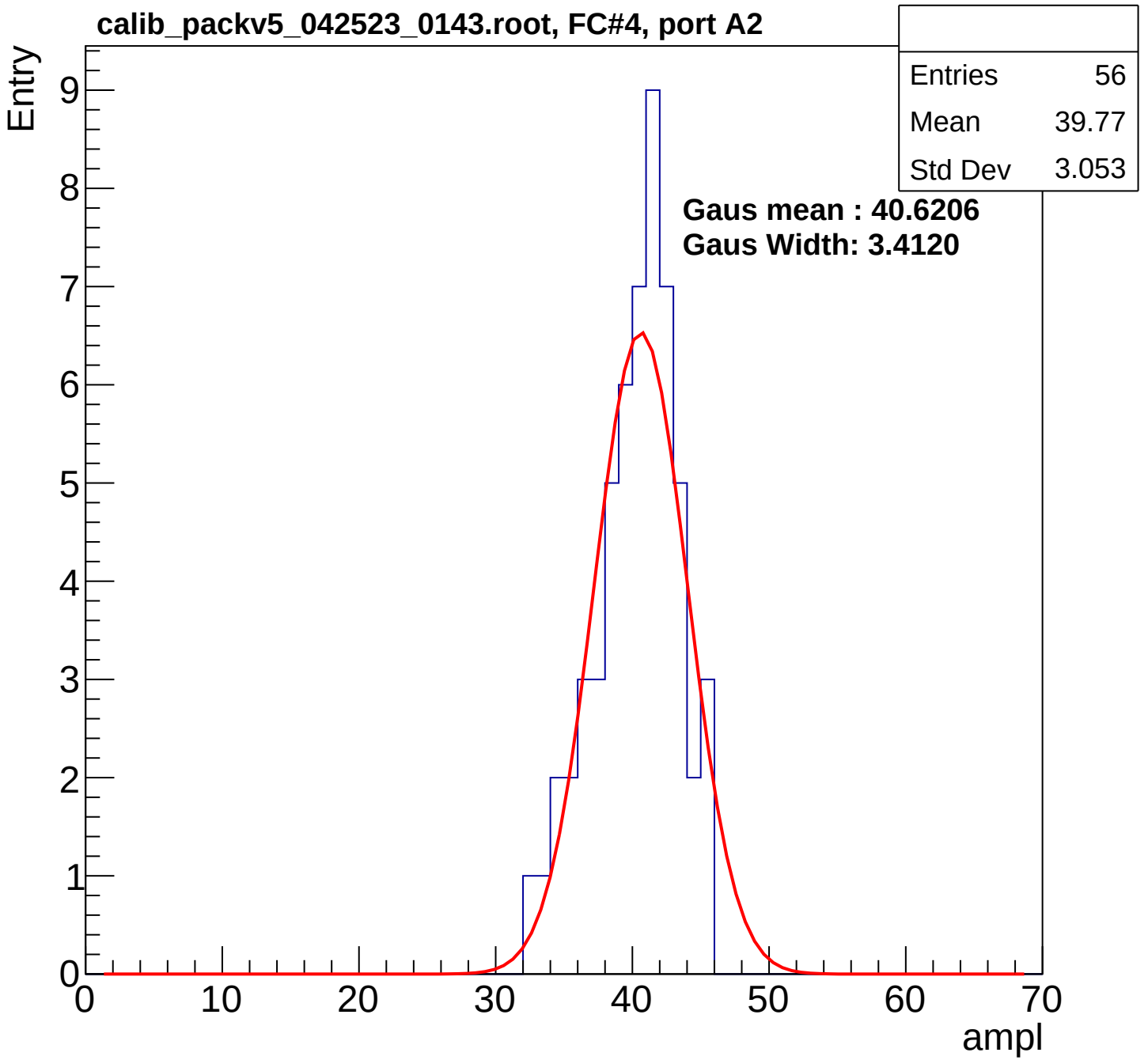
B1L100S, U26-ch42, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 39.77
Std Dev 3.053
Gaus mean : 40.6206
Gaus Width: 3.4120
Entry
ampl
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2
3
4
5
6
7
8
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0
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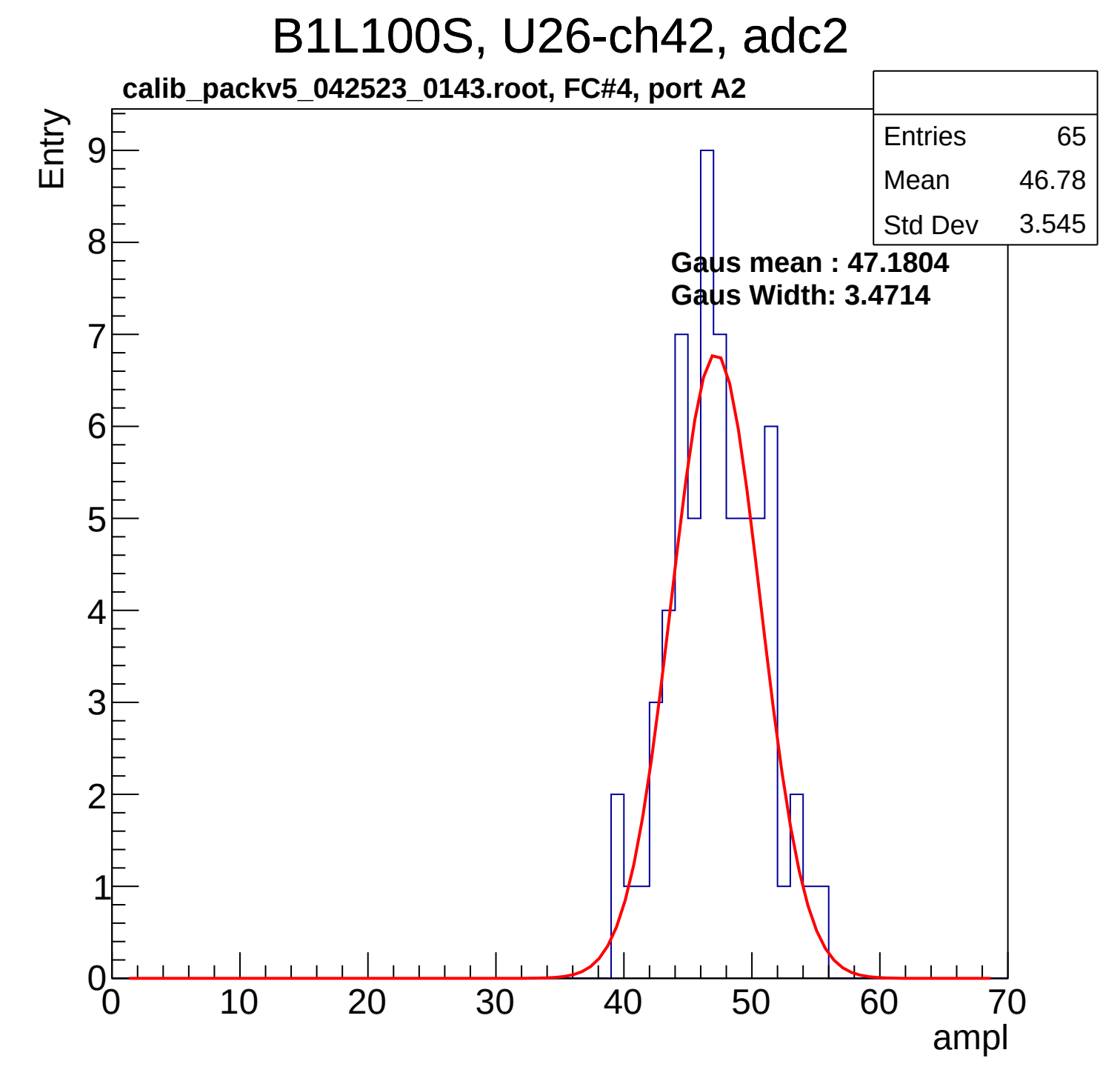
B1L100S, U26-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 46.78
Std Dev 3.545
Gaus mean : 47.1804
Gaus Width: 3.4714
Entry
ampl
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2
3
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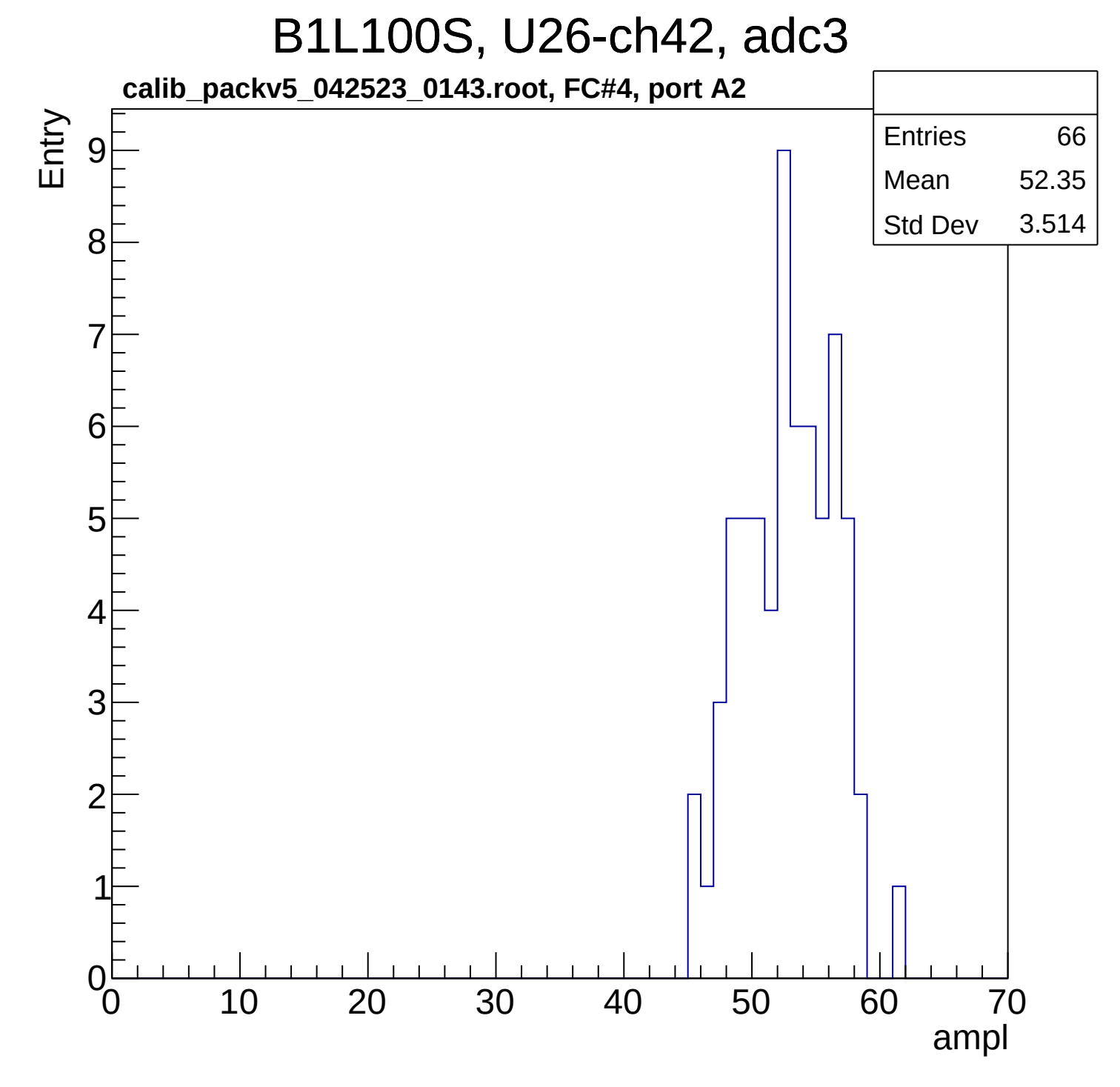
B1L100S, U26-ch42, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 52.35
Std Dev 3.514
Entry
ampl
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7
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0
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40
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60
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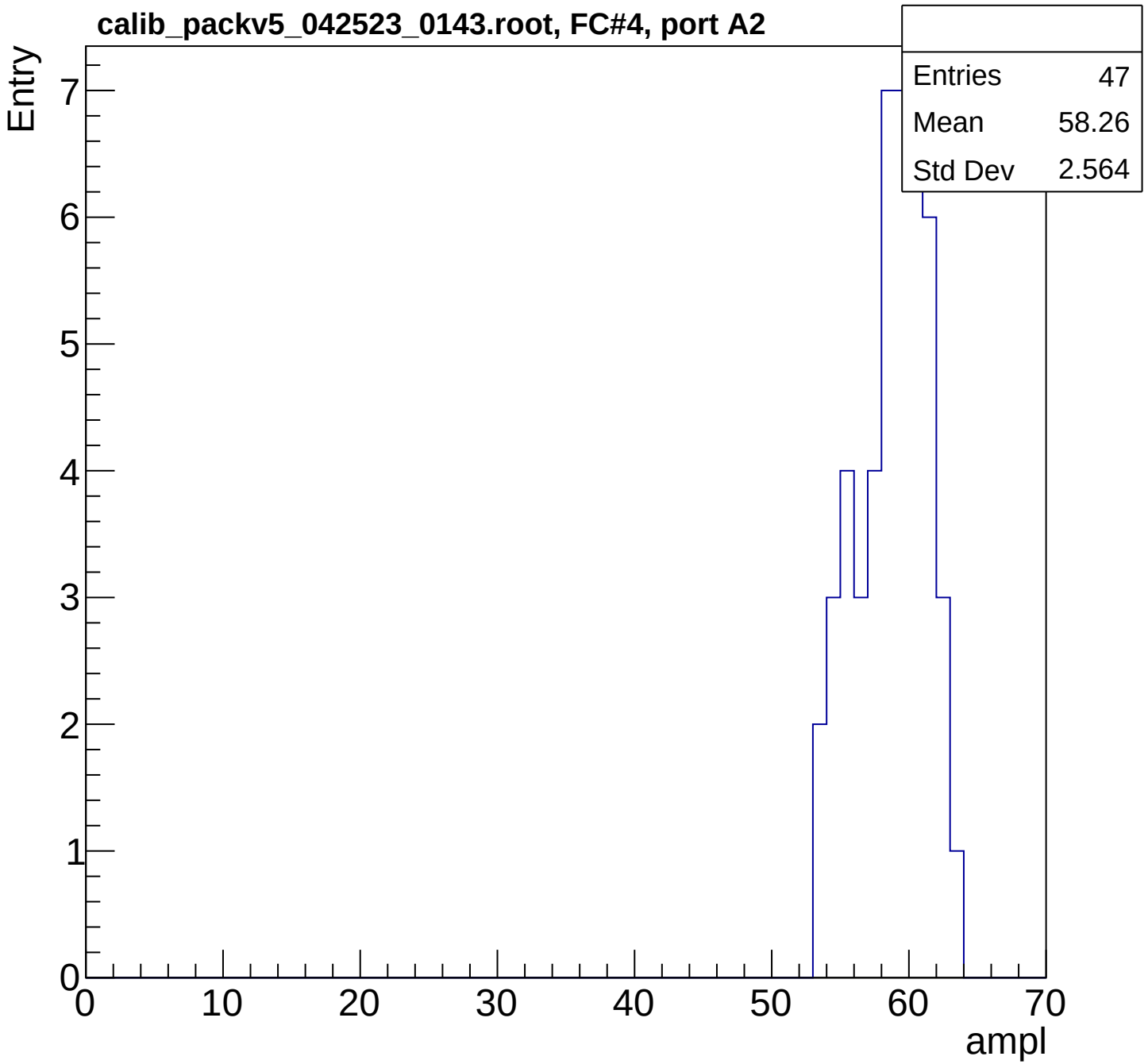
B1L100S, U26-ch42, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.26
Std Dev 2.564
Entry
ampl
0
1
2
3
4
5
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0
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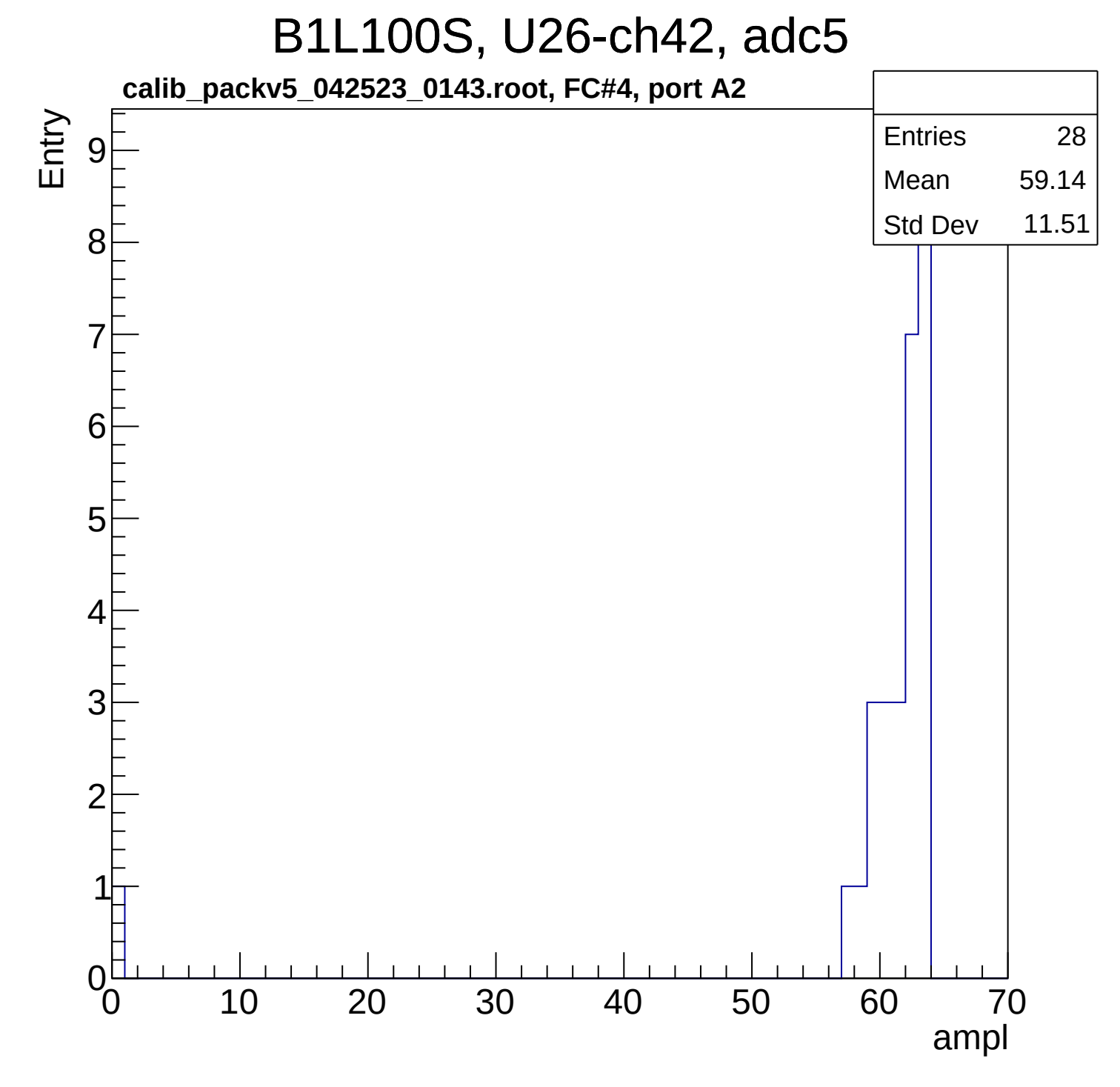
B1L100S, U26-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 59.14
Std Dev 11.51
Entry
ampl
0
1
2
3
4
5
6
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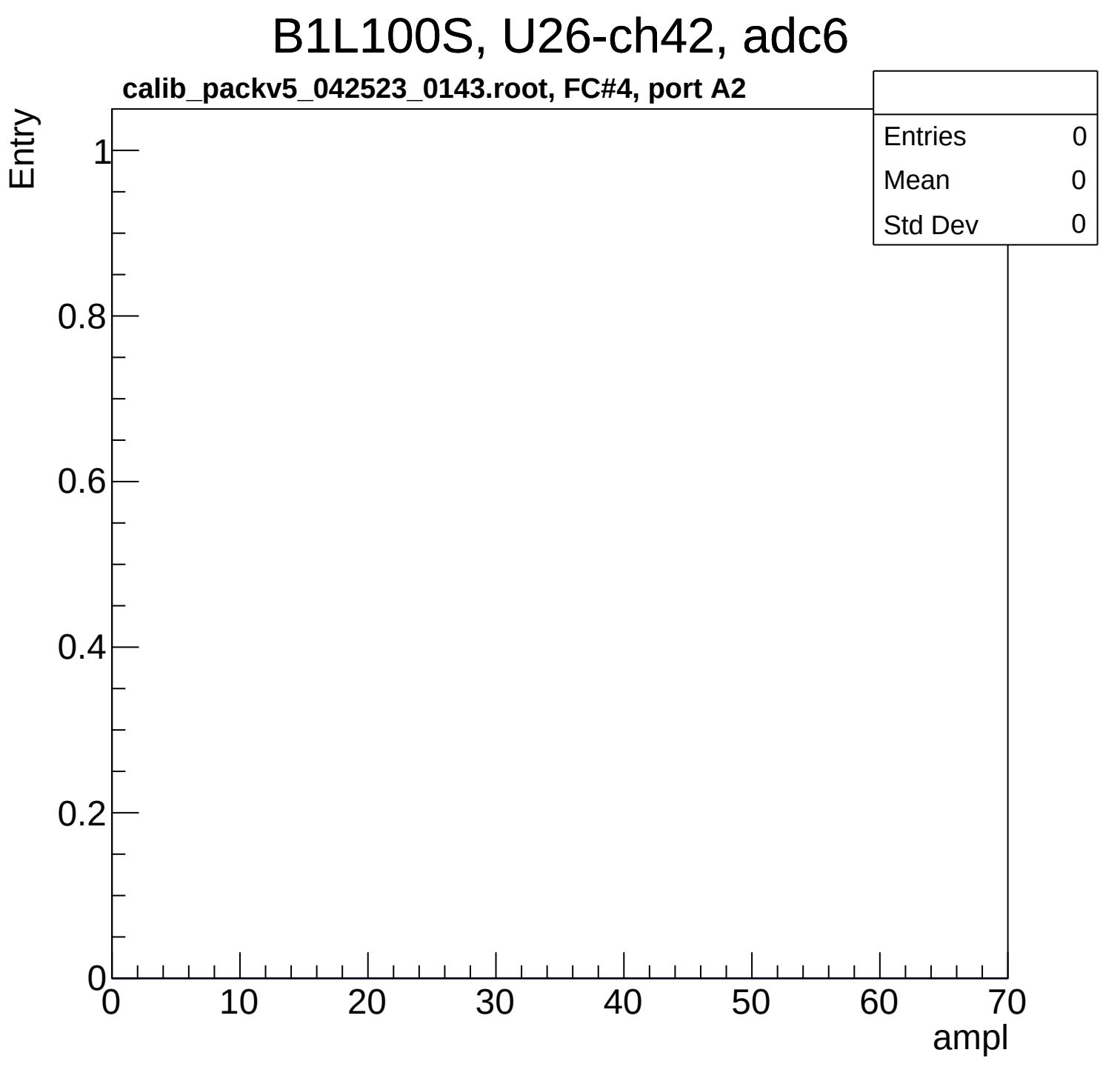
B1L100S, U26-ch42, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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30
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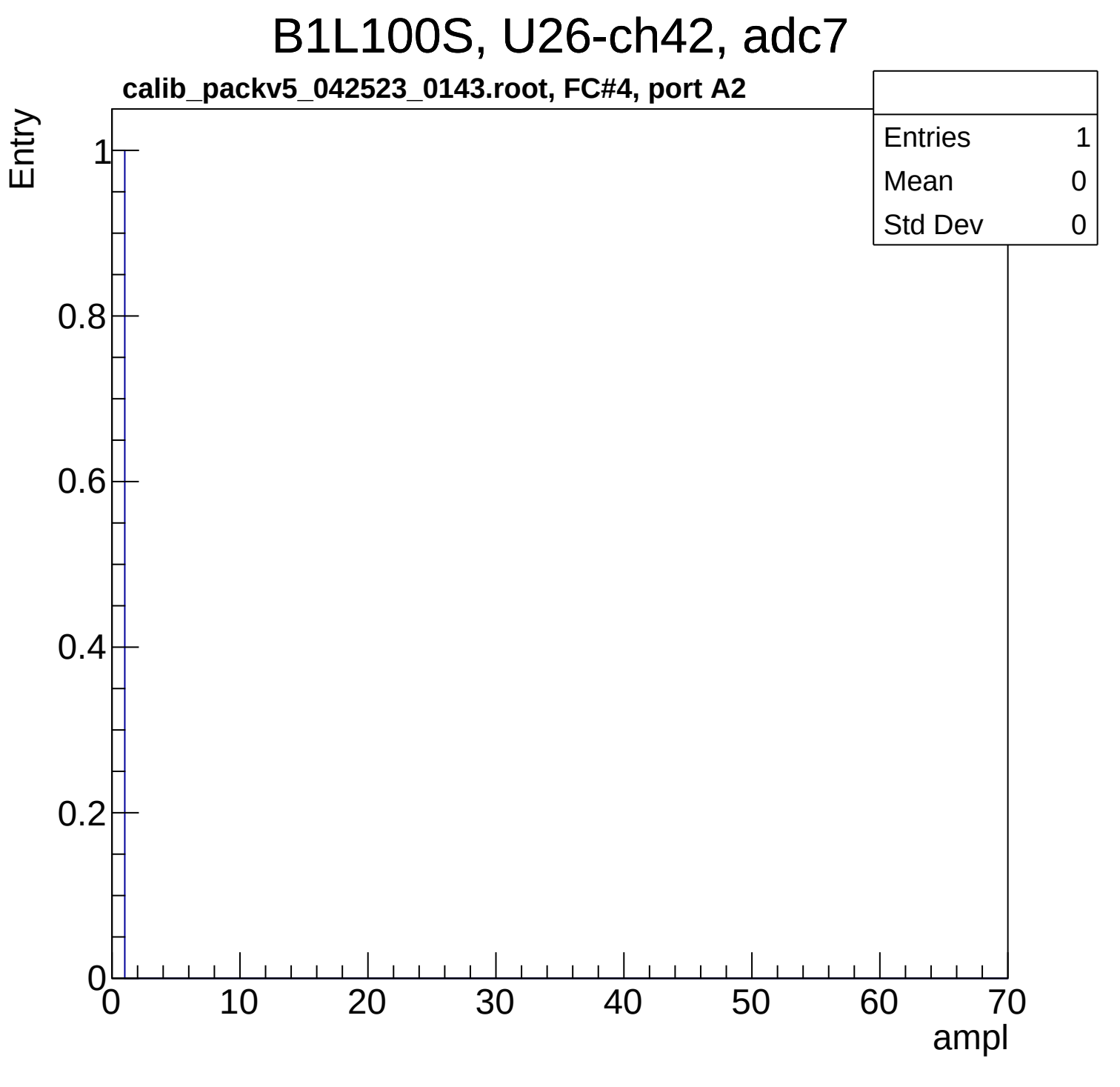
50
60
70
ampl


B1L100S, U26-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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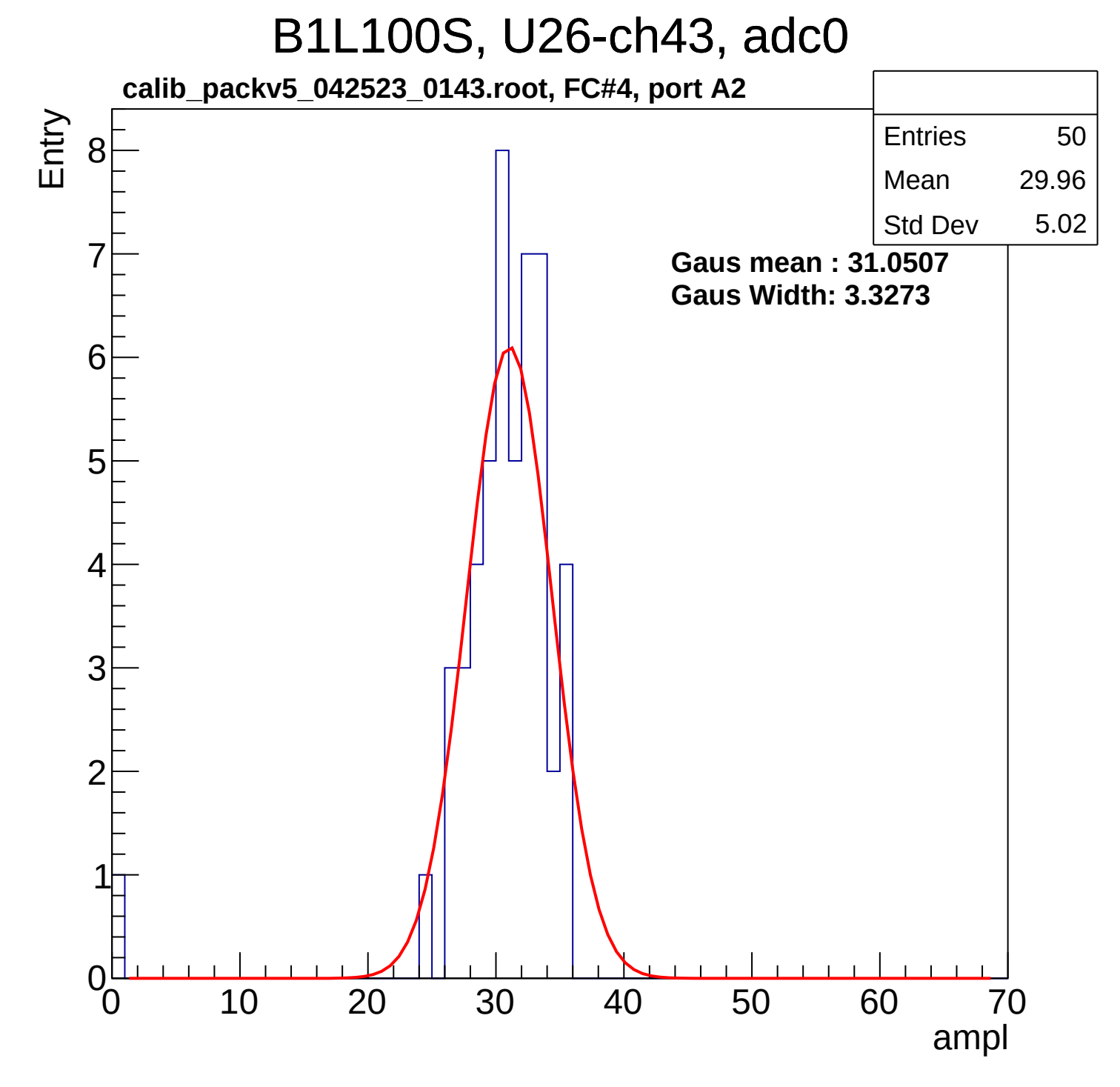
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch43, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 29.96
Std Dev 5.02
Gaus mean : 31.0507
Gaus Width: 3.3273
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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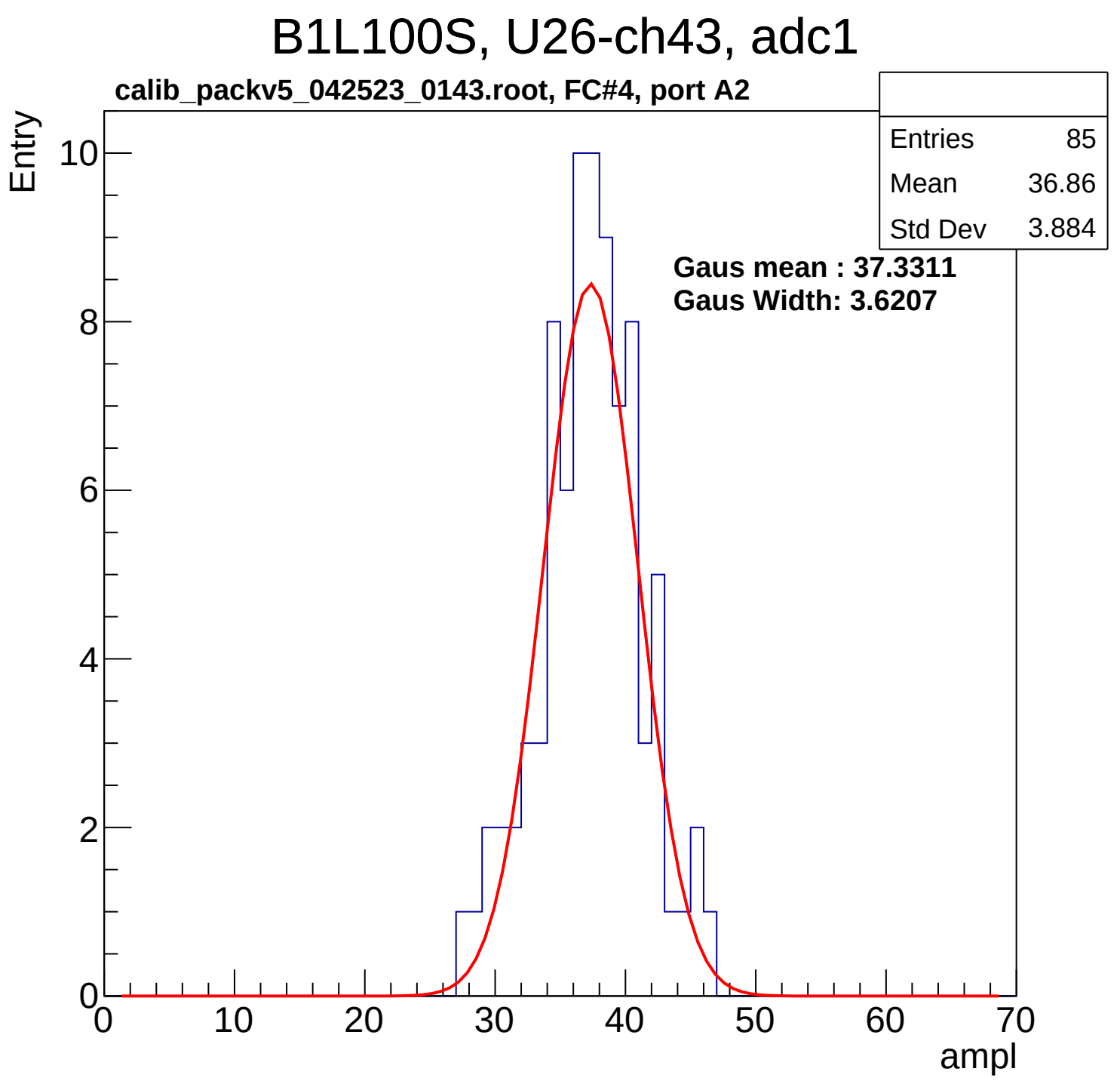
B1L100S, U26-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 36.86
Std Dev 3.884
Gaus mean : 37.3311
Gaus Width: 3.6207
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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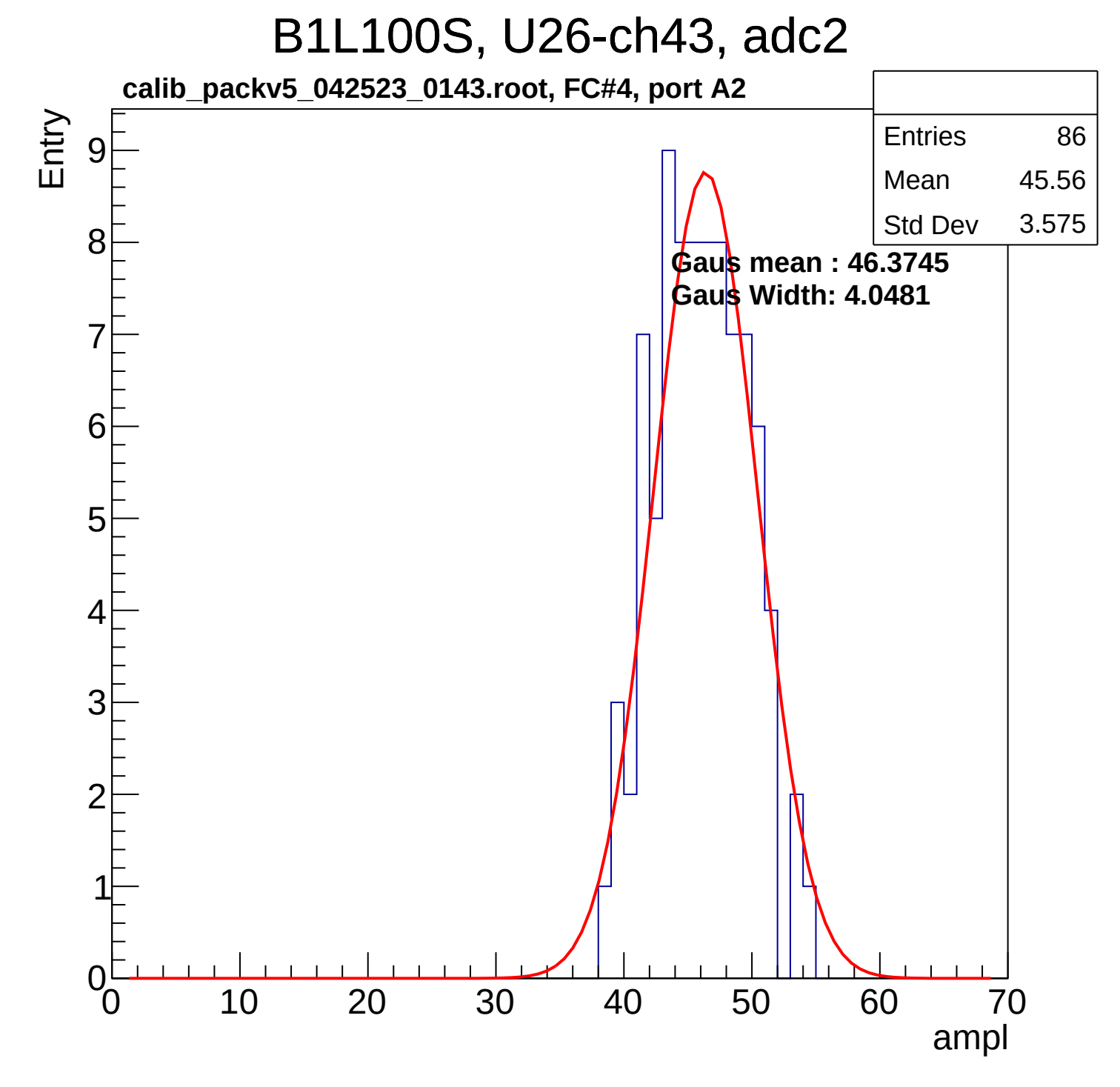
B1L100S, U26-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 45.56
Std Dev 3.575
Gaus mean : 46.3745
Gaus Width: 4.0481
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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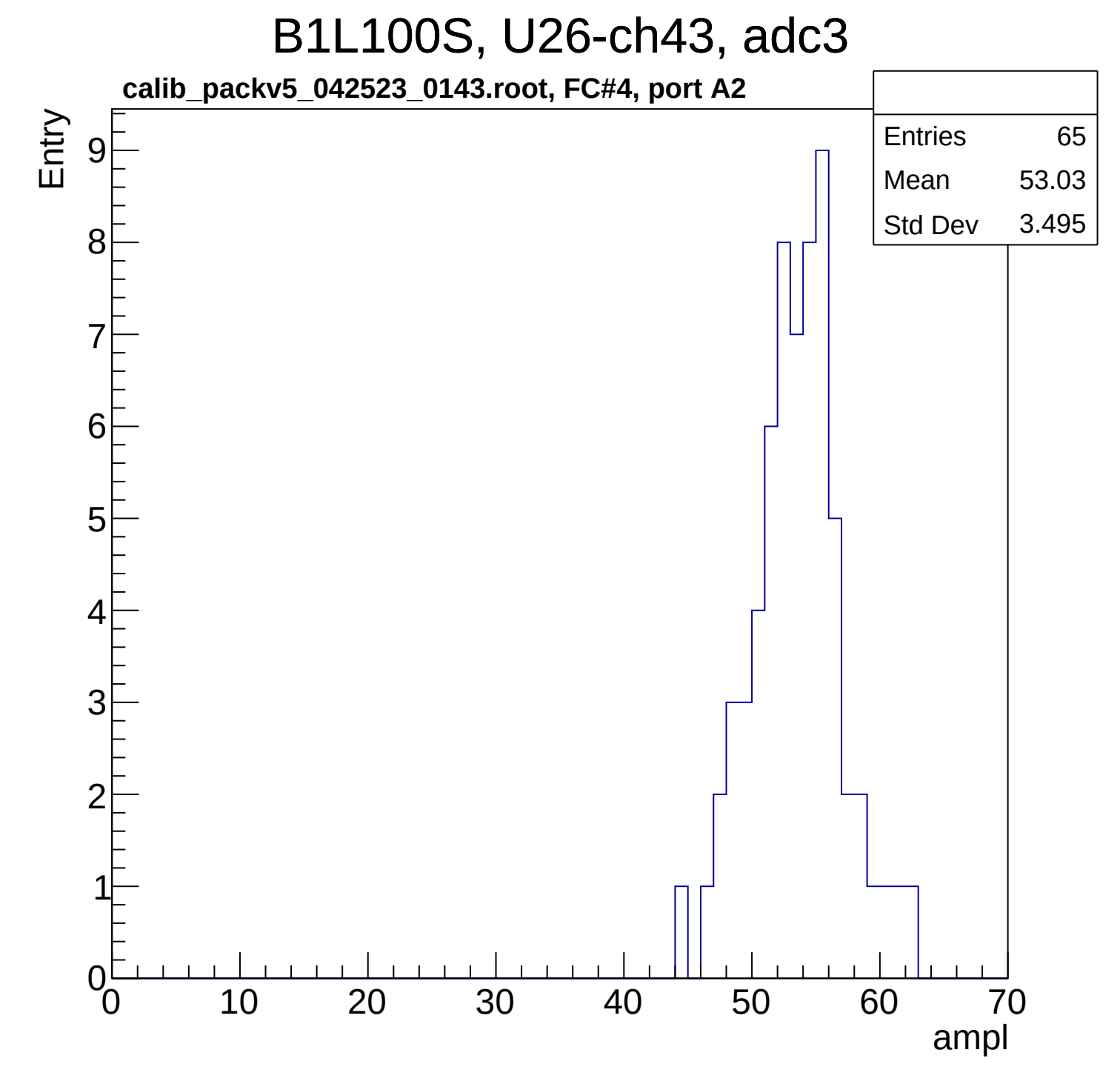
B1L100S, U26-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 53.03
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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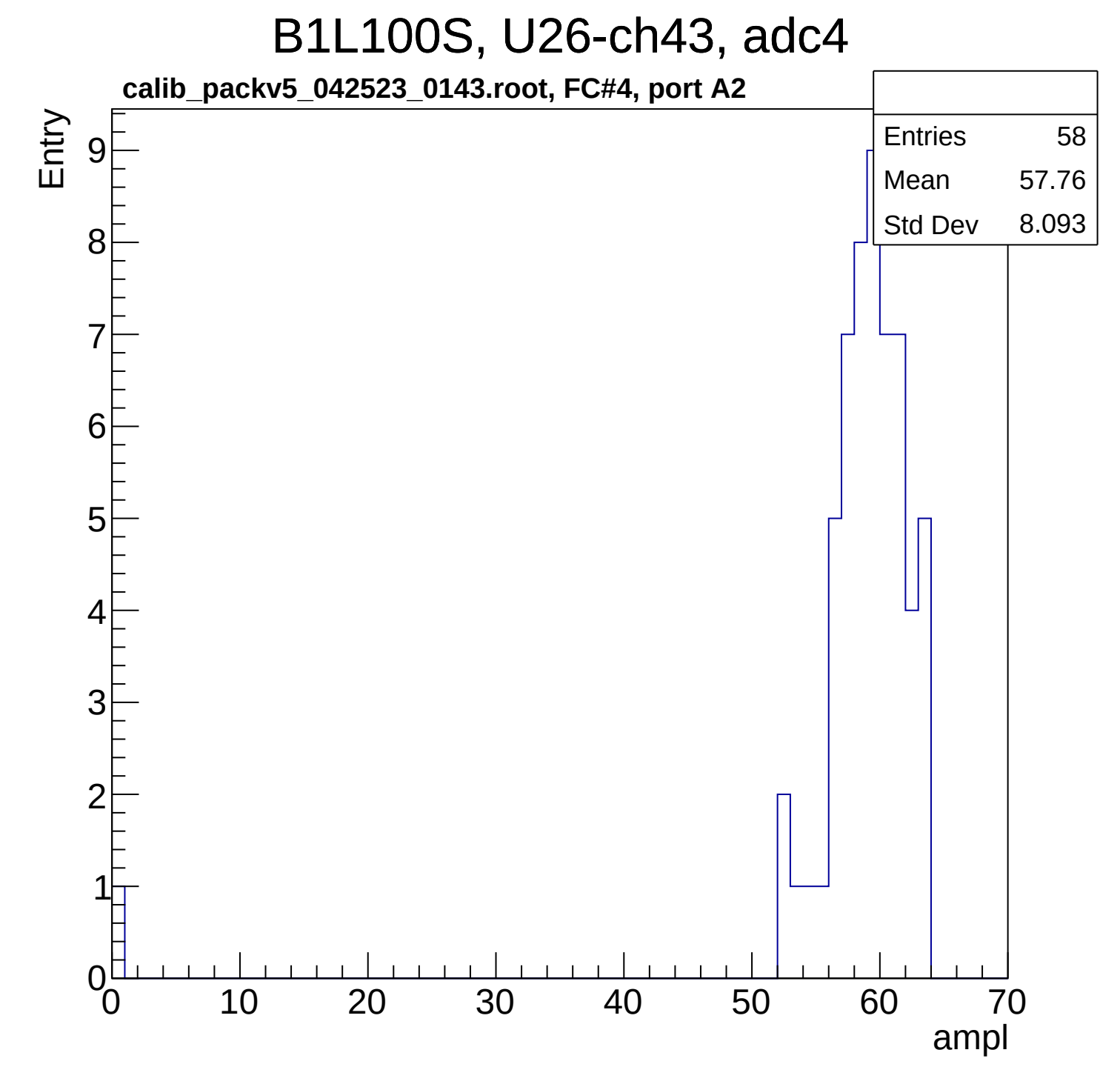
B1L100S, U26-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.76
Std Dev 8.093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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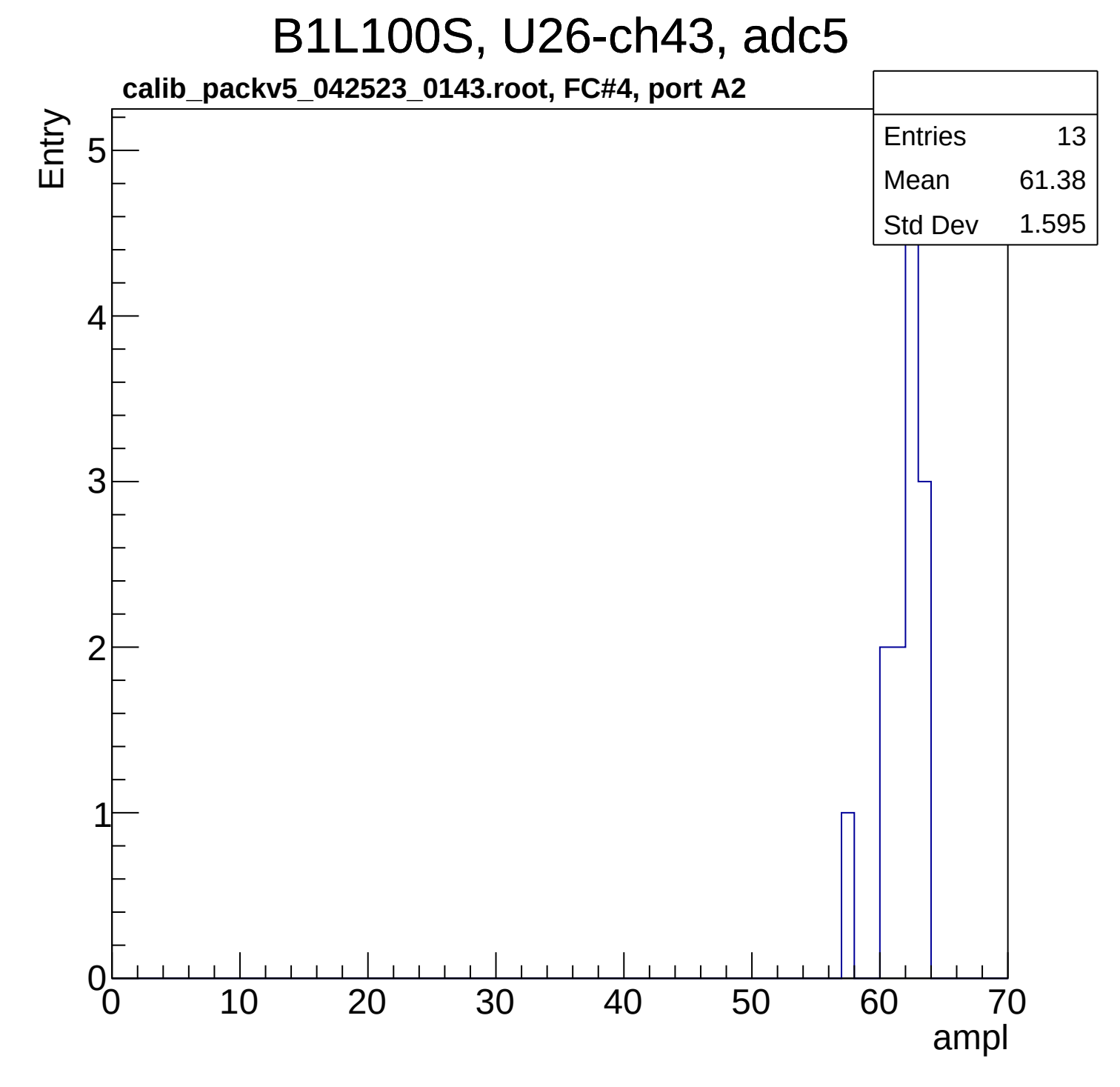
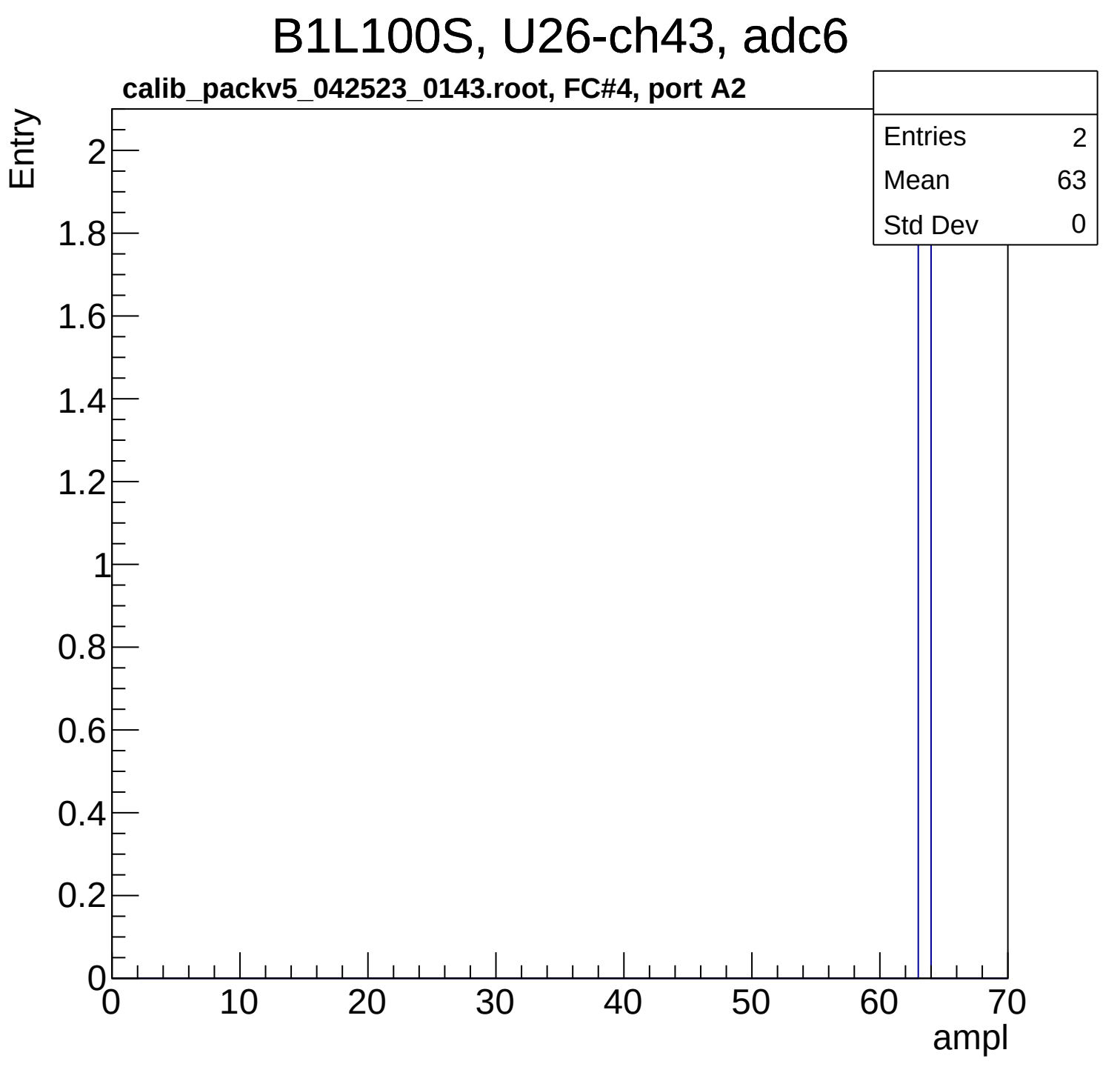
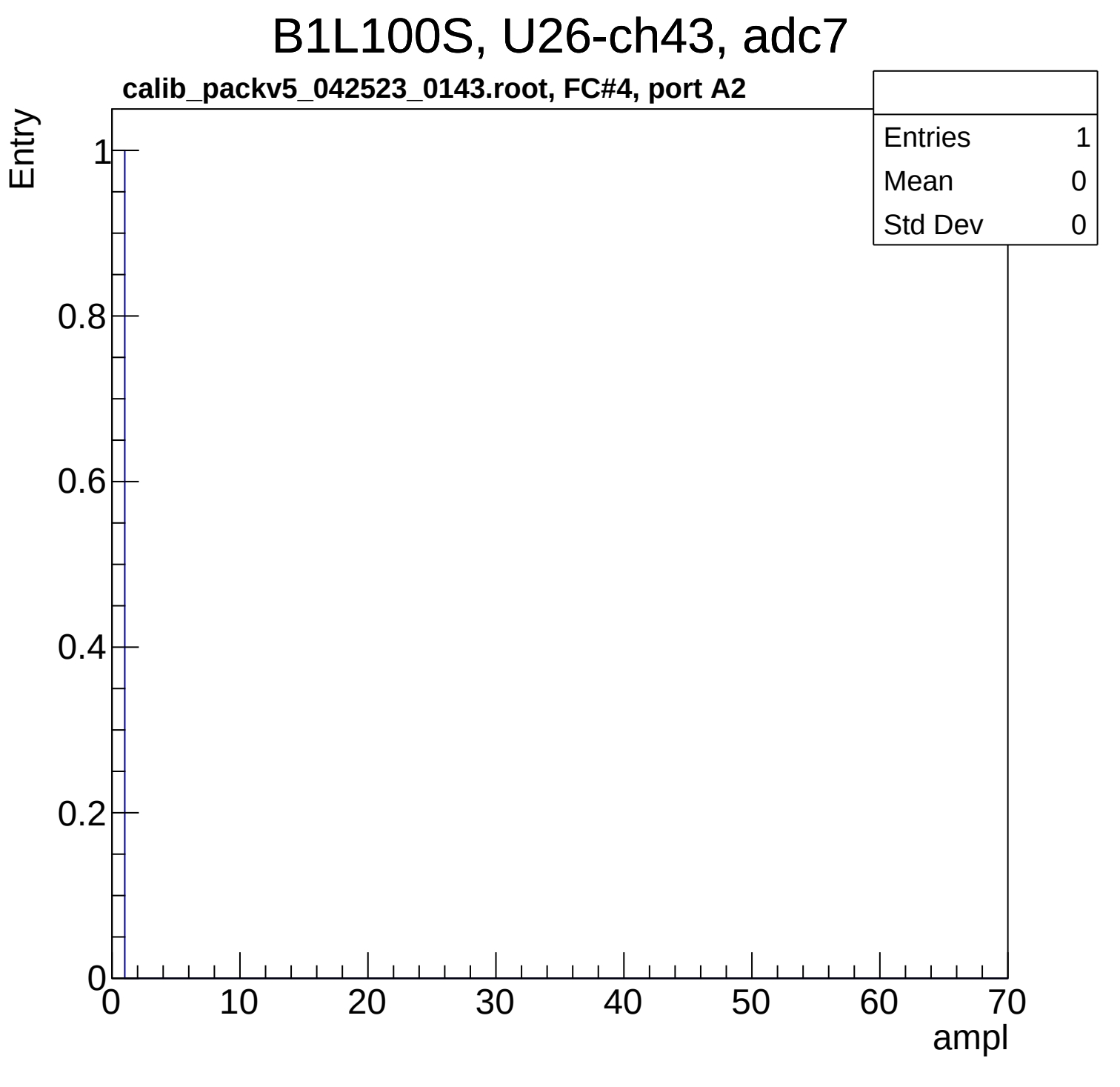

B1L100S, U26-ch43, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 61.38
Std Dev 1.595
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U26-ch43, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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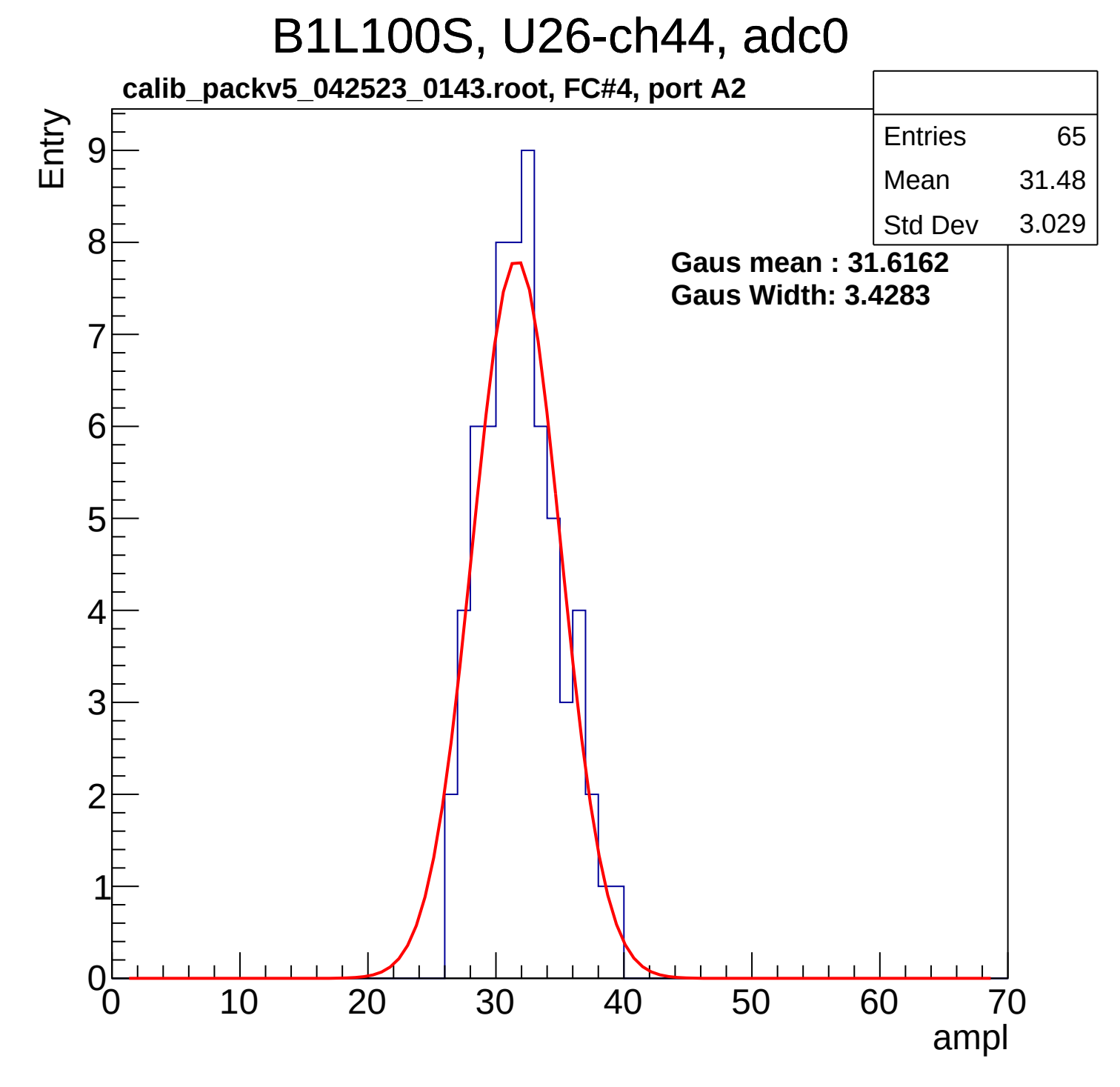
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 31.48
Std Dev 3.029
Gaus mean : 31.6162
Gaus Width: 3.4283
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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40
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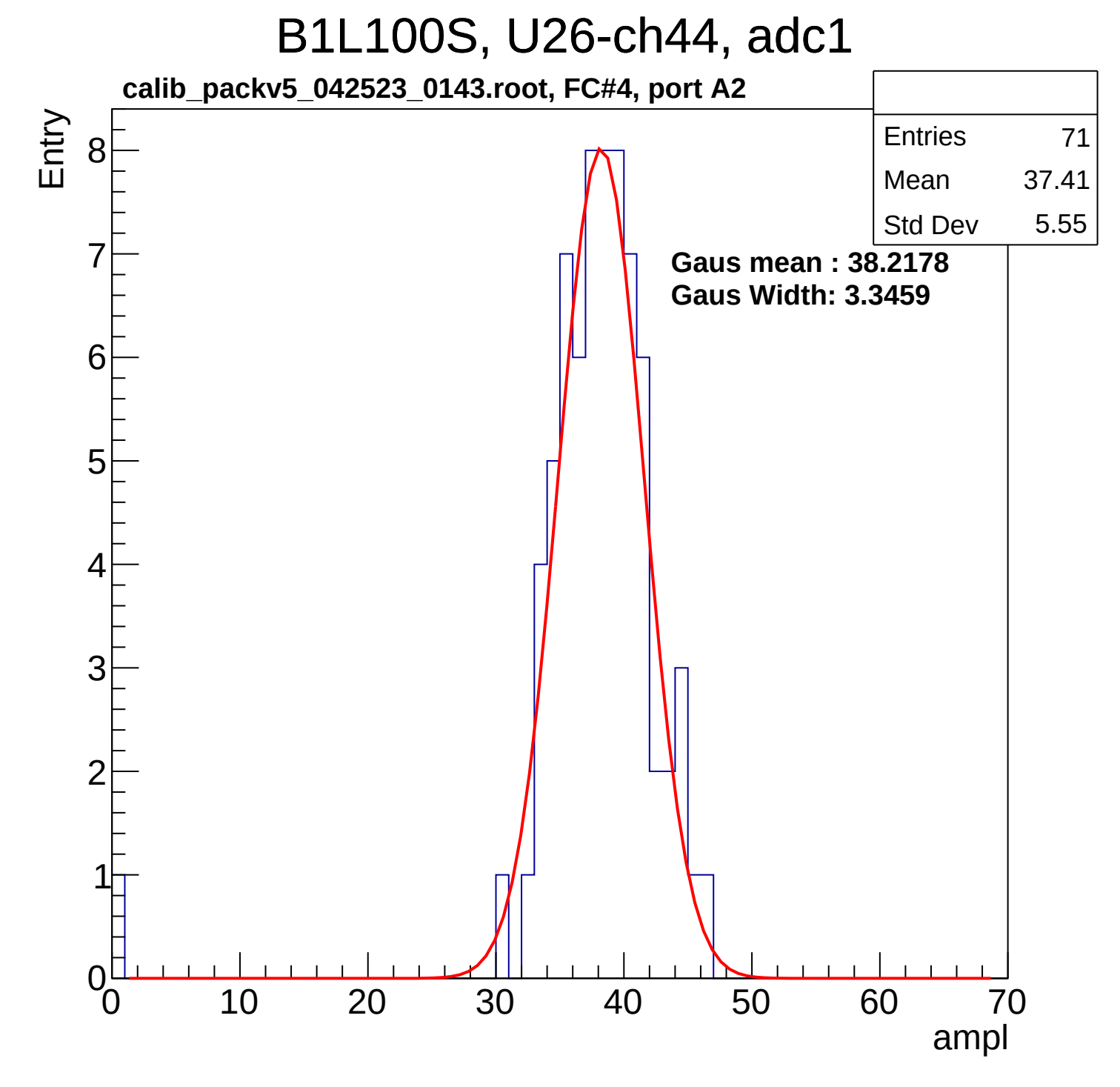
60
70


B1L100S, U26-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.41
Std Dev 5.55
Gaus mean : 38.2178
Gaus Width: 3.3459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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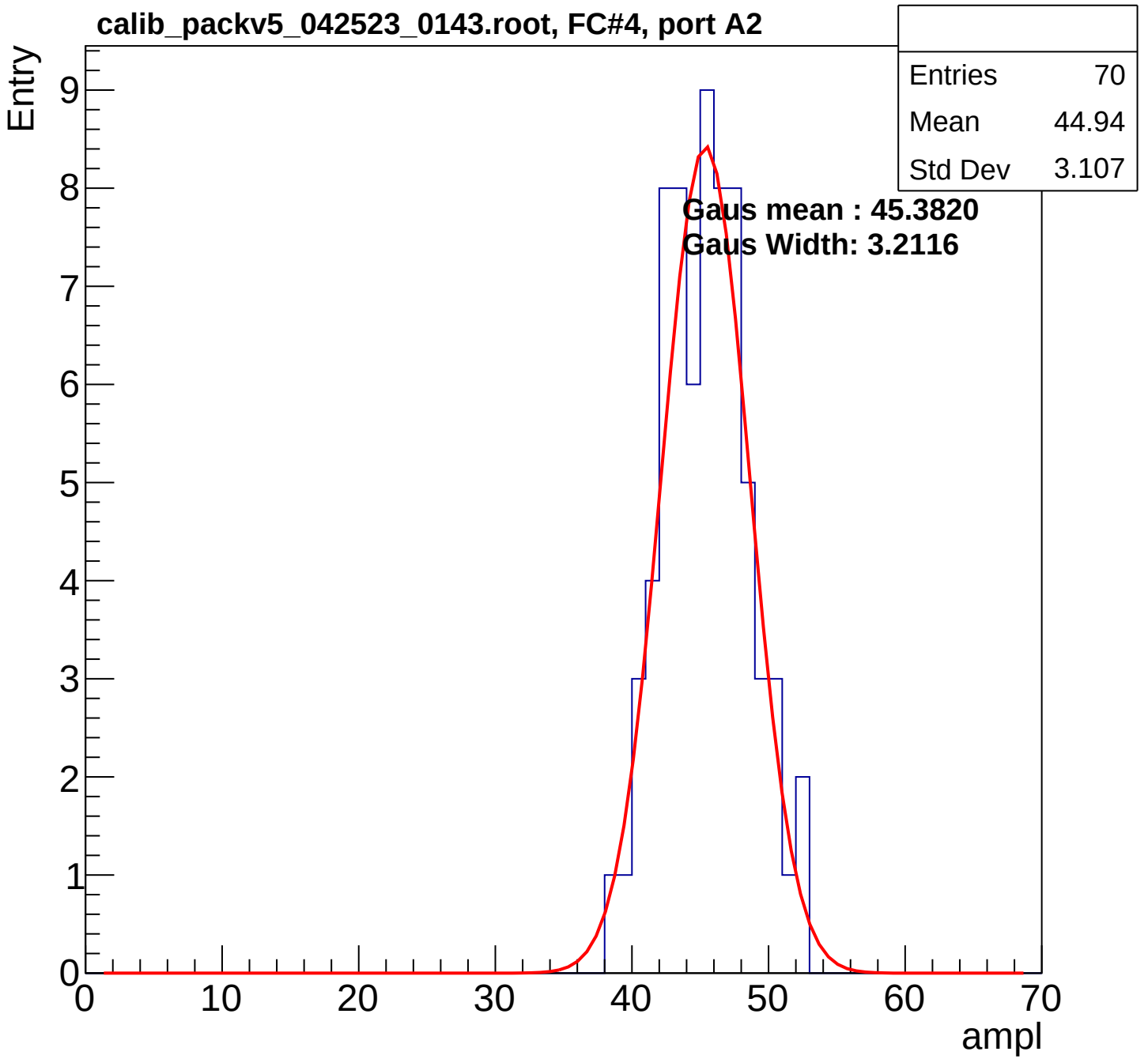
B1L100S, U26-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.94
Std Dev 3.107
Gaus mean : 45.3820
Gaus Width: 3.2116
Entry
ampl
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10
20
30
40
50
60
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1
2
3
4
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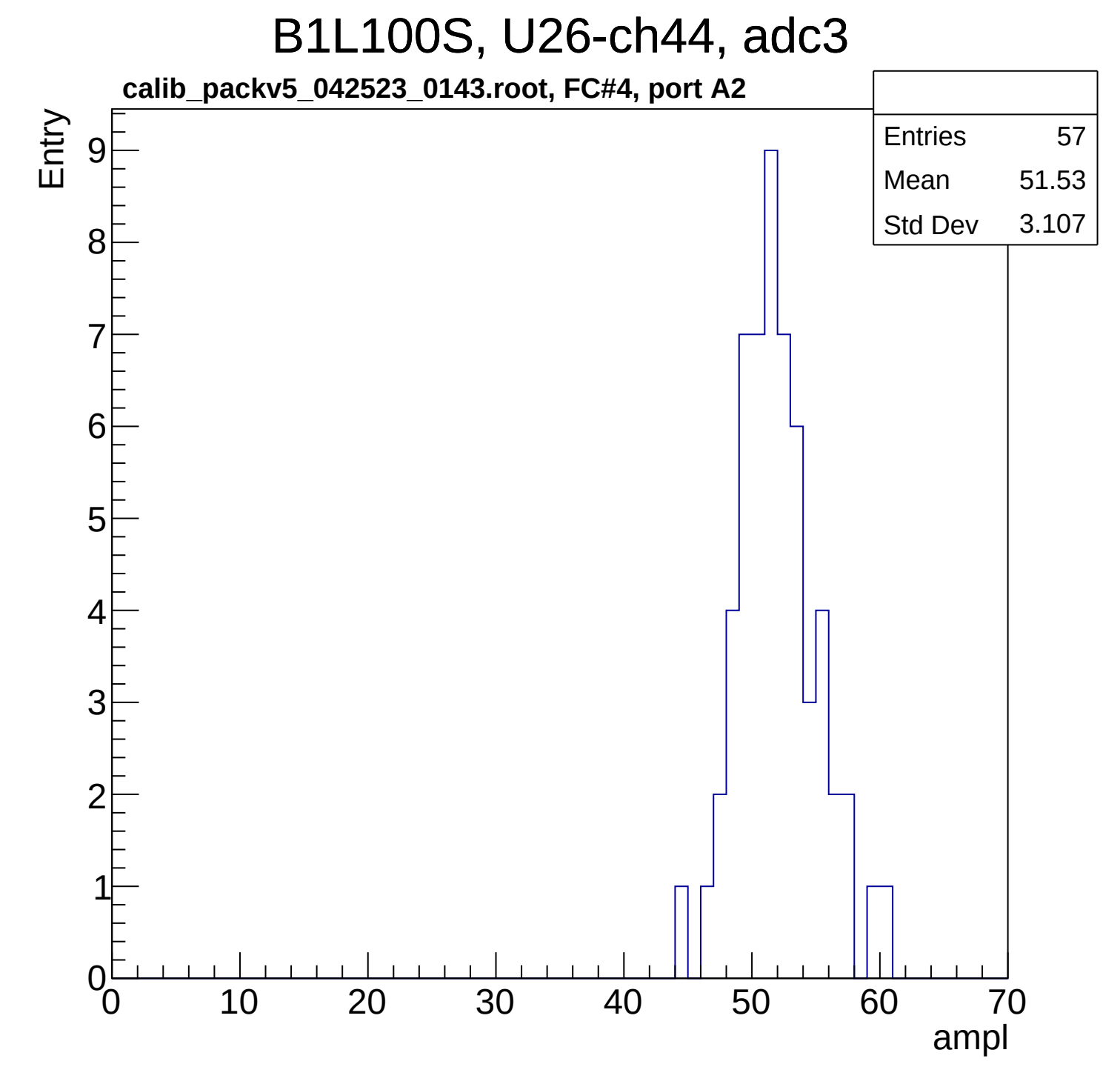
6
7
8
9


B1L100S, U26-ch44, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 51.53
Std Dev 3.107
Entry
ampl
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2
3
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6
7
8
9
0
10
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60
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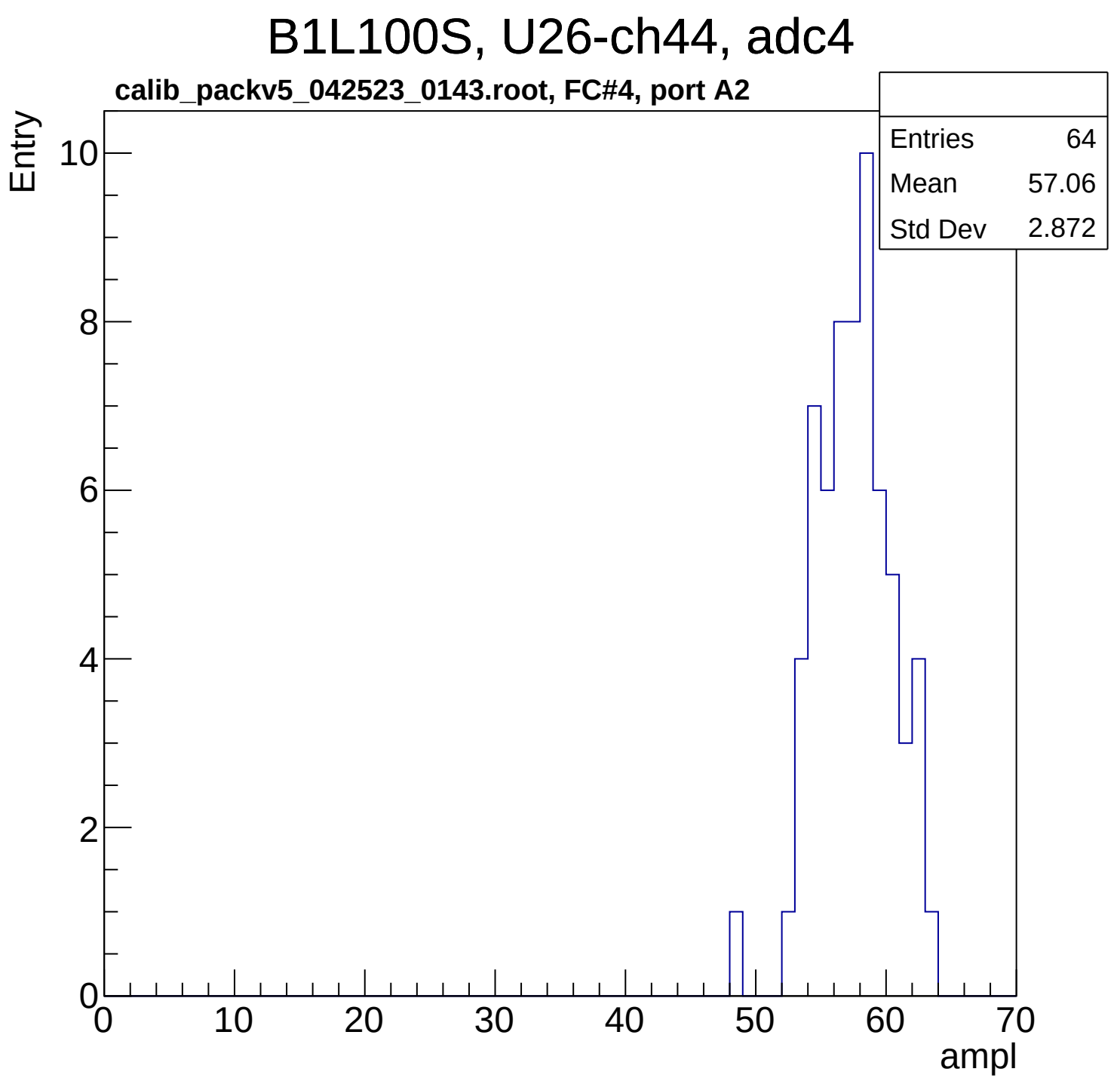
B1L100S, U26-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 57.06
Std Dev 2.872
Entry
ampl
0
2
4
6
8
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0
10
20
30
40
50
60
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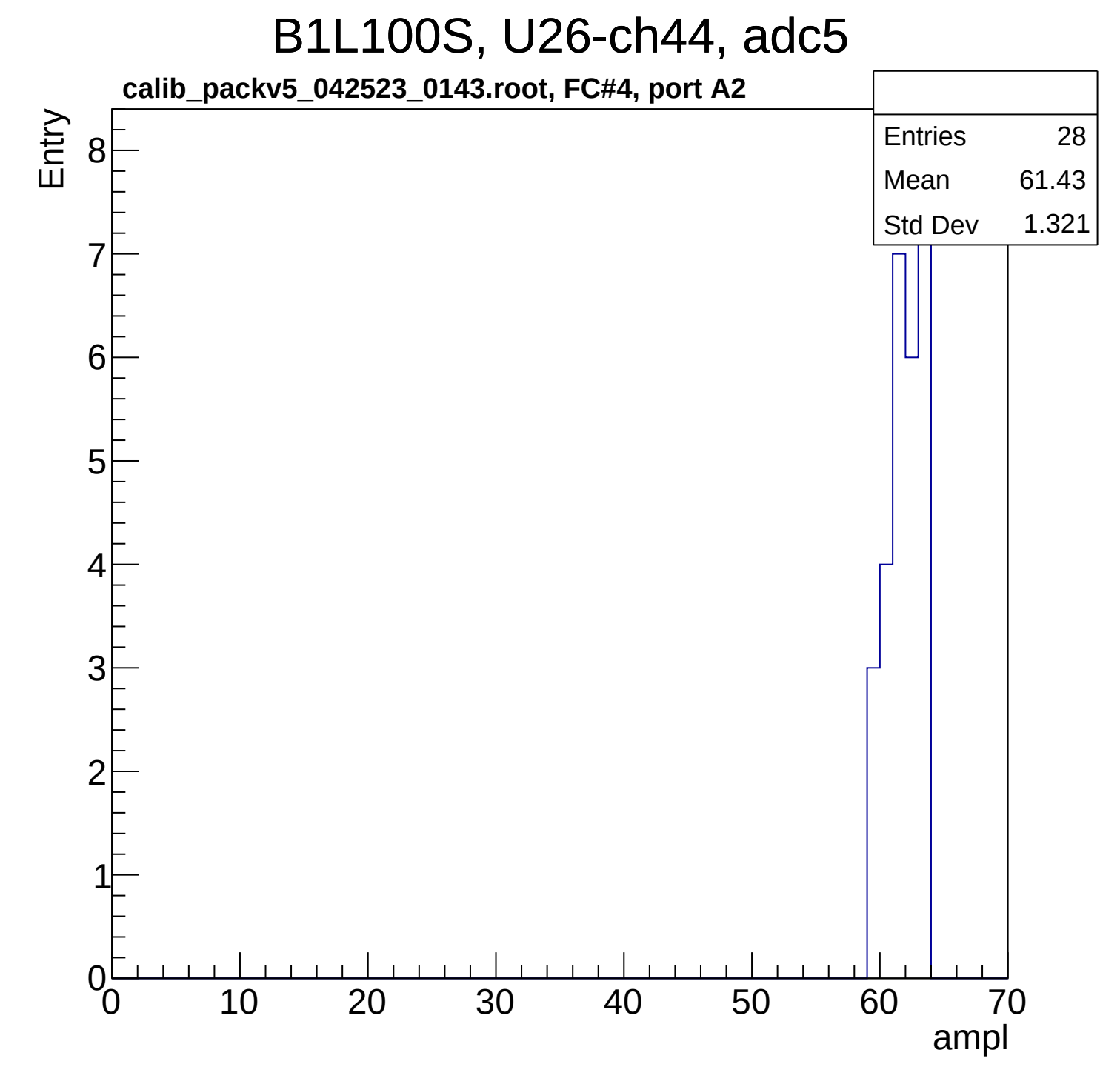
B1L100S, U26-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 61.43
Std Dev 1.321
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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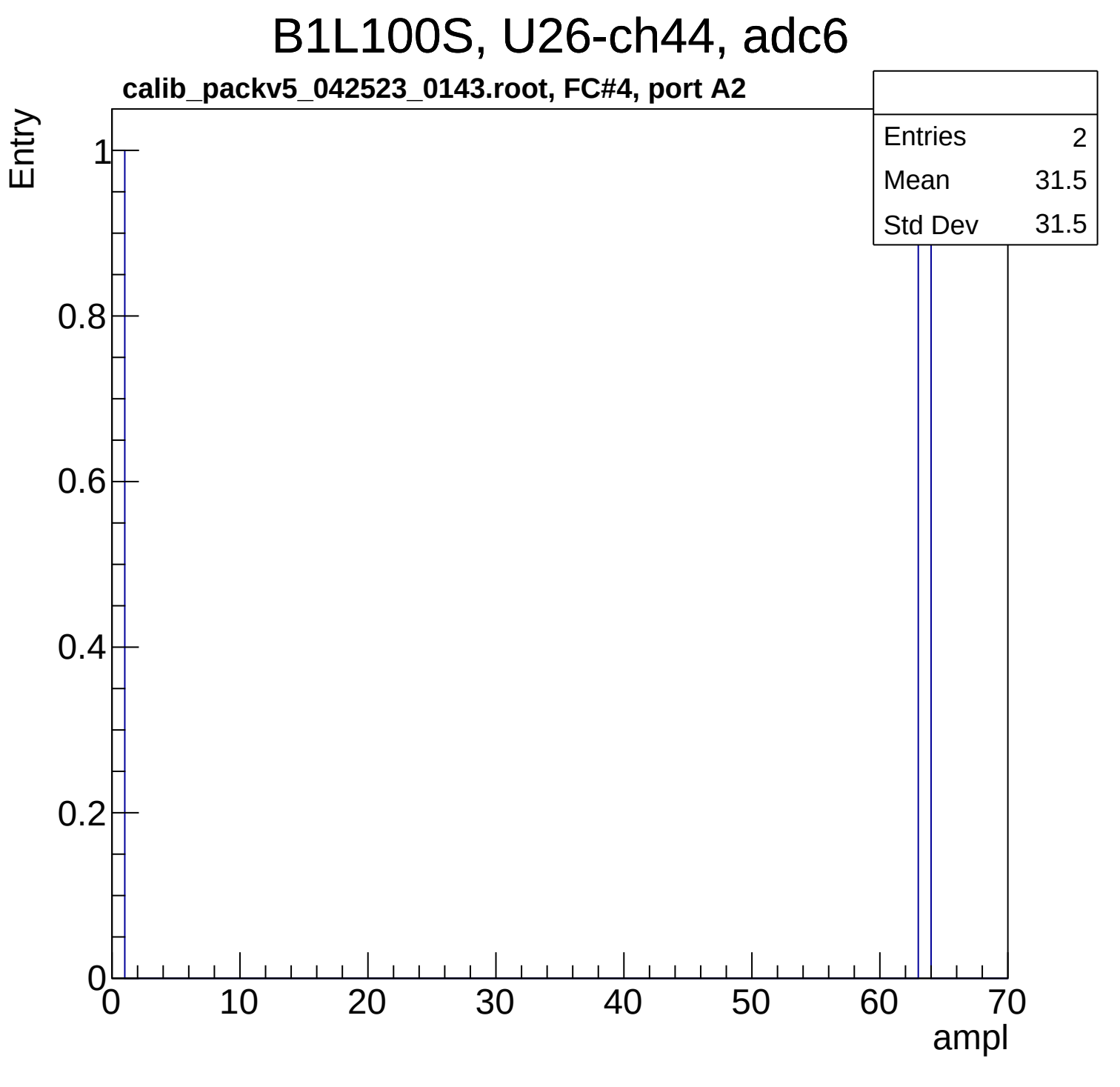
B1L100S, U26-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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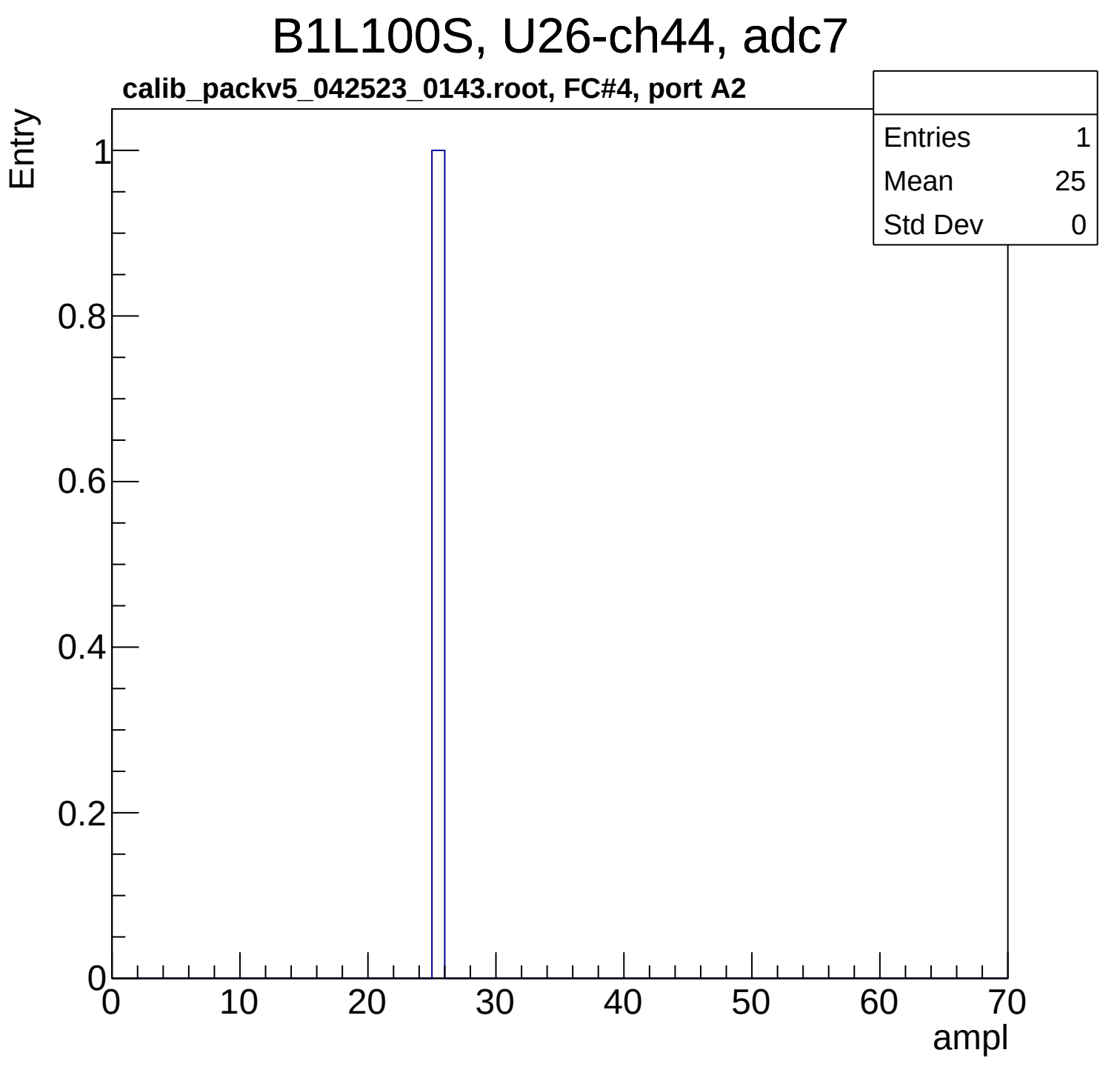
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 25
Std Dev 0
Entry
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0.6
0.4
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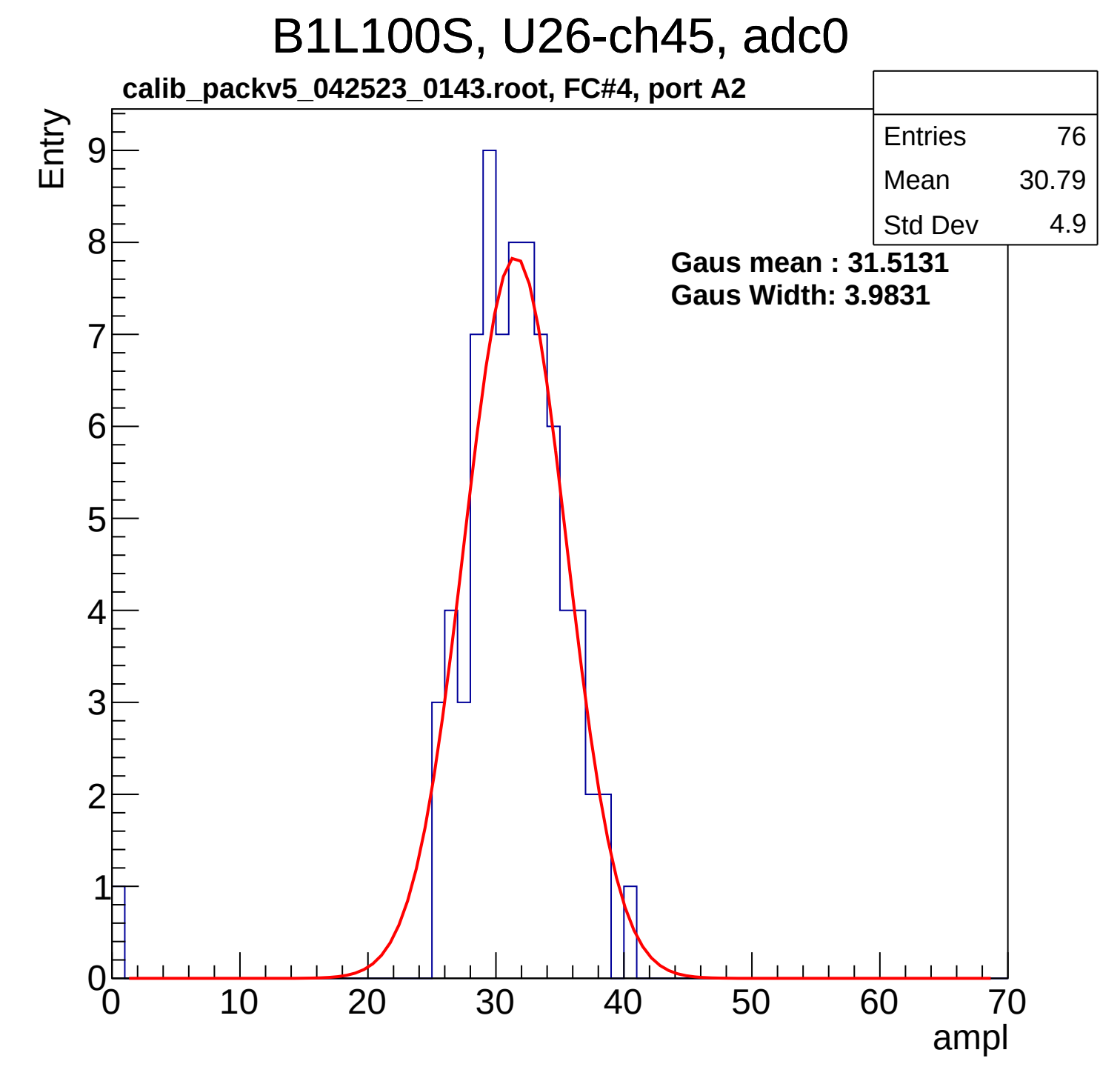
60
70
ampl


B1L100S, U26-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.79
Std Dev 4.9
Gaus mean : 31.5131
Gaus Width: 3.9831
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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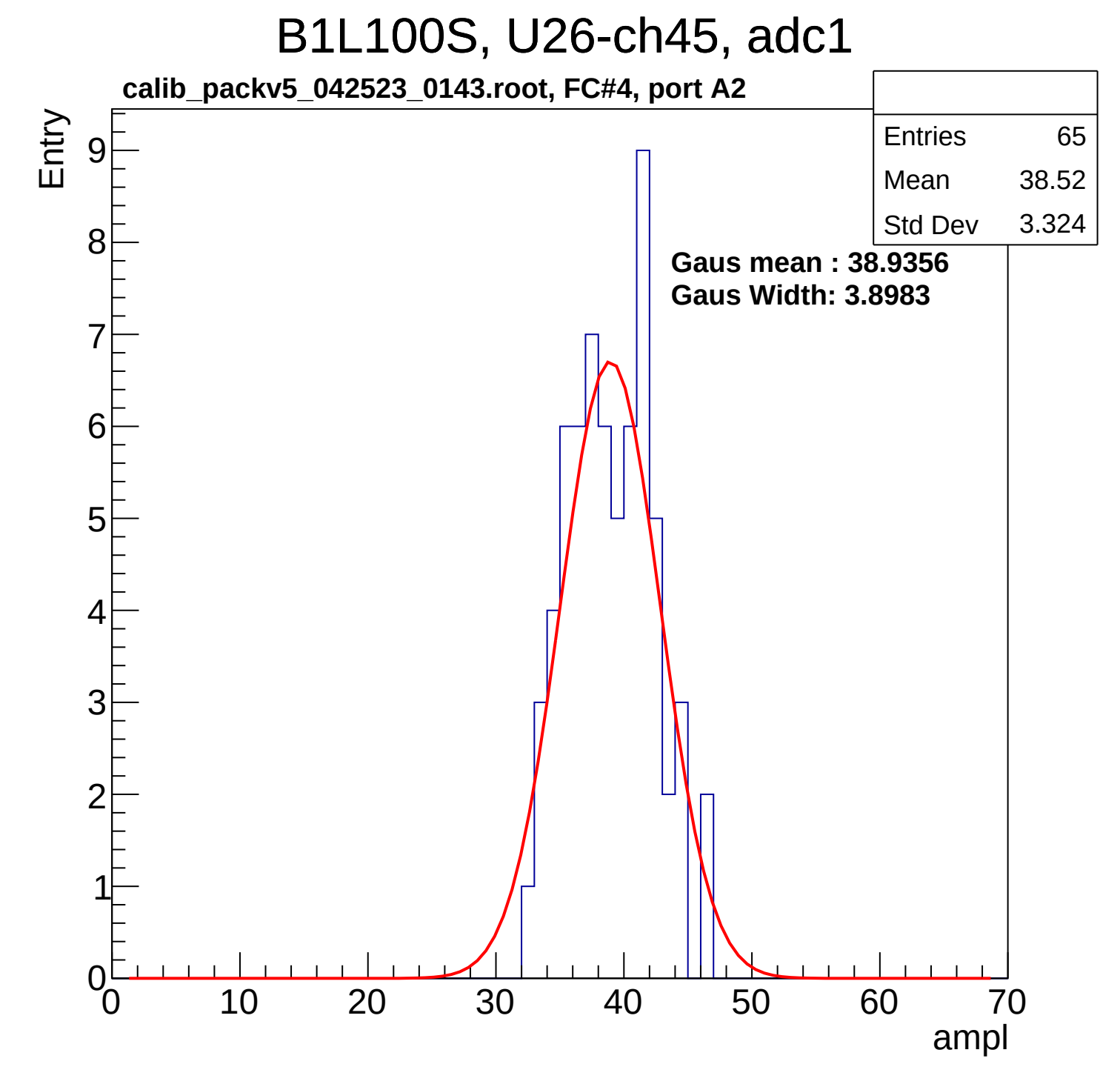
60
70


B1L100S, U26-ch45, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 38.52
Std Dev 3.324
Gaus mean : 38.9356
Gaus Width: 3.8983
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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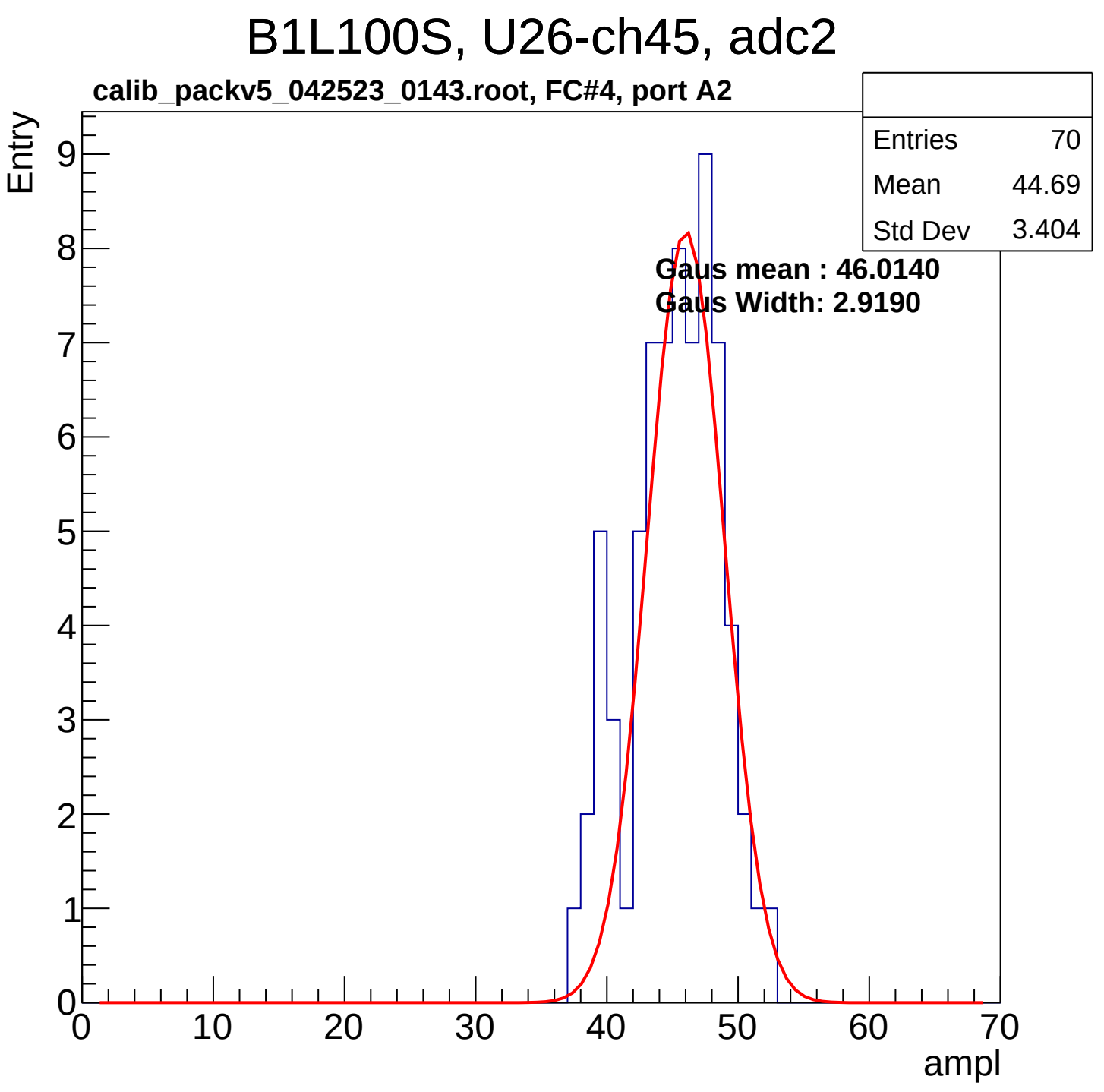
60
70


B1L100S, U26-ch45, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.69
Std Dev 3.404
Gaus mean : 46.0140
Gaus Width: 2.9190
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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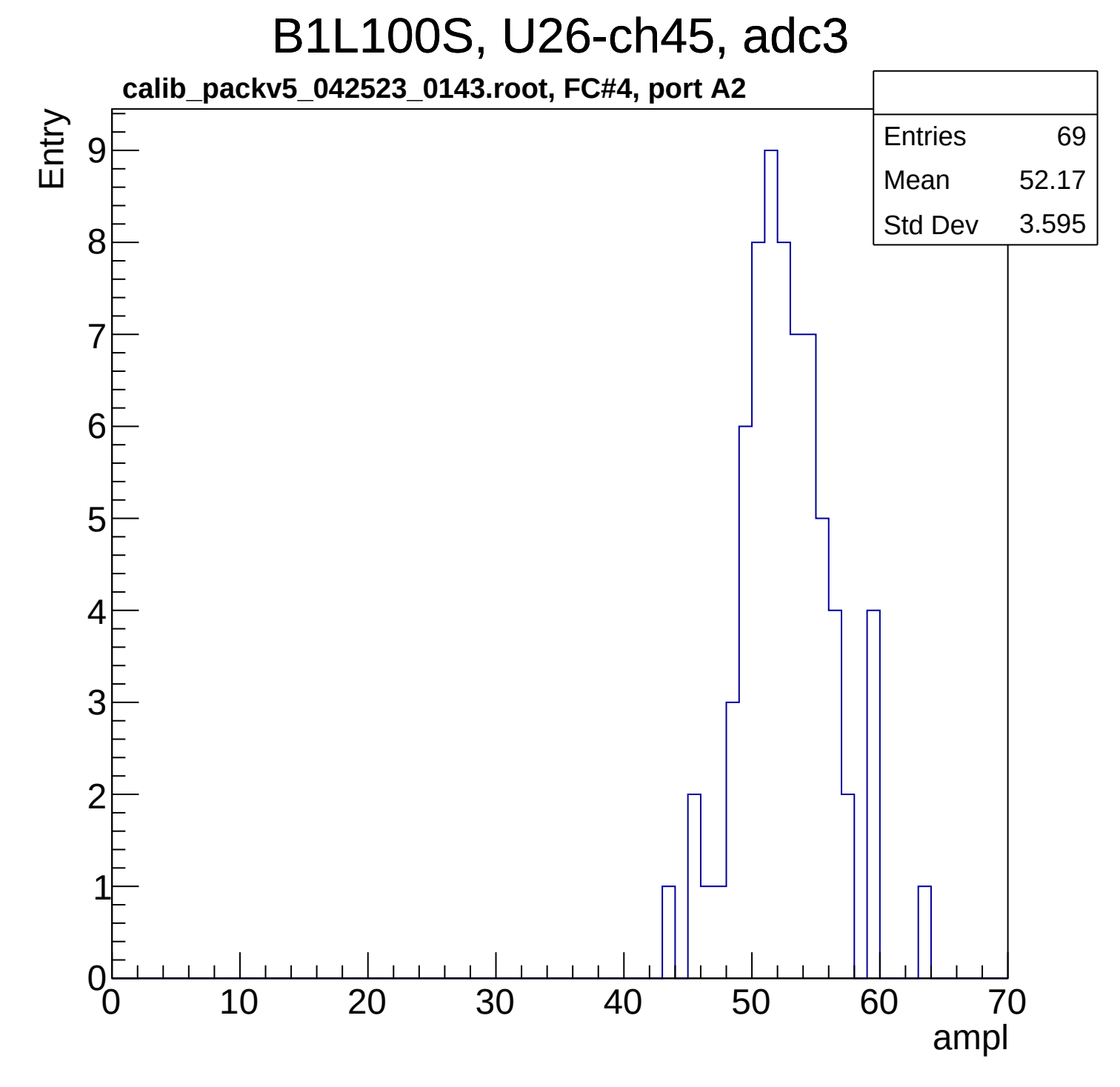
60
70


B1L100S, U26-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 52.17
Std Dev 3.595
Entry
ampl
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3
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6
7
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0
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60
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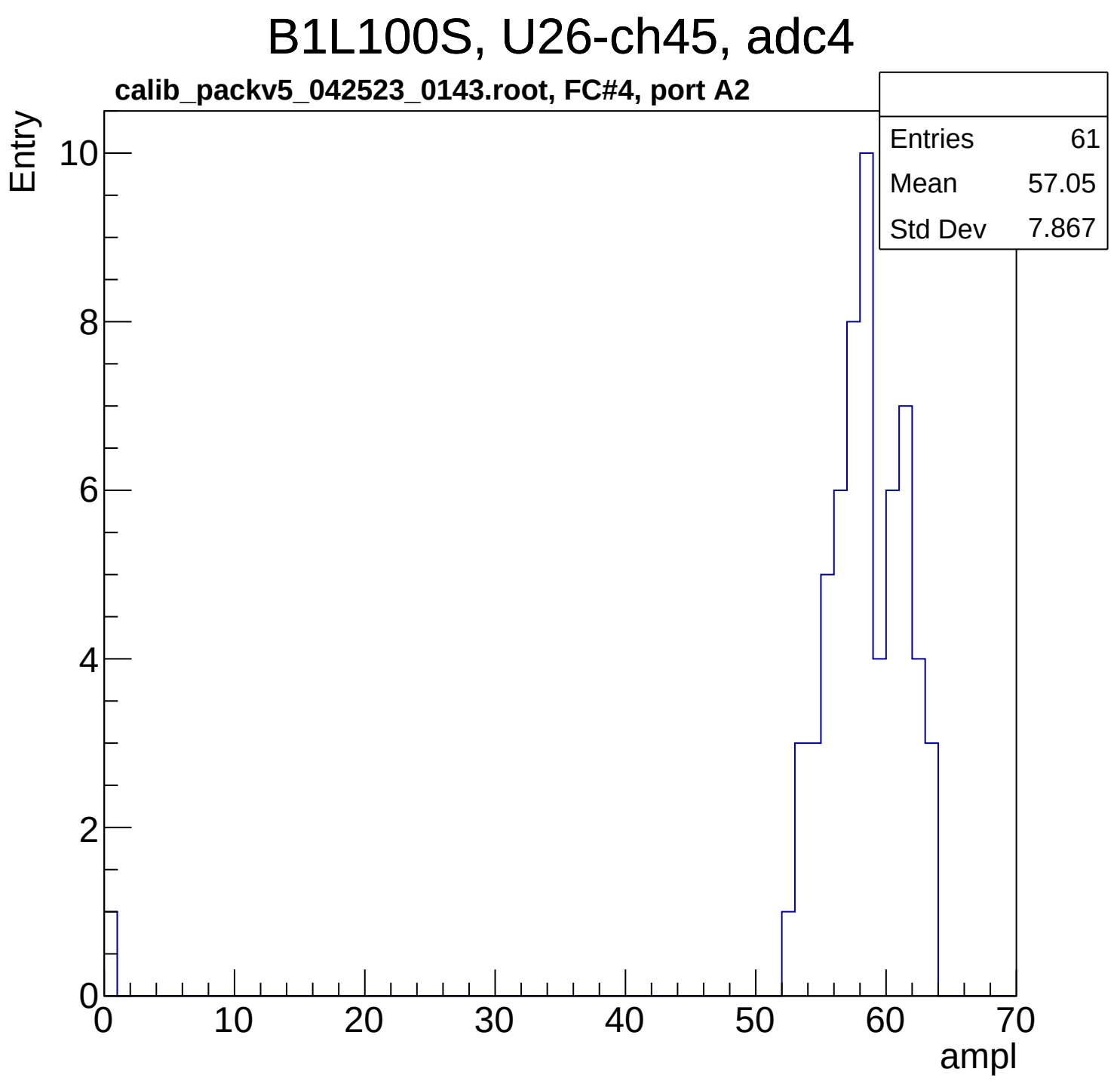
B1L100S, U26-ch45, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.05
Std Dev 7.867
Entry
ampl
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0
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60
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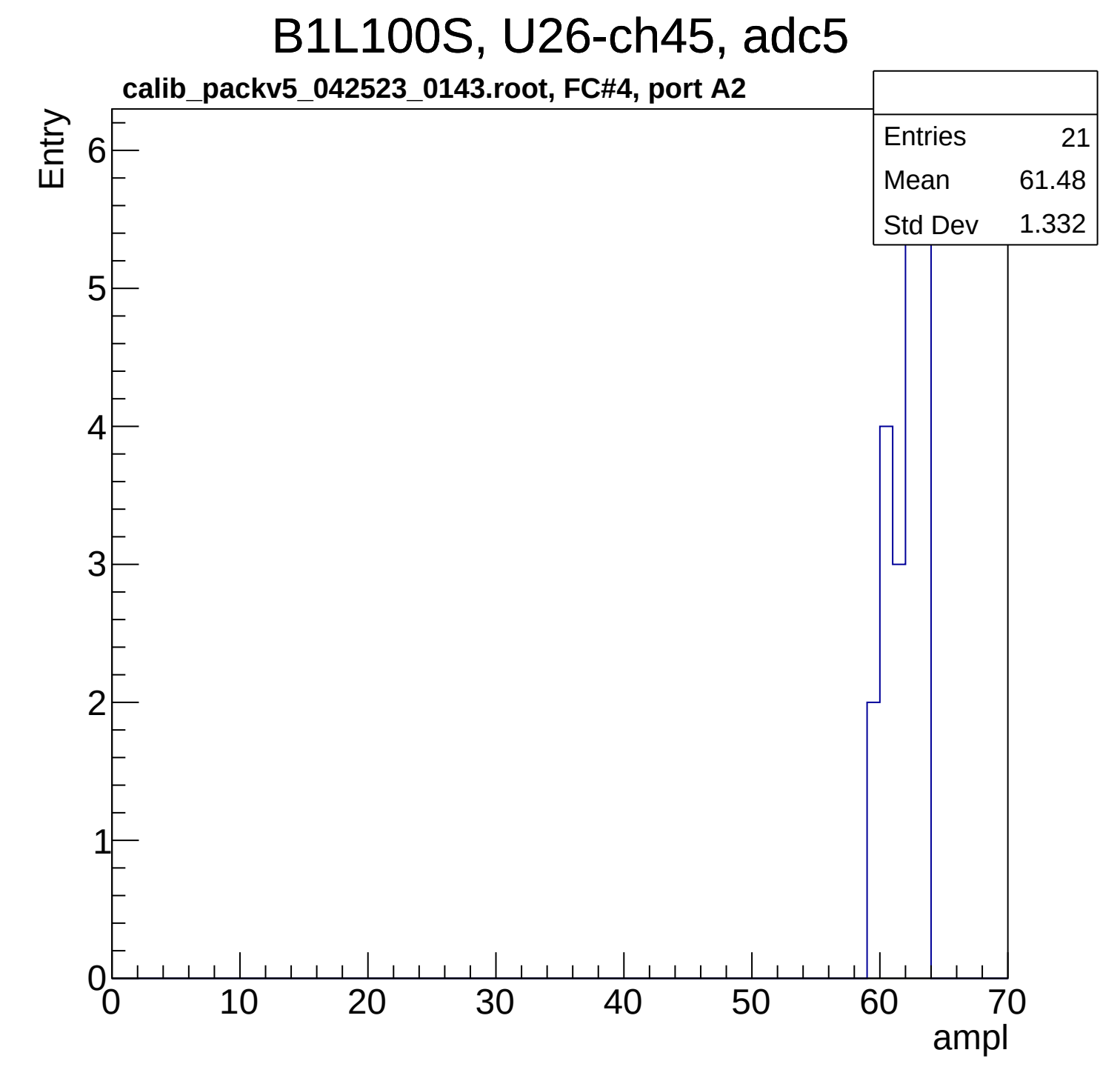
B1L100S, U26-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 21
Mean 61.48
Std Dev 1.332
Entry
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2
3
4
5
6
0
10
20
30
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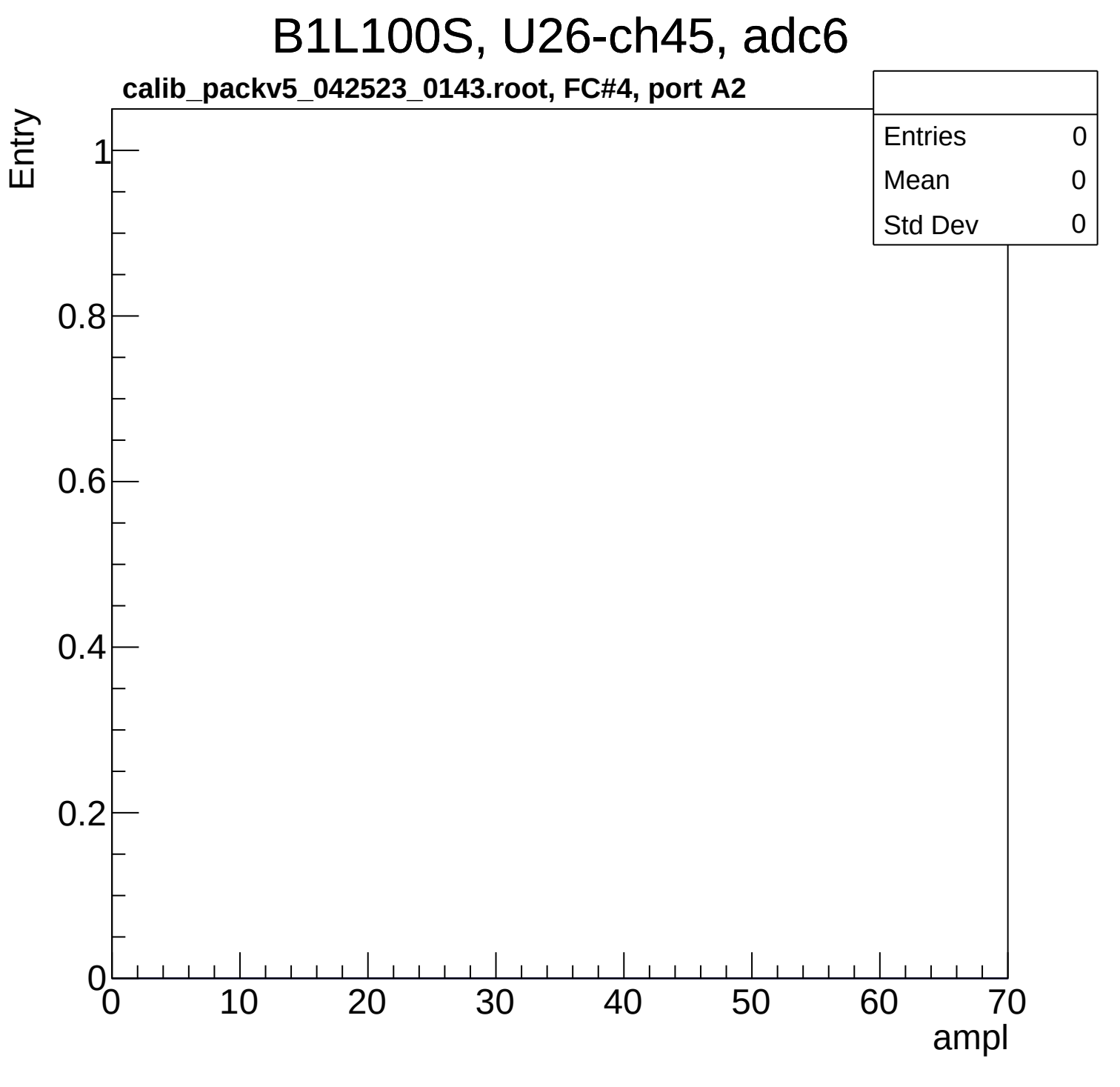
50
60
70
ampl


B1L100S, U26-ch45, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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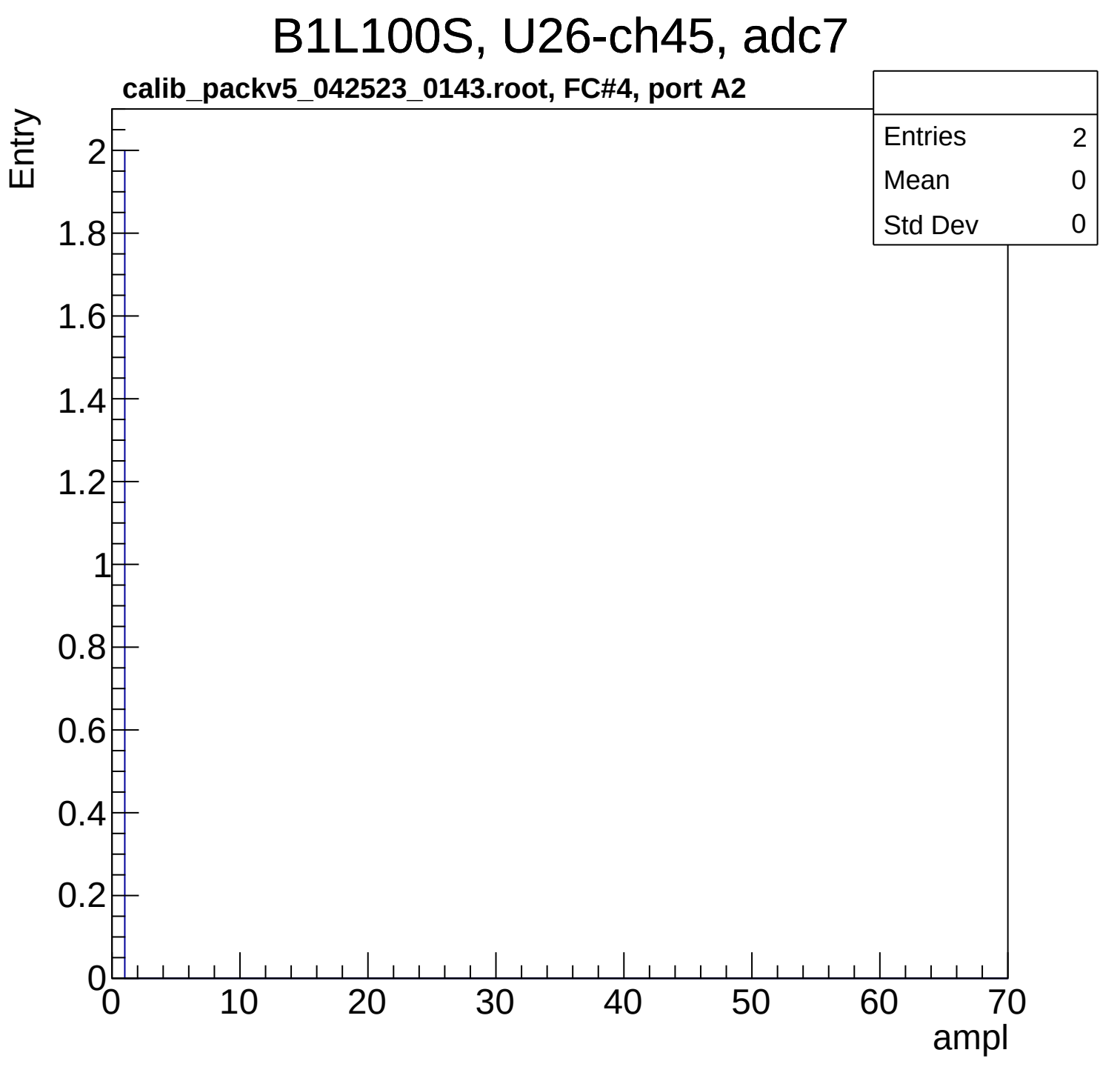
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch45, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
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10
20
30
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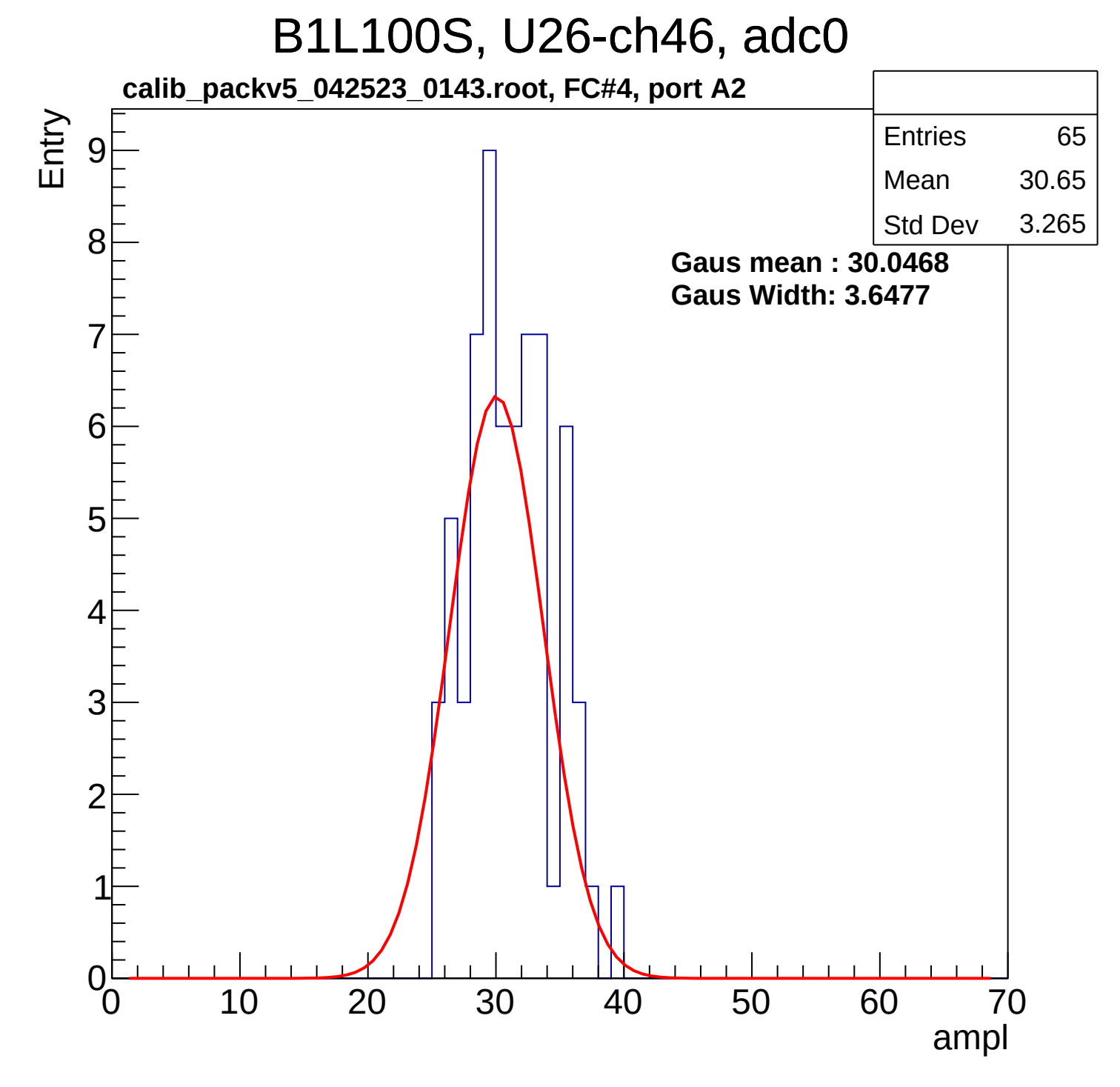
50
60
70
ampl


B1L100S, U26-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.65
Std Dev 3.265
Gaus mean : 30.0468
Gaus Width: 3.6477
Entry
ampl
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2
3
4
5
6
7
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0
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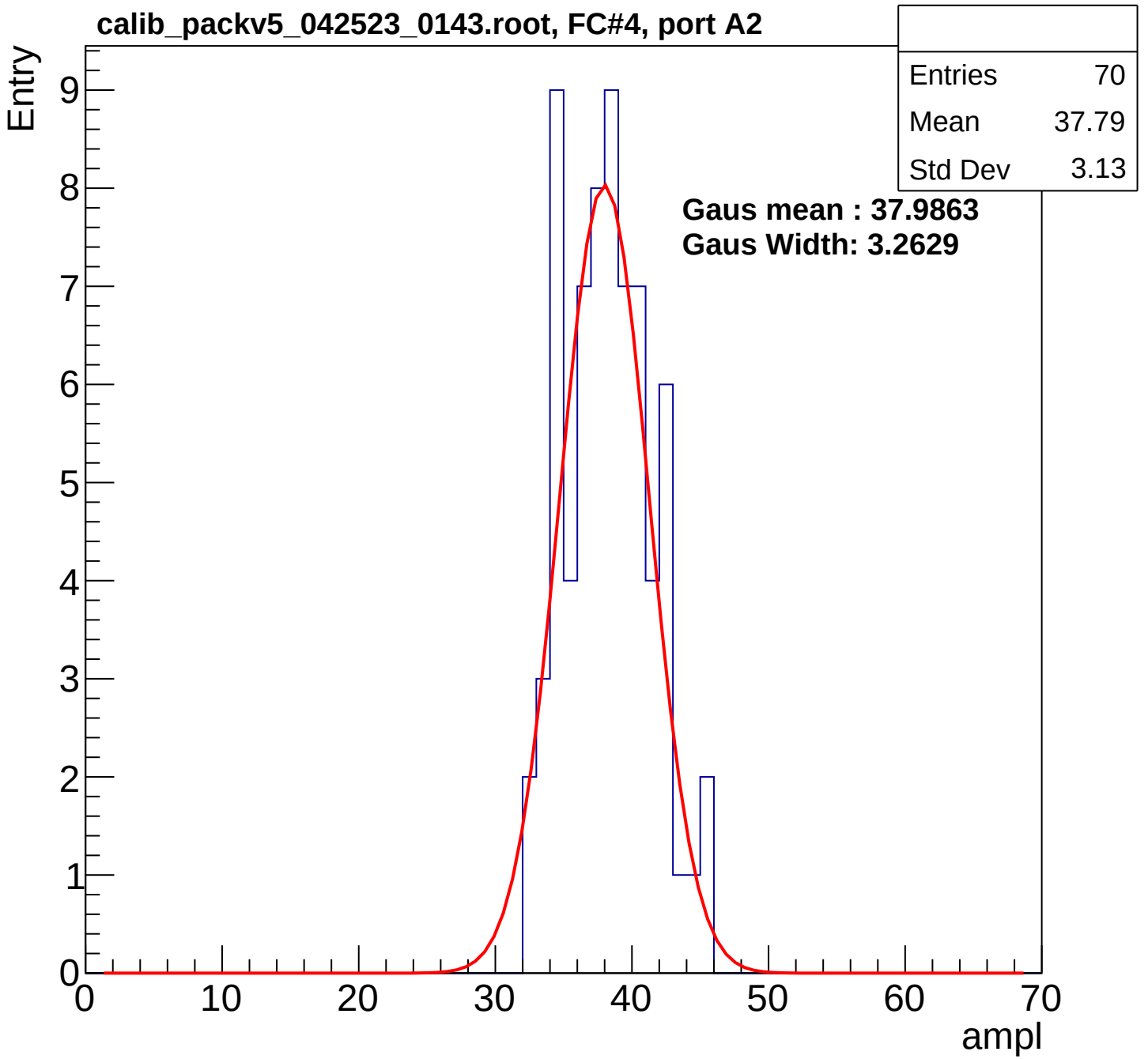
B1L100S, U26-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.79
Std Dev 3.13
Gaus mean : 37.9863
Gaus Width: 3.2629
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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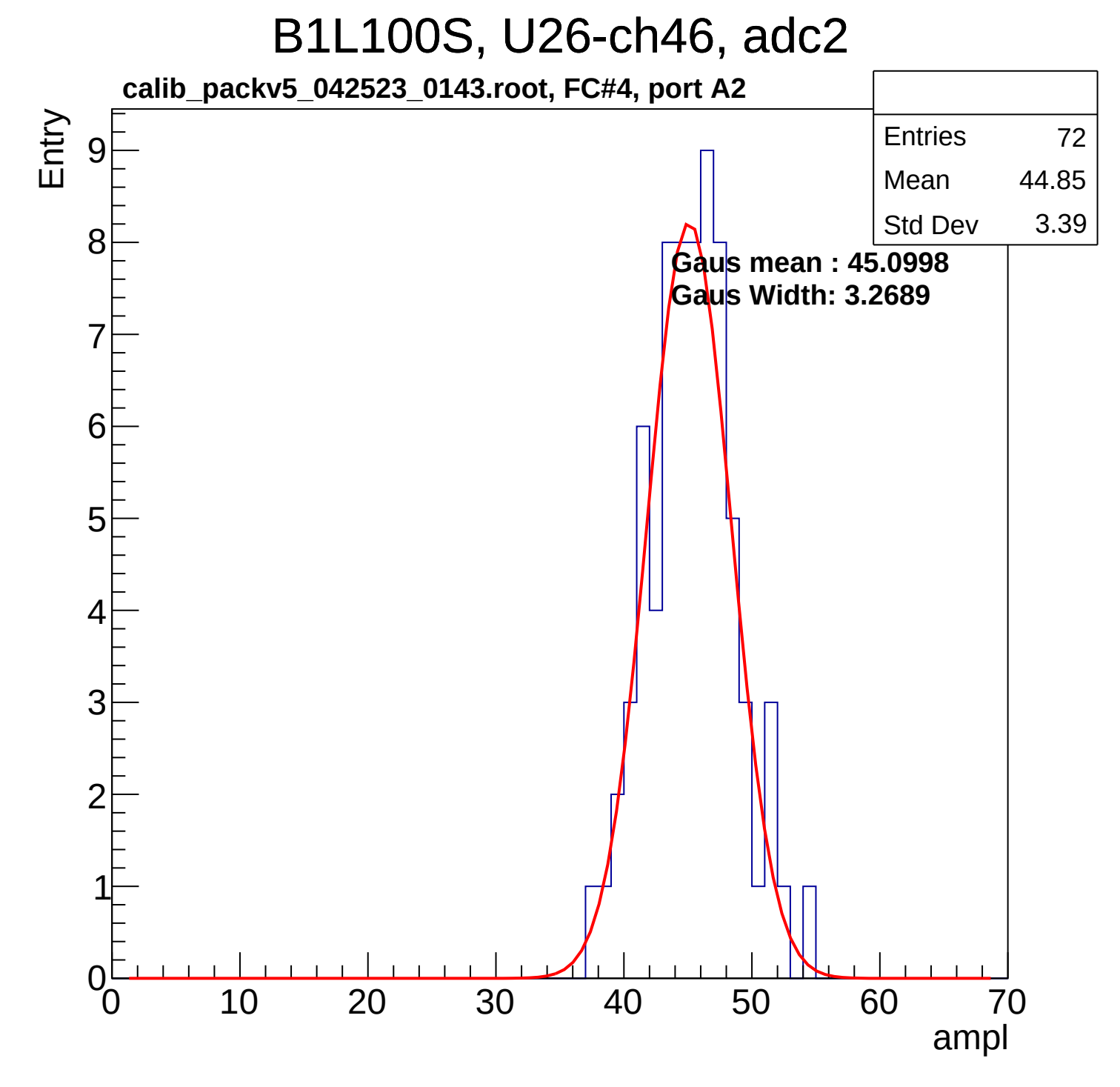
B1L100S, U26-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.85
Std Dev 3.39
Gaus mean : 45.0998
Gaus Width: 3.2689
Entry
ampl
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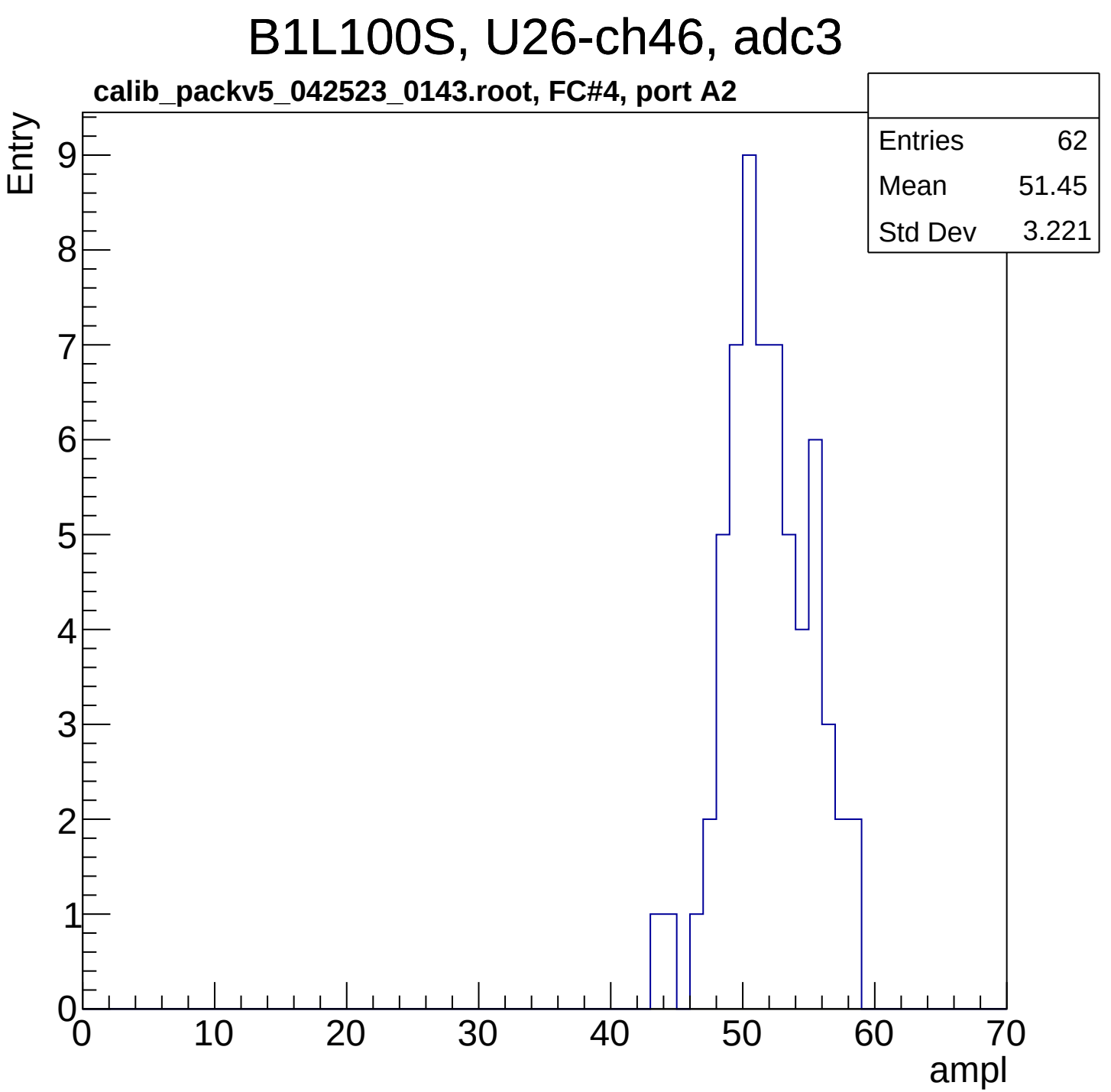
B1L100S, U26-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.45
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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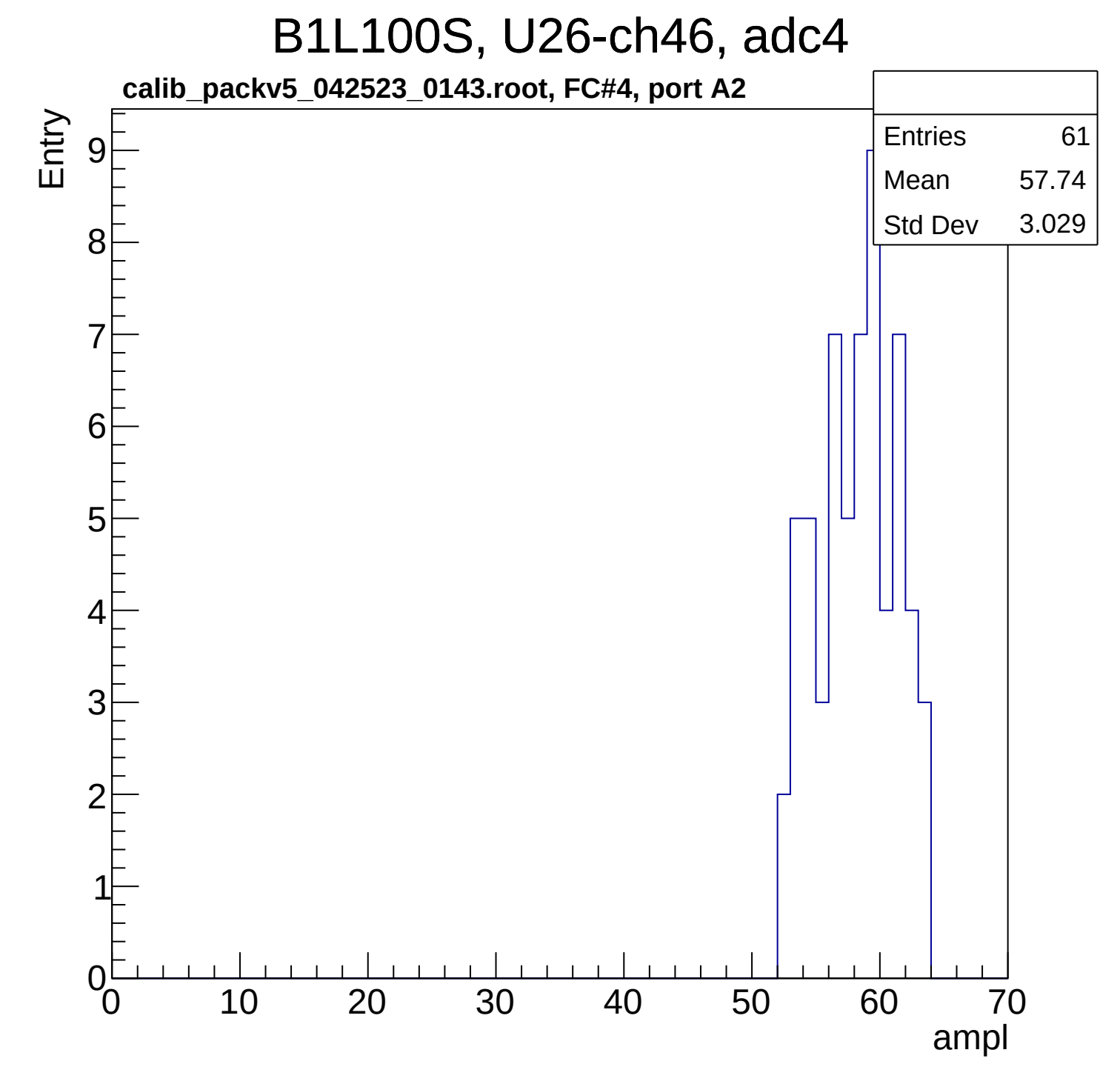
40
50
60
70


B1L100S, U26-ch46, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 57.74
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
10
20
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40
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60
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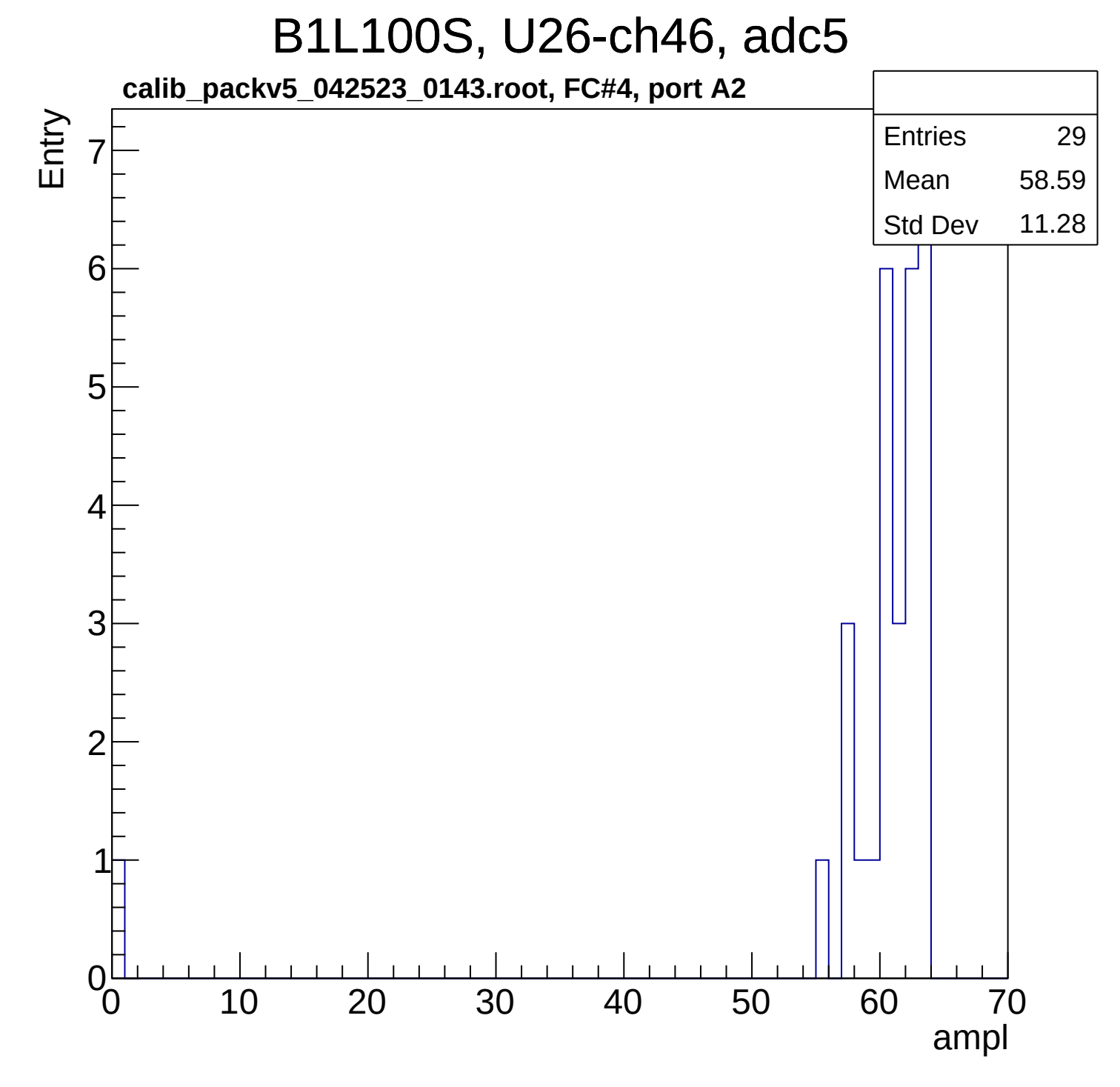
B1L100S, U26-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.59
Std Dev 11.28
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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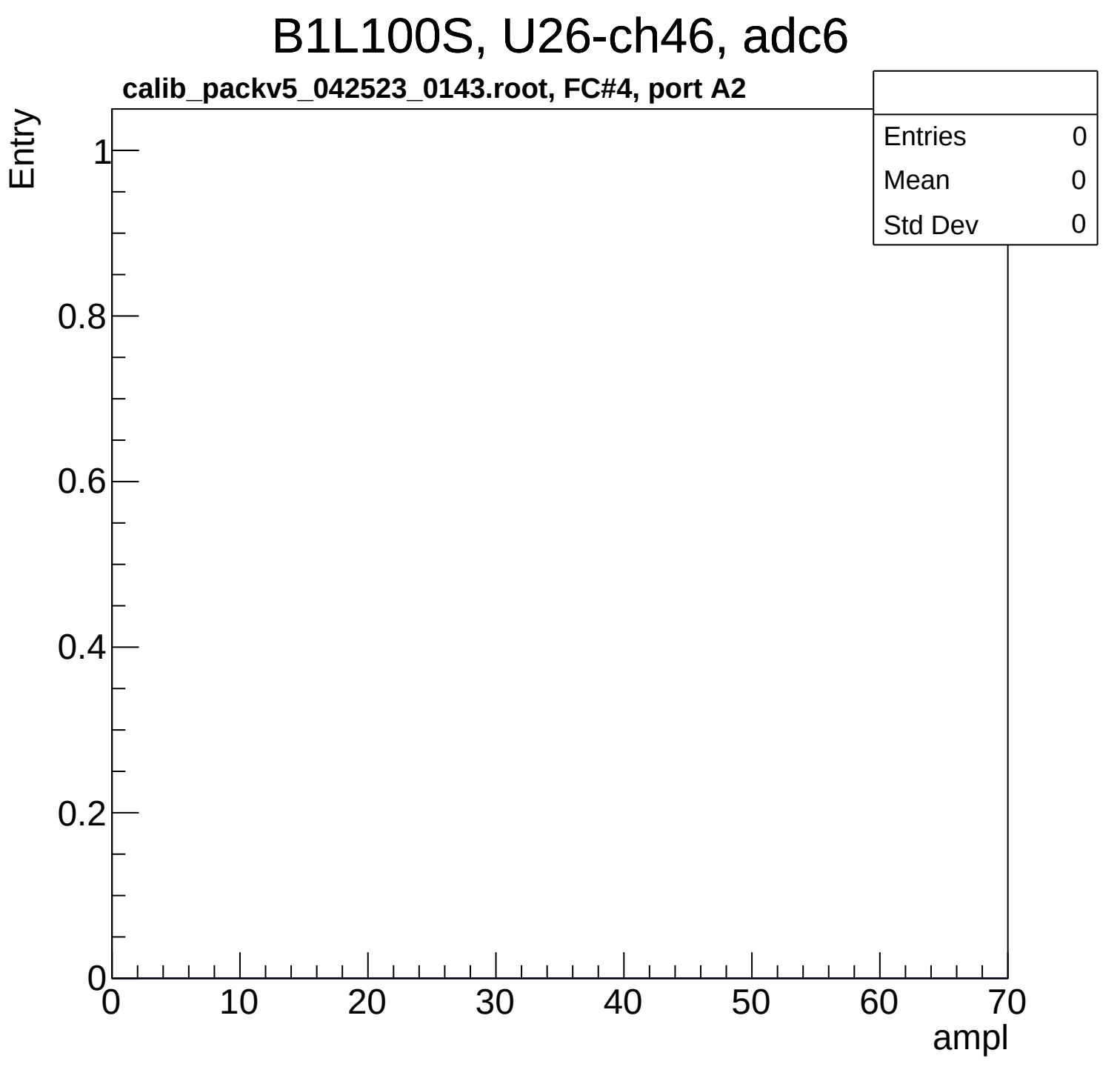
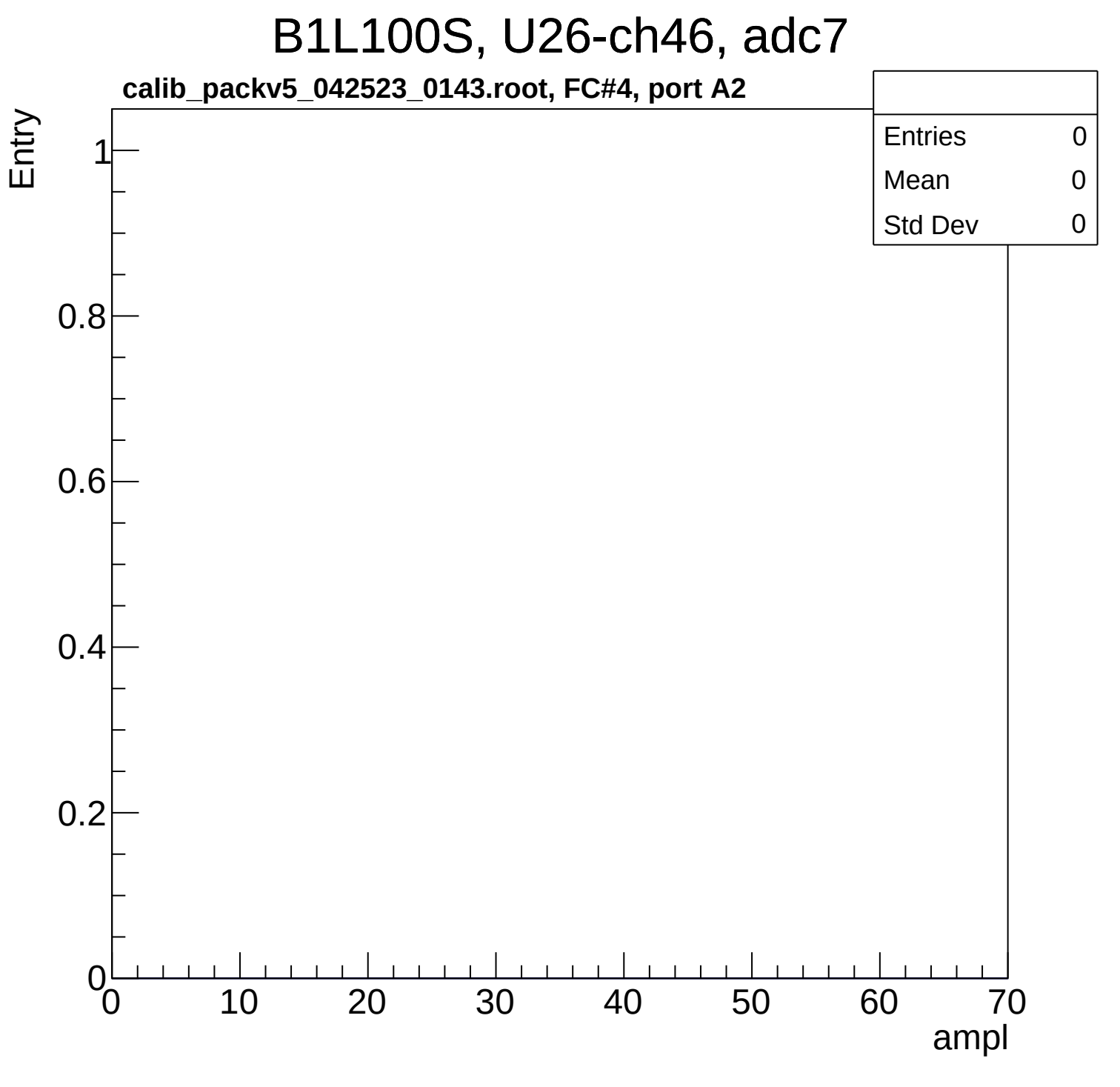
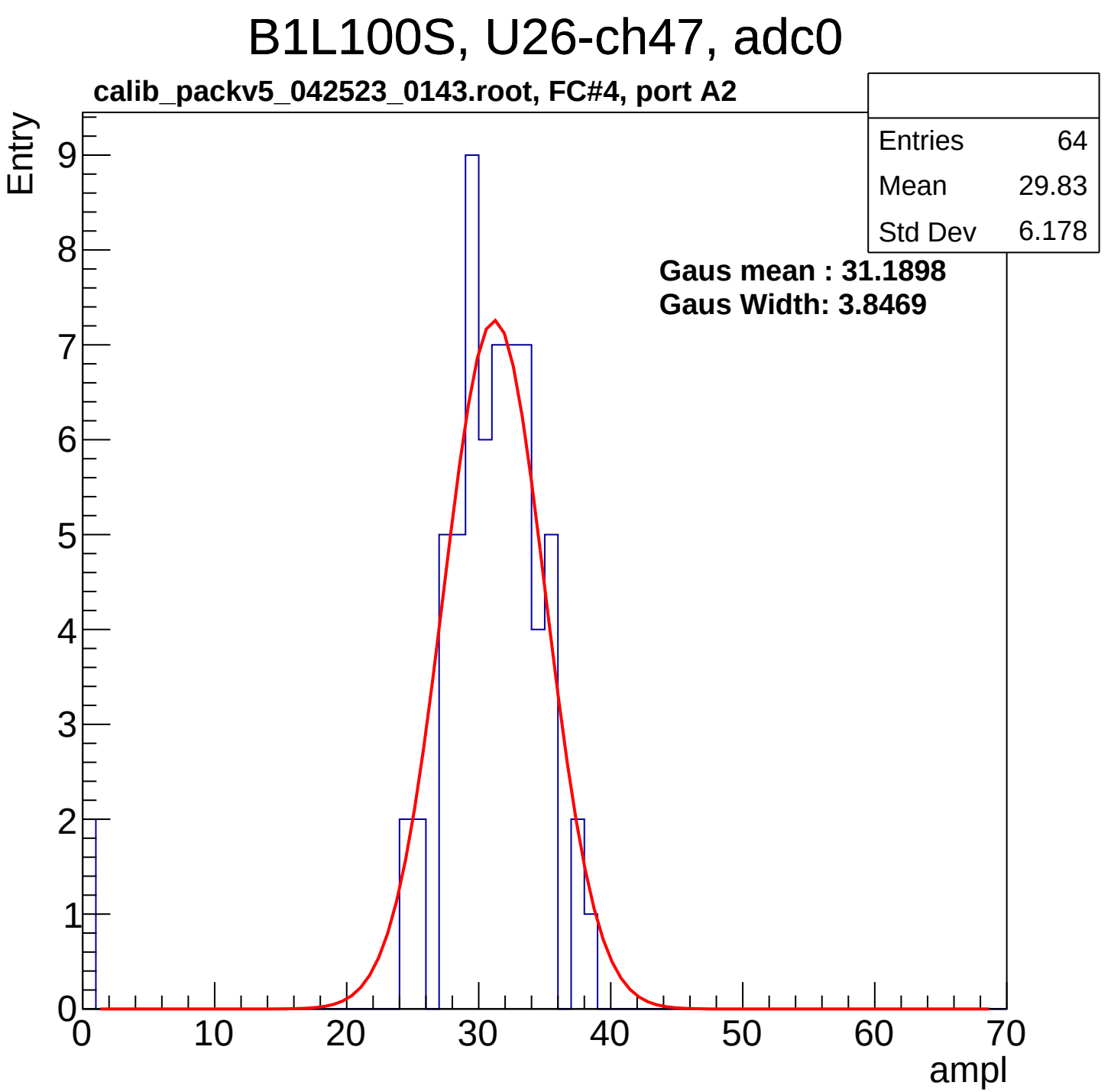
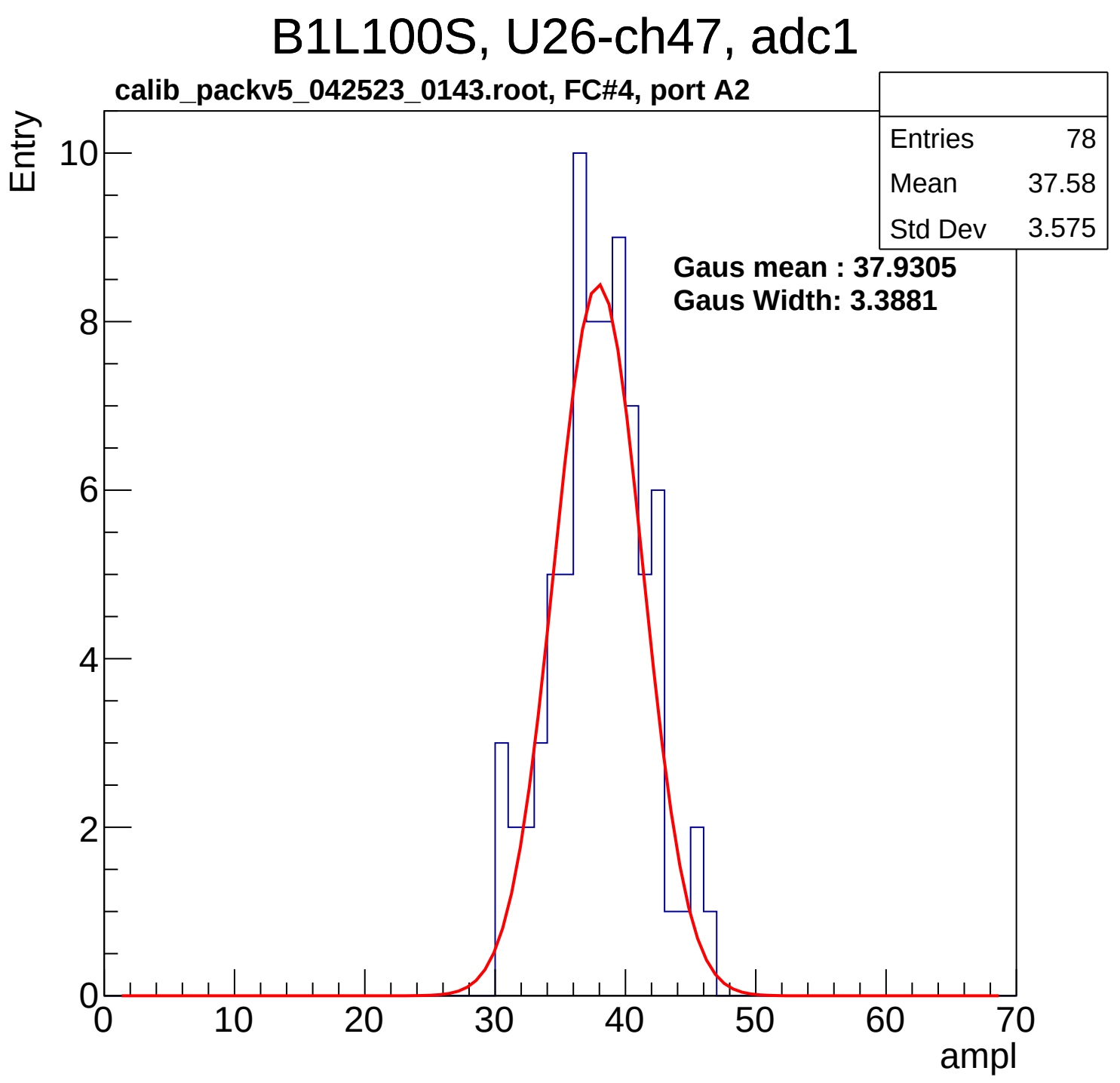
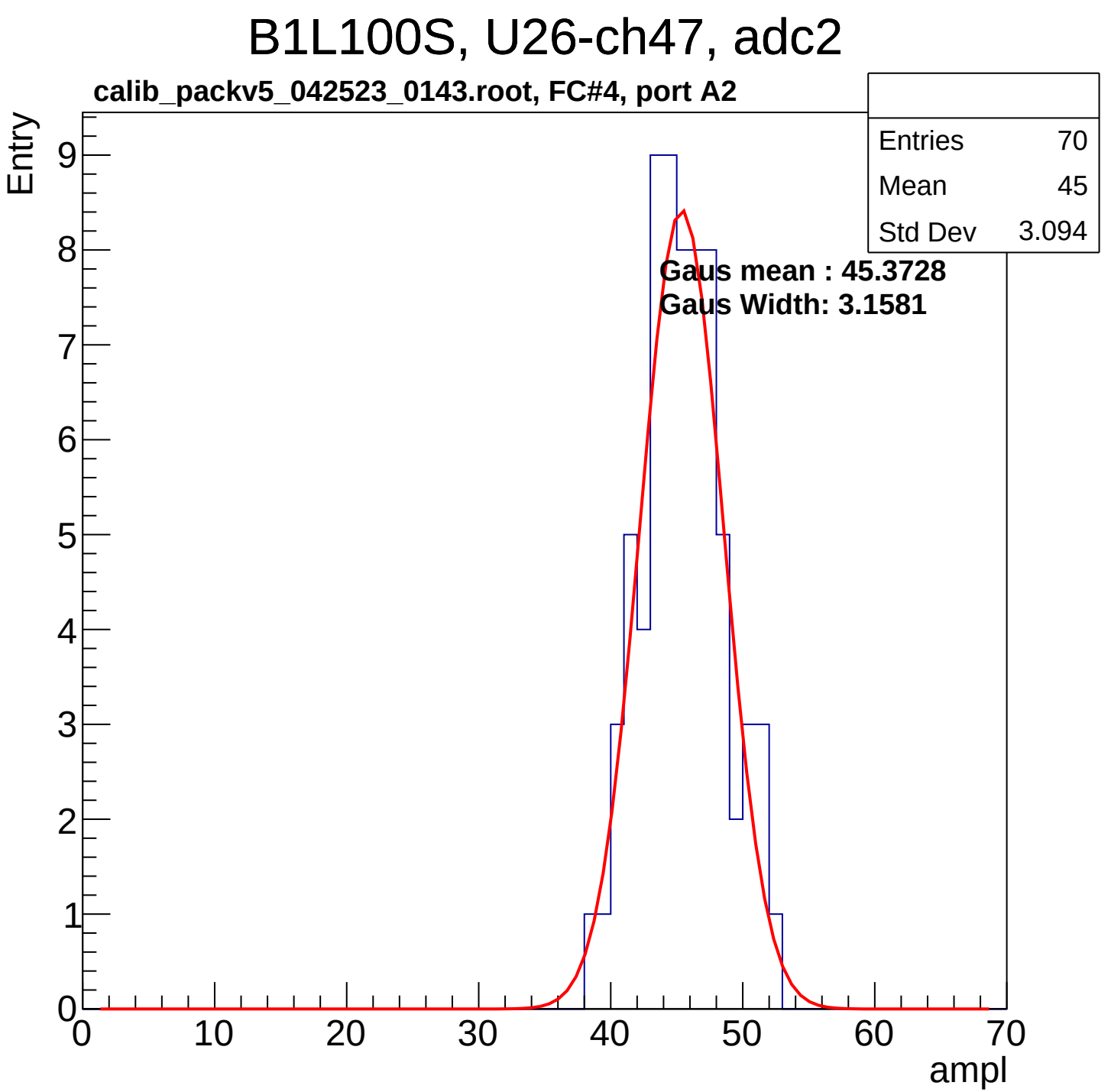
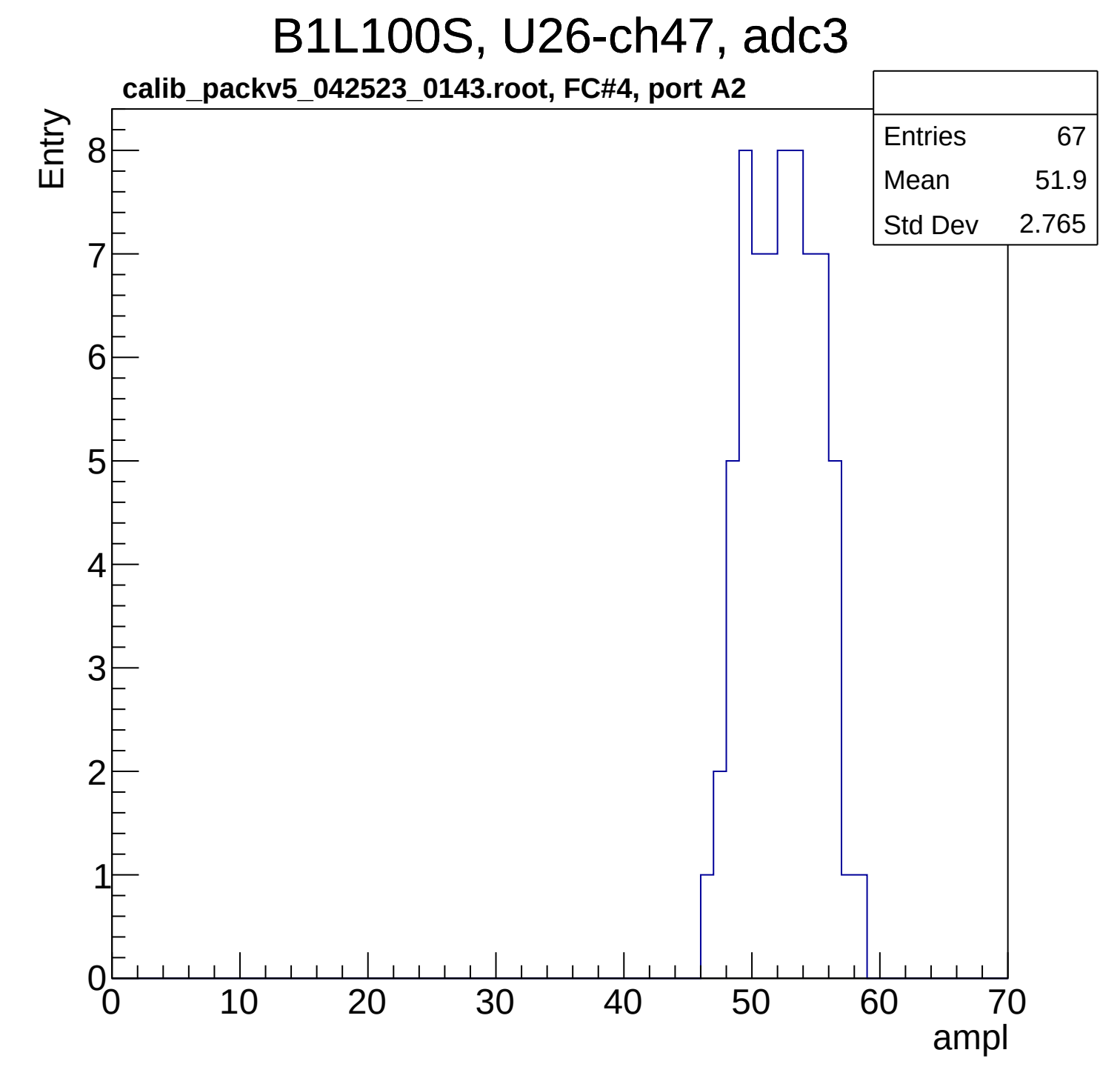

B1L100S, U26-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.83
Std Dev 6.178
Gaus mean : 31.1898
Gaus Width: 3.8469
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37.58
Std Dev 3.575
Gaus mean : 37.9305
Gaus Width: 3.3881
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 45
Std Dev 3.094
Gaus mean : 45.3728
Gaus Width: 3.1581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 51.9
Std Dev 2.765
Entry
ampl
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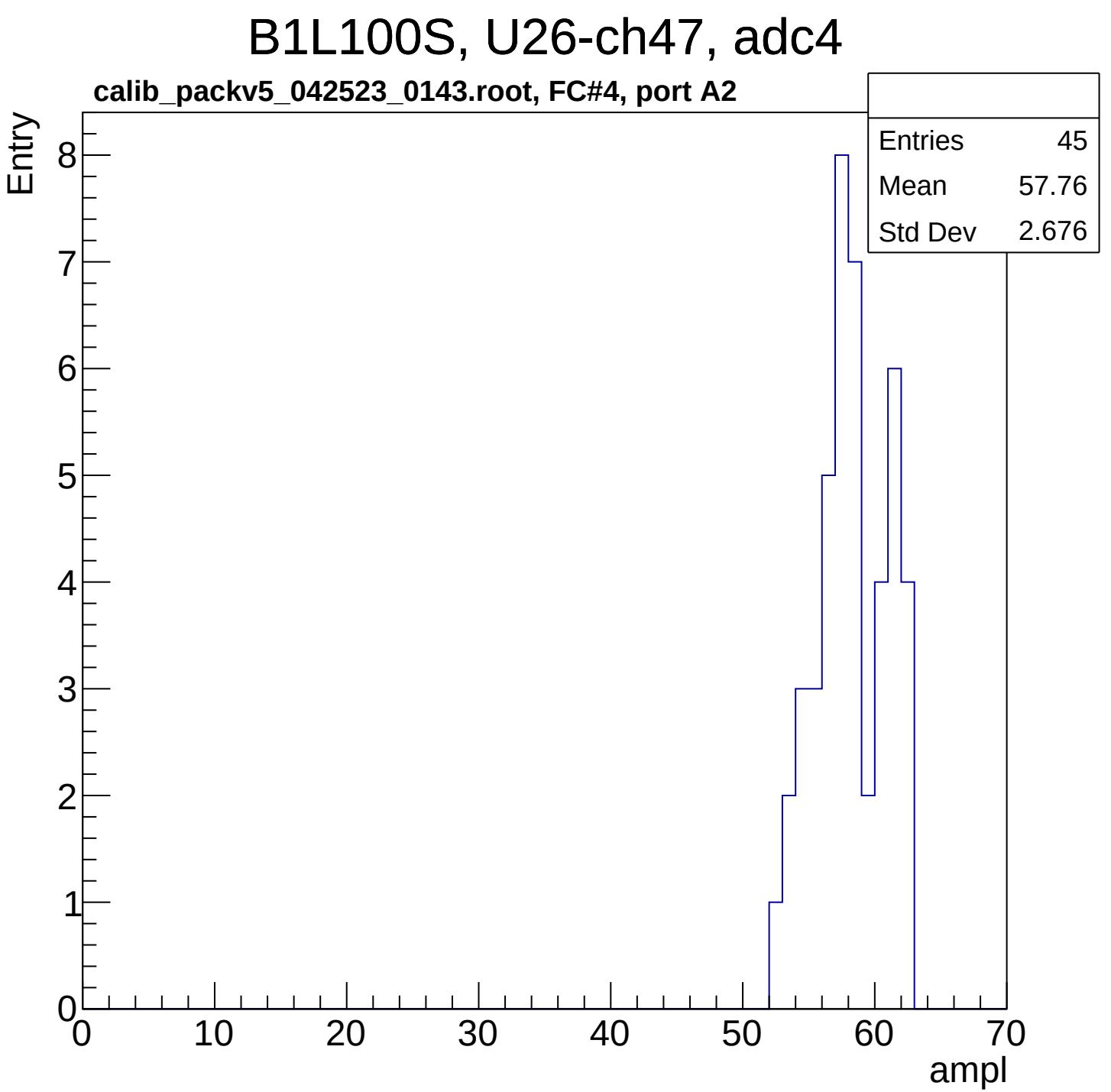
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 57.76
Std Dev 2.676
Entry
ampl
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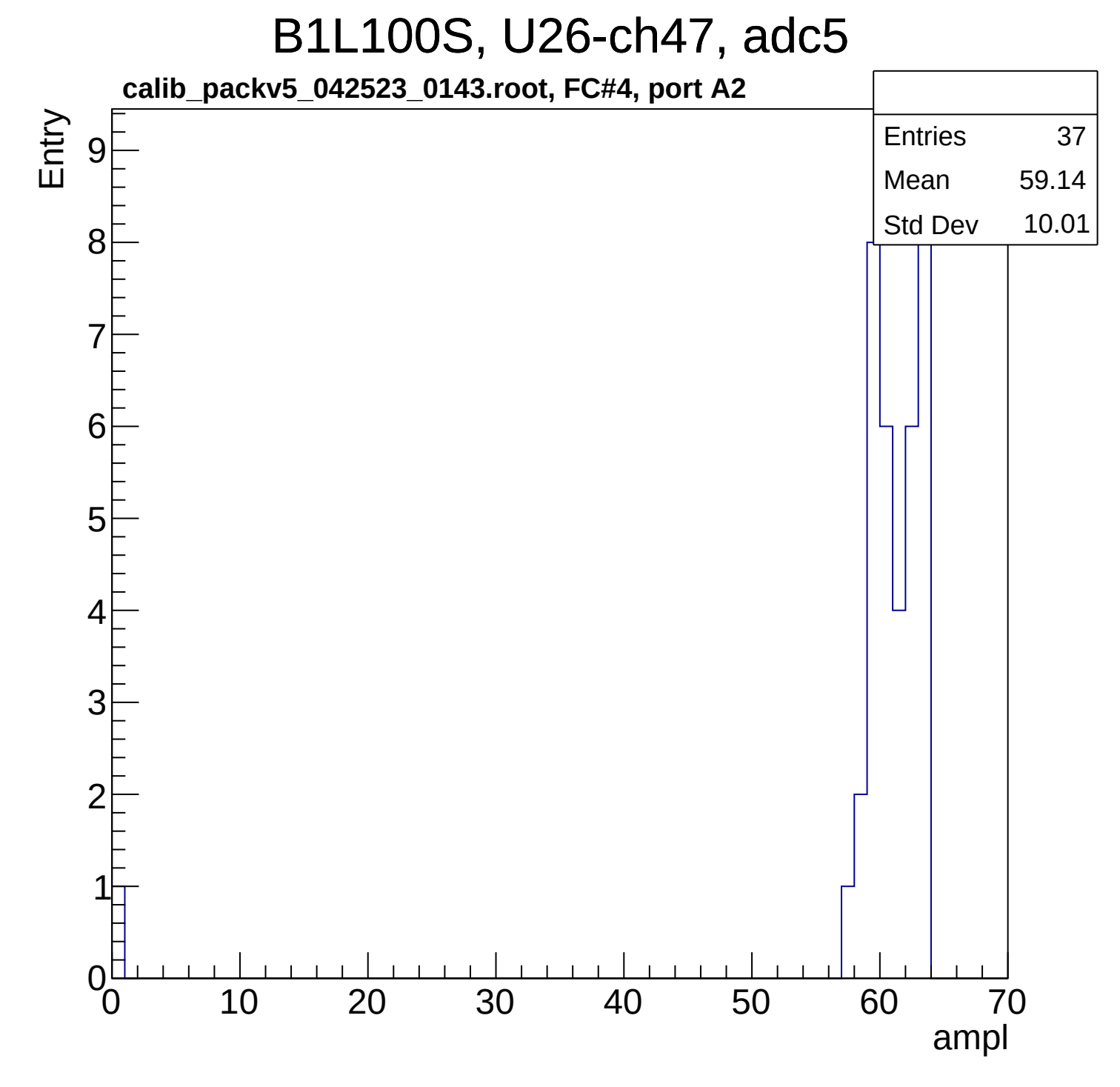
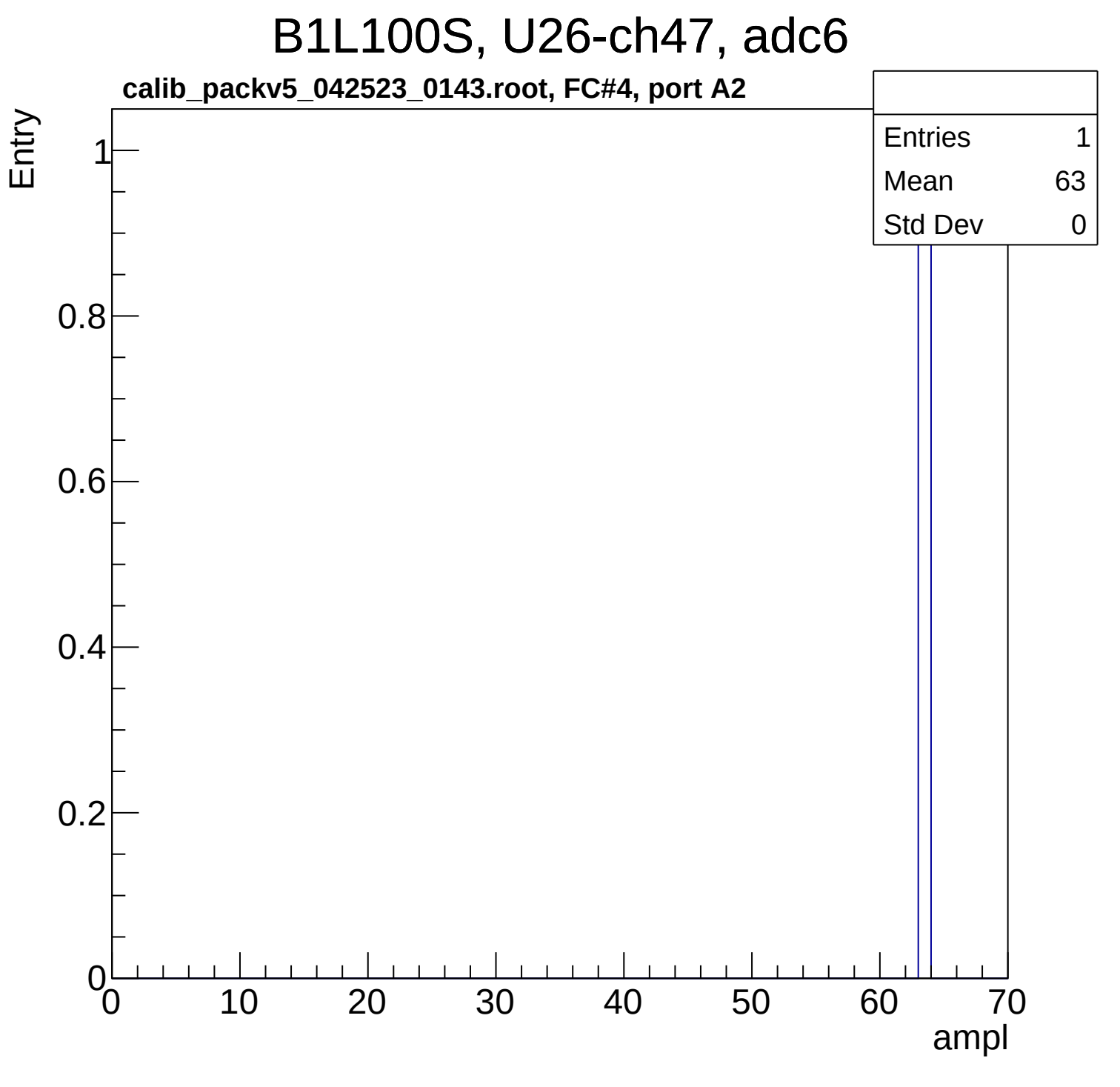
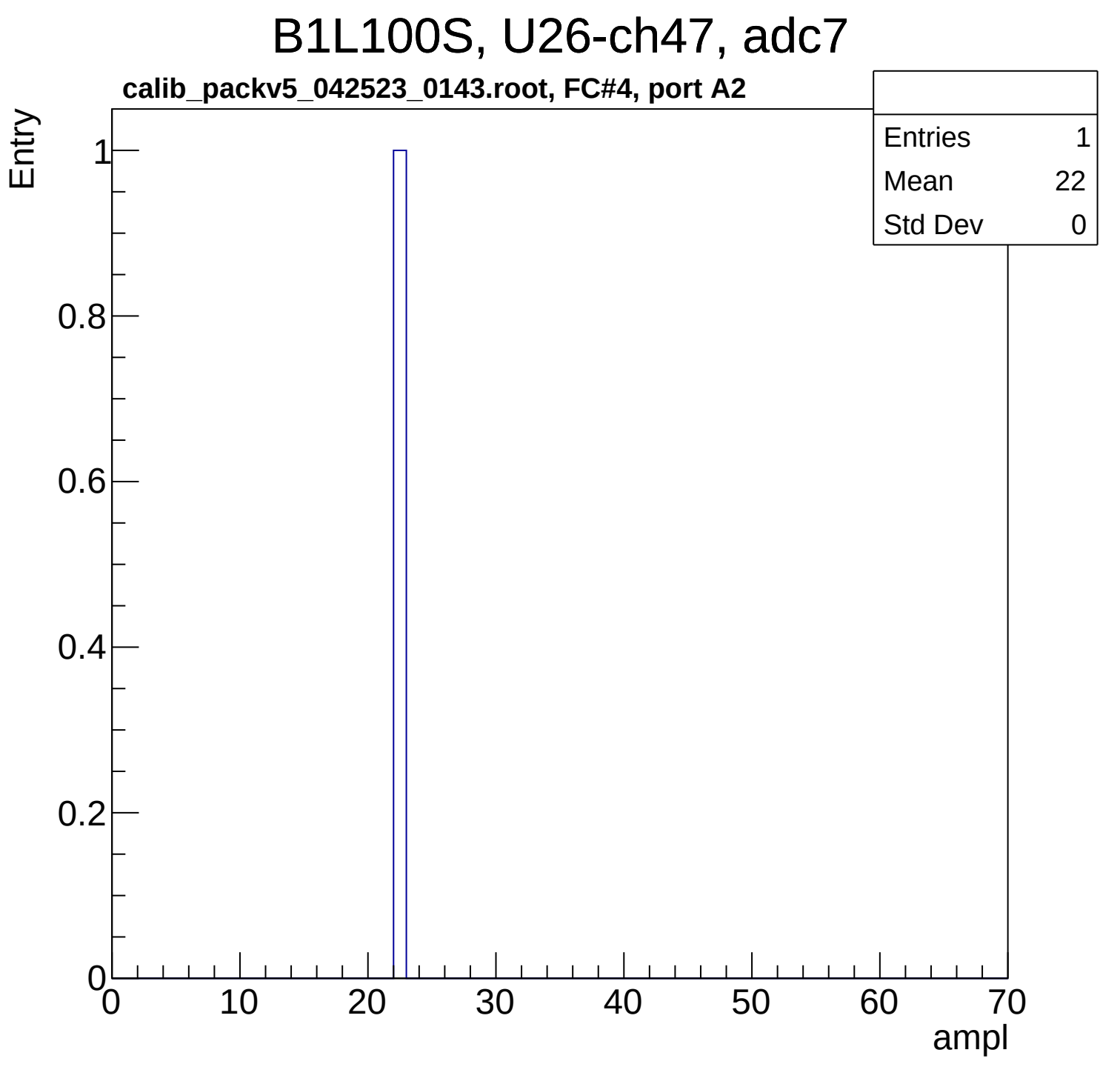
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.14
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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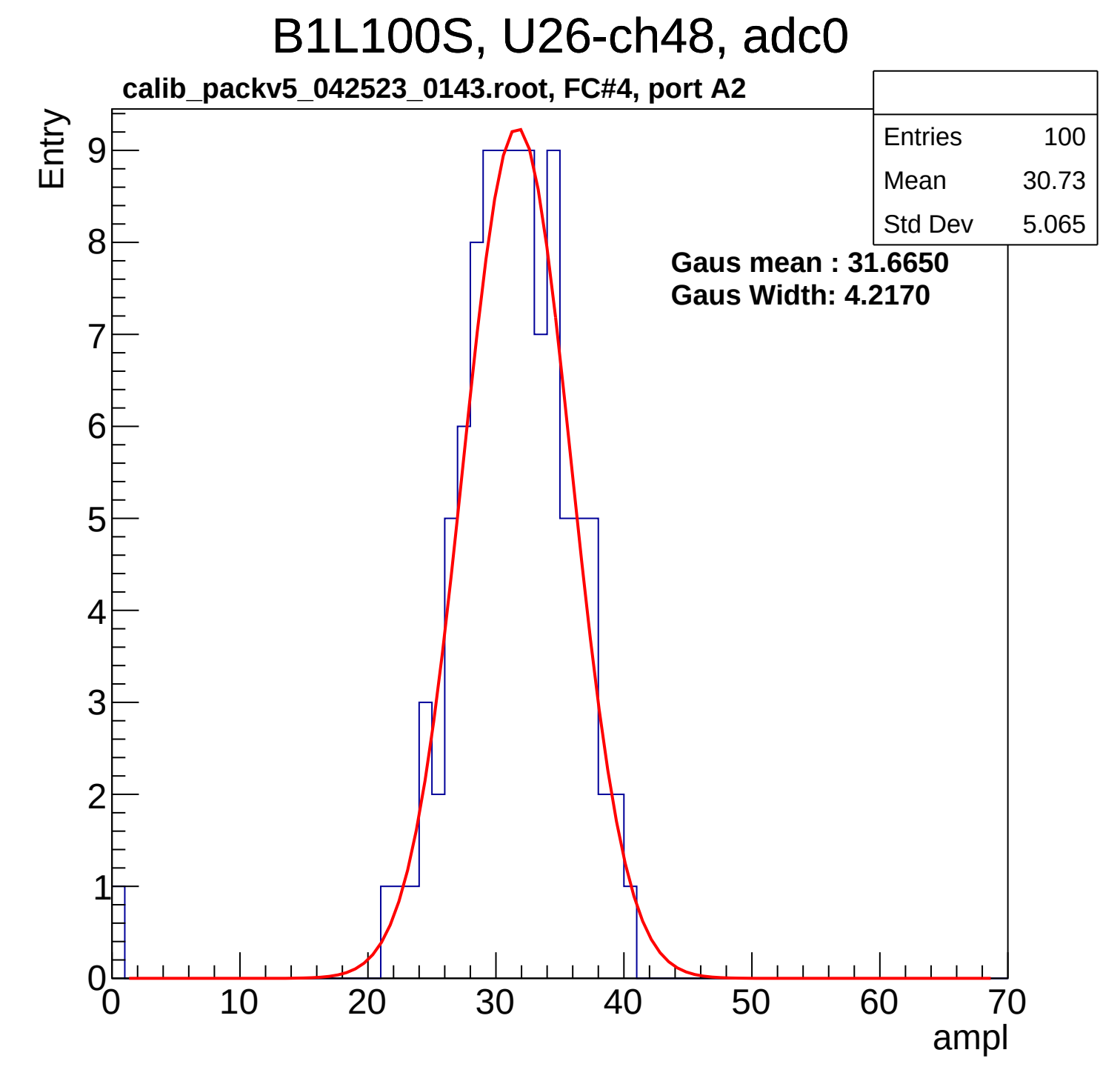
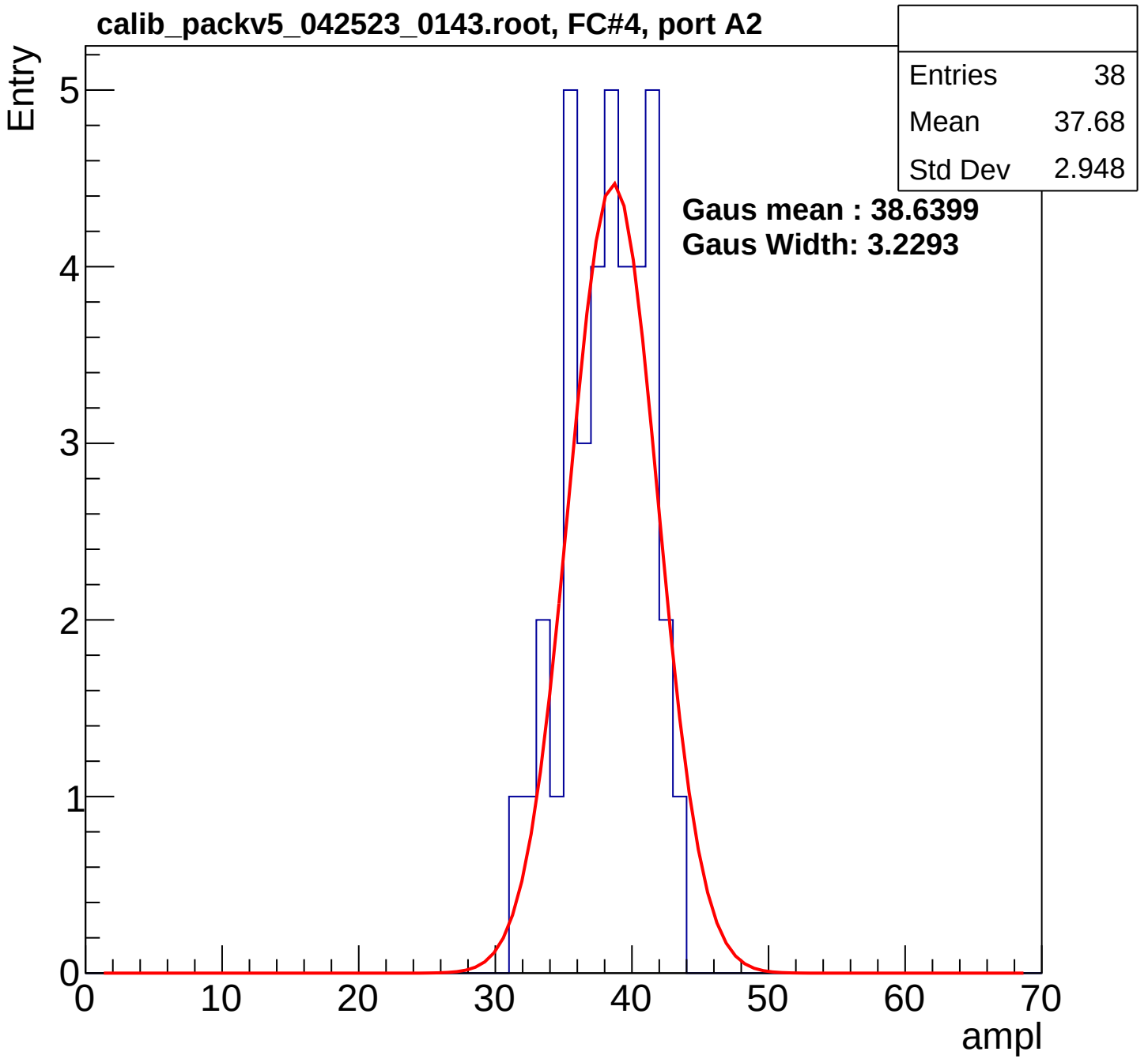
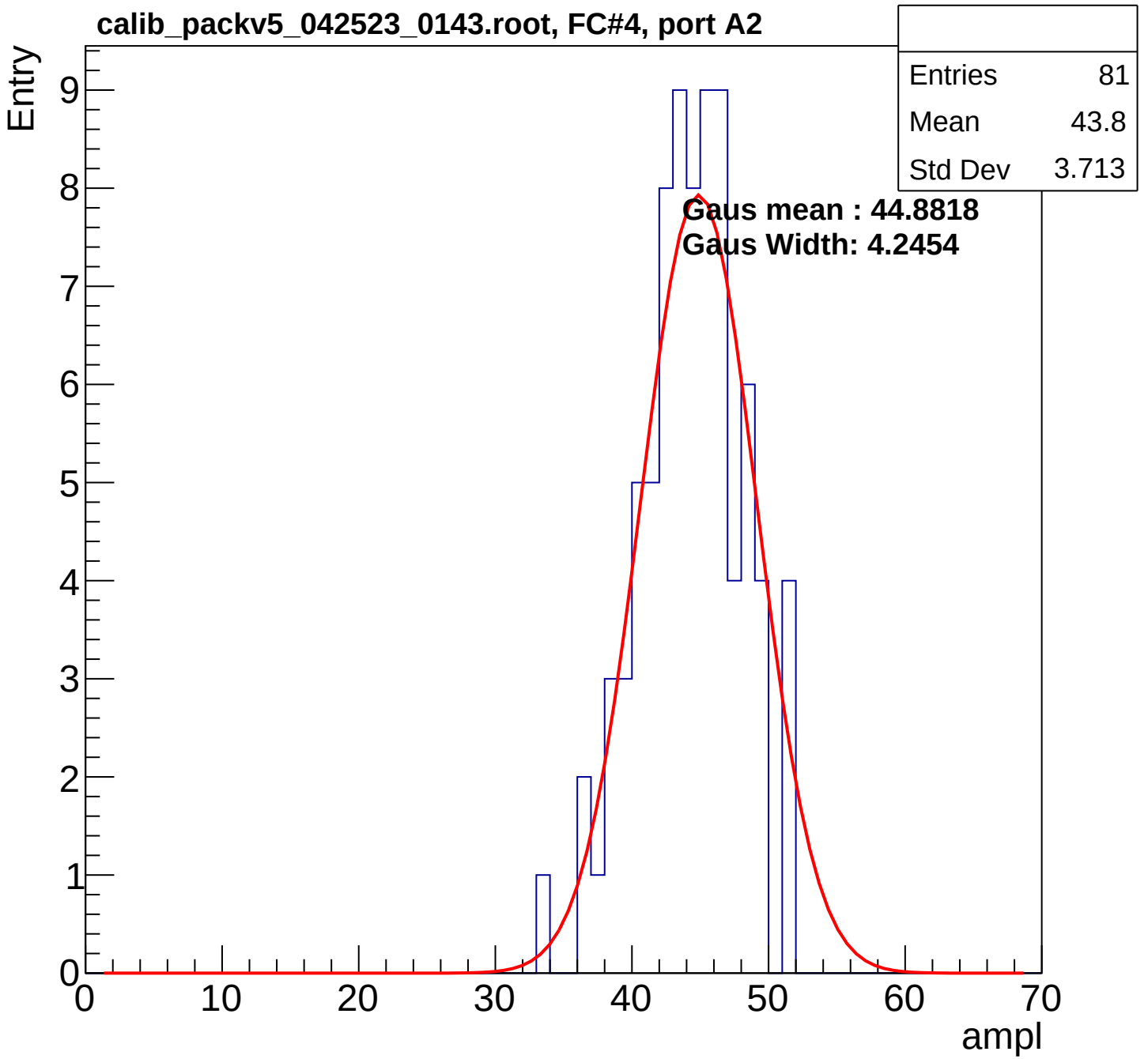
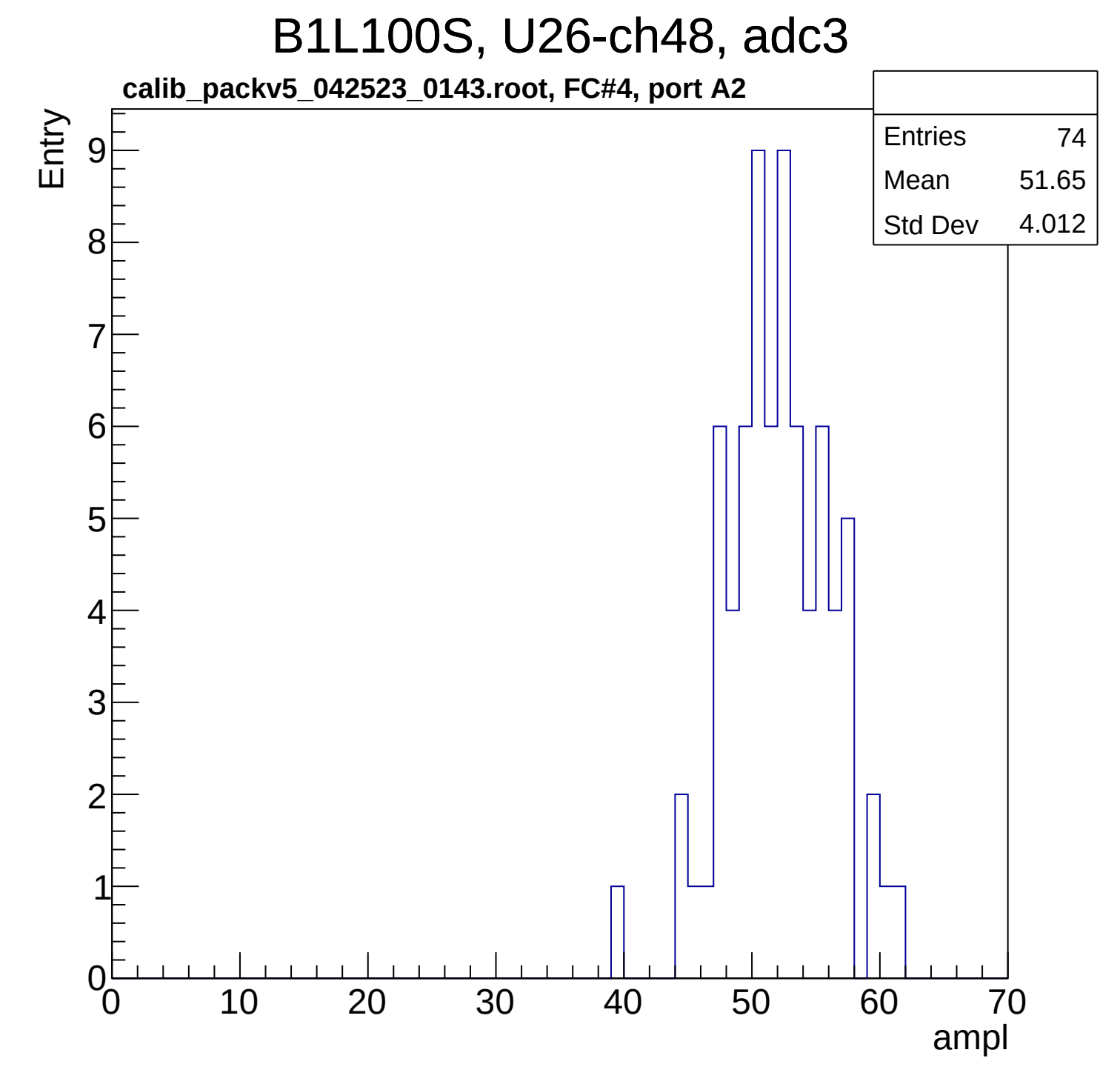
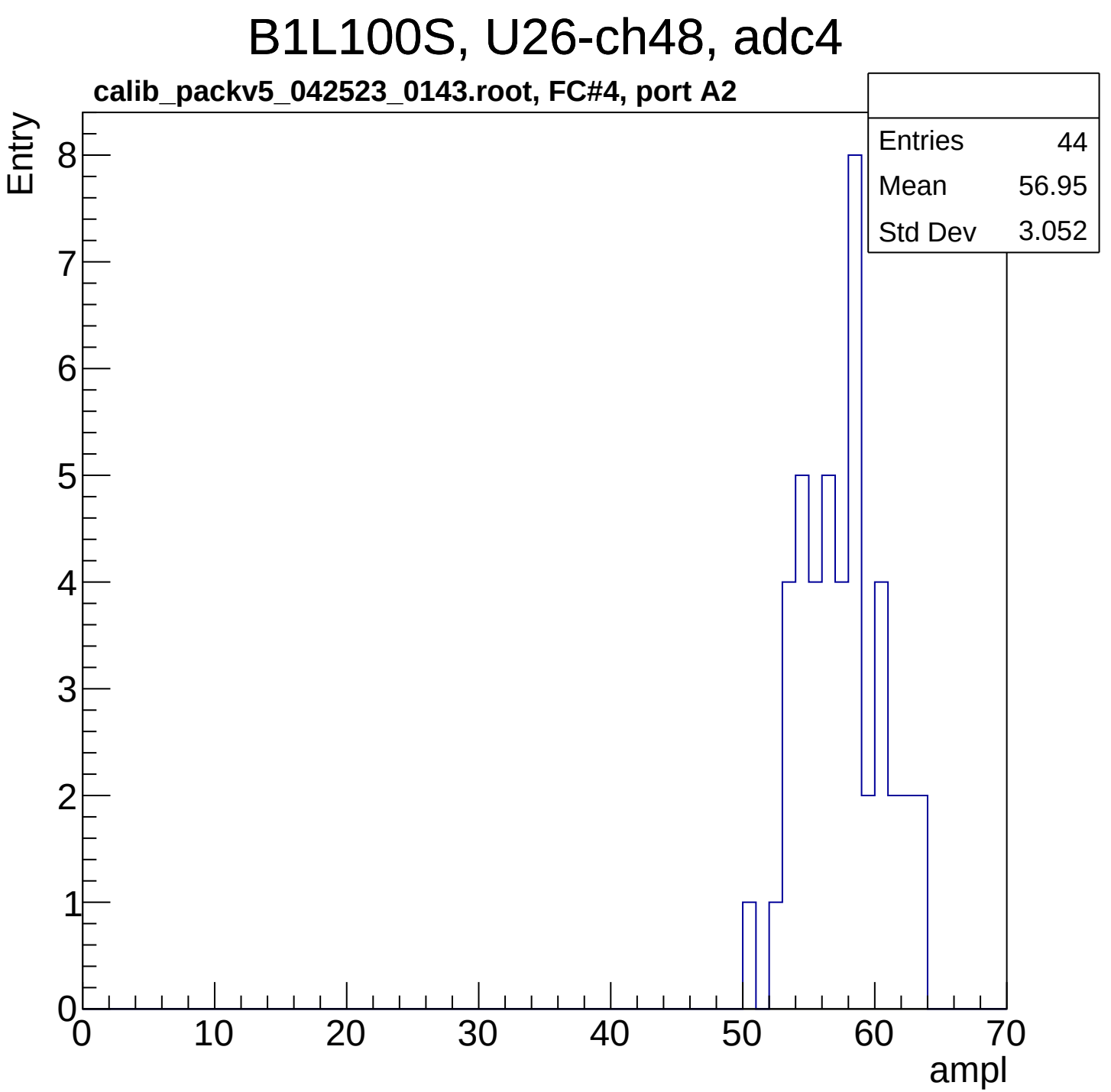
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 100
Mean 30.73
Std Dev 5.065
Gaus mean : 31.6650
Gaus Width: 4.2170
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 37.68
Std Dev 2.948
Gaus mean : 38.6399
Gaus Width: 3.2293
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 43.8
Std Dev 3.713
Gaus mean : 44.8818
Gaus Width: 4.2454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.65
Std Dev 4.012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 56.95
Std Dev 3.052
Entry
ampl
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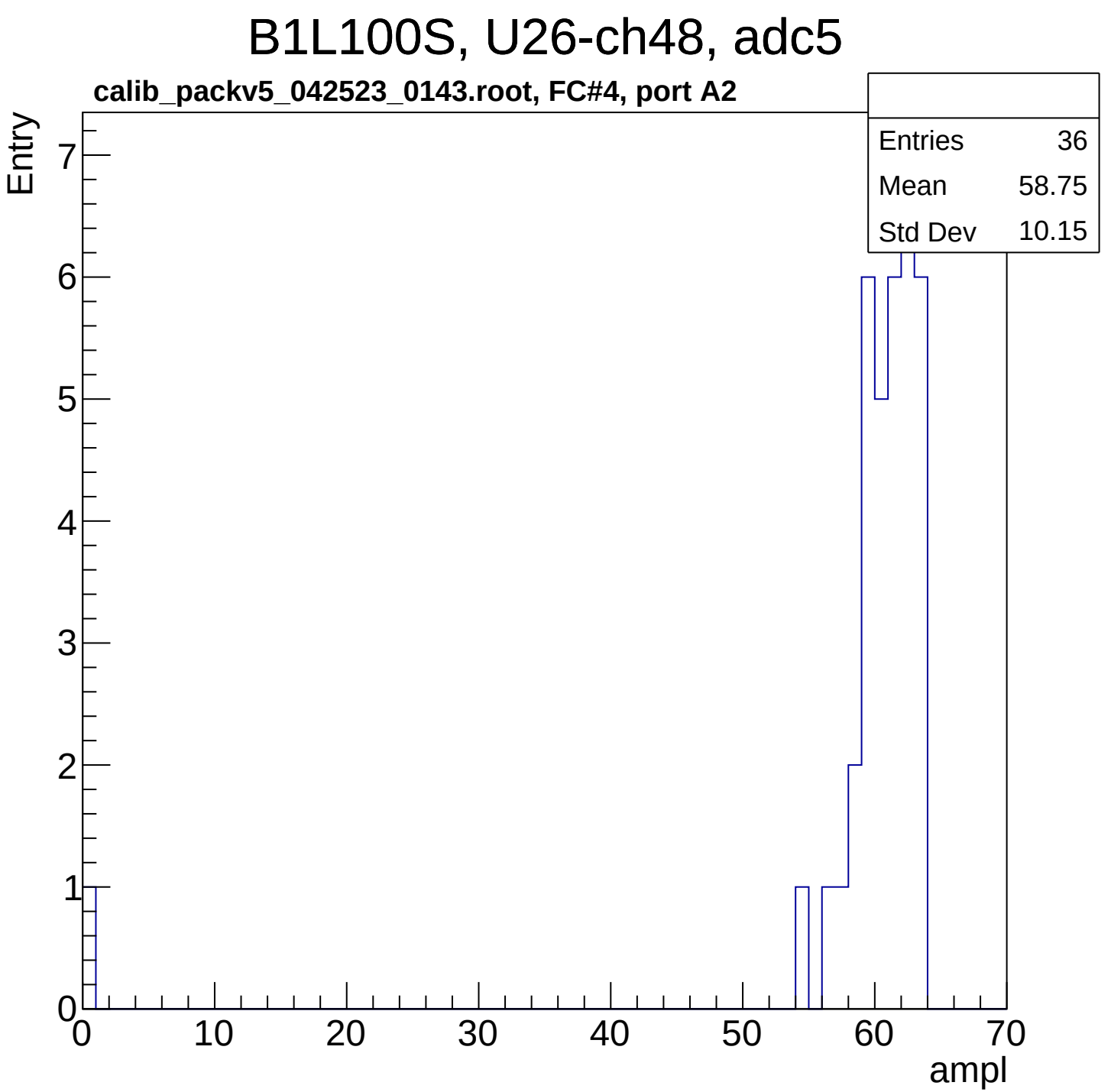
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.75
Std Dev 10.15
Entry
ampl
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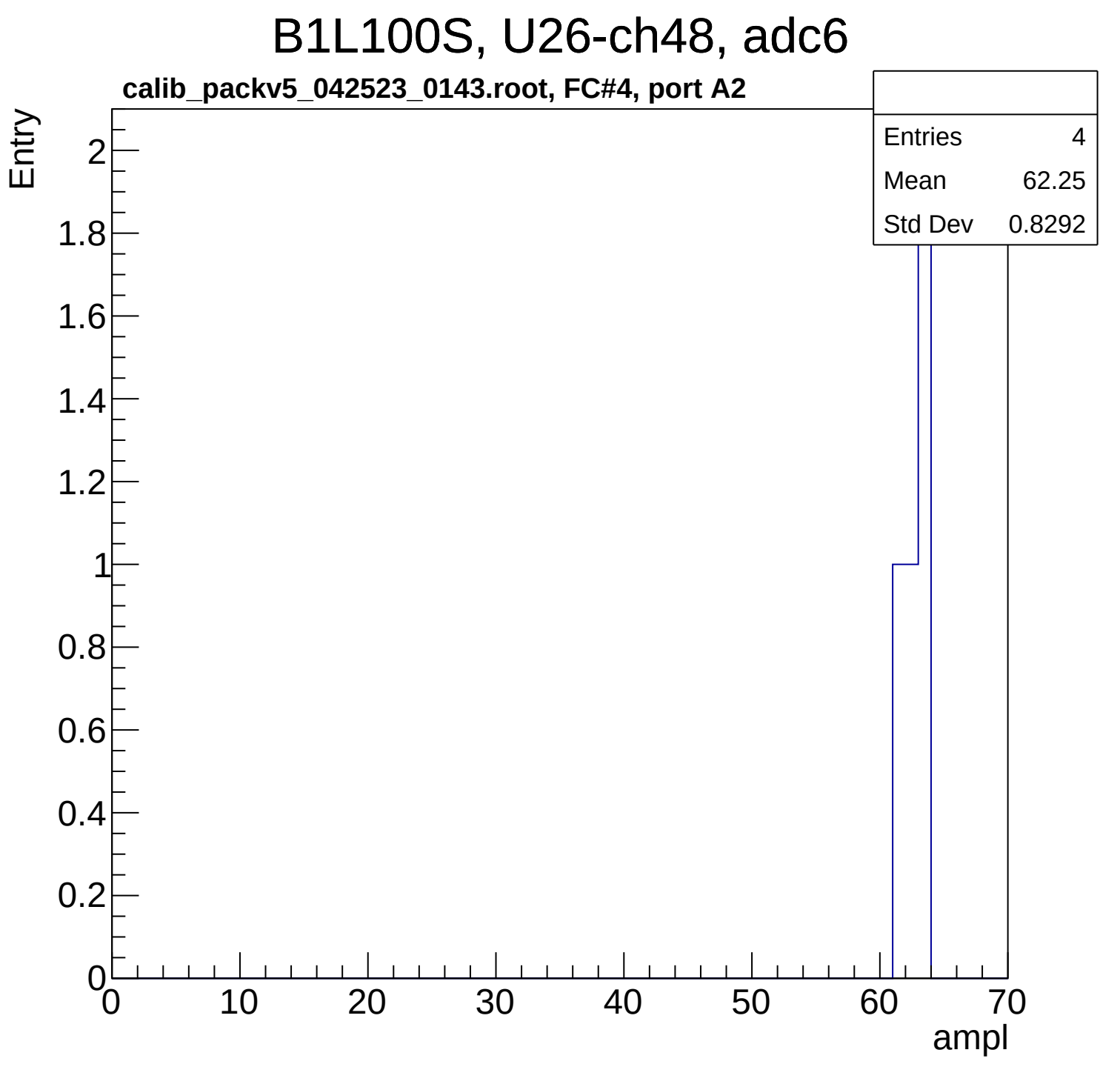
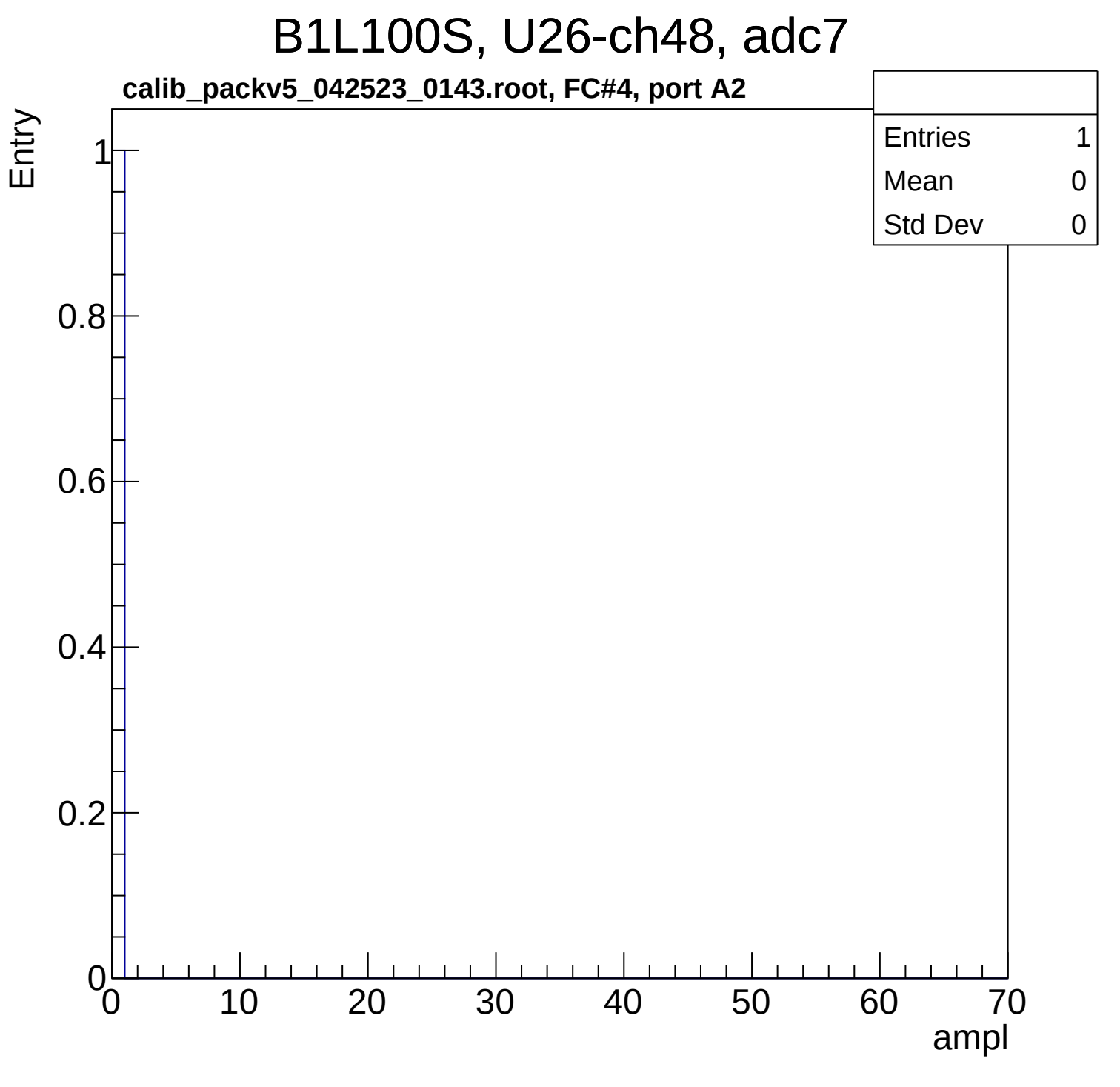
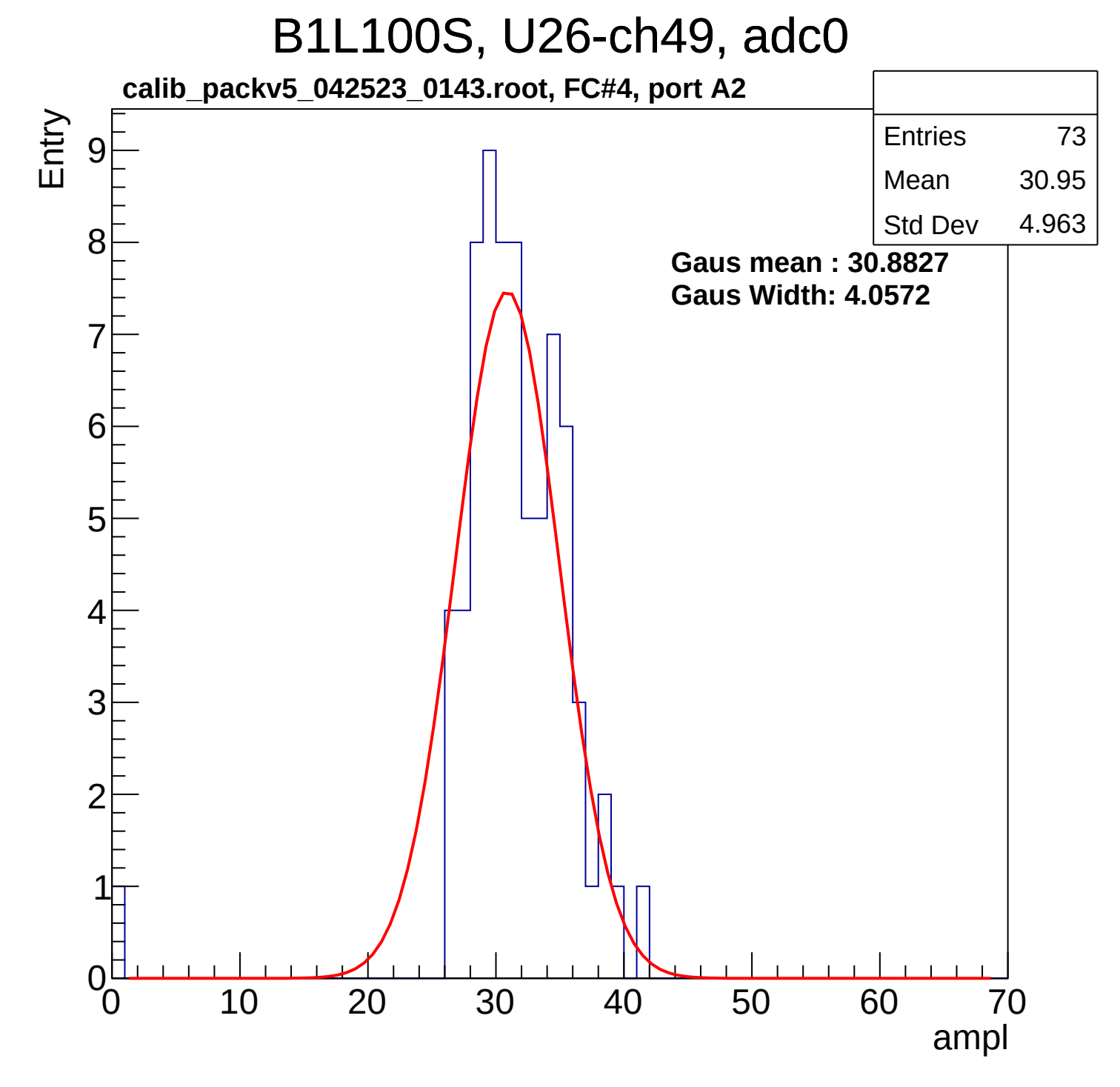
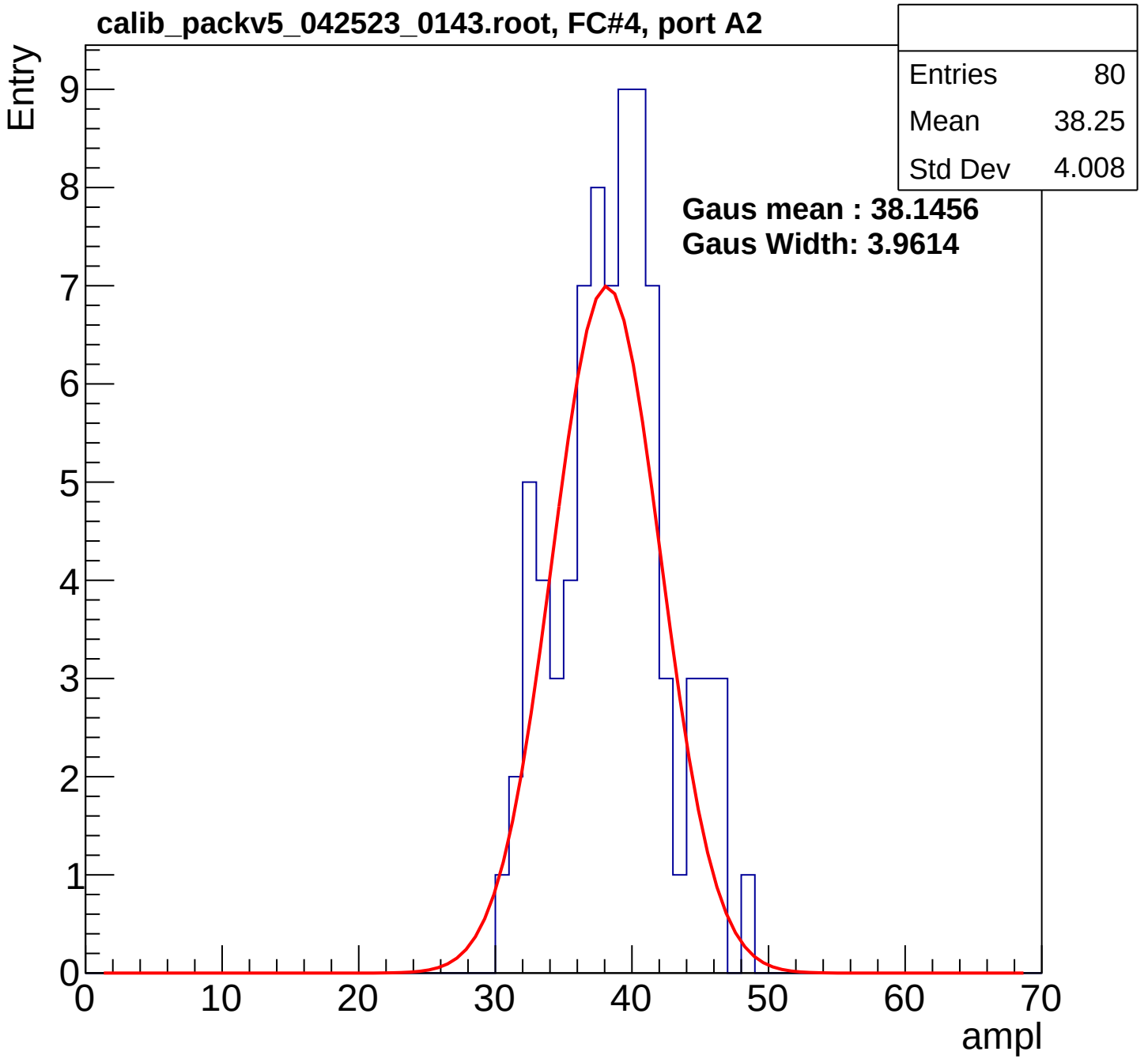
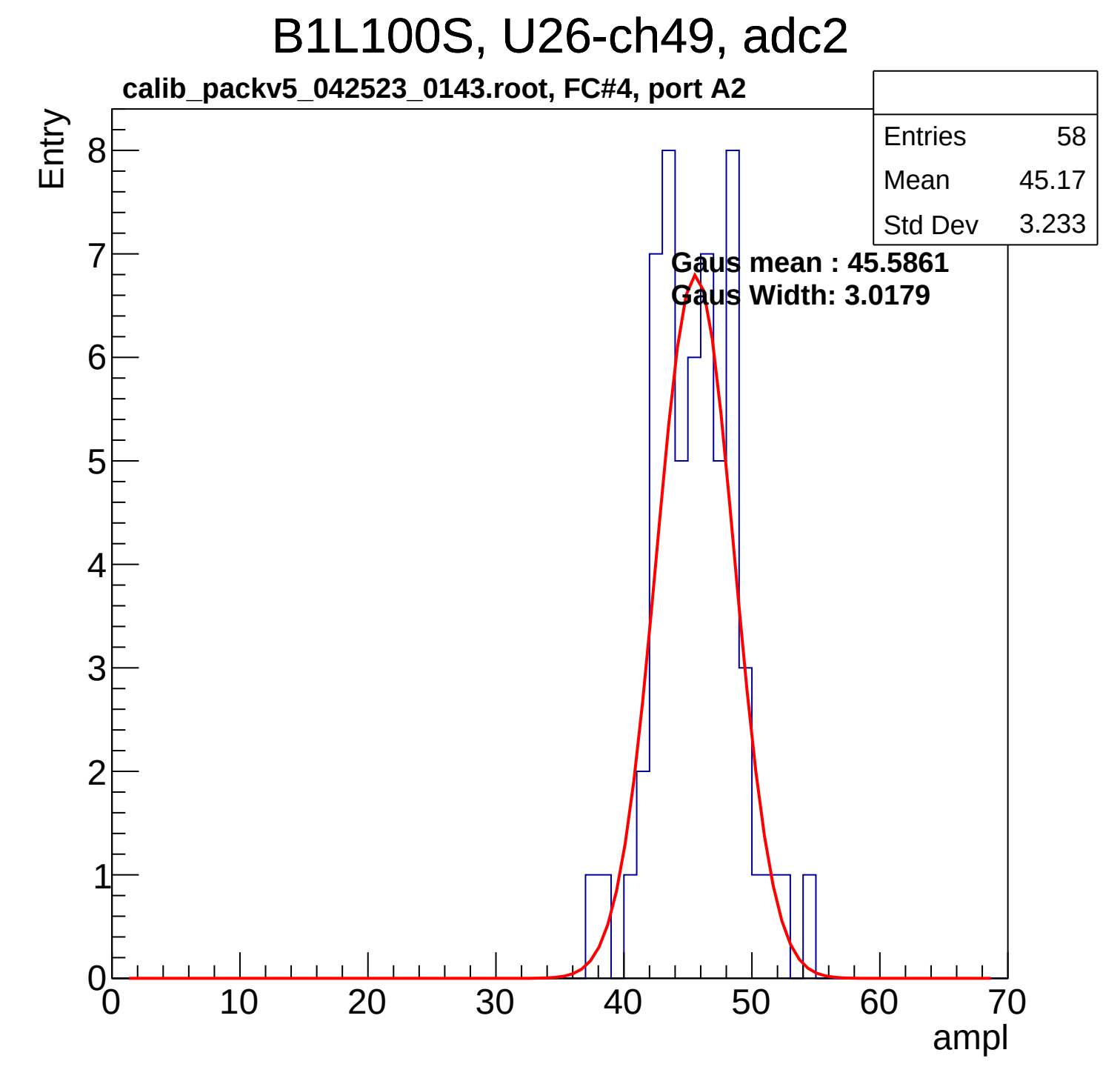
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch48, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch49, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30.95
Std Dev 4.963
Gaus mean : 30.8827
Gaus Width: 4.0572
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 38.25
Std Dev 4.008
Gaus mean : 38.1456
Gaus Width: 3.9614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 45.17
Std Dev 3.233
Gaus mean : 45.5861
Gaus Width: 3.0179
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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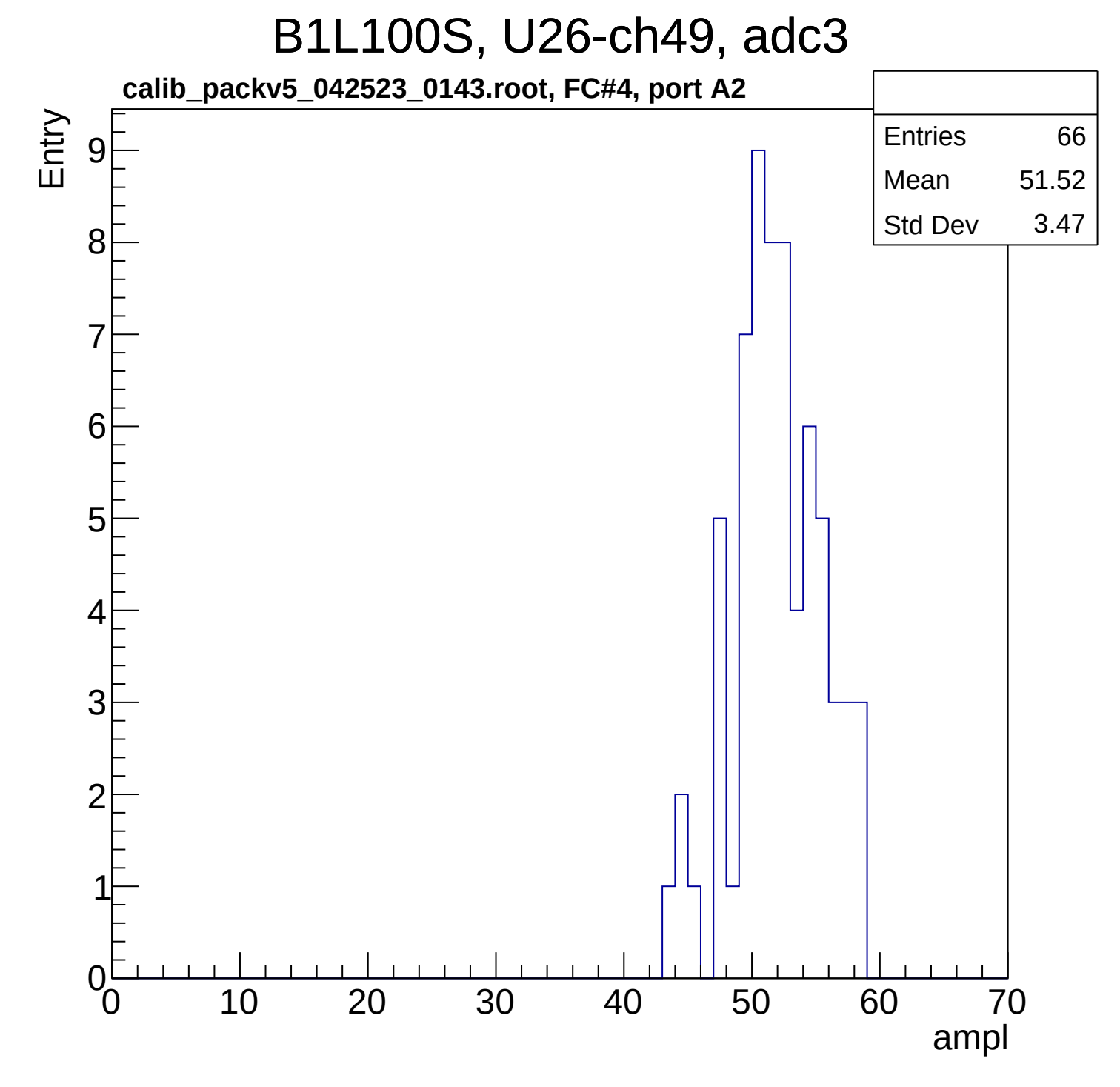
B1L100S, U26-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 51.52
Std Dev 3.47
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
50
60
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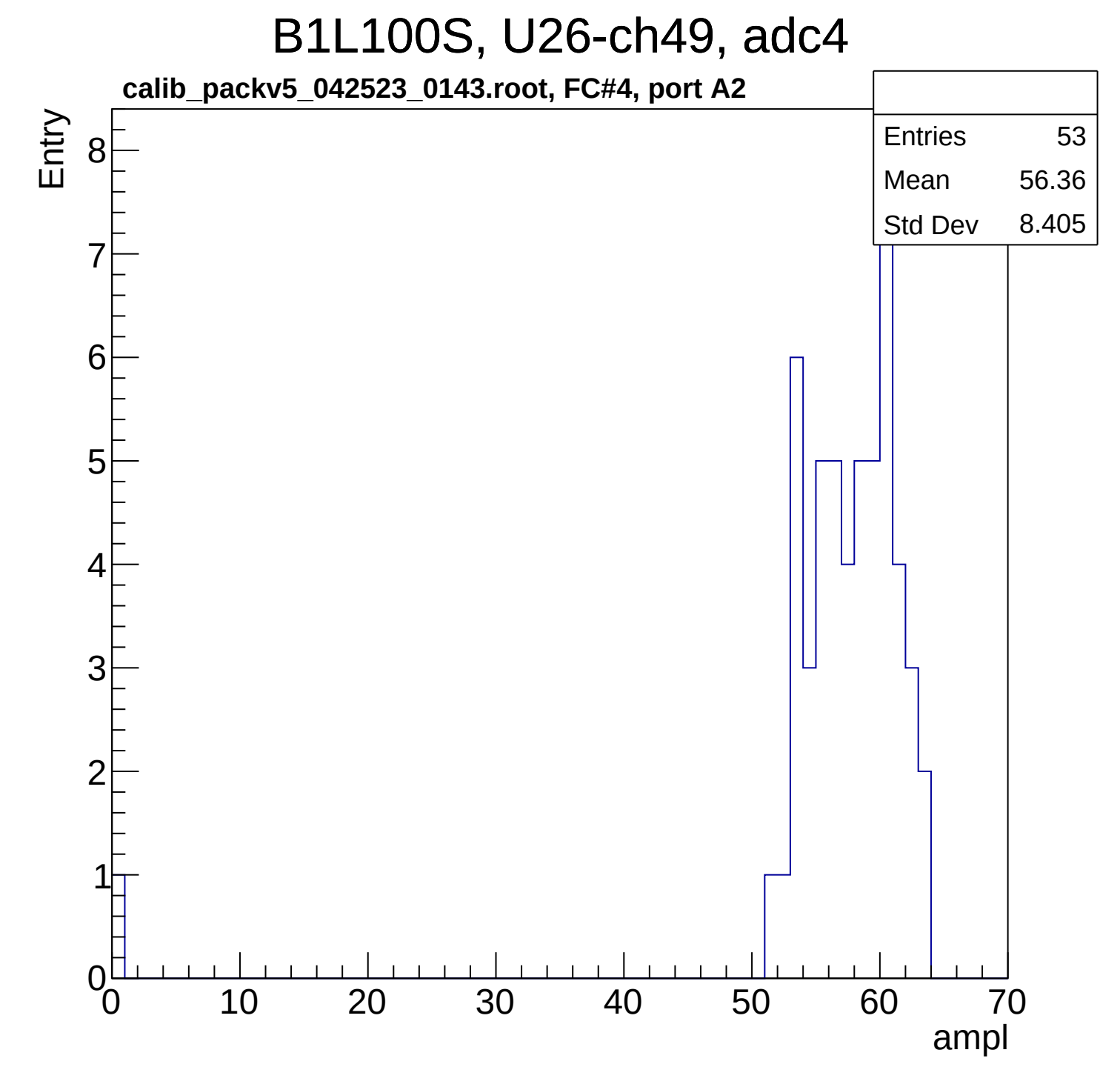
B1L100S, U26-ch49, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.36
Std Dev 8.405
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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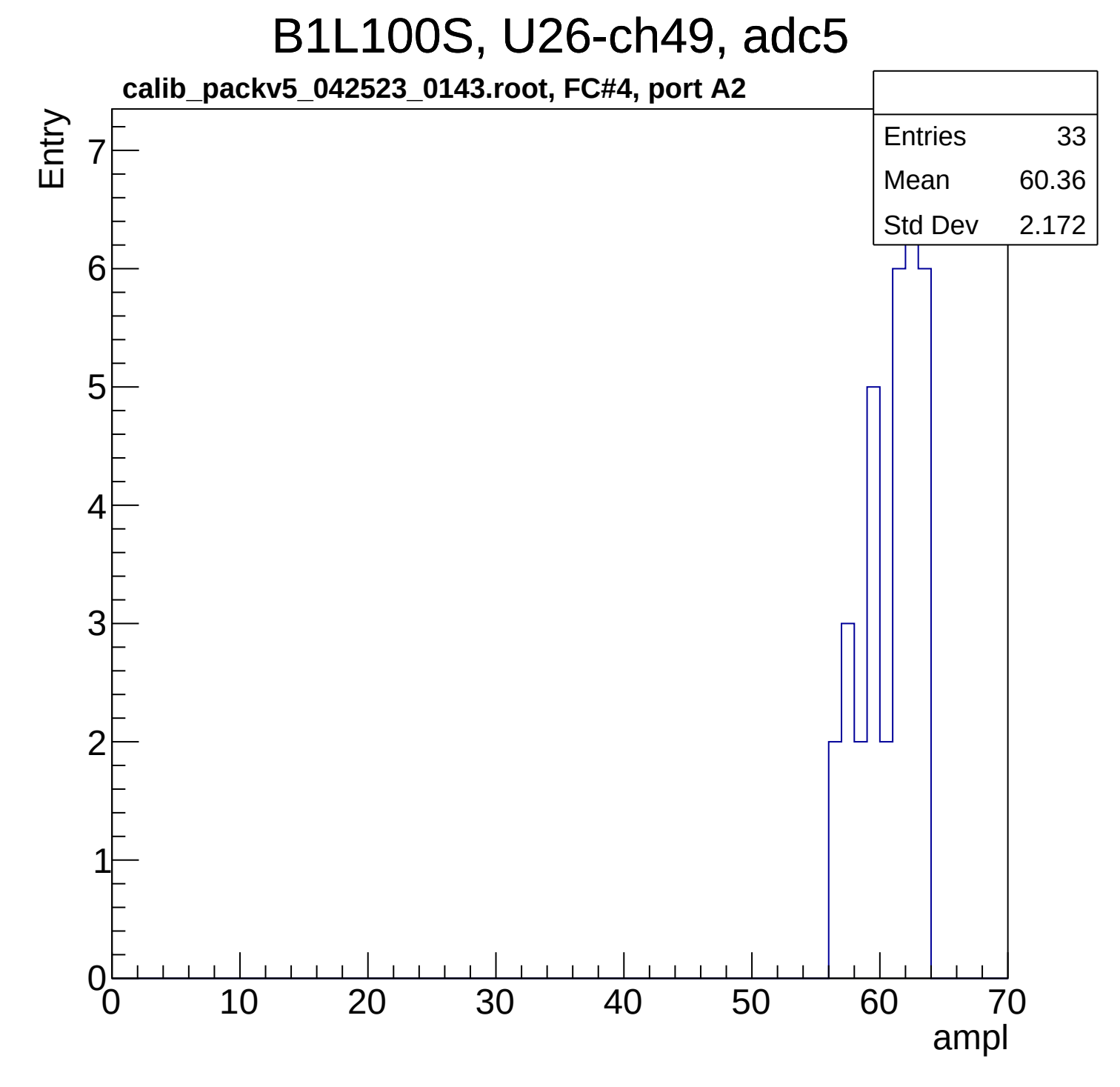
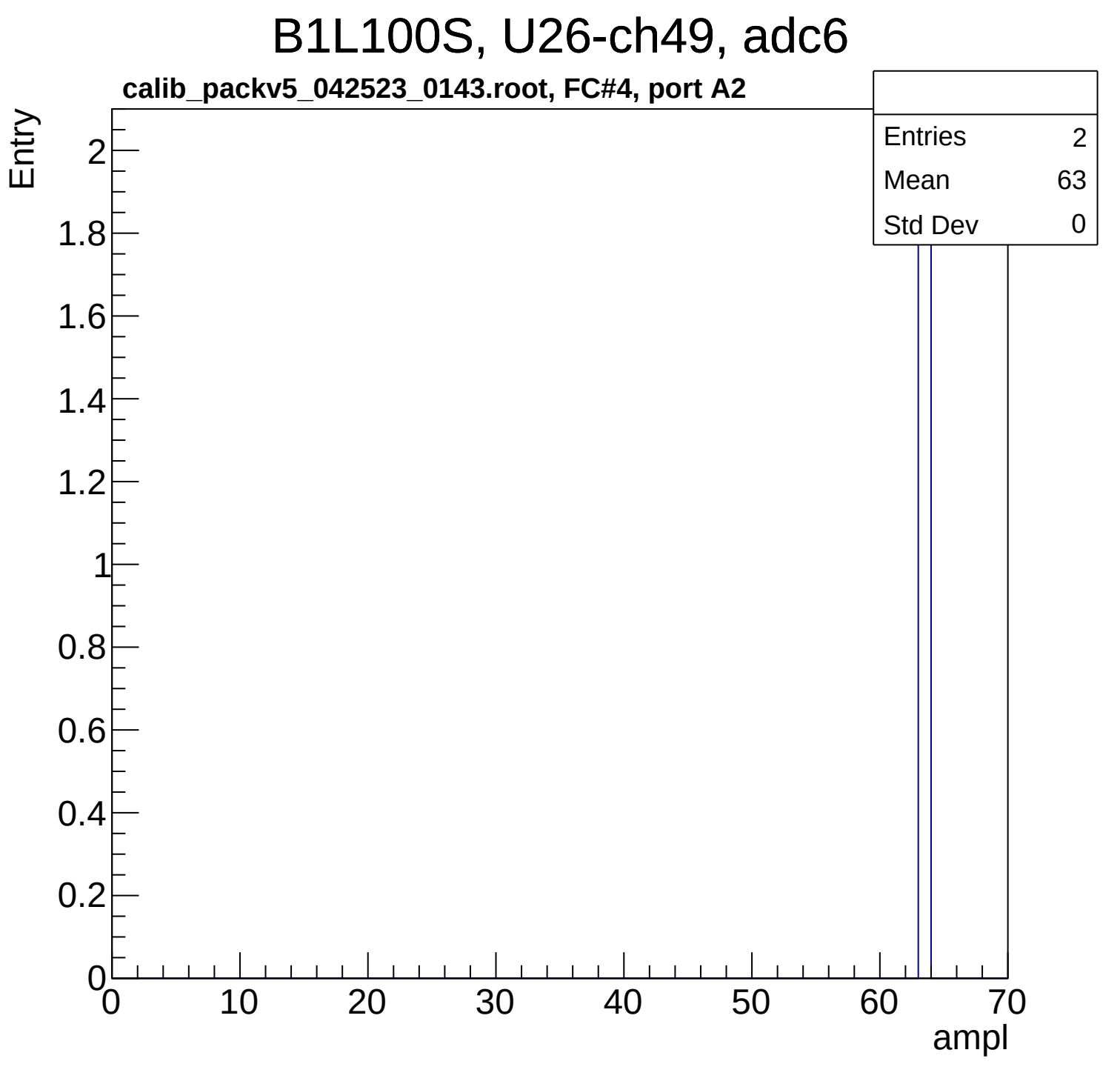
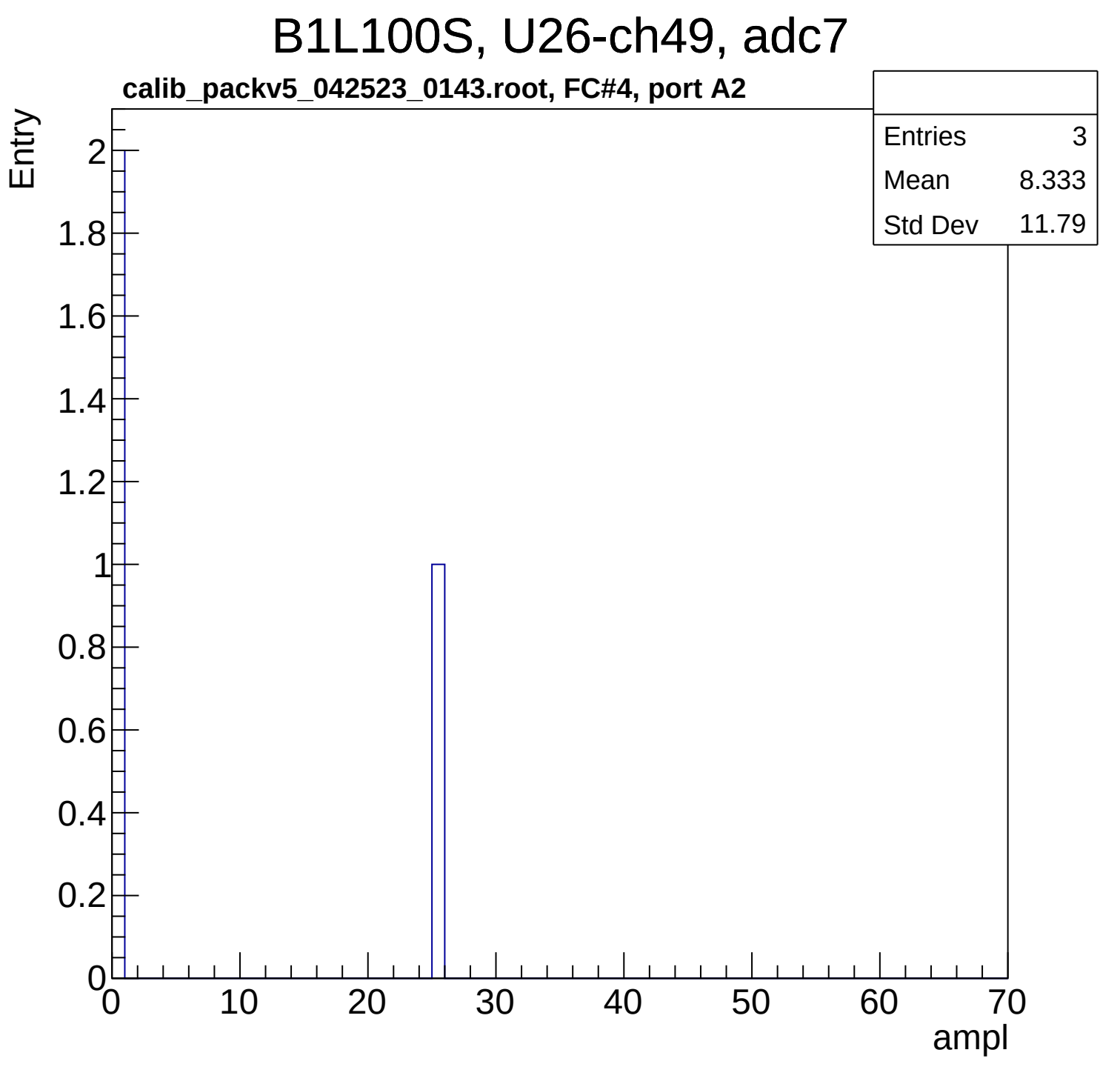

B1L100S, U26-ch49, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 60.36
Std Dev 2.172
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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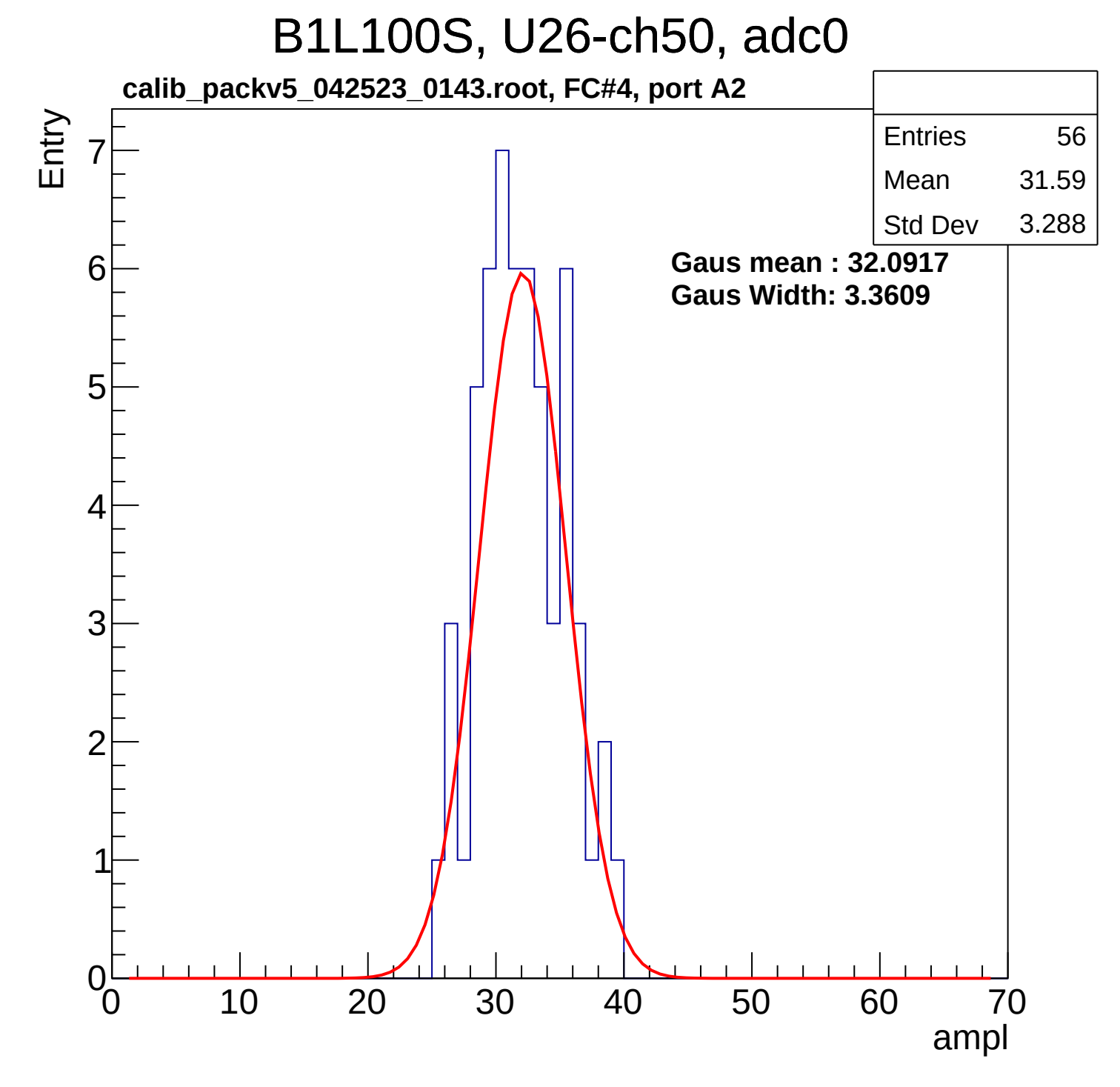
B1L100S, U26-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 31.59
Std Dev 3.288
Gaus mean : 32.0917
Gaus Width: 3.3609
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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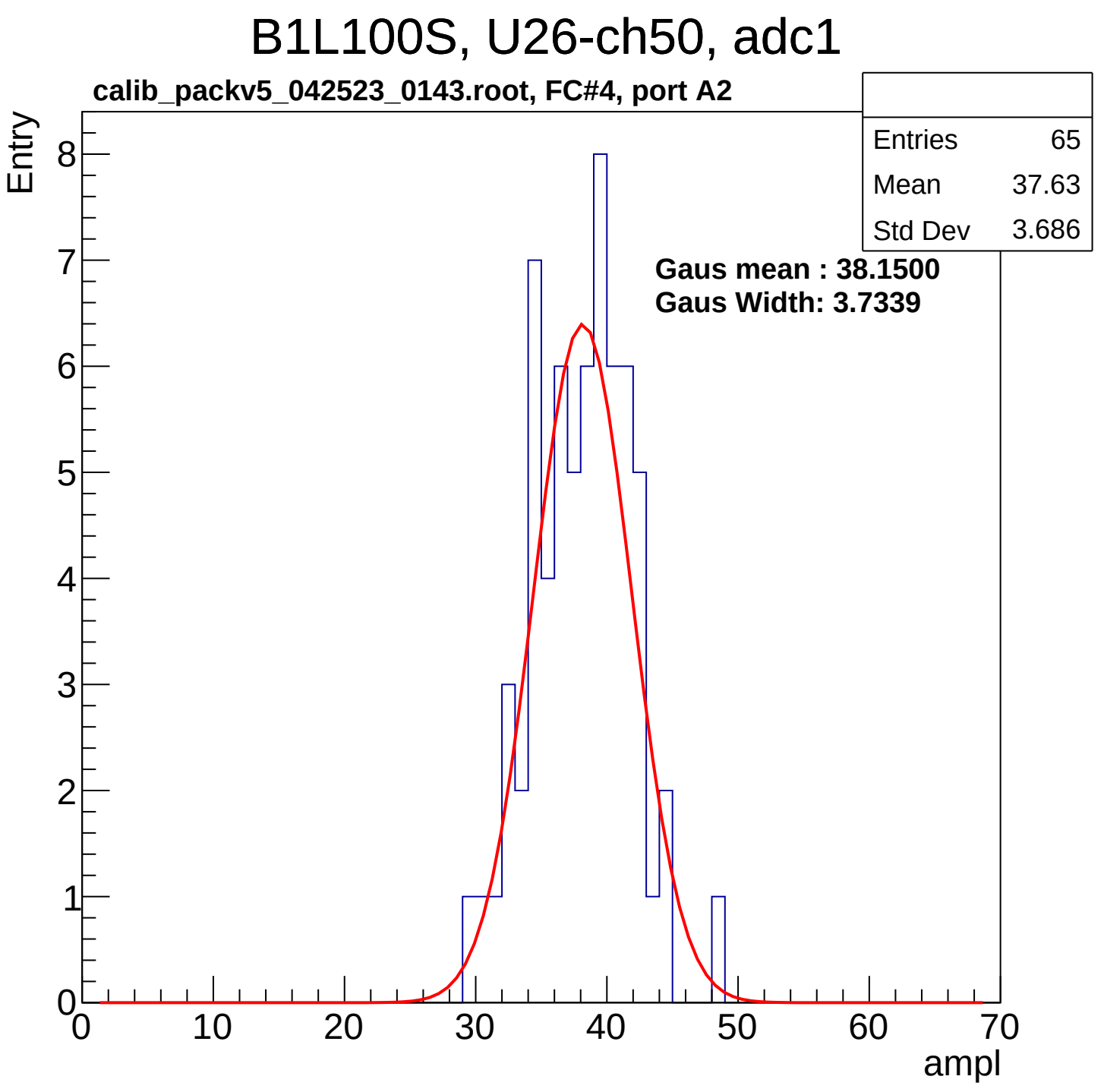
B1L100S, U26-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 37.63
Std Dev 3.686
Gaus mean : 38.1500
Gaus Width: 3.7339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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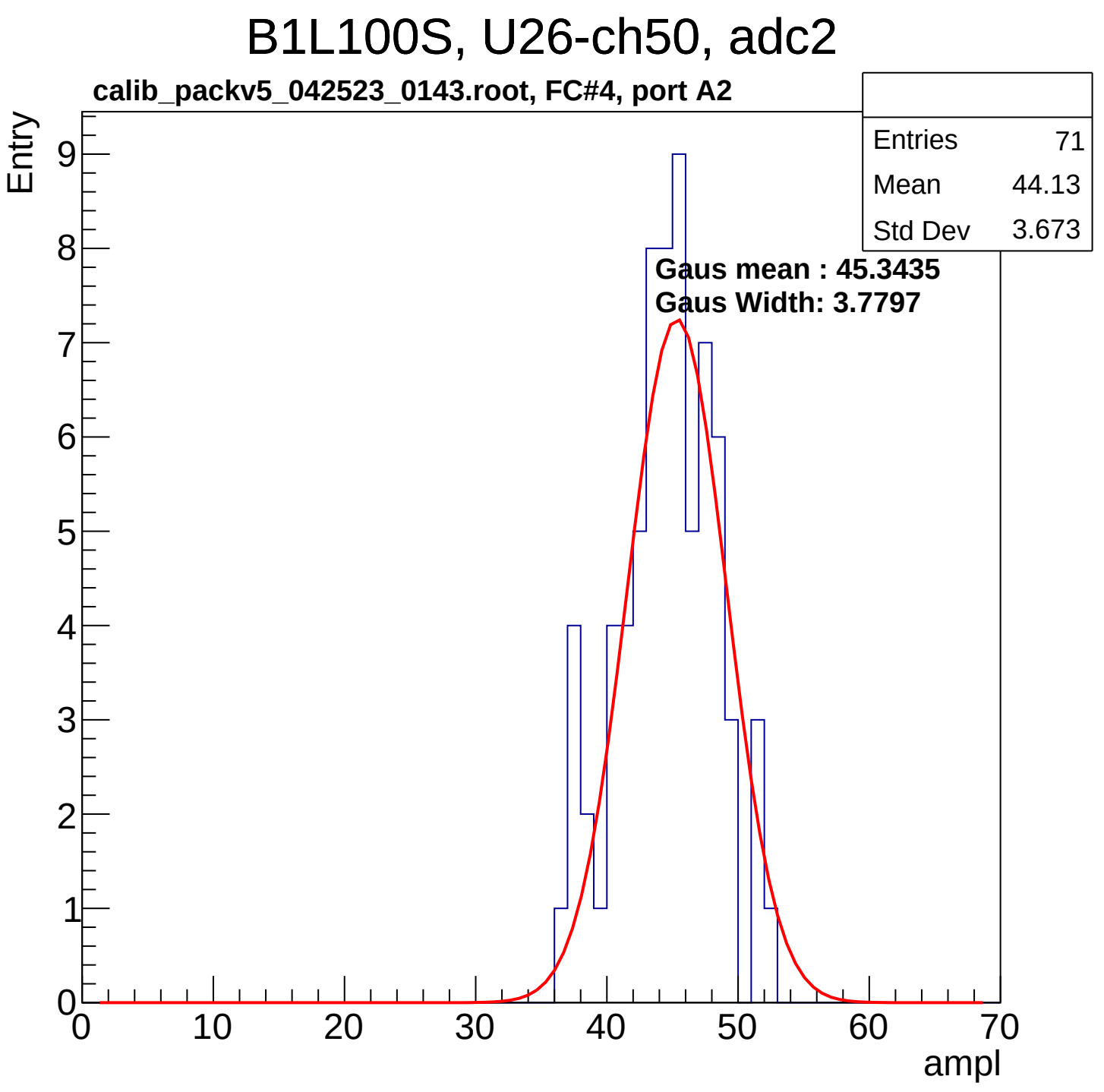
B1L100S, U26-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 44.13
Std Dev 3.673
Gaus mean : 45.3435
Gaus Width: 3.7797
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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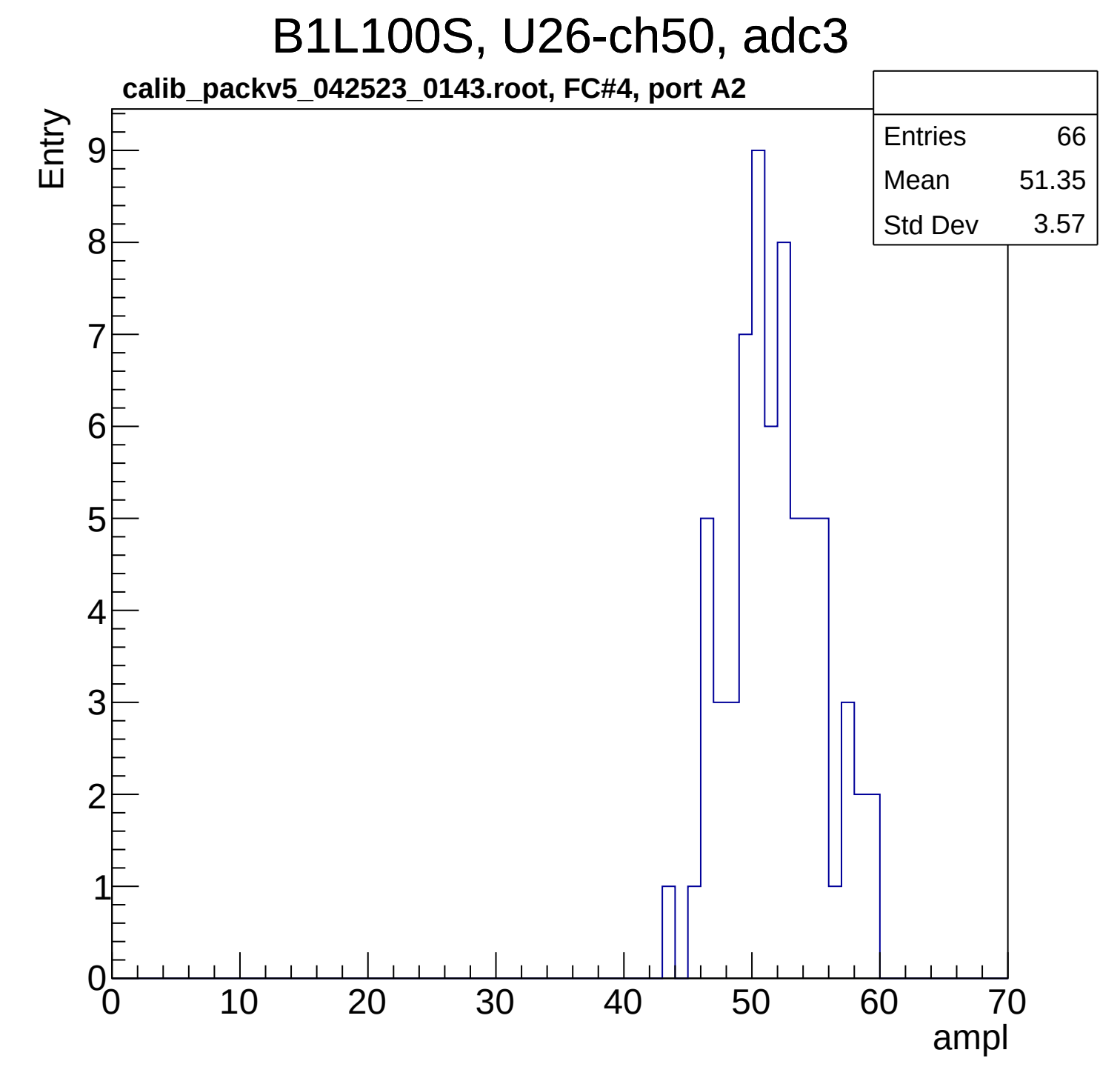
B1L100S, U26-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 51.35
Std Dev 3.57
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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40
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60
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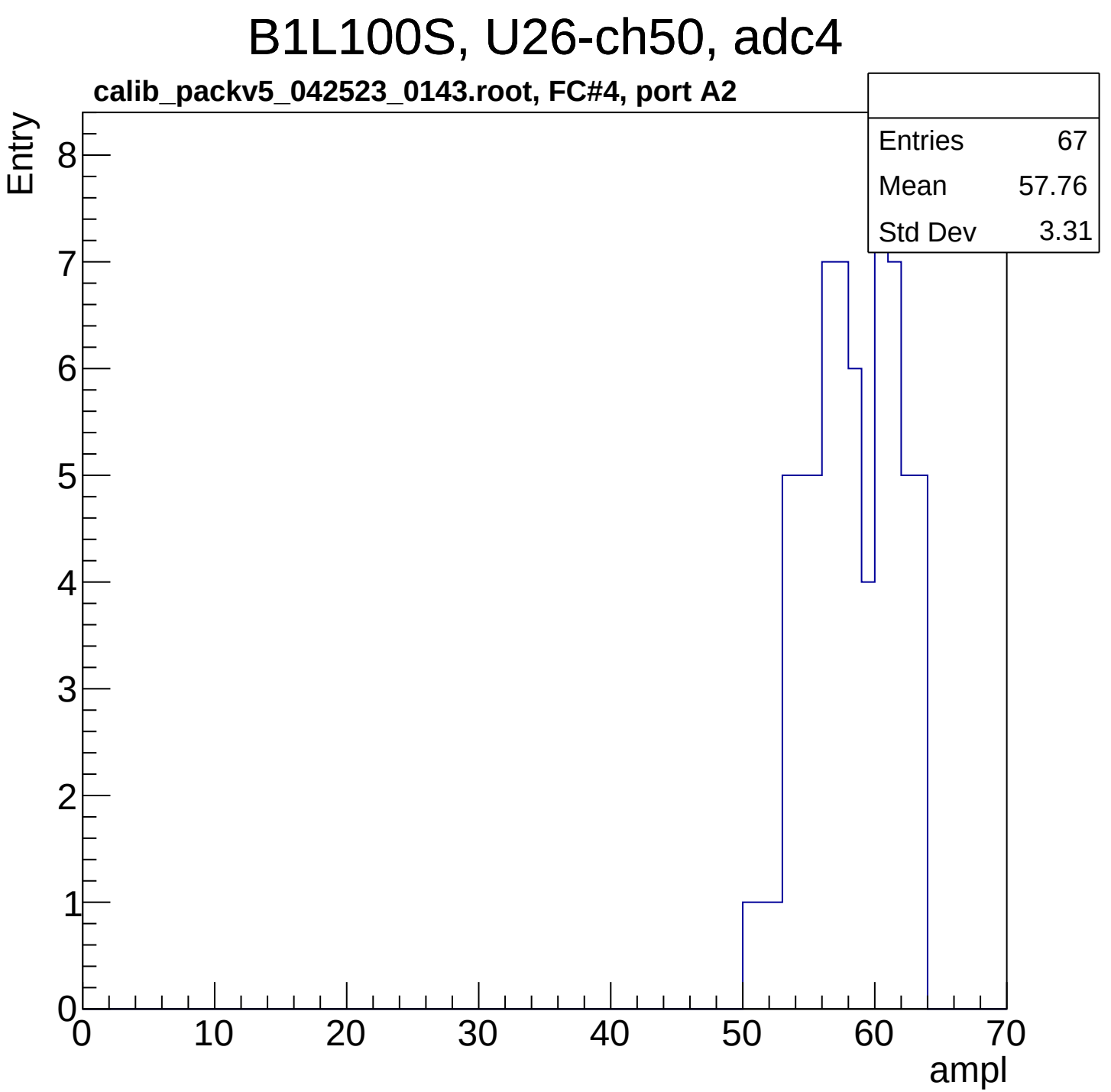
B1L100S, U26-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 57.76
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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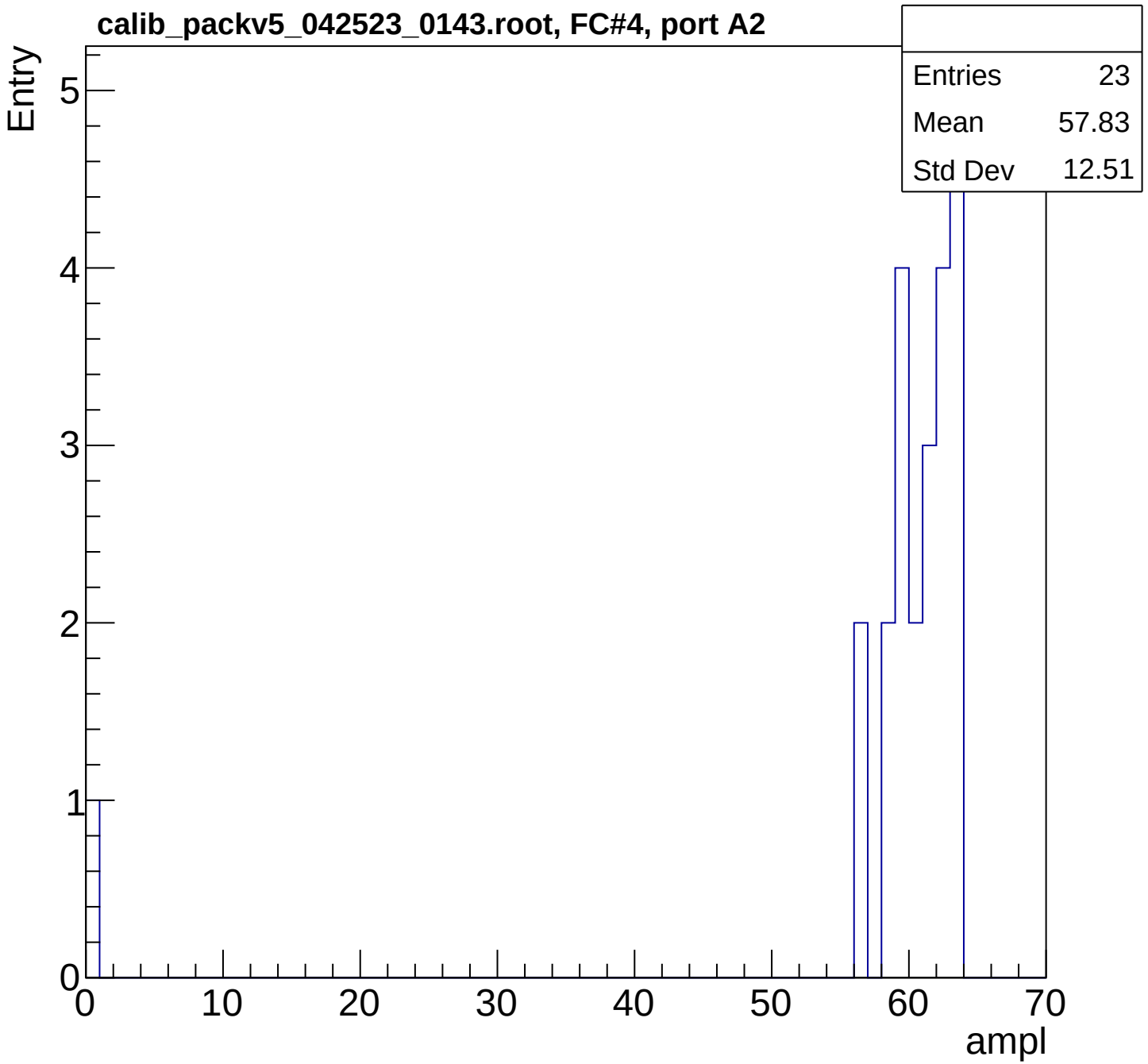
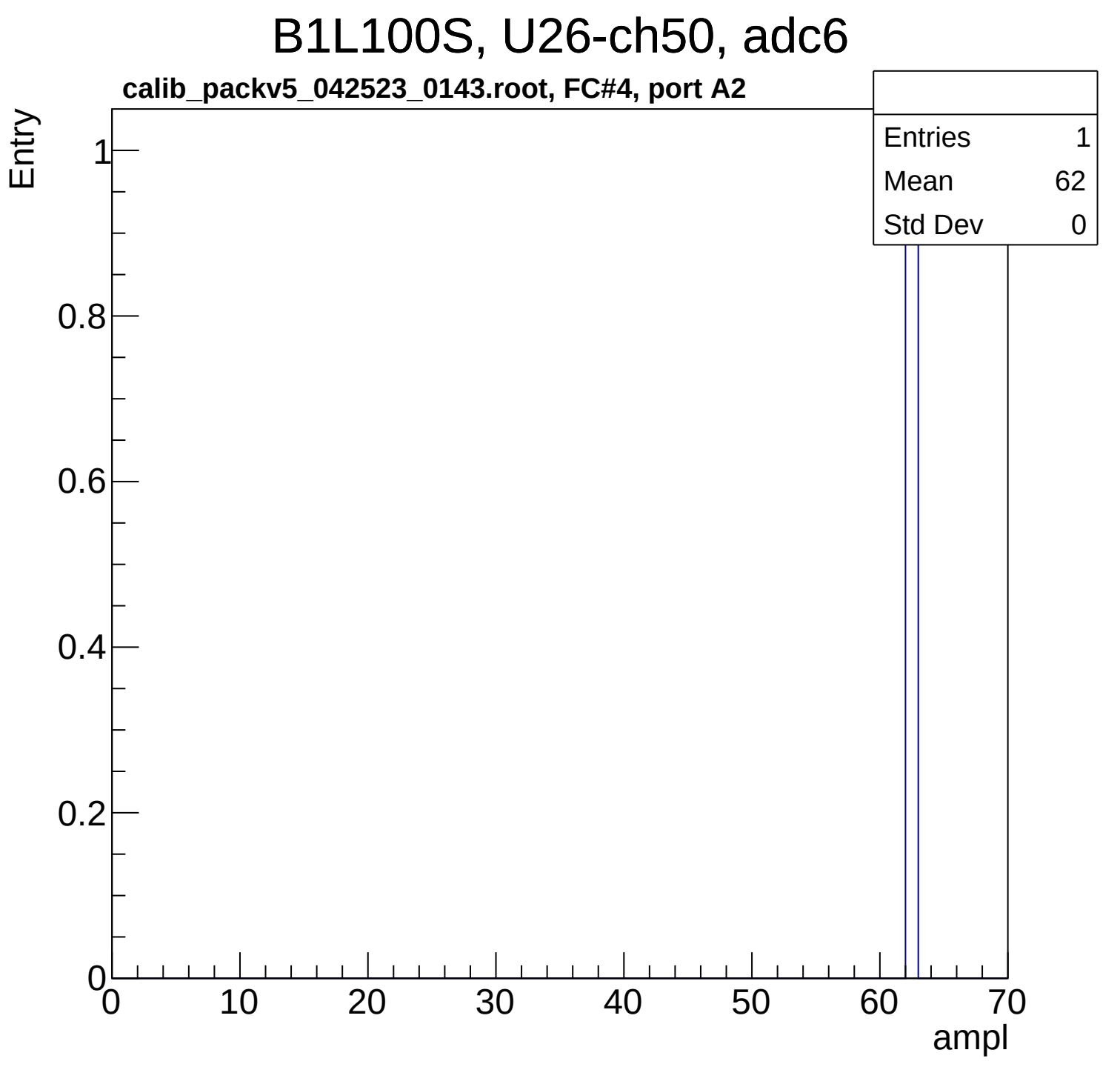
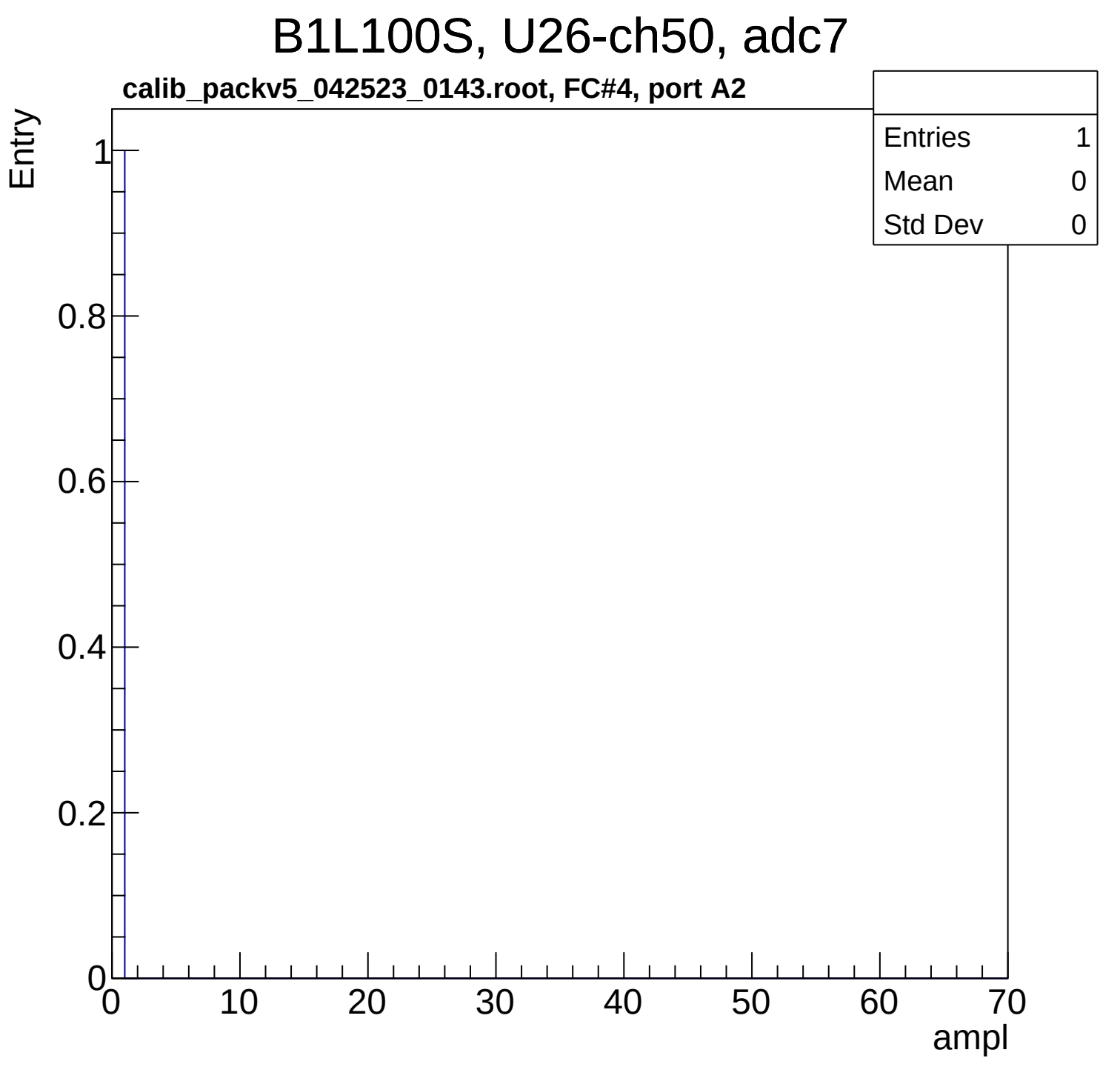
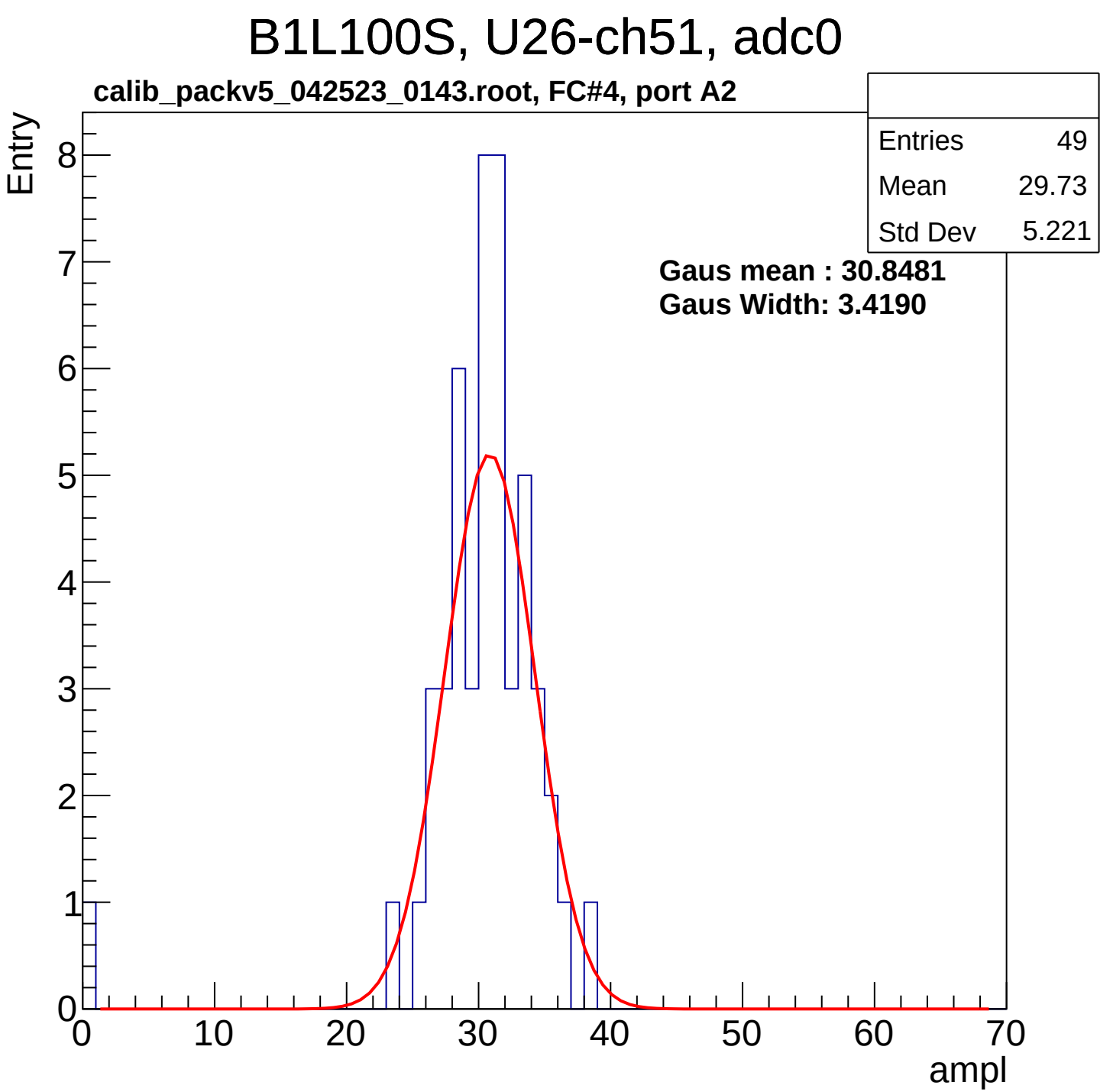
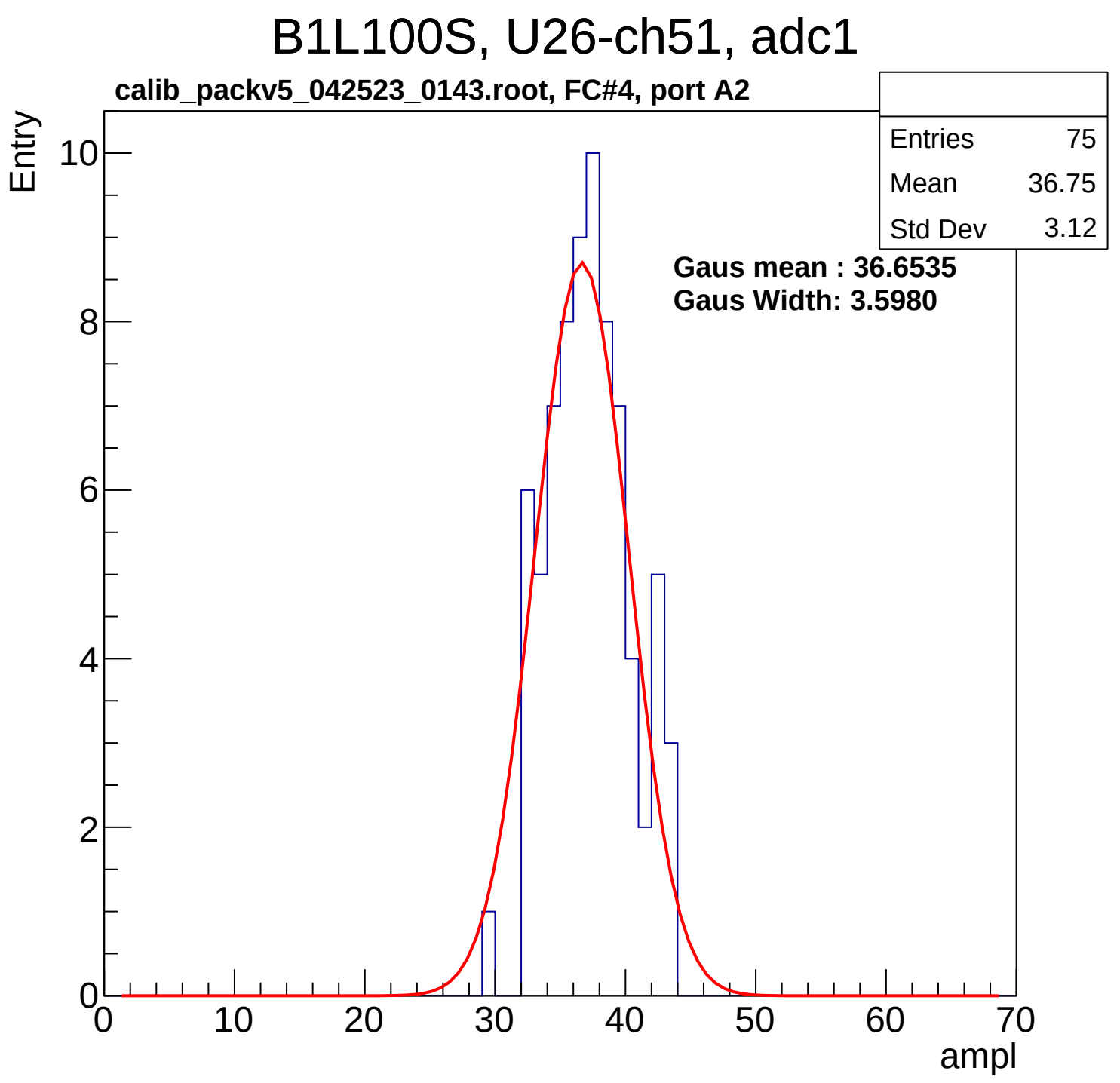
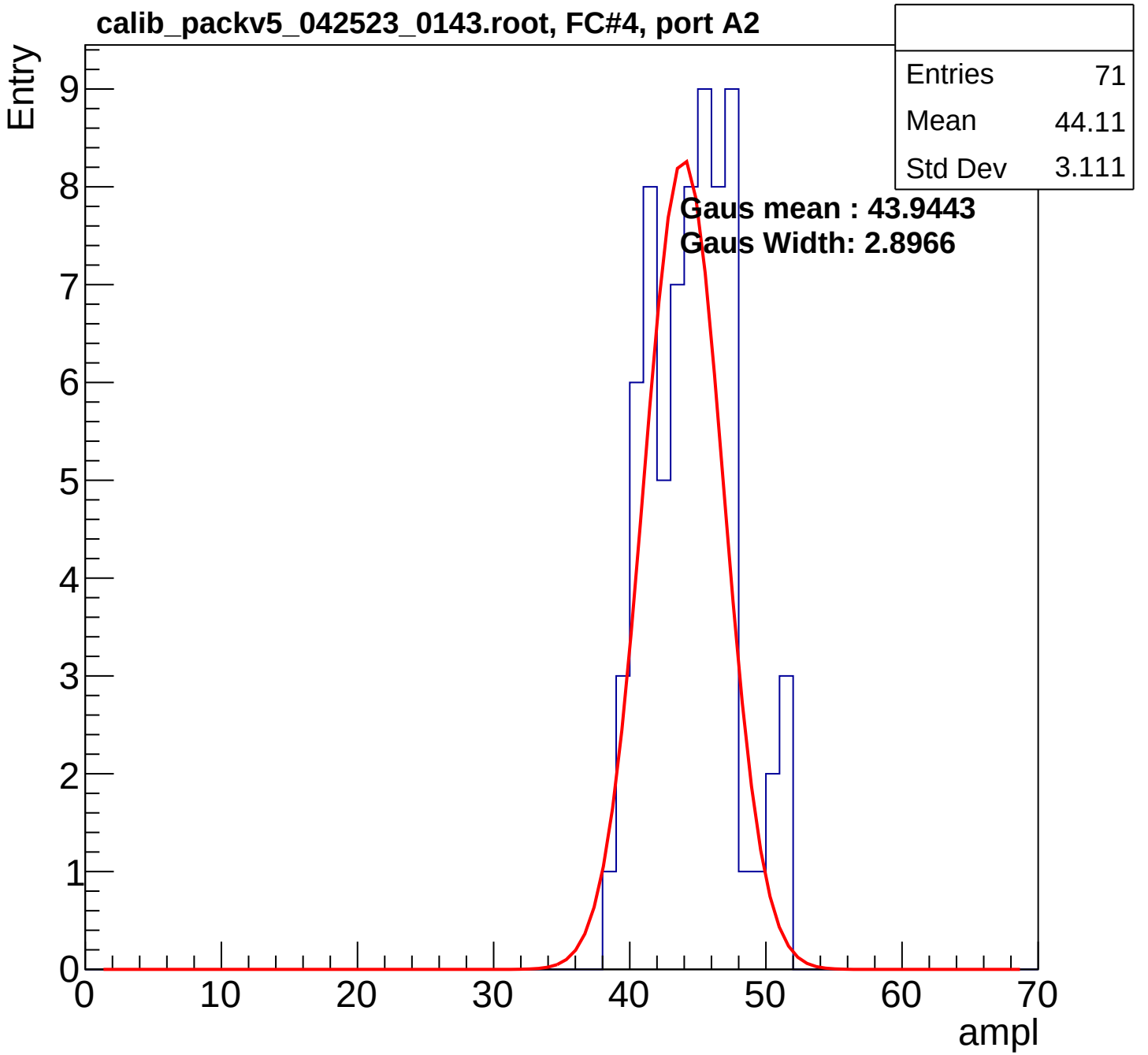

B1L100S, U26-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 57.83
Std Dev 12.51
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

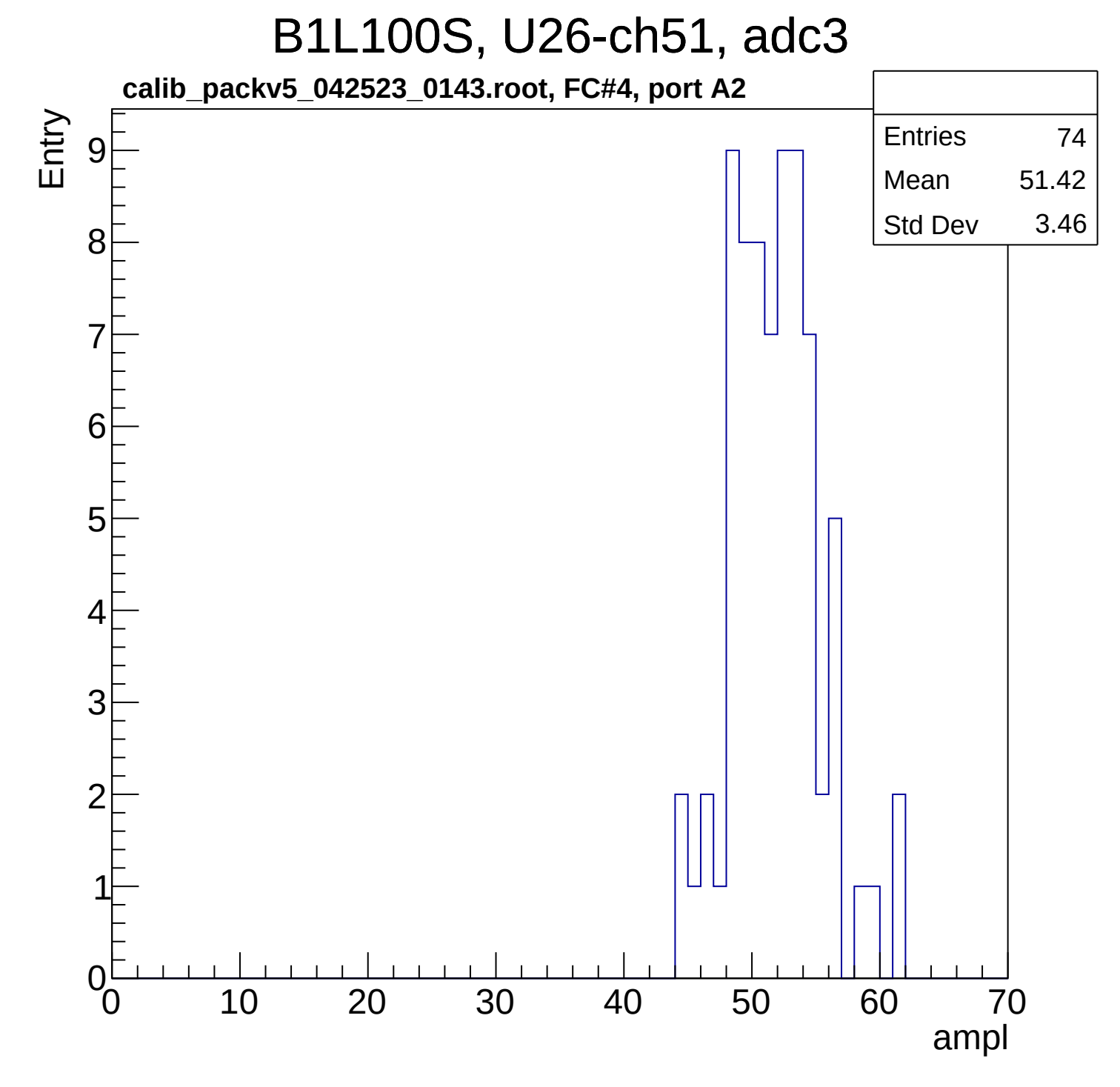

B1L100S, U26-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

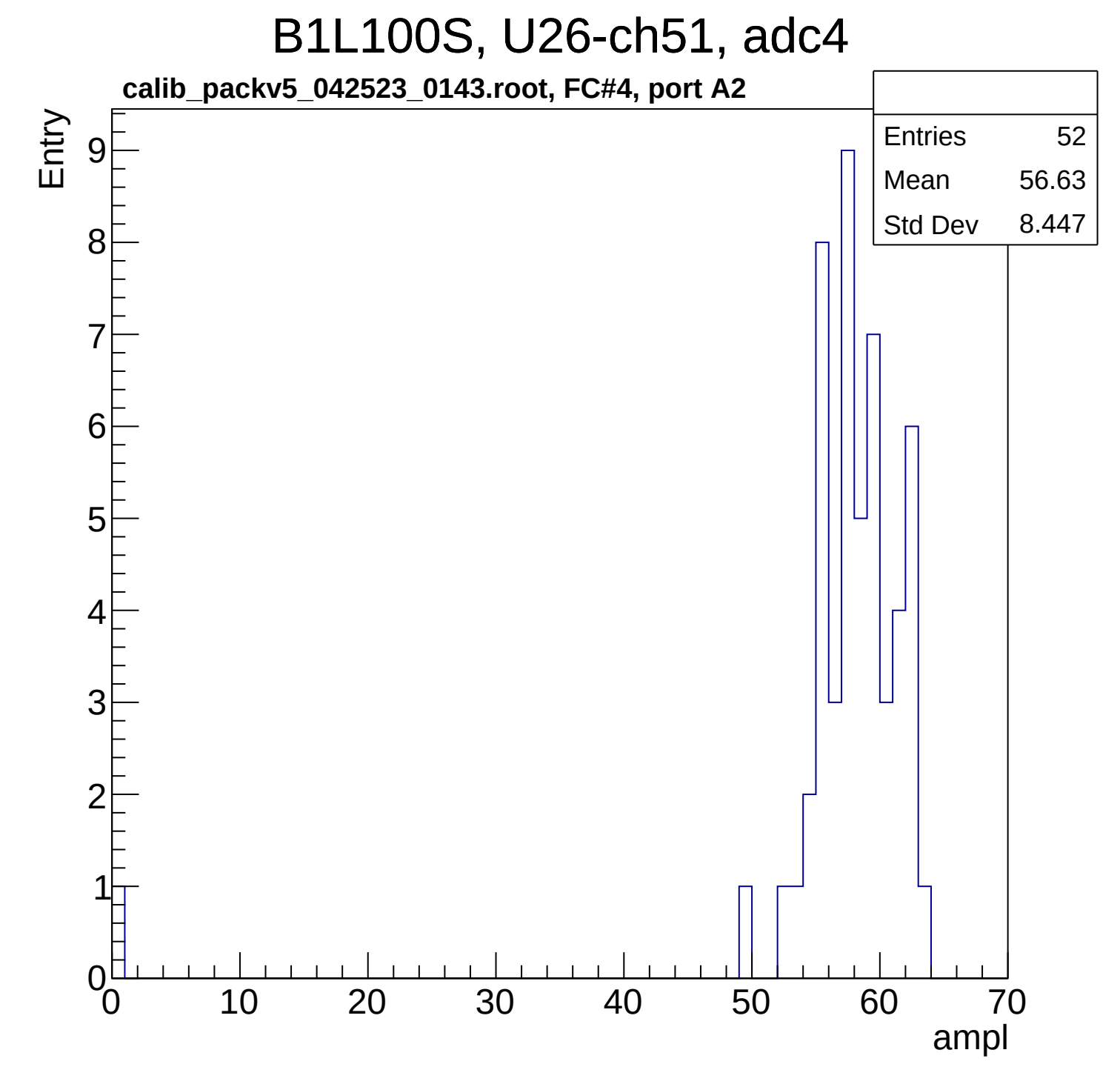

B1L100S, U26-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

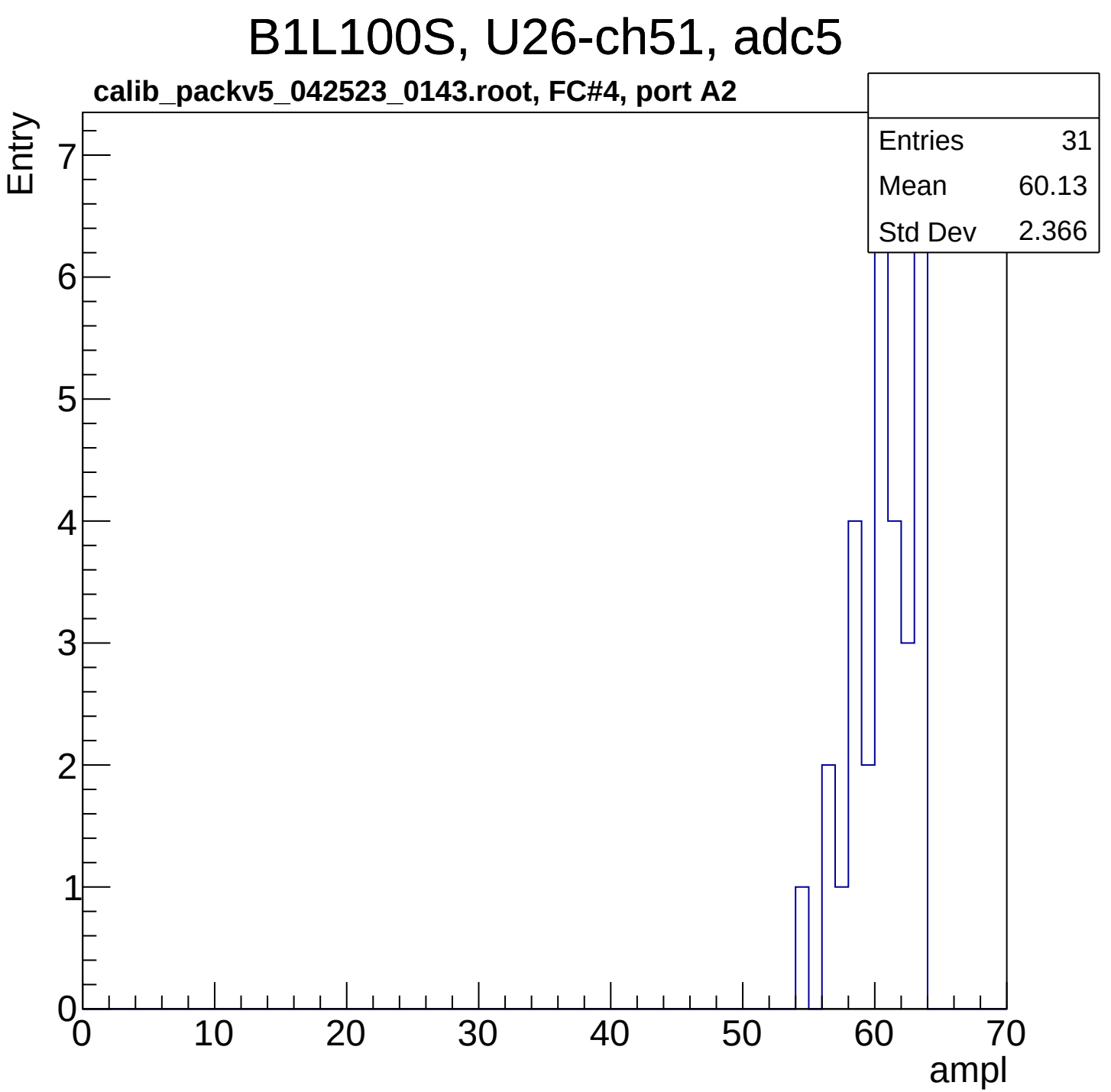

B1L100S, U26-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 29.73
Std Dev 5.221
Gaus mean : 30.8481
Gaus Width: 3.4190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

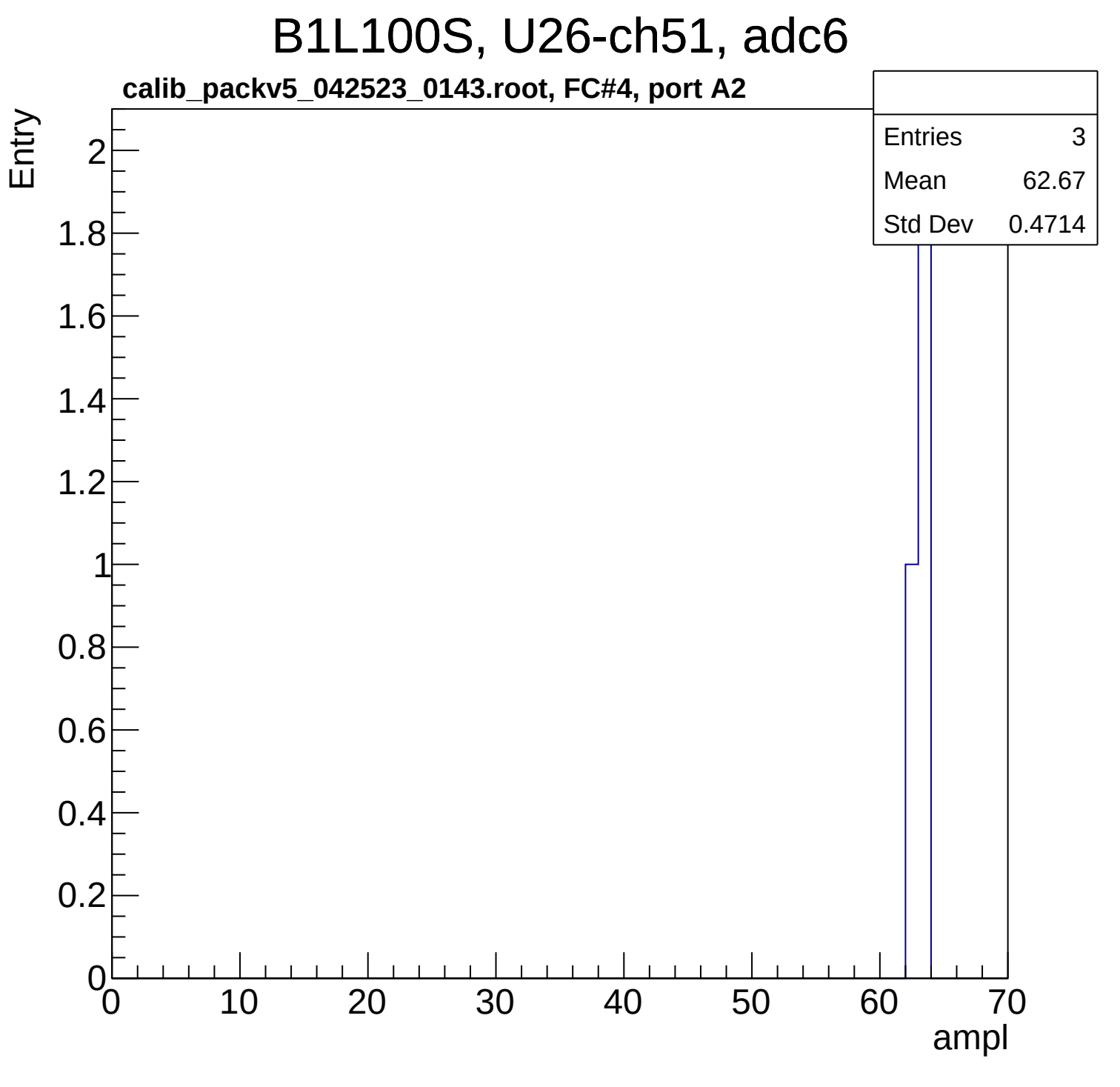

B1L100S, U26-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.75
Std Dev 3.12
Gaus mean : 36.6535
Gaus Width: 3.5980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

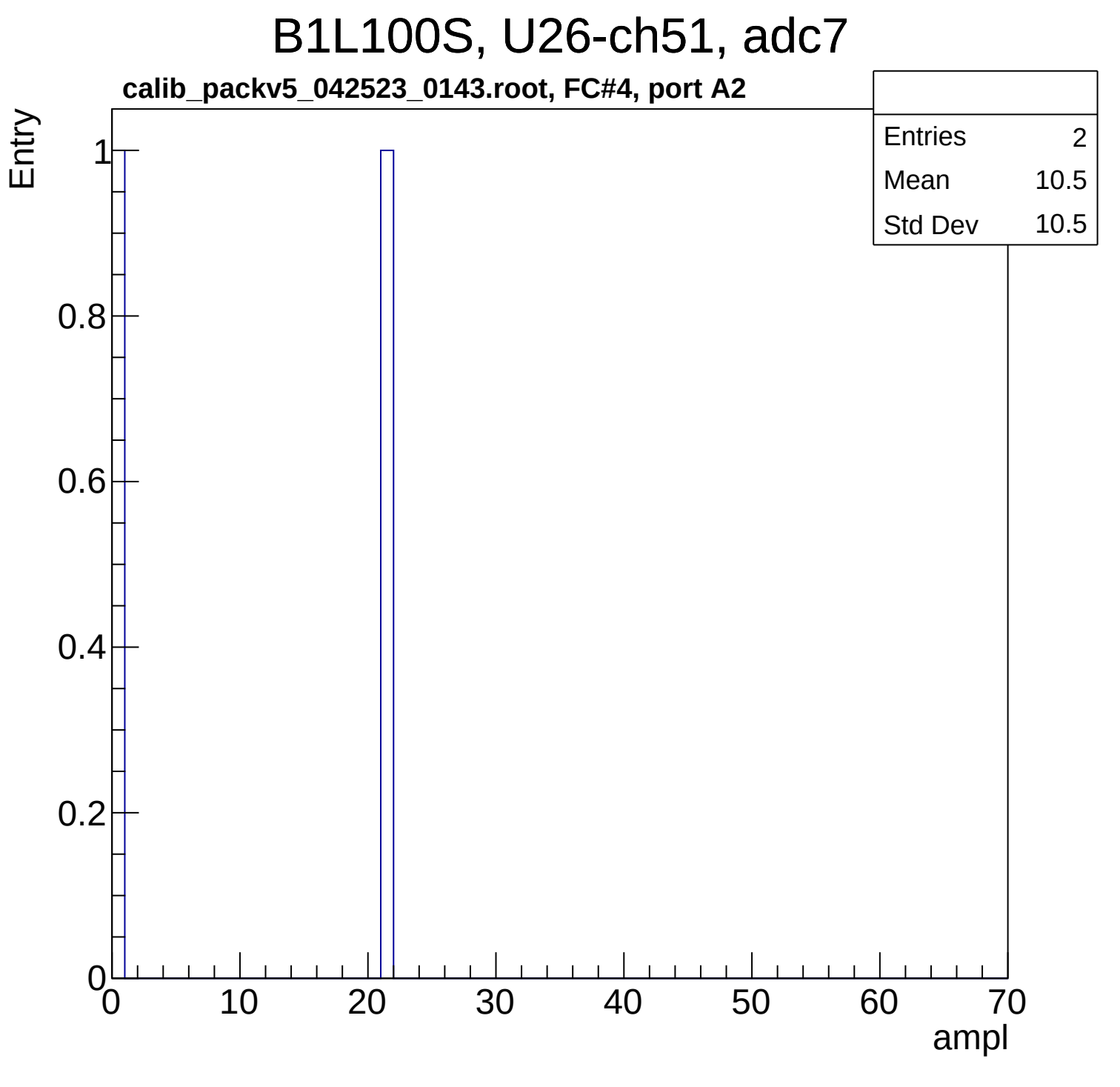

B1L100S, U26-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 44.11
Std Dev 3.111
Gaus mean : 43.9443
Gaus Width: 2.8966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

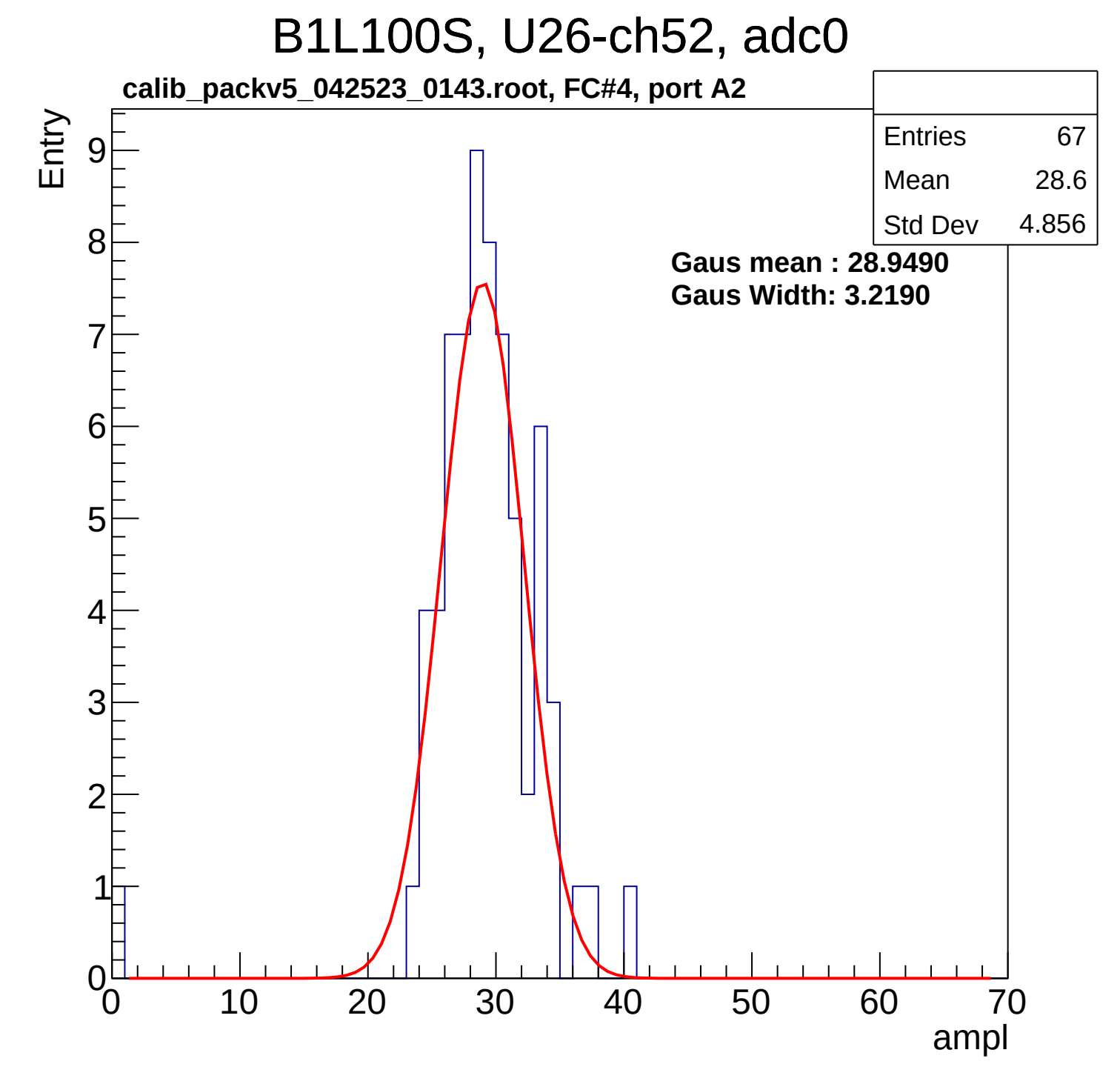

B1L100S, U26-ch51, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 51.42
Std Dev 3.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

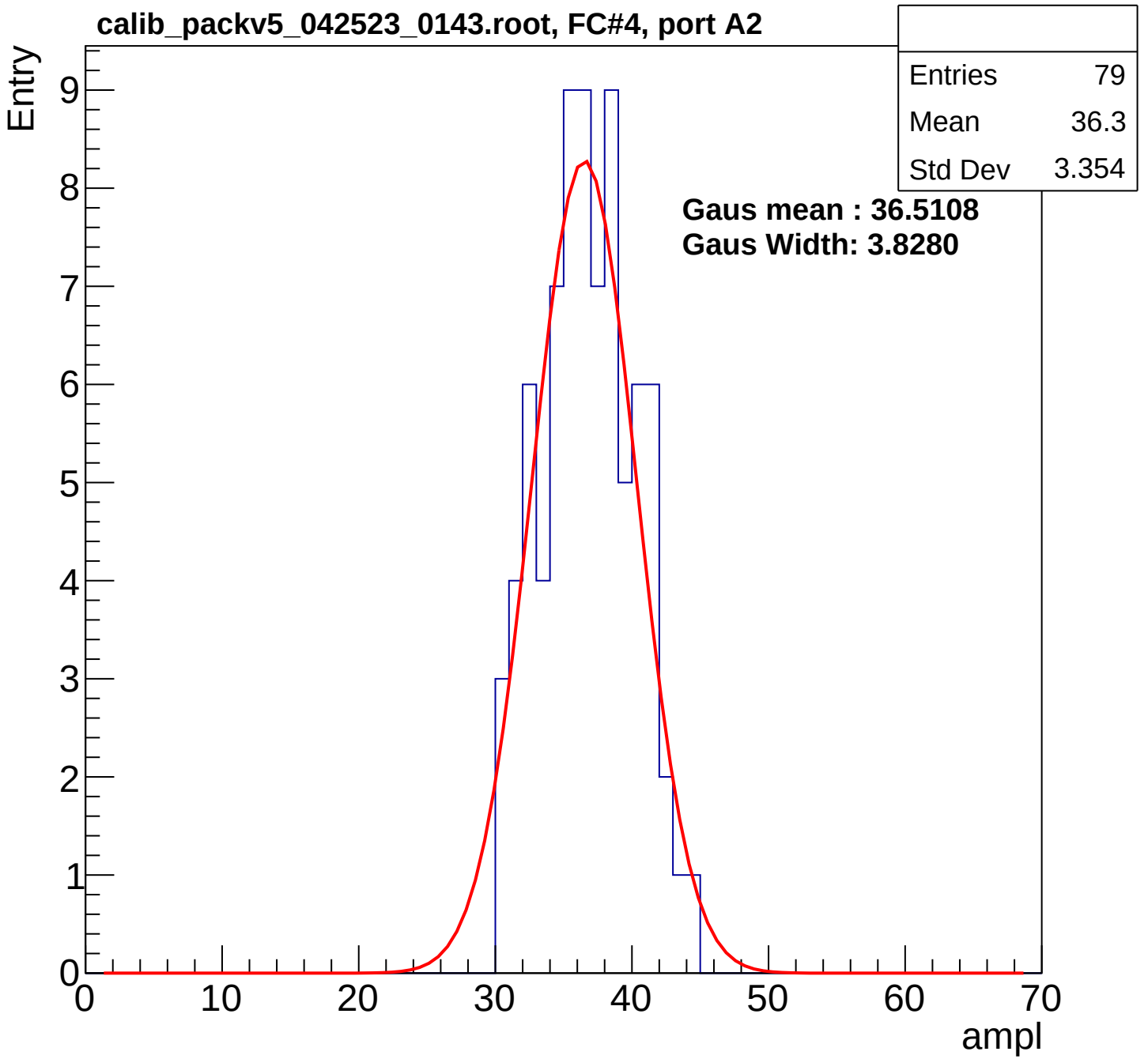

B1L100S, U26-ch51, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.63
Std Dev 8.447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.13
Std Dev 2.366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.6
Std Dev 4.856
Gaus mean : 28.9490
Gaus Width: 3.2190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 36.3
Std Dev 3.354
Gaus mean : 36.5108
Gaus Width: 3.8280
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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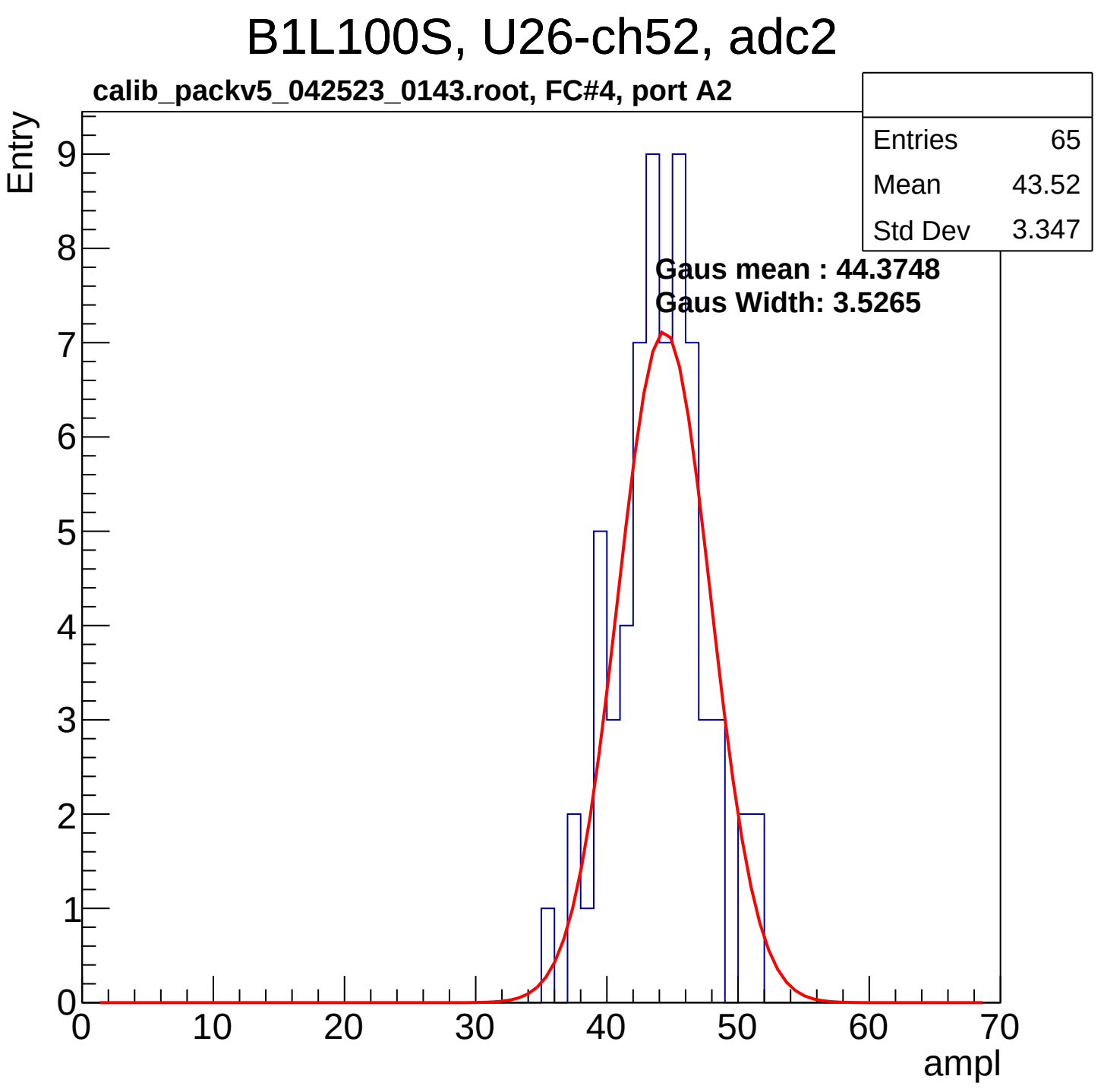
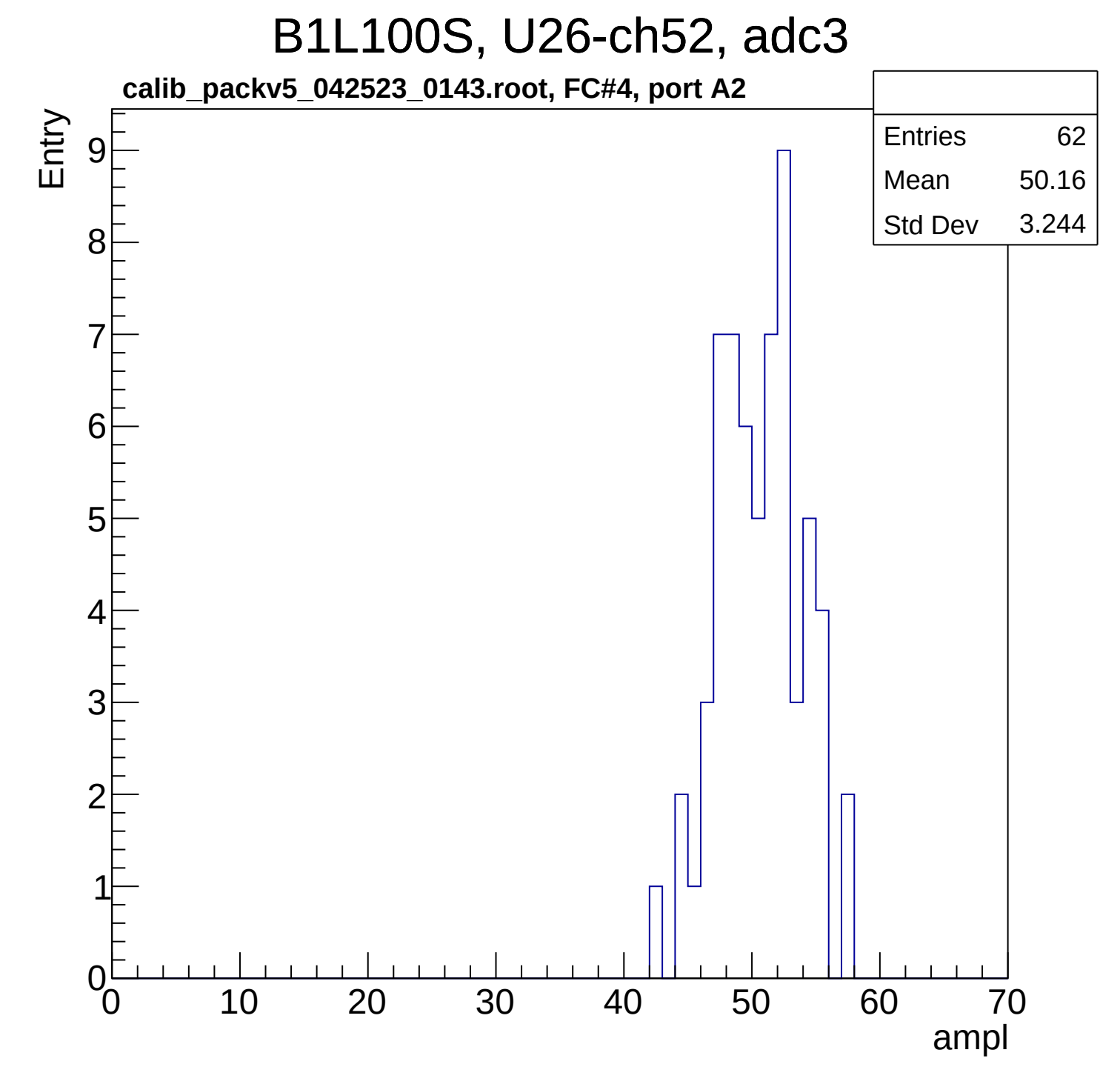

B1L100S, U26-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.52
Std Dev 3.347
Gaus mean : 44.3748
Gaus Width: 3.5265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch52, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.16
Std Dev 3.244
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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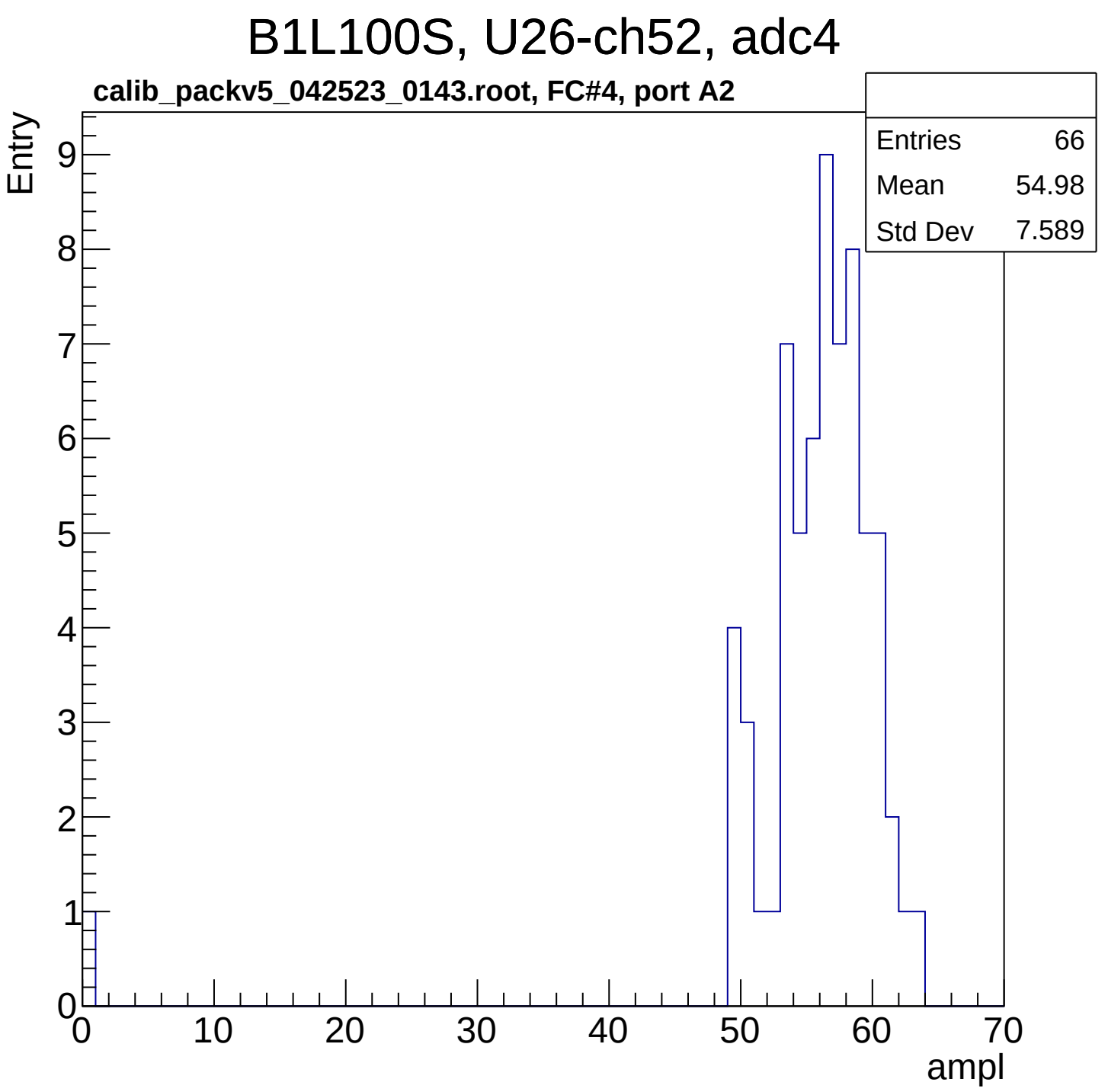
B1L100S, U26-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 54.98
Std Dev 7.589
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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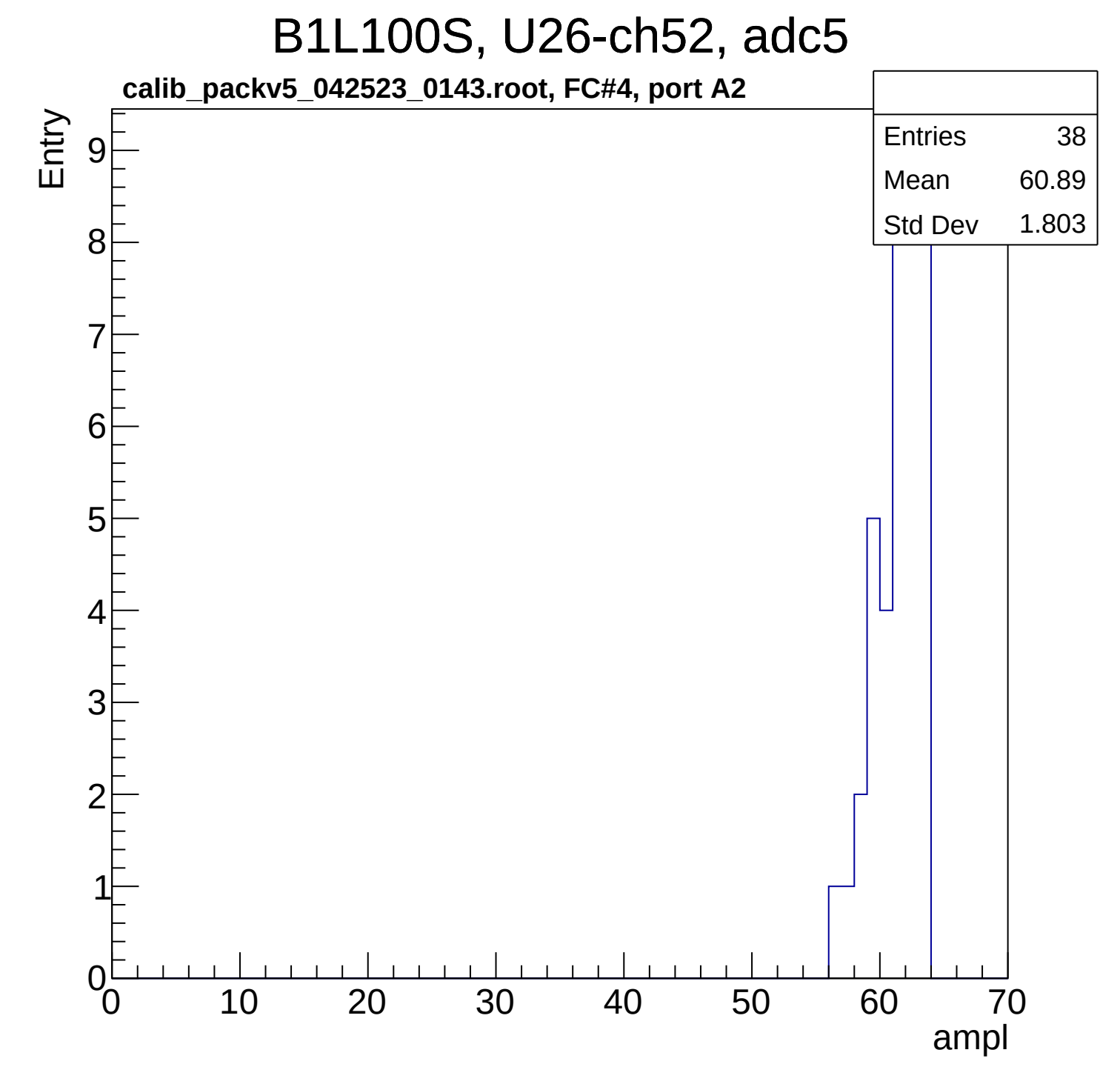
B1L100S, U26-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.89
Std Dev 1.803
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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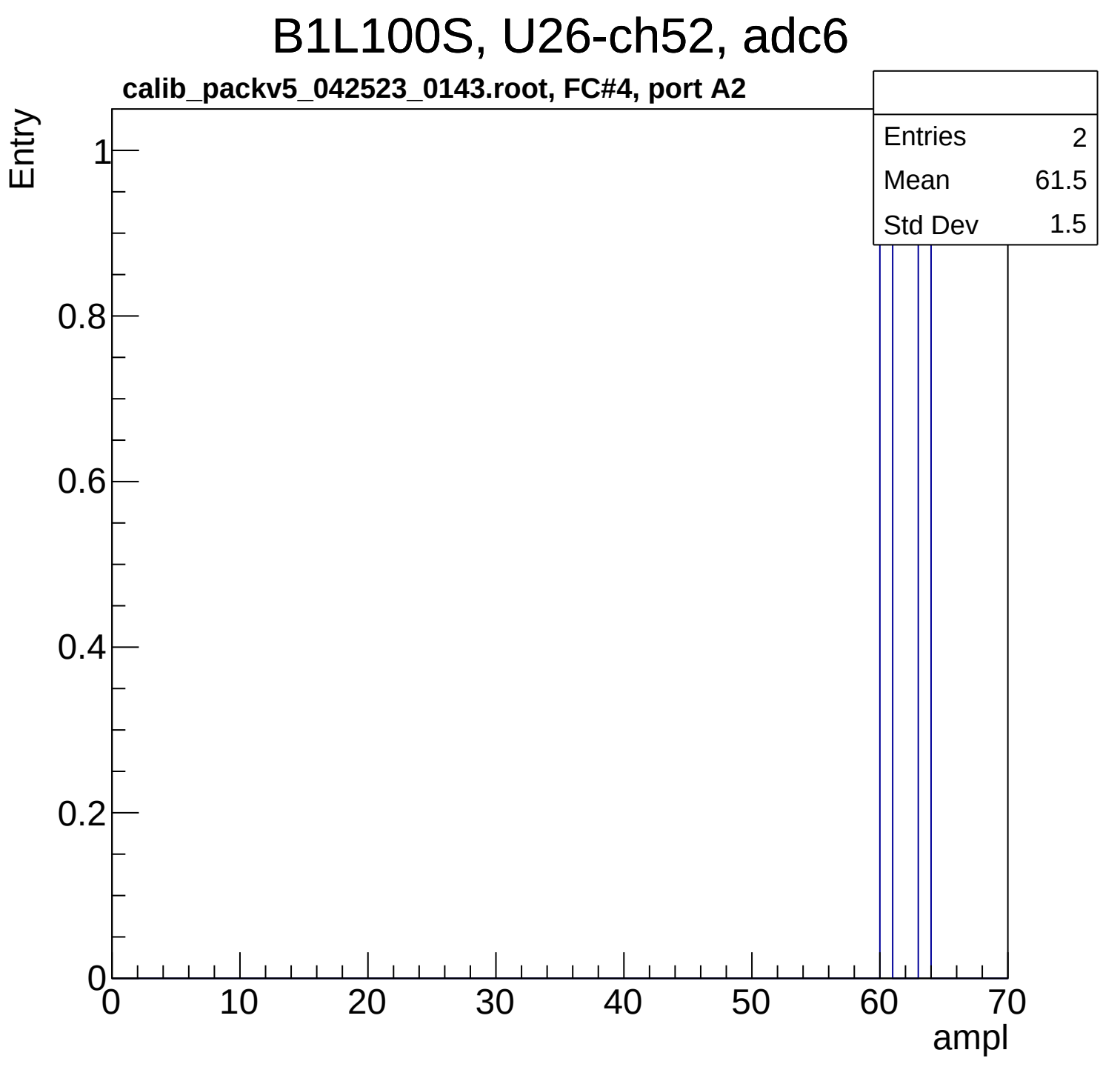
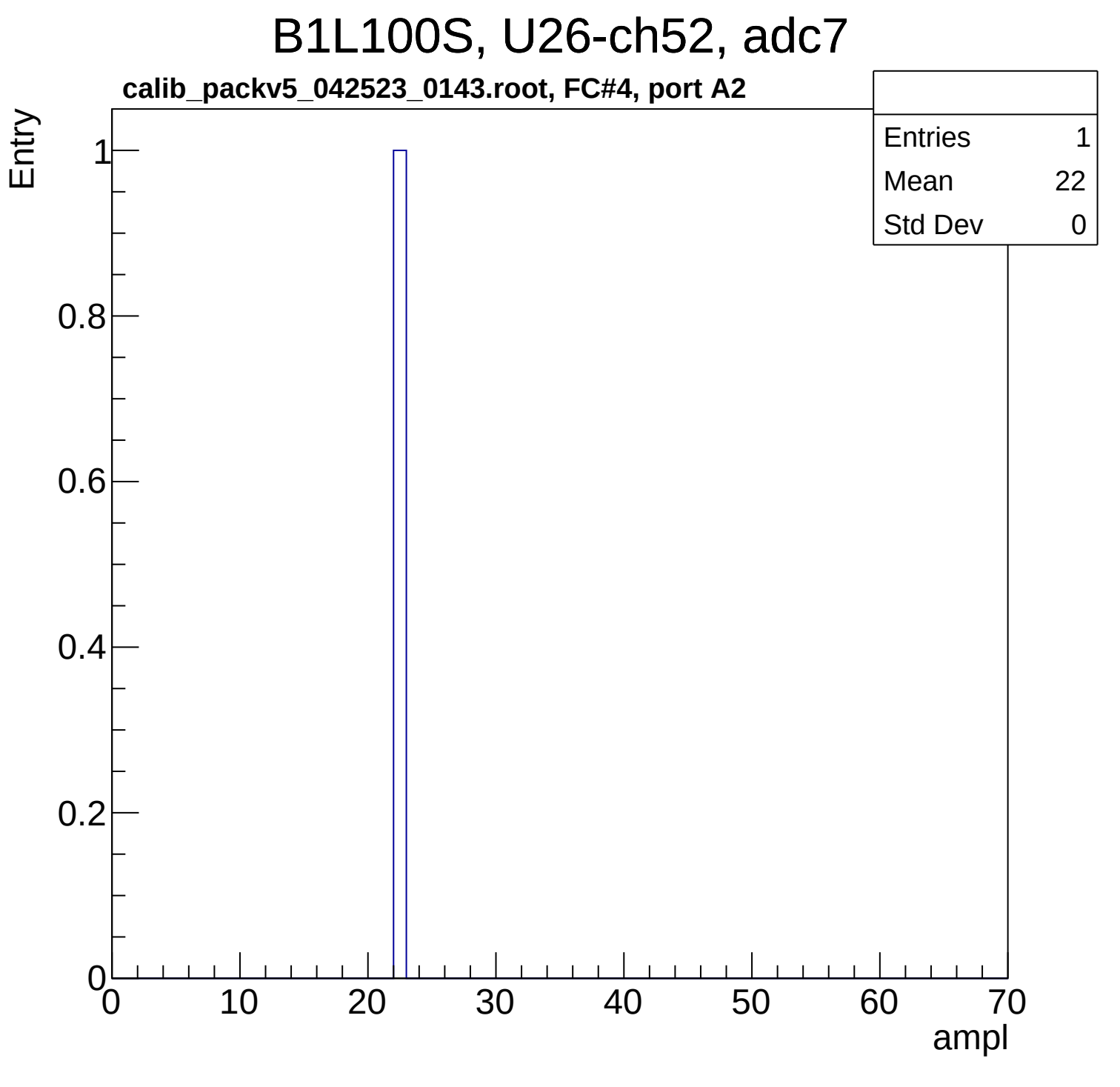

B1L100S, U26-ch52, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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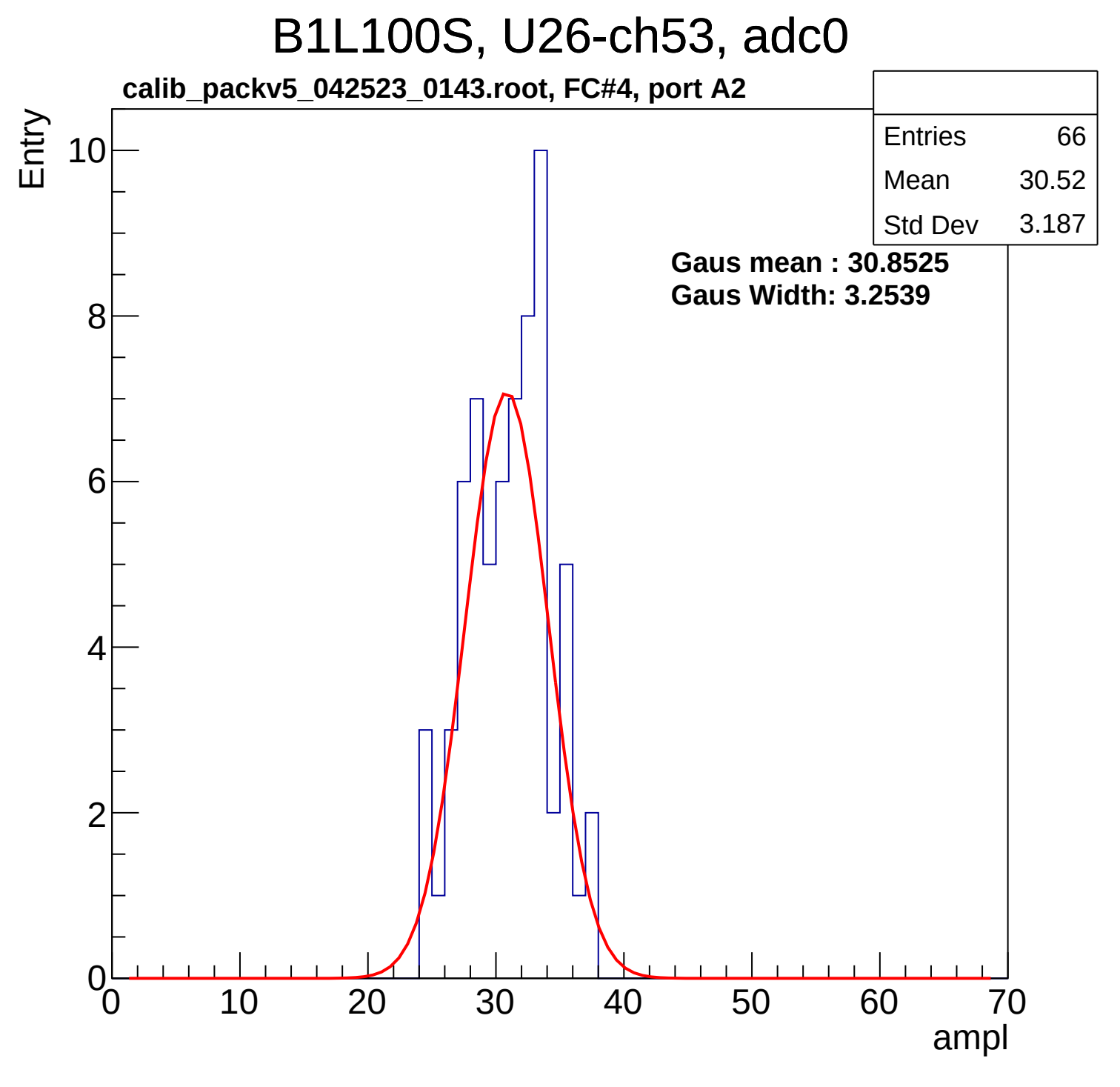
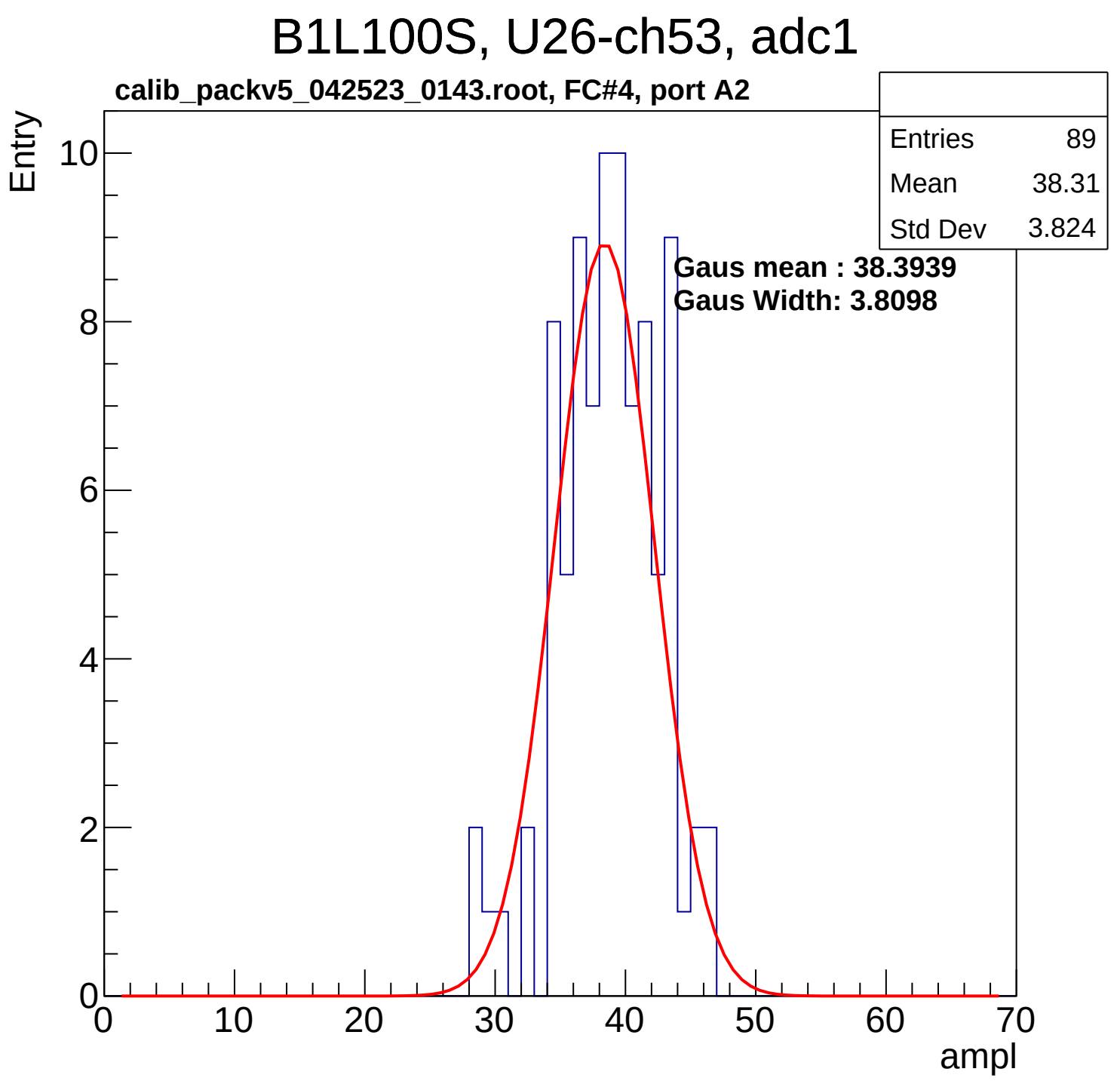
30
40
50
60
70
ampl


B1L100S, U26-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.52
Std Dev 3.187
Gaus mean : 30.8525
Gaus Width: 3.2539
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 38.31
Std Dev 3.824
Gaus mean : 38.3939
Gaus Width: 3.8098
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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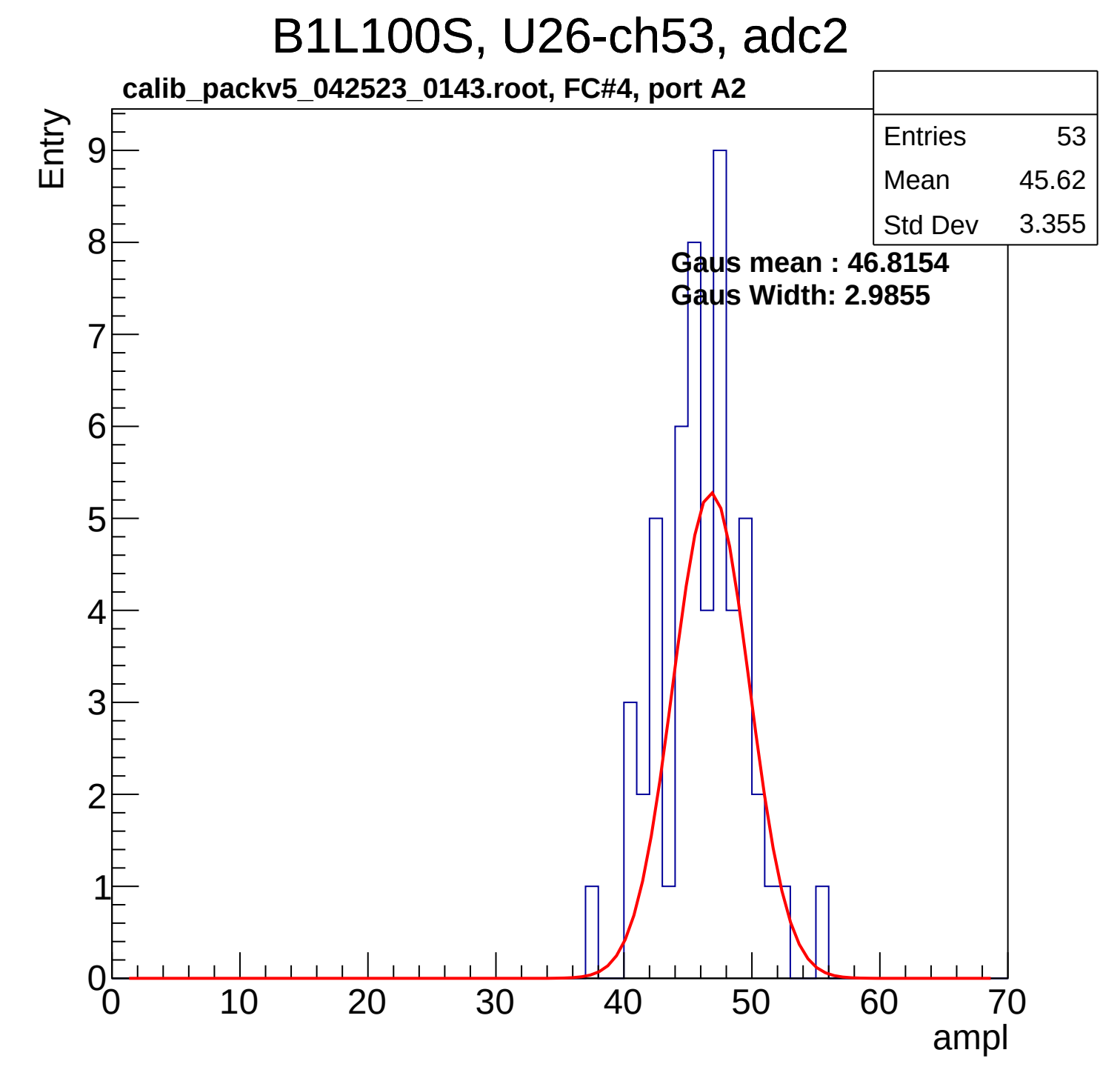
B1L100S, U26-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 45.62
Std Dev 3.355
Gaus mean : 46.8154
Gaus Width: 2.9855
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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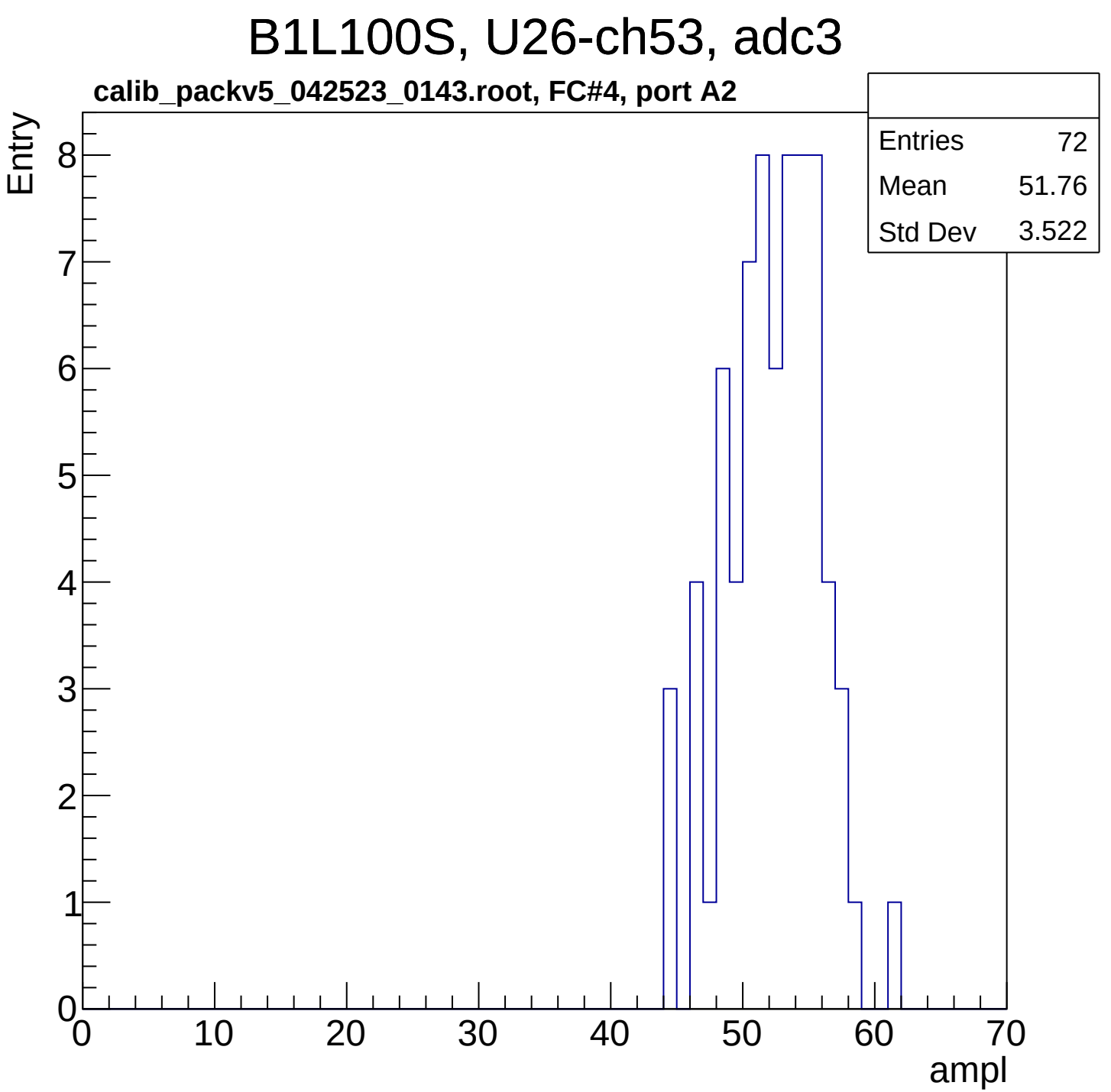
B1L100S, U26-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 51.76
Std Dev 3.522
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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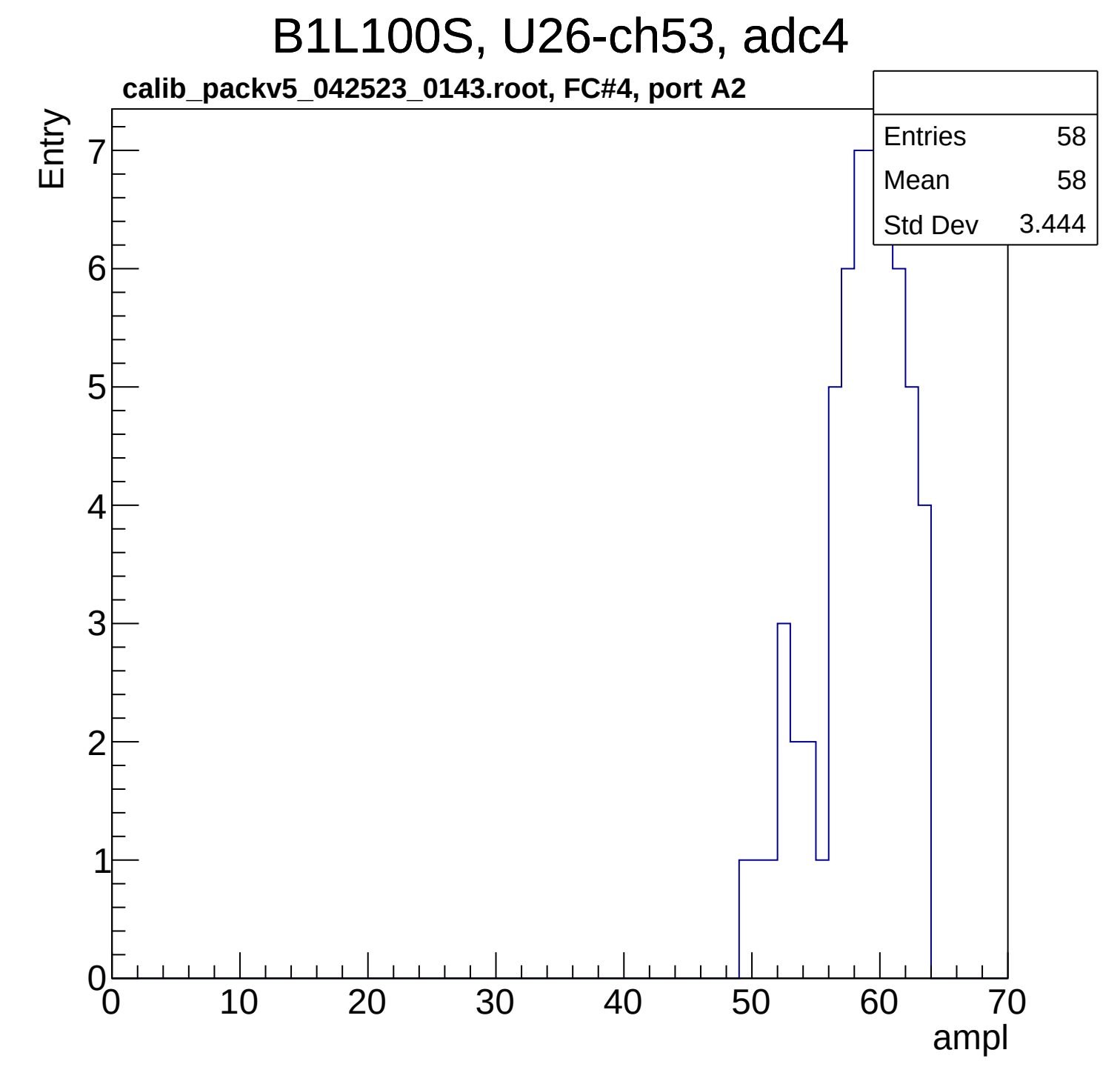
B1L100S, U26-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 58
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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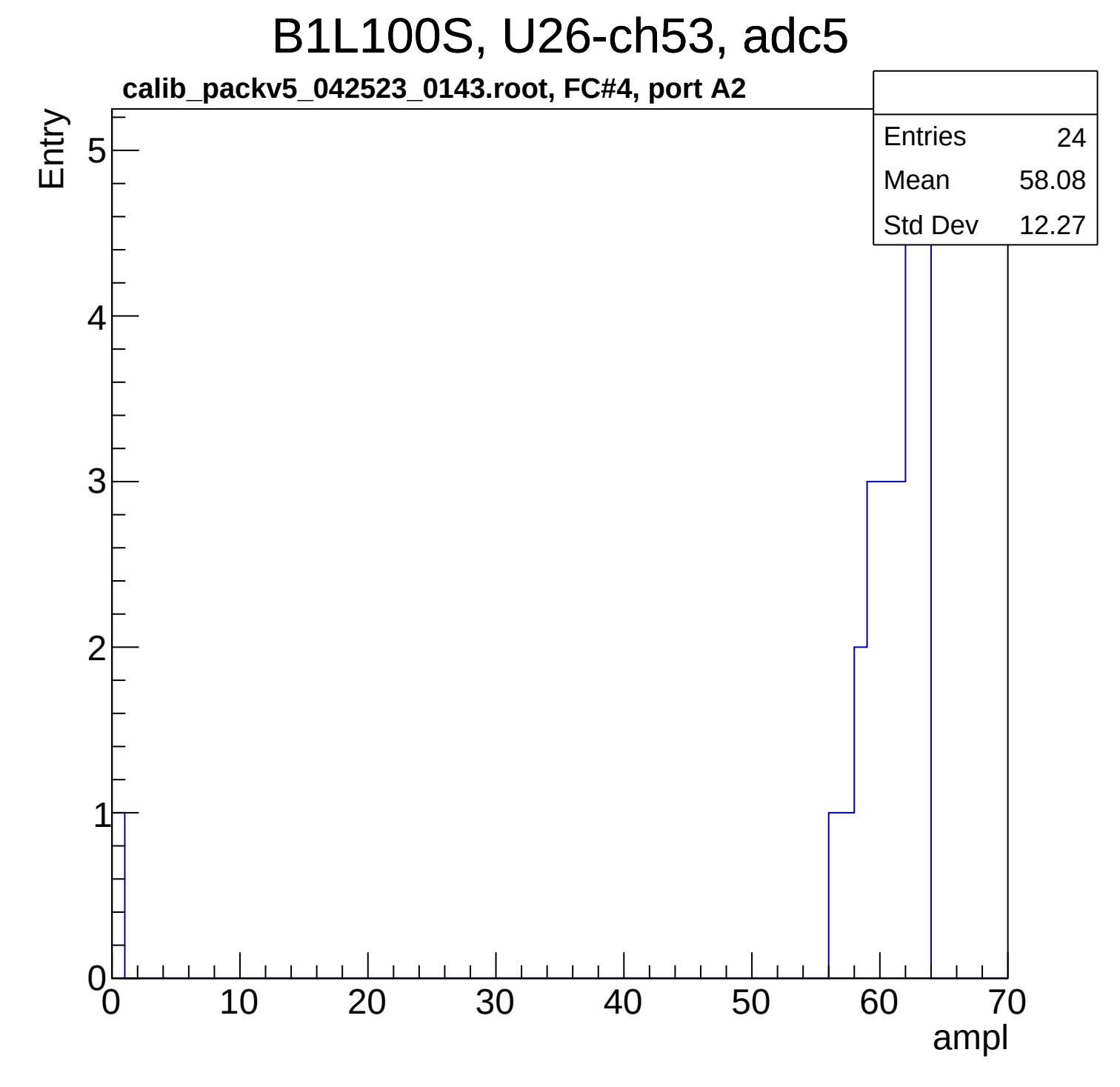
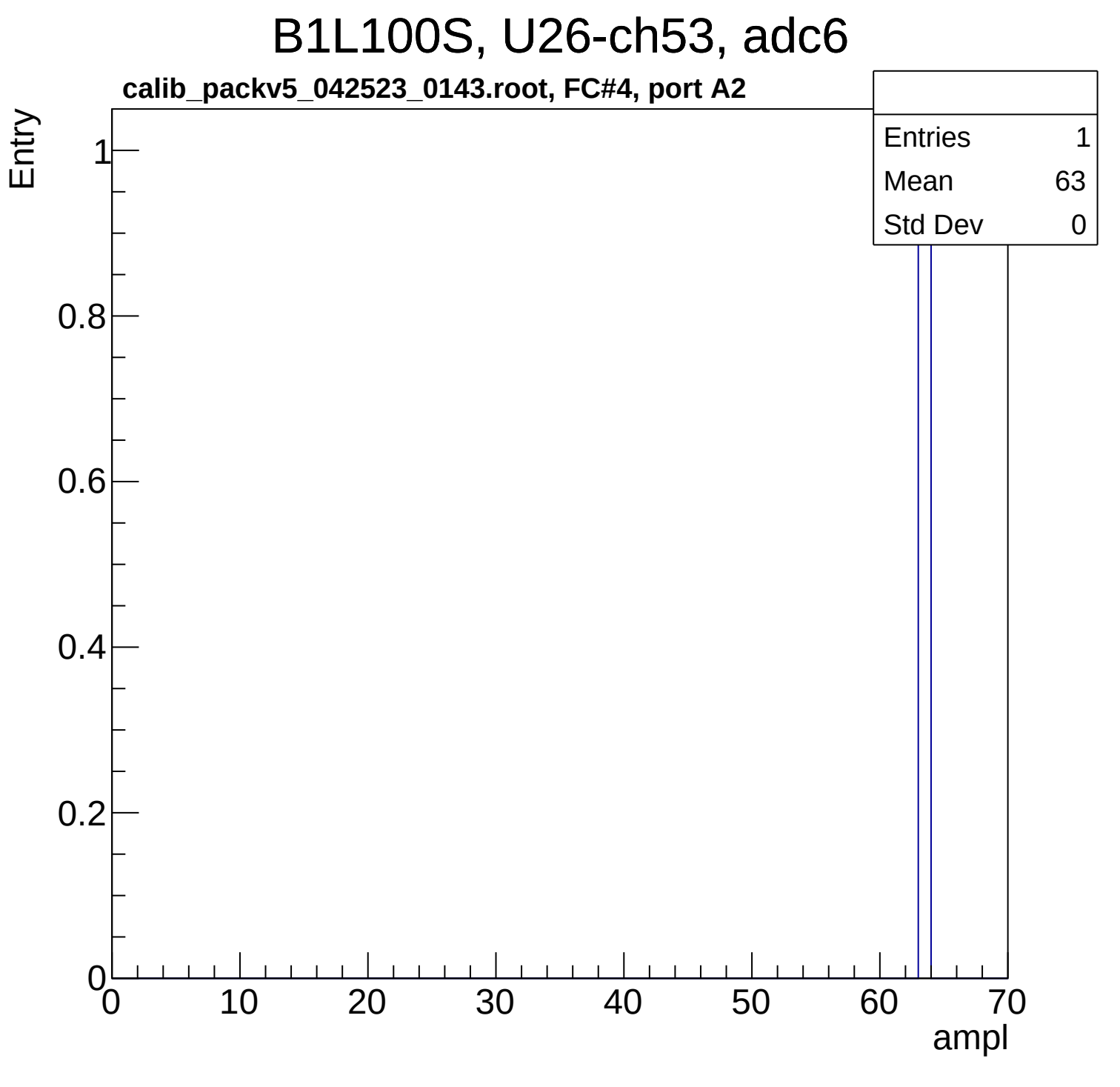
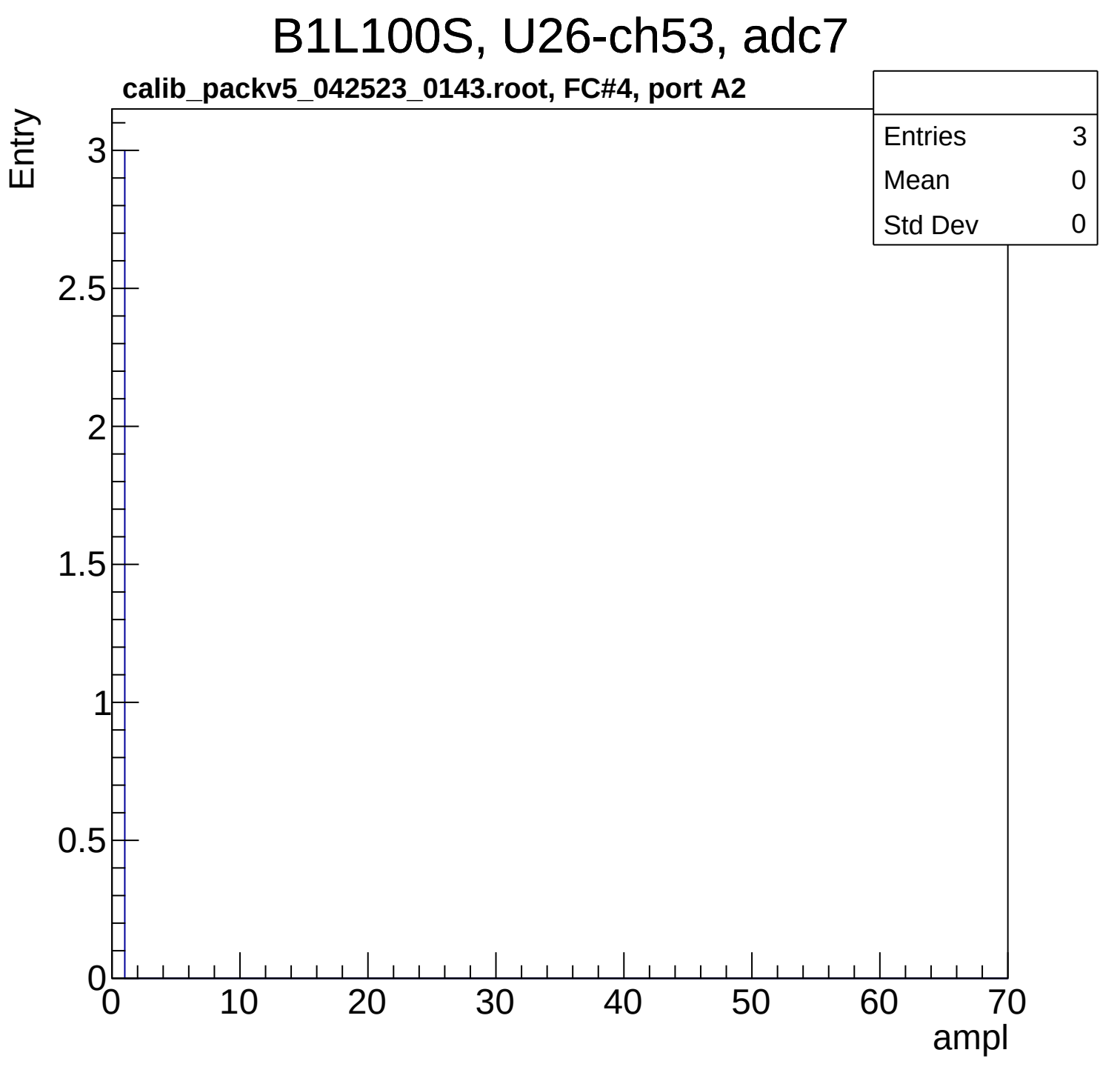
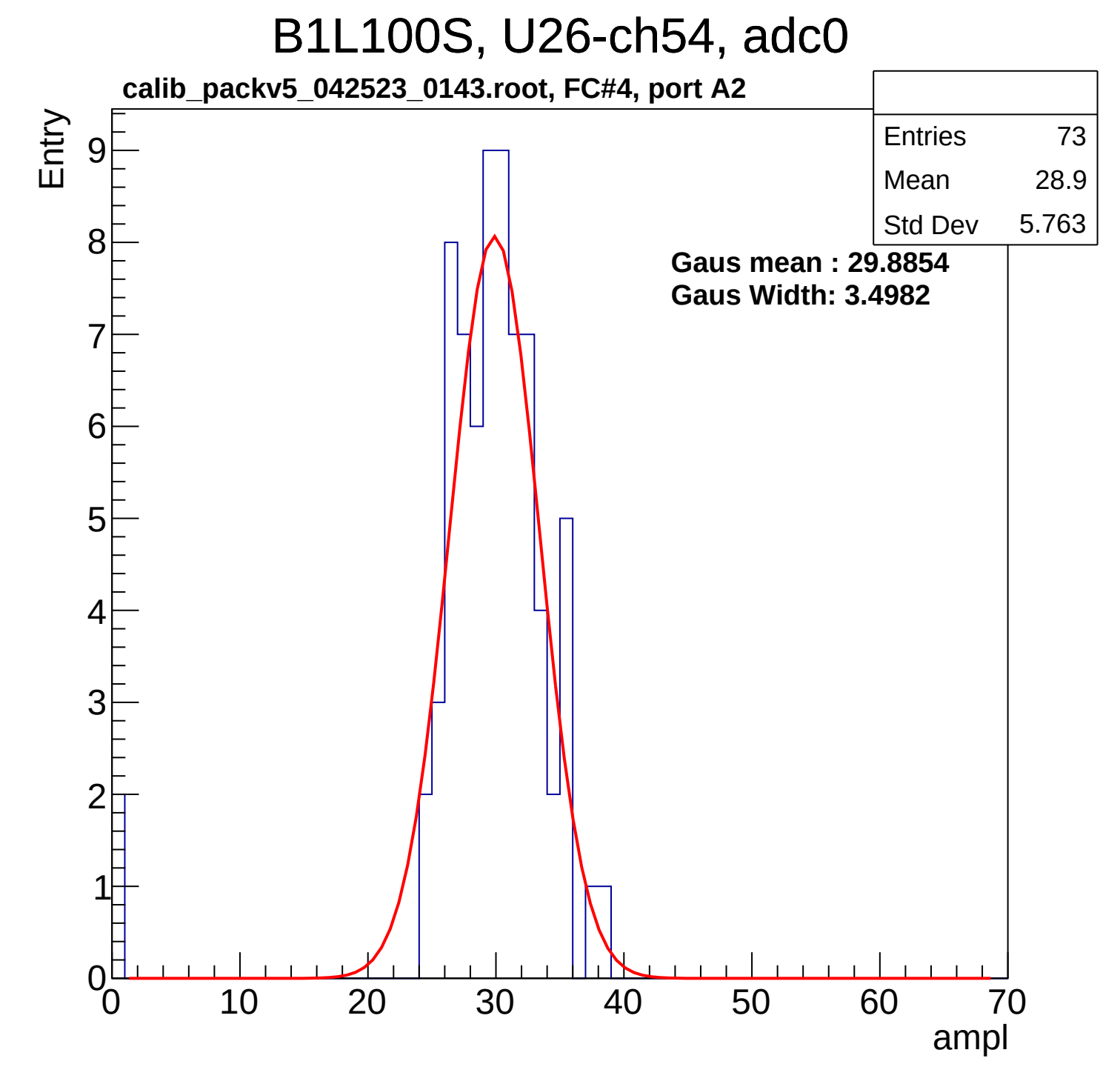
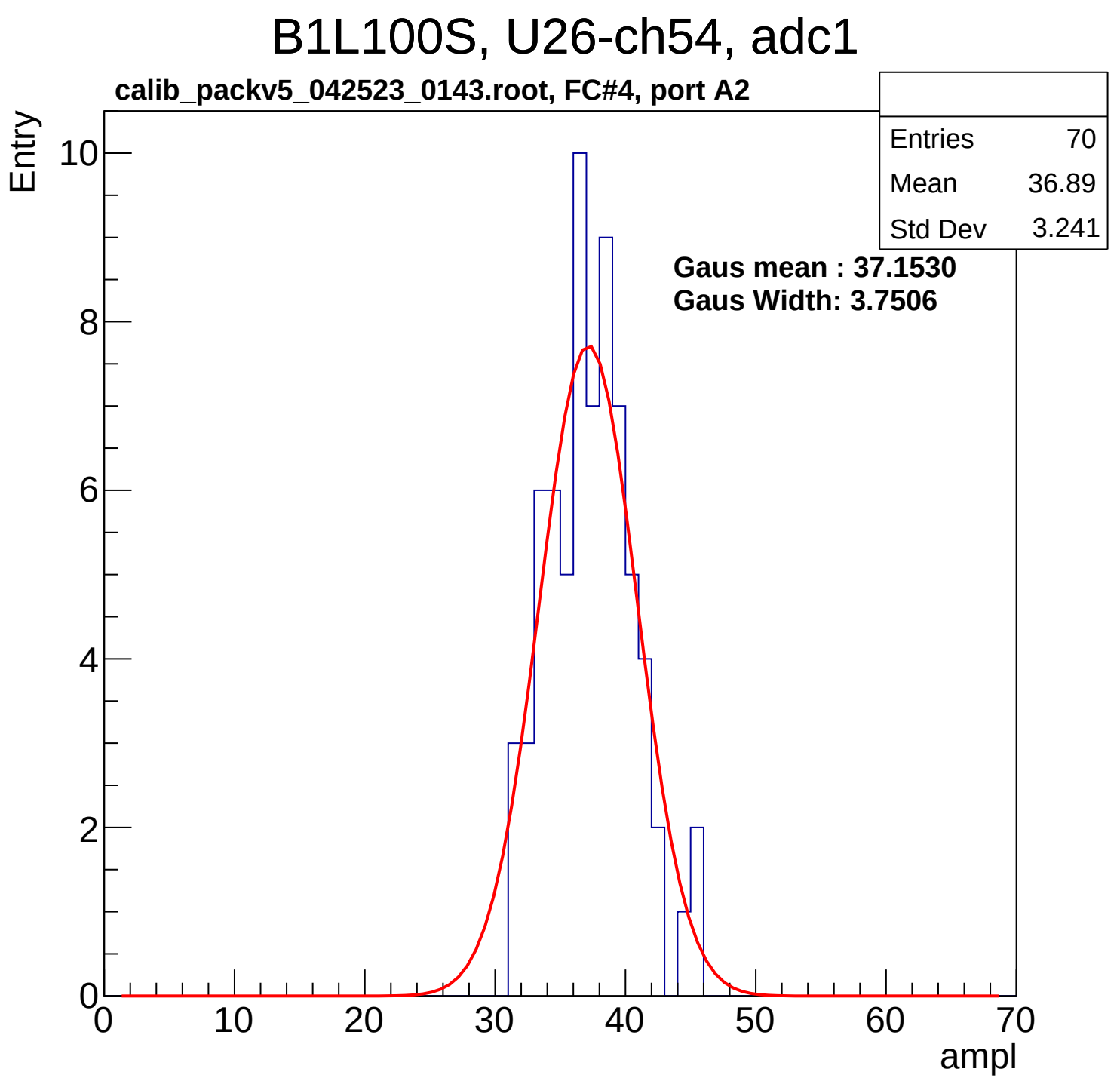
40
50
60
70


B1L100S, U26-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.08
Std Dev 12.27
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch53, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 28.9
Std Dev 5.763
Gaus mean : 29.8854
Gaus Width: 3.4982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.89
Std Dev 3.241
Gaus mean : 37.1530
Gaus Width: 3.7506
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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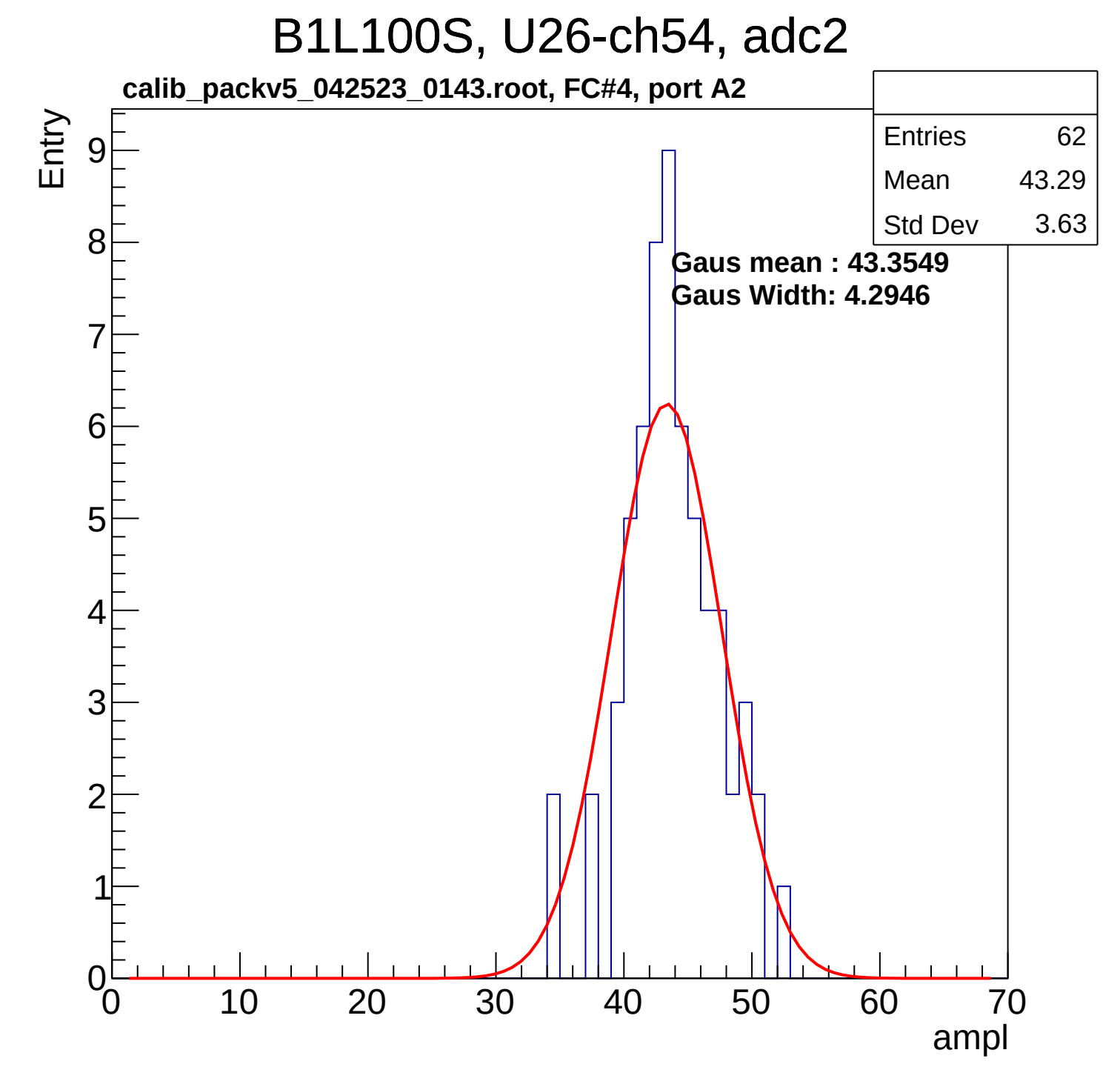
B1L100S, U26-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.29
Std Dev 3.63
Gaus mean : 43.3549
Gaus Width: 4.2946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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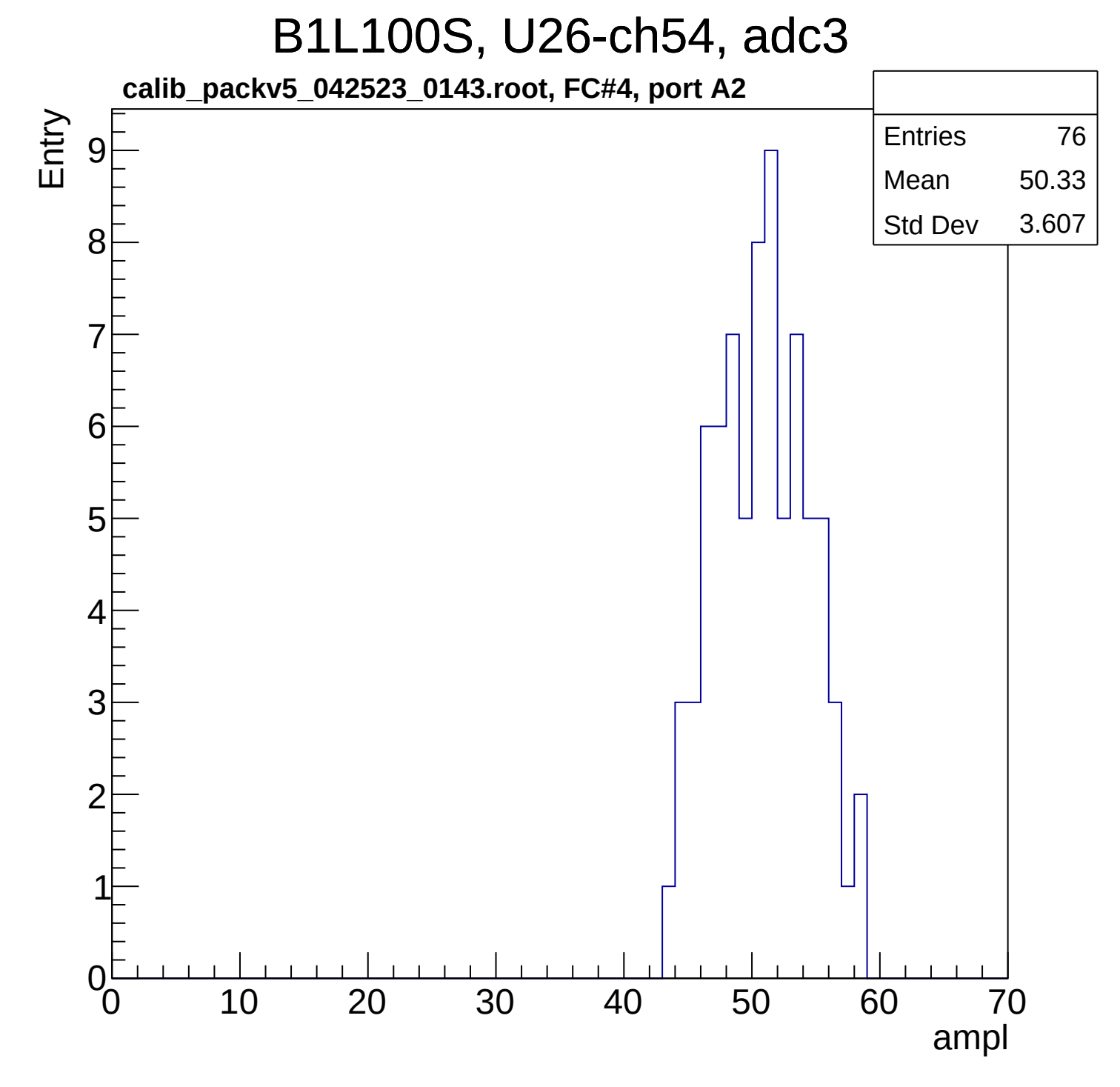
B1L100S, U26-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 50.33
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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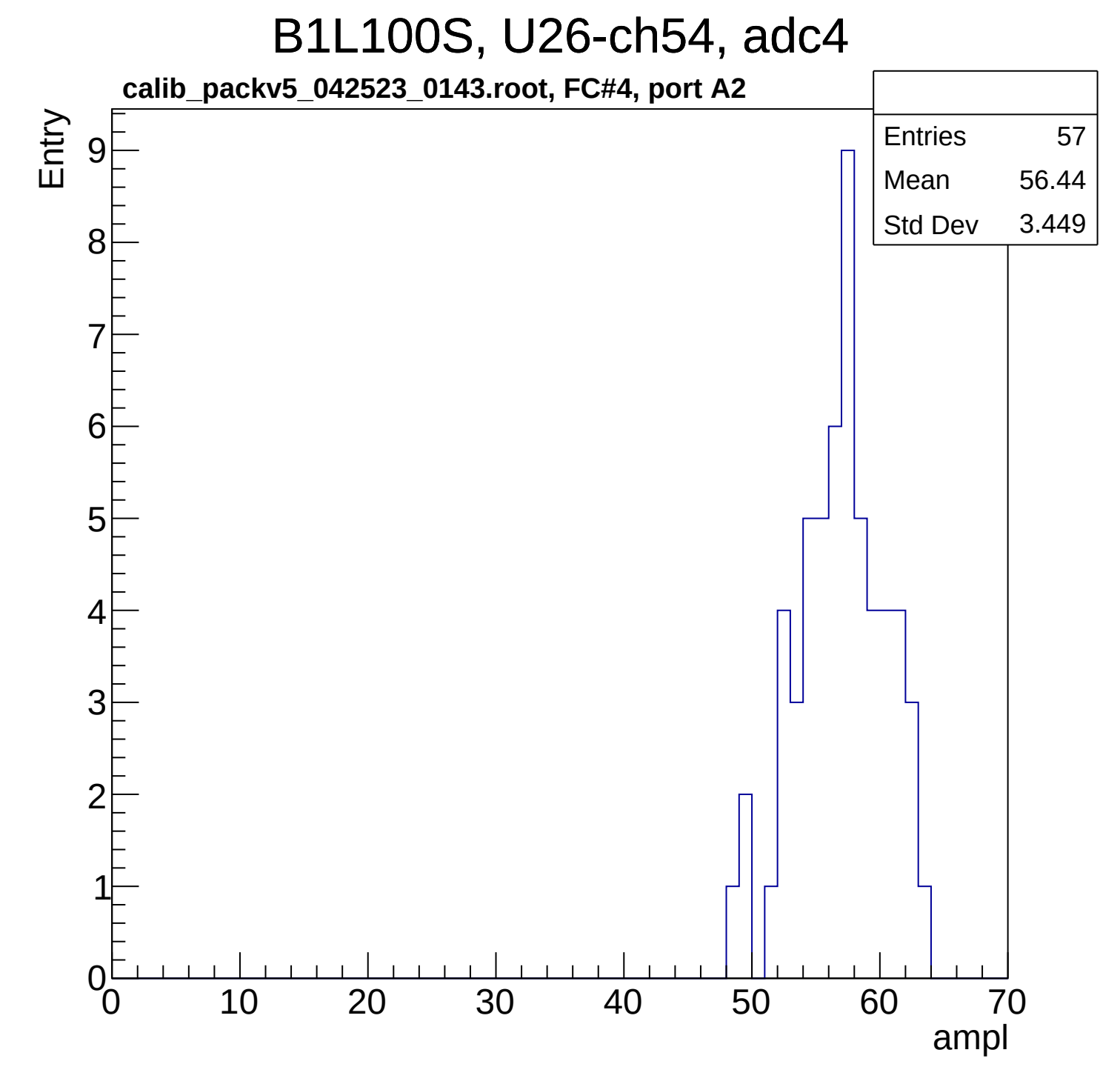
B1L100S, U26-ch54, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.44
Std Dev 3.449
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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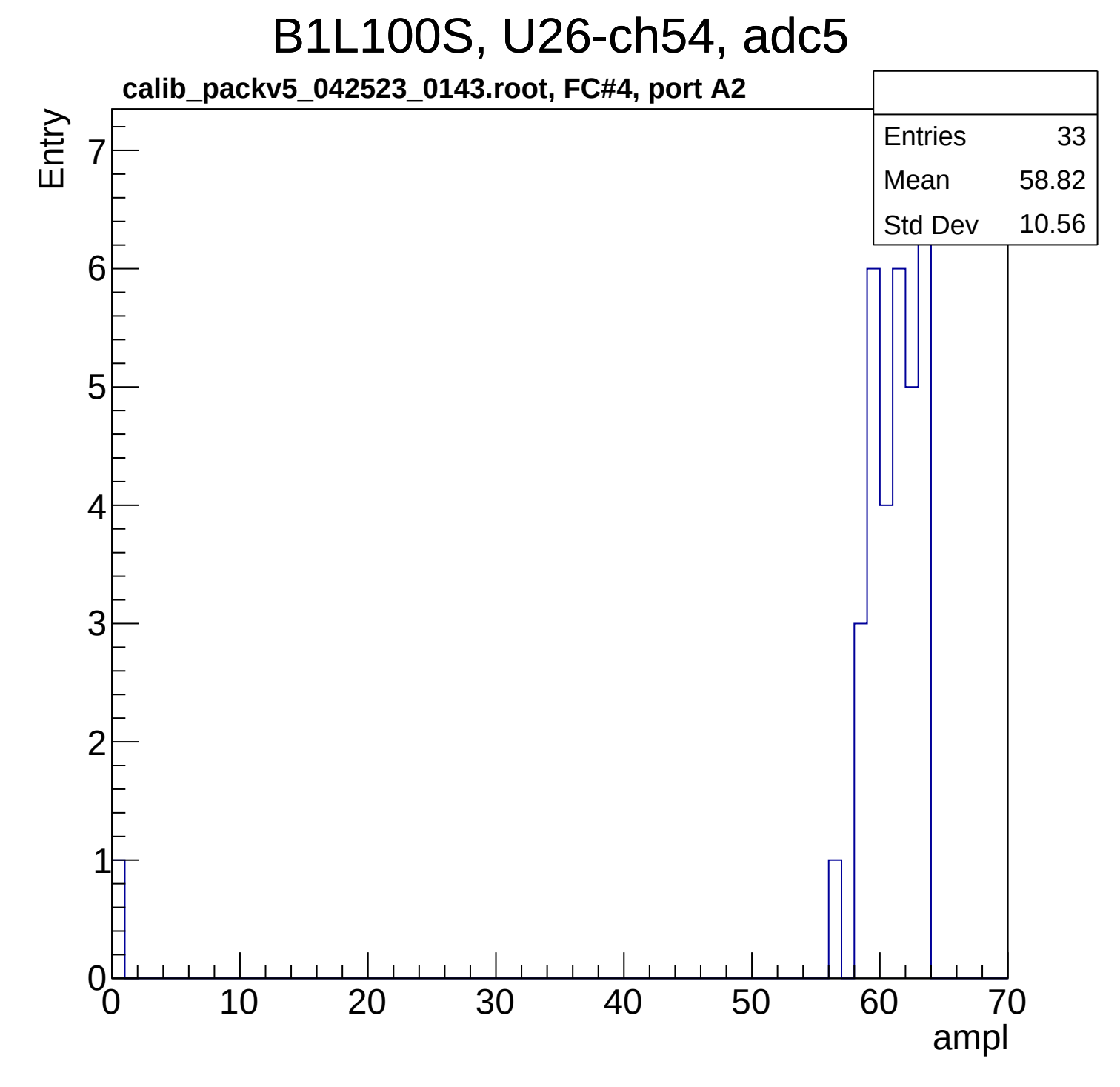
B1L100S, U26-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.82
Std Dev 10.56
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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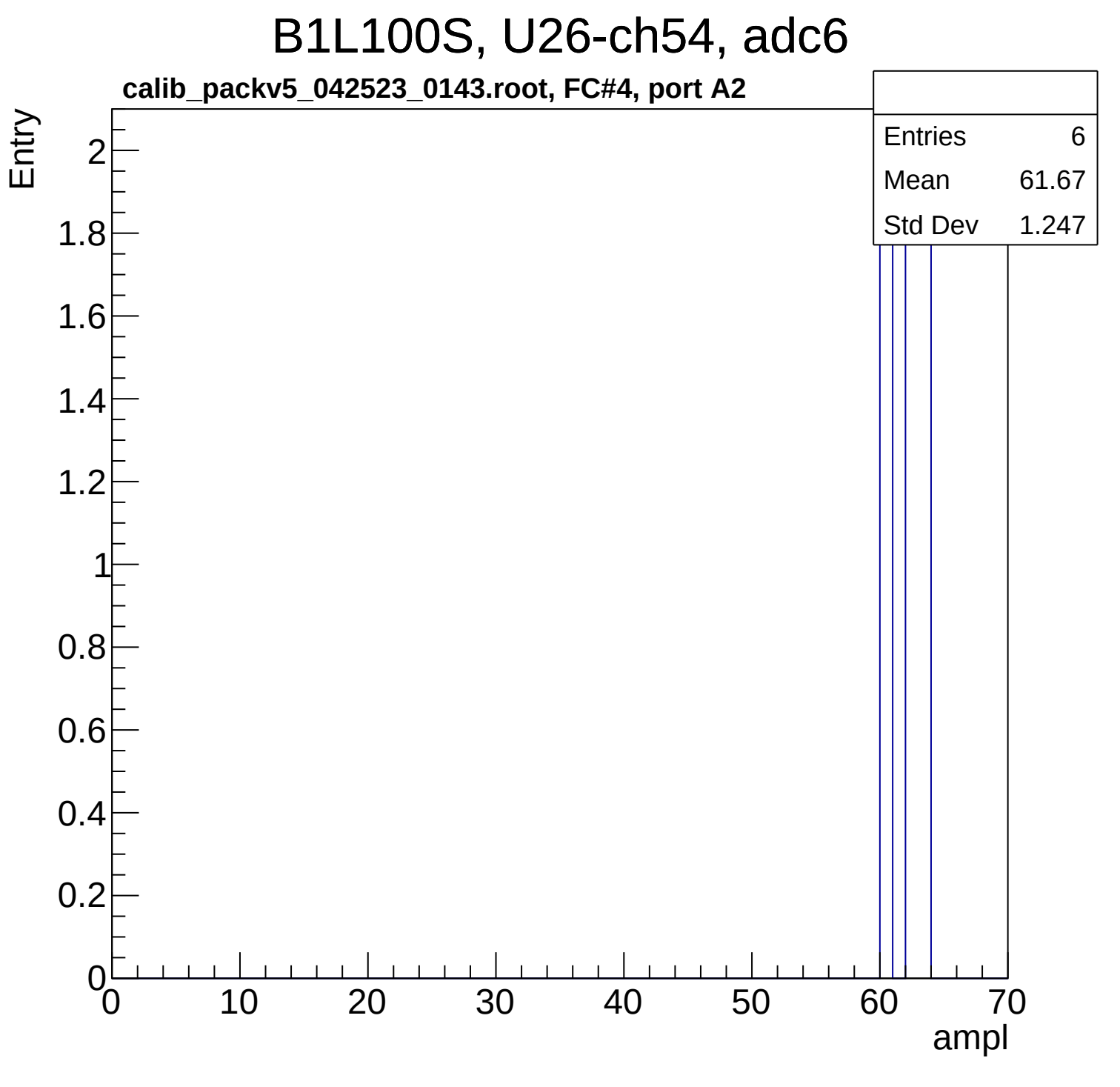
B1L100S, U26-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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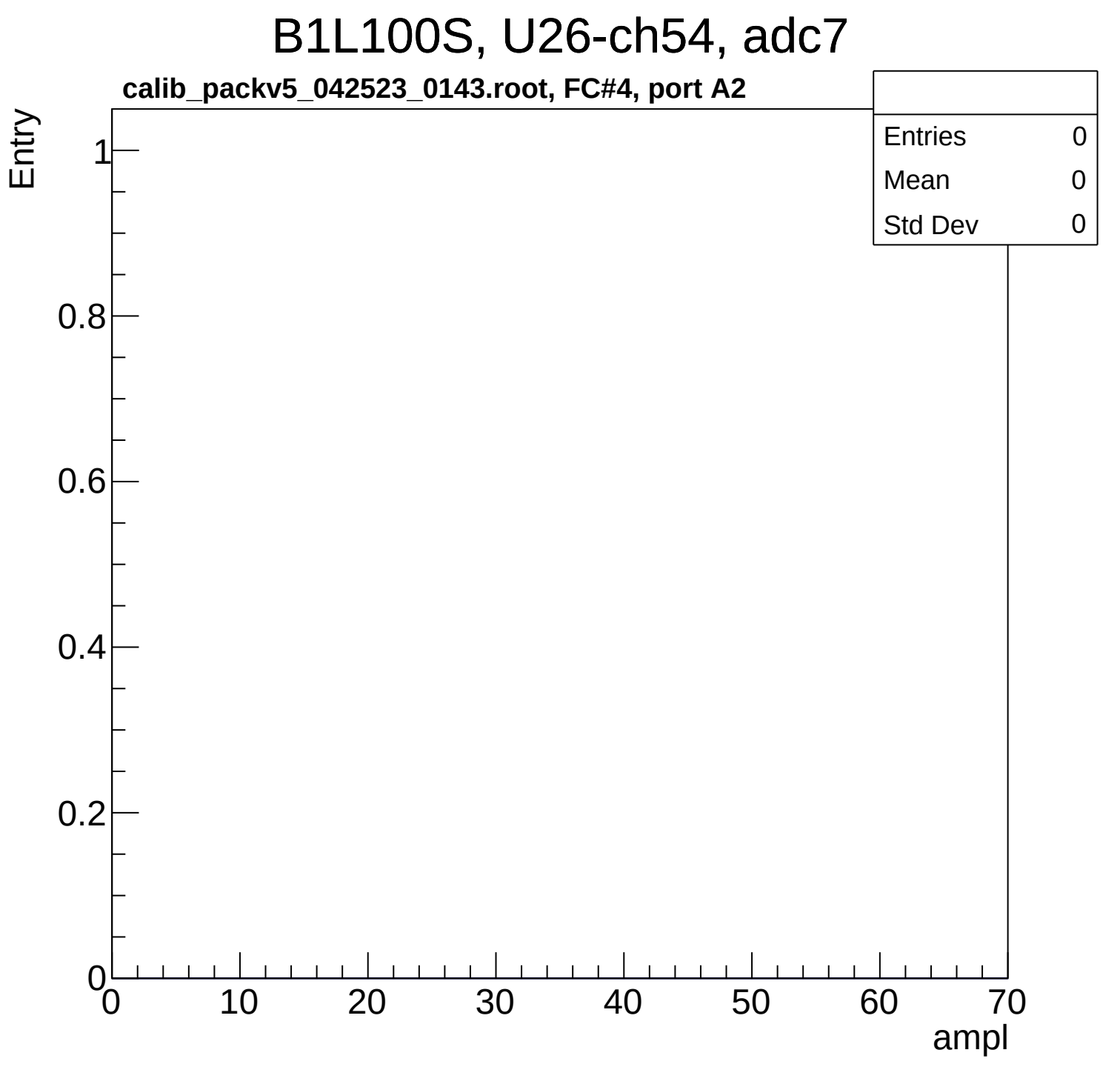
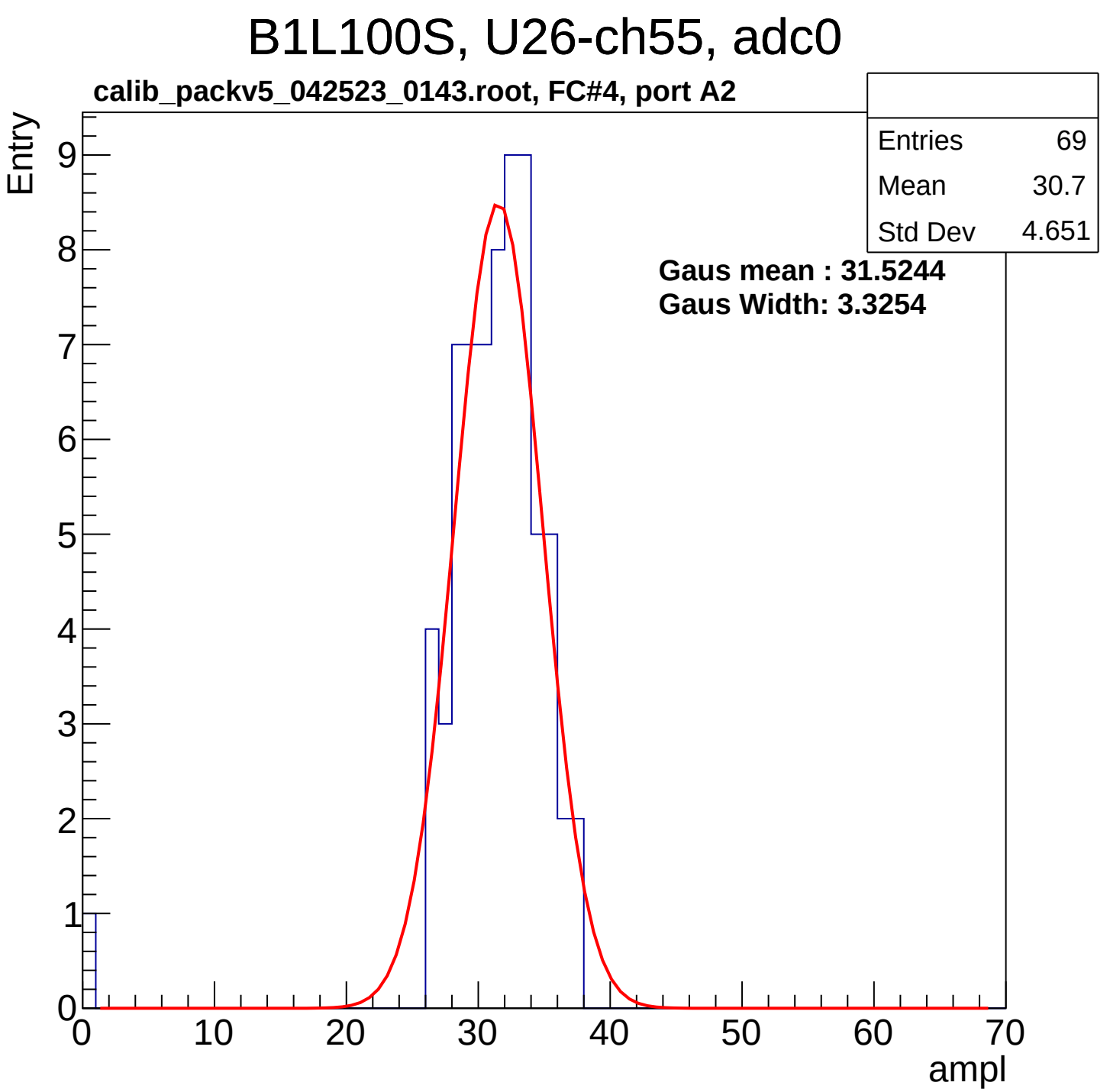

B1L100S, U26-ch54, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 30.7
Std Dev 4.651
Gaus mean : 31.5244
Gaus Width: 3.3254
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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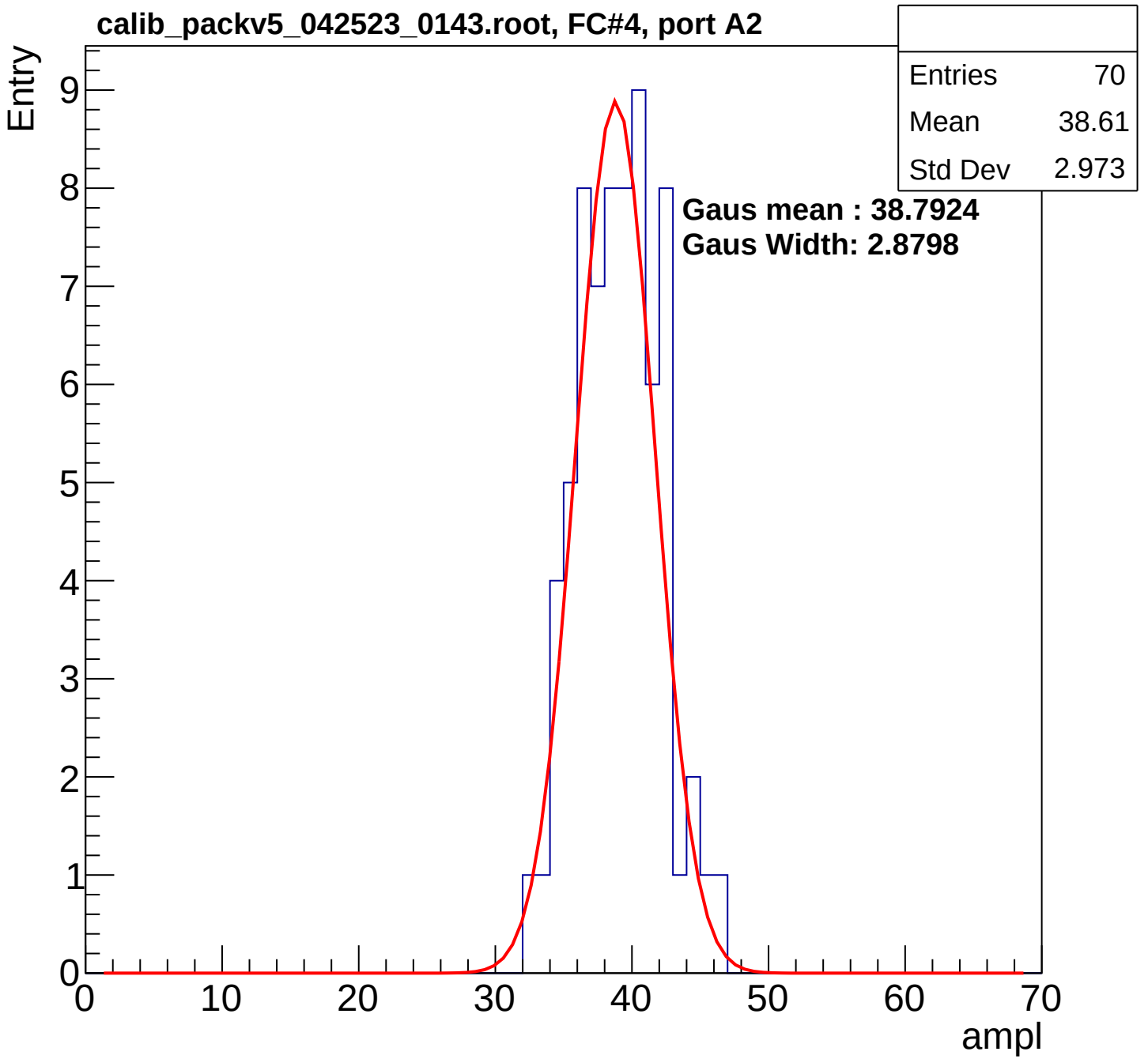
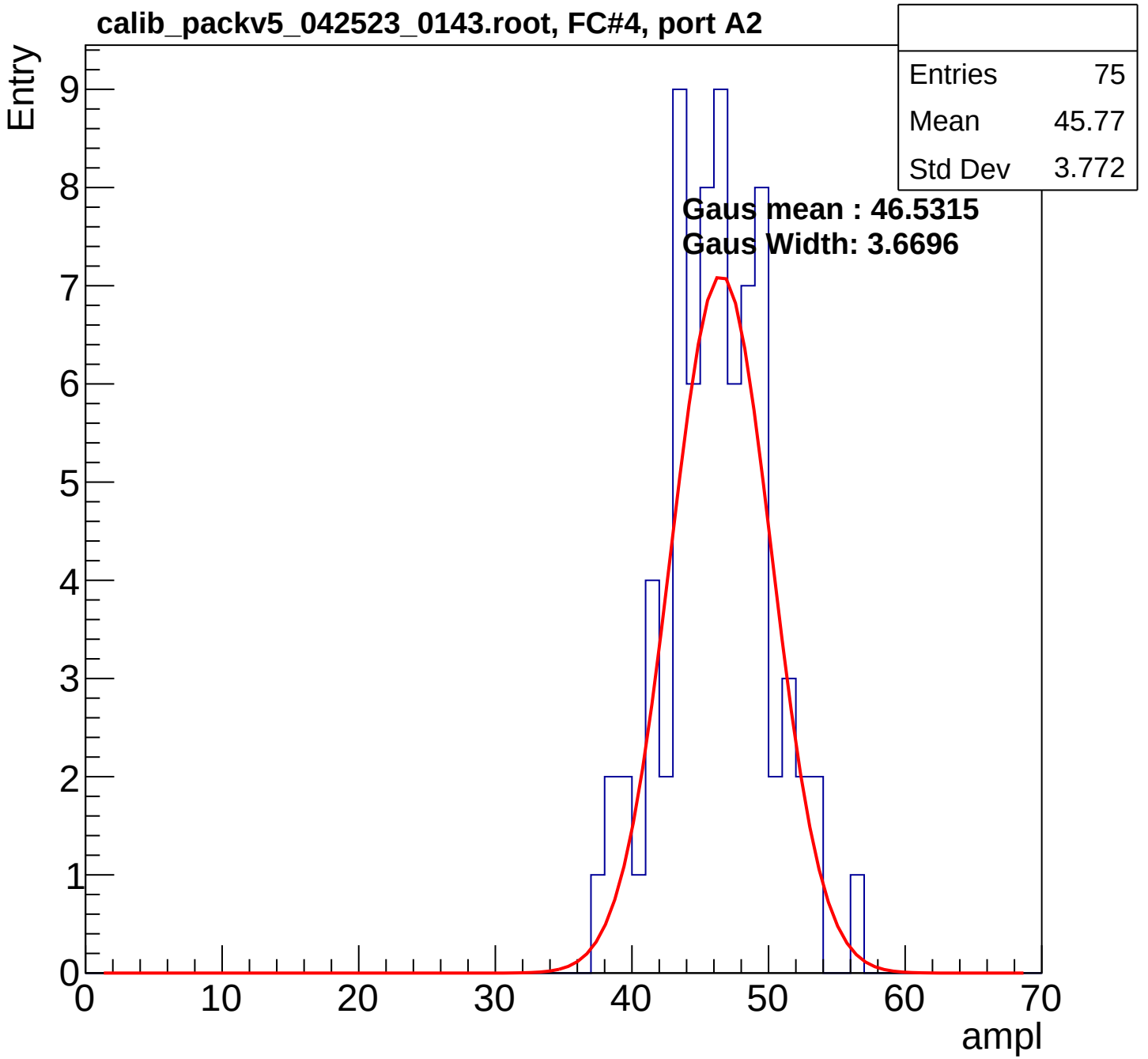

B1L100S, U26-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 38.61
Std Dev 2.973
Gaus mean : 38.7924
Gaus Width: 2.8798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch55, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 45.77
Std Dev 3.772
Gaus mean : 46.5315
Gaus Width: 3.6696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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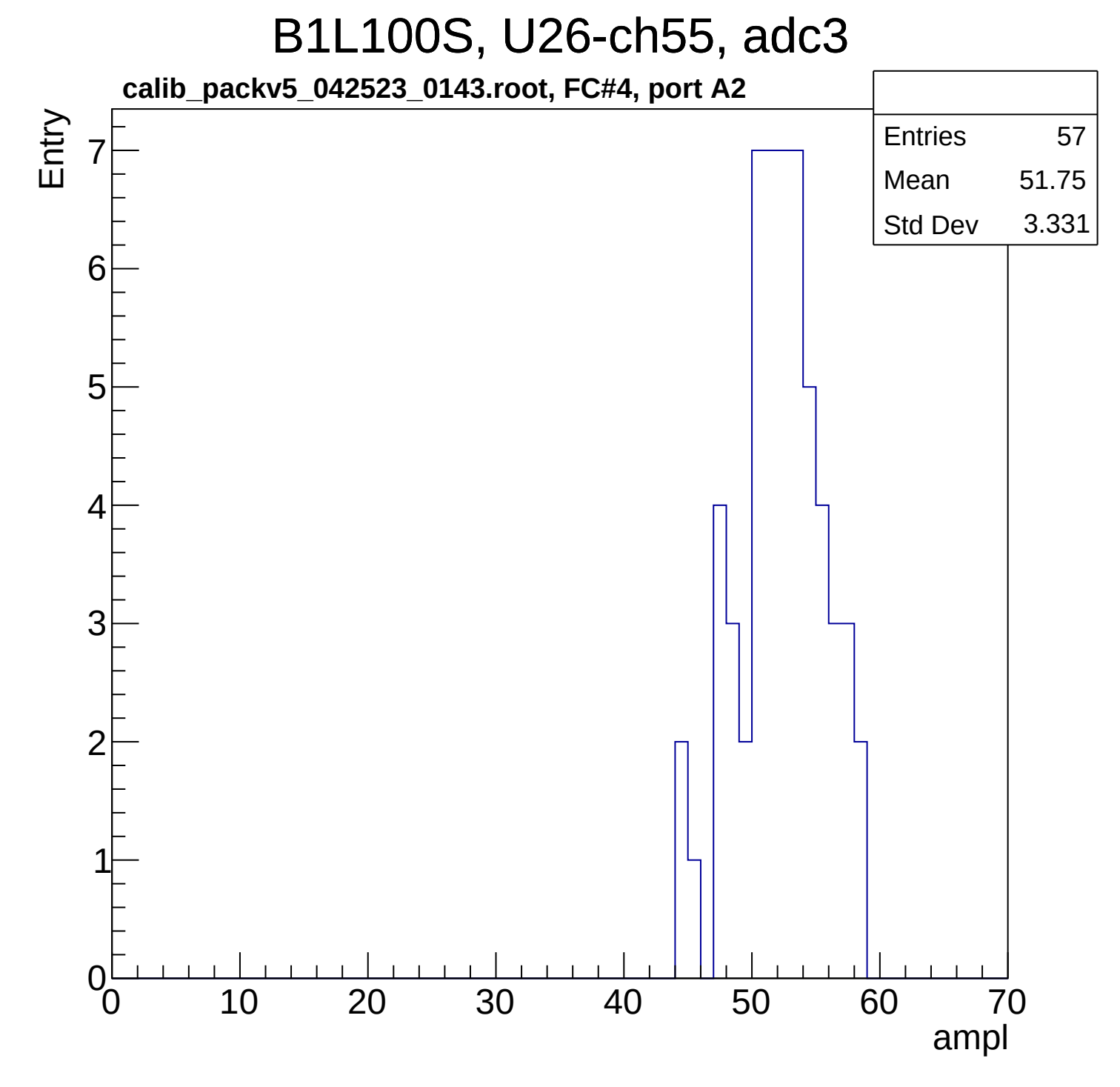
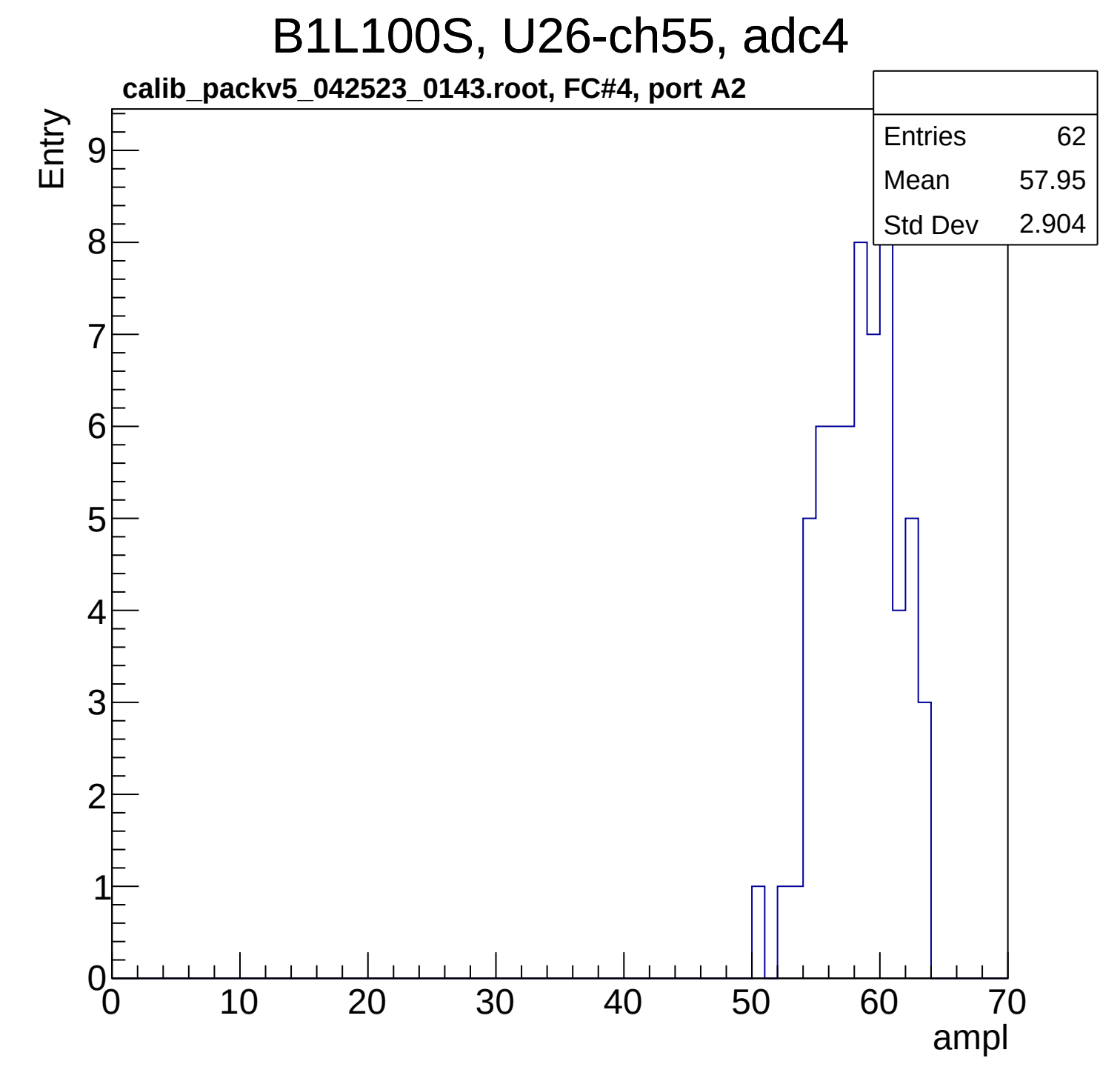

B1L100S, U26-ch55, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 51.75
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.95
Std Dev 2.904
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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60
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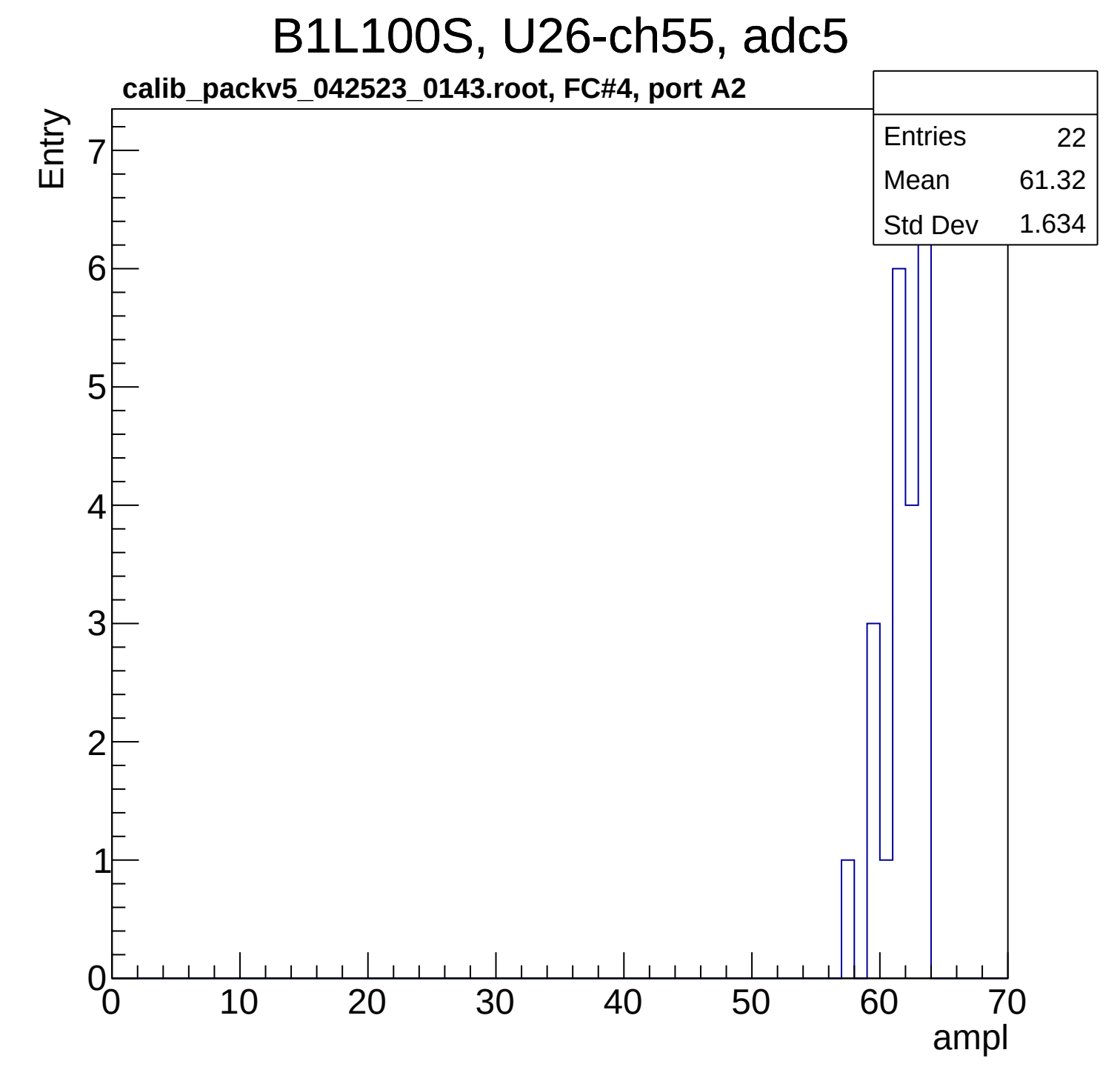
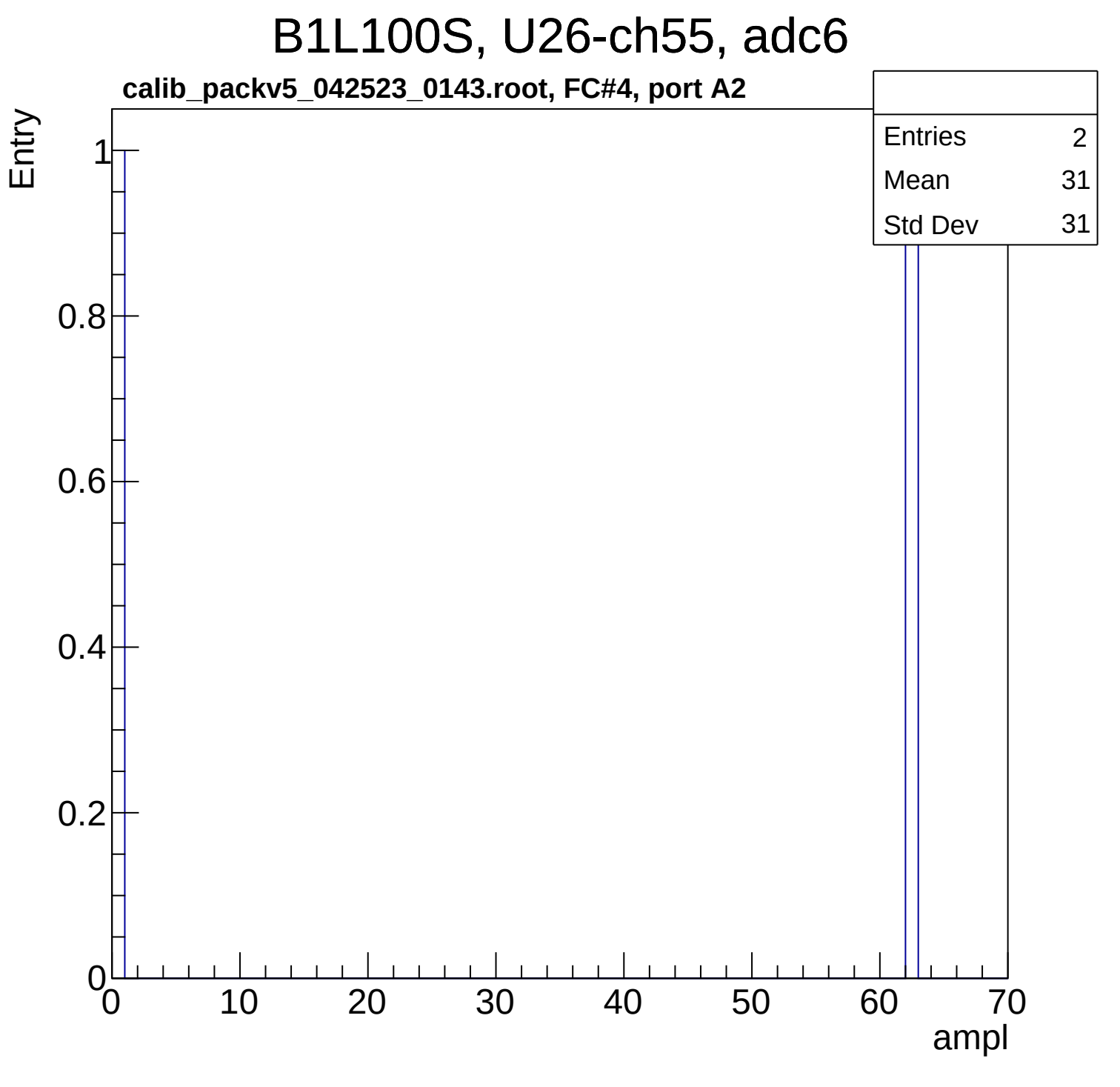
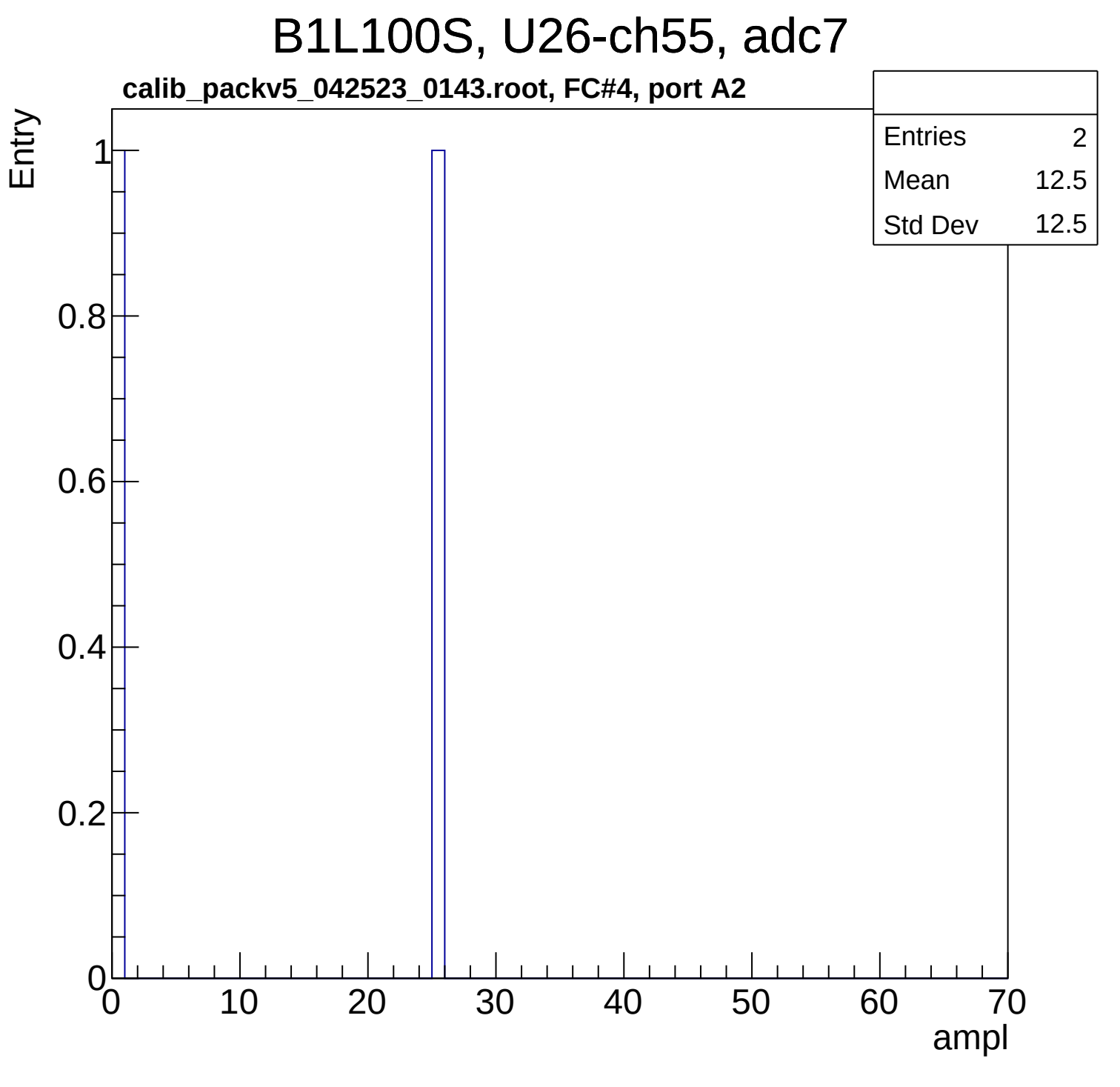
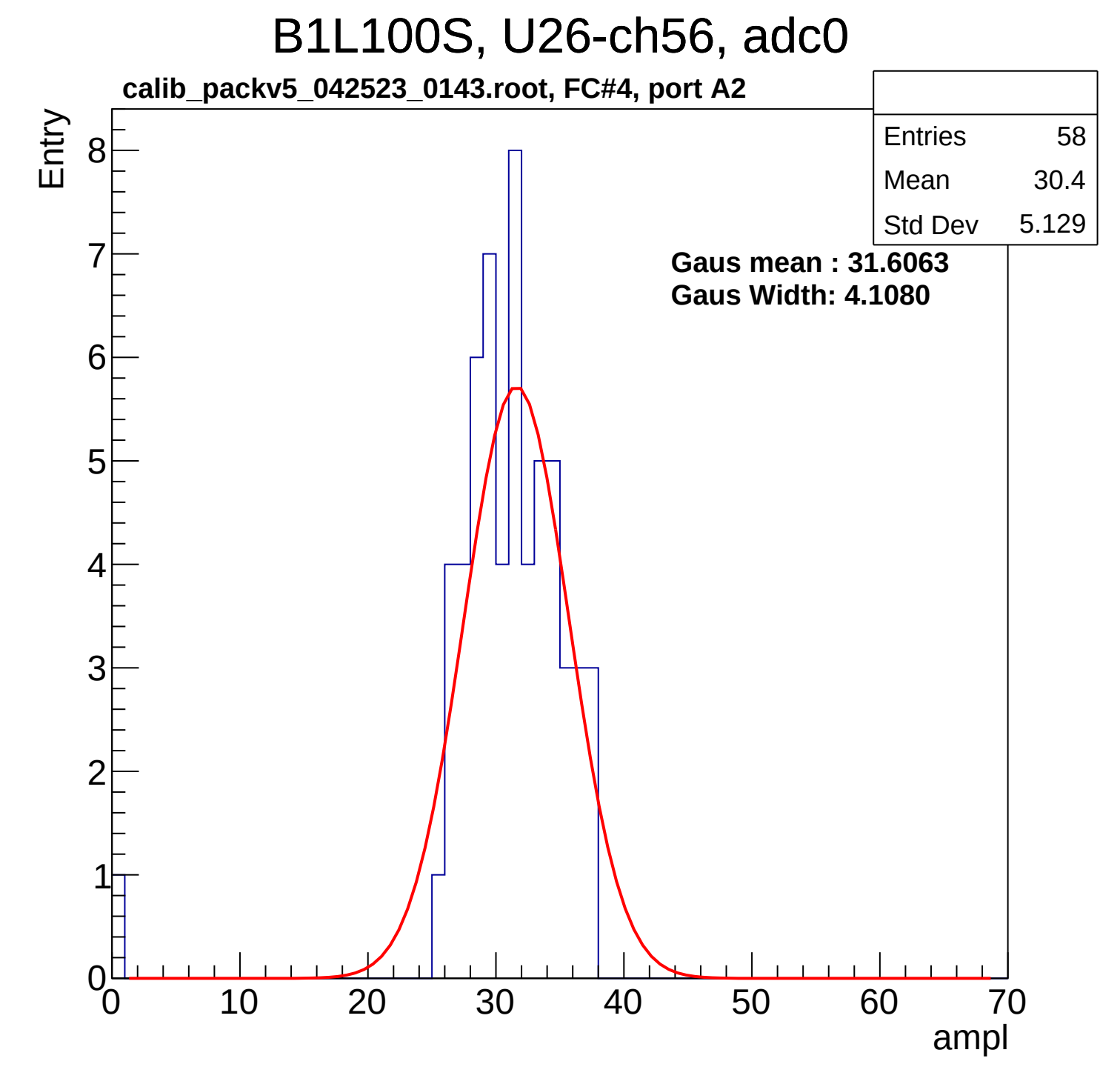
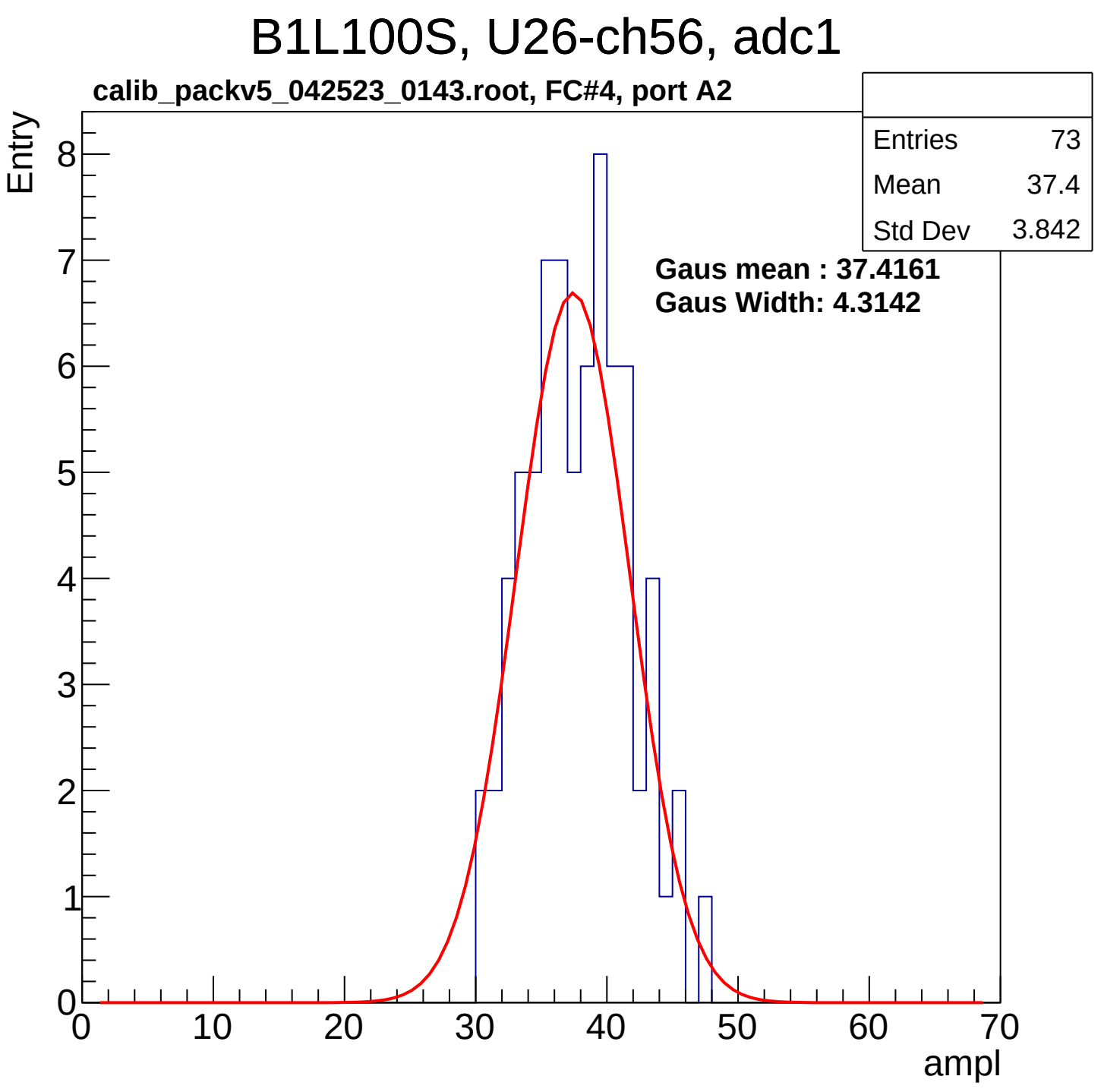
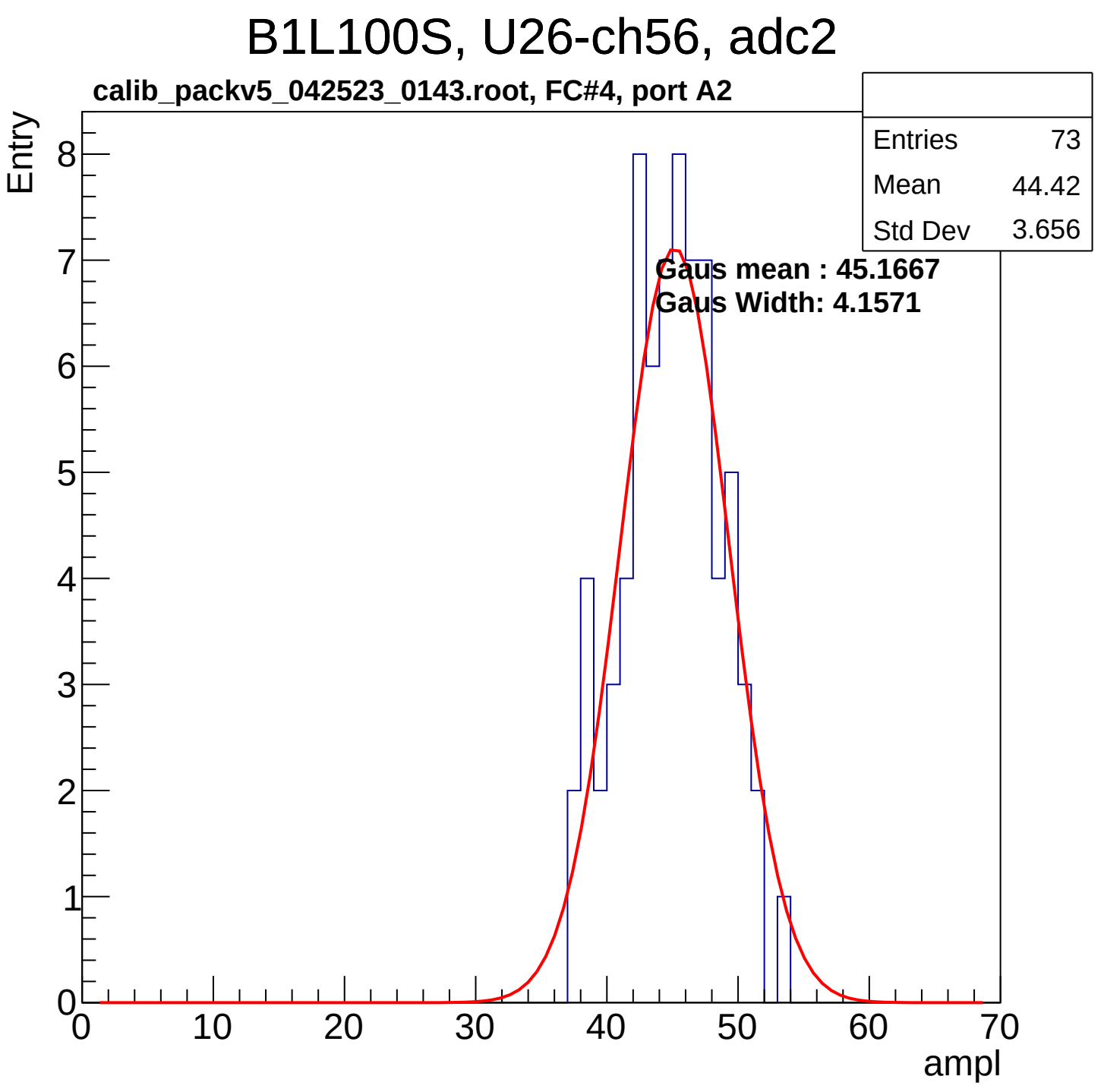

B1L100S, U26-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 61.32
Std Dev 1.634
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

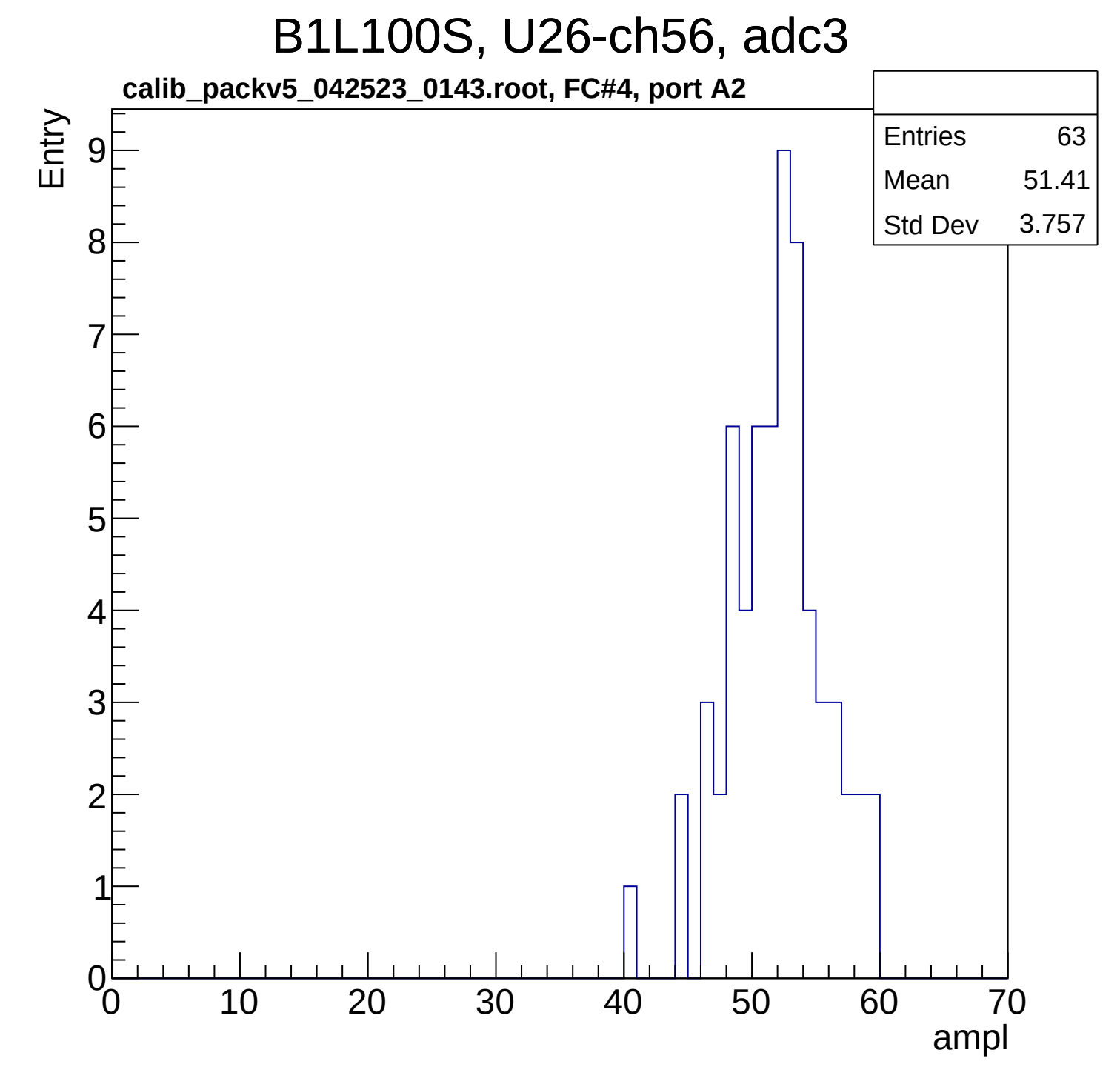

B1L100S, U26-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch55, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 30.4
Std Dev 5.129
Gaus mean : 31.6063
Gaus Width: 4.1080
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.4
Std Dev 3.842
Gaus mean : 37.4161
Gaus Width: 4.3142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 44.42
Std Dev 3.656
Gaus mean : 45.1667
Gaus Width: 4.1571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.41
Std Dev 3.757
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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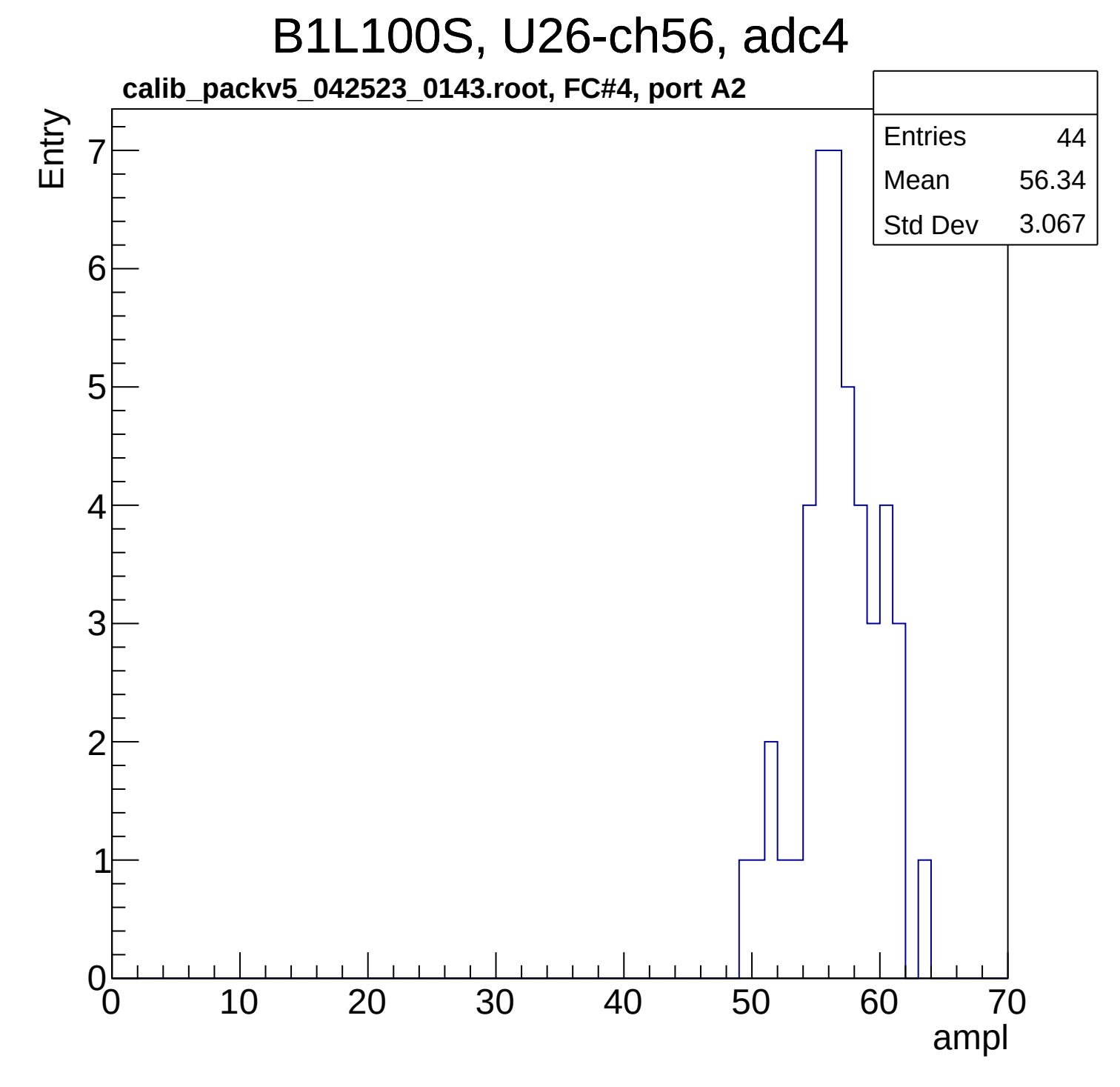
60
70


B1L100S, U26-ch56, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 56.34
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
0
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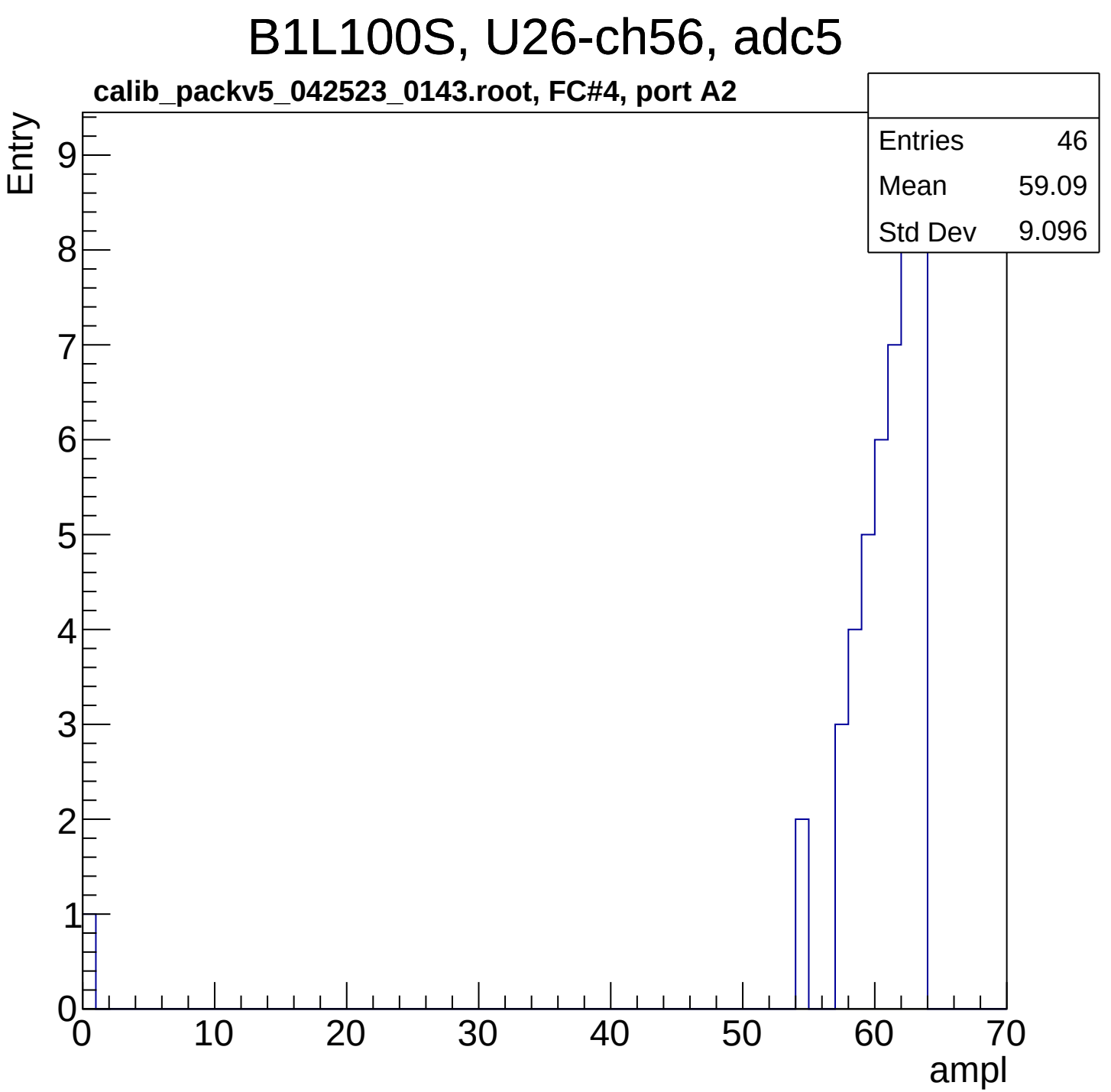
40
50
60
70


B1L100S, U26-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 59.09
Std Dev 9.096
Entry
ampl
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2
3
4
5
6
7
8
9
0
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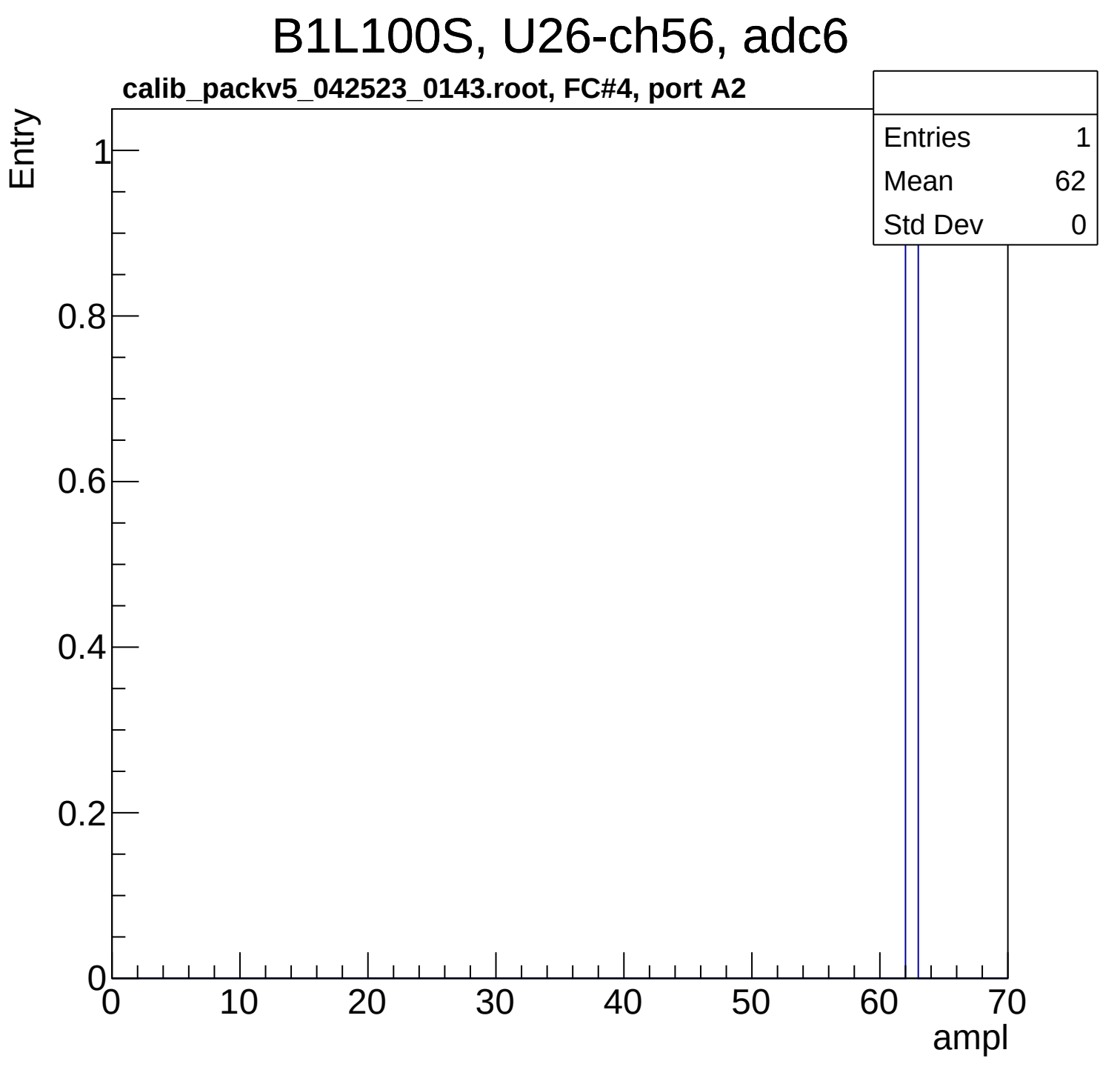
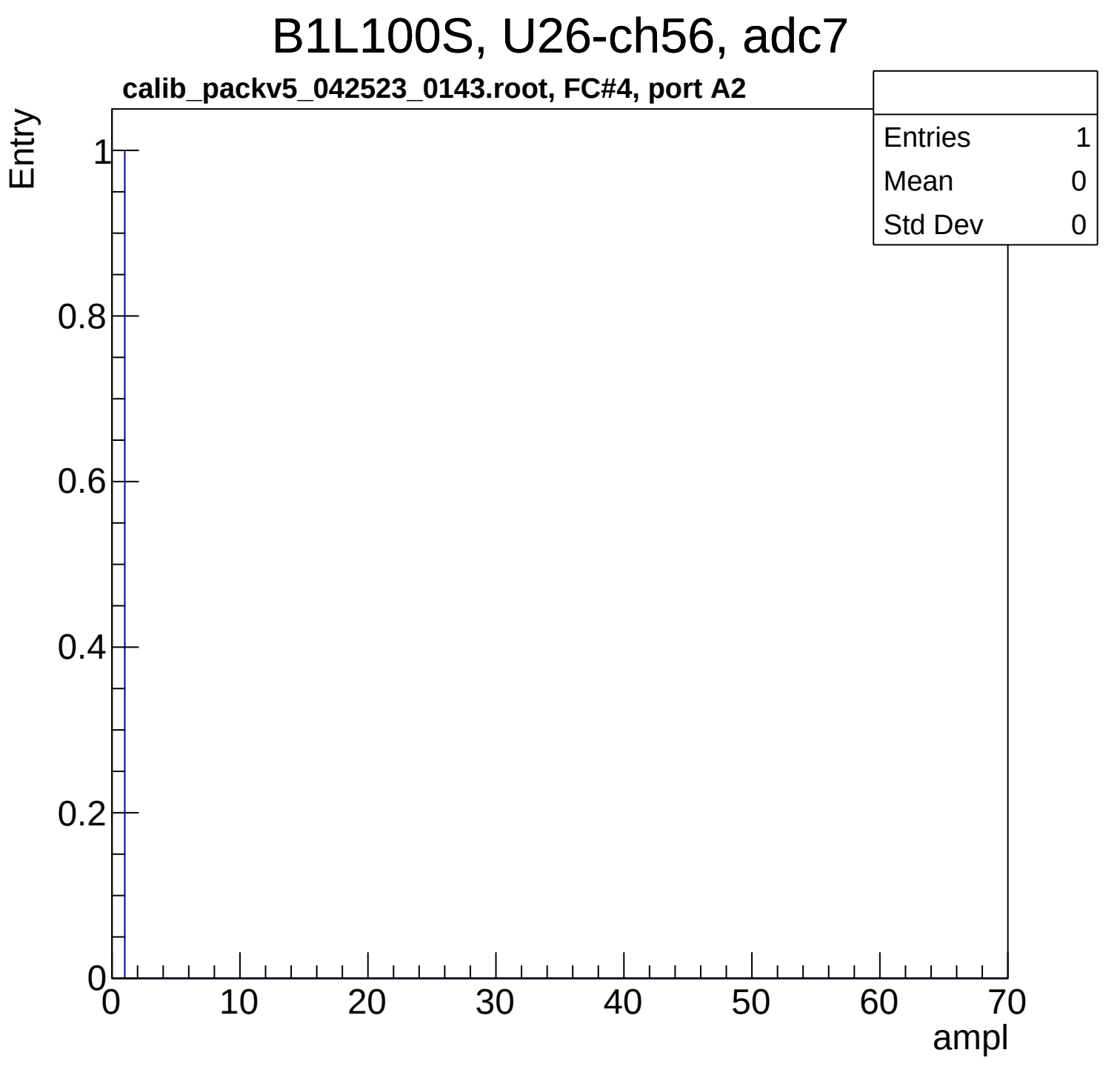
20
30
40
50
60
70


B1L100S, U26-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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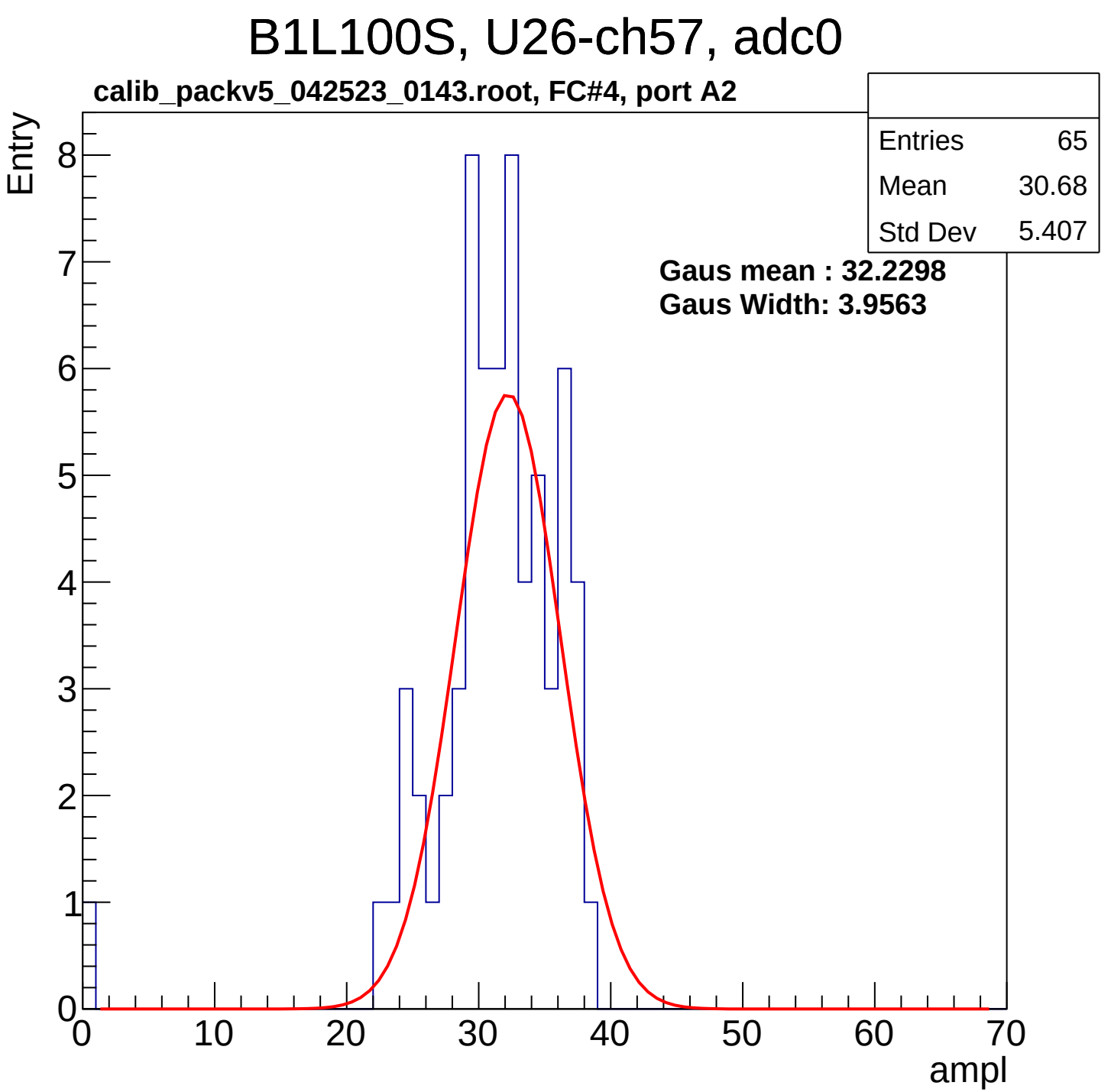
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.68
Std Dev 5.407
Gaus mean : 32.2298
Gaus Width: 3.9563
Entry
ampl
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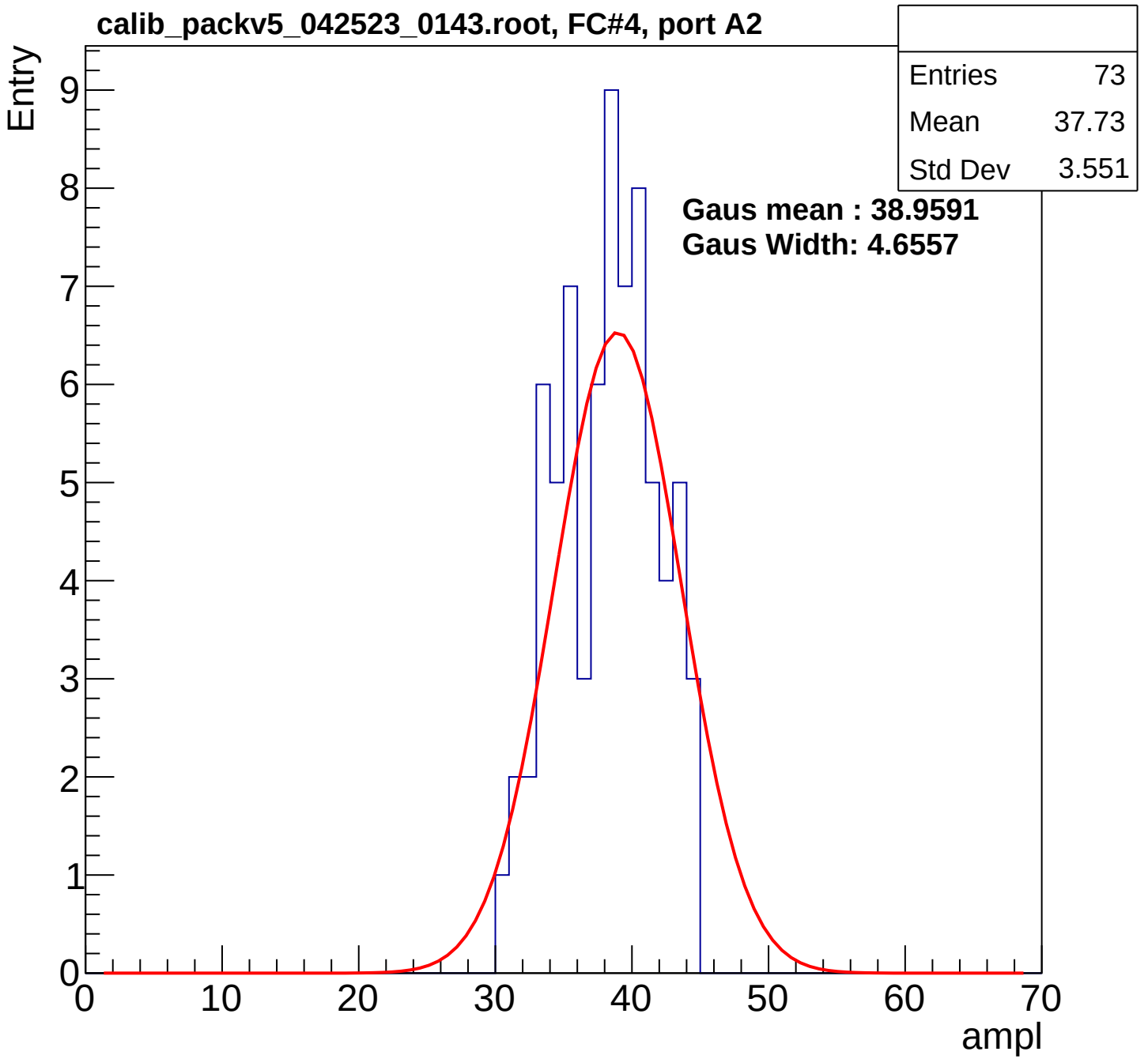
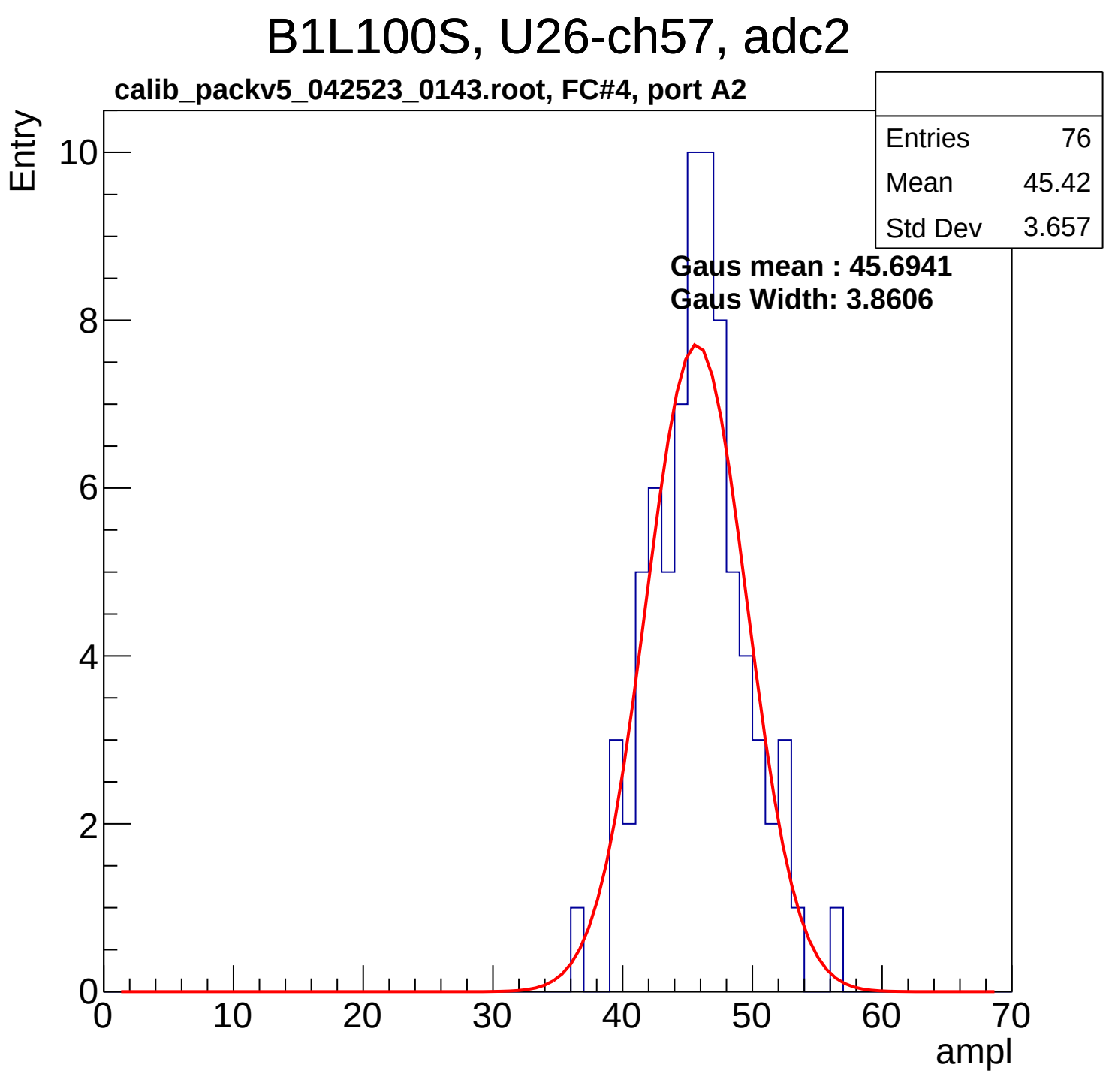
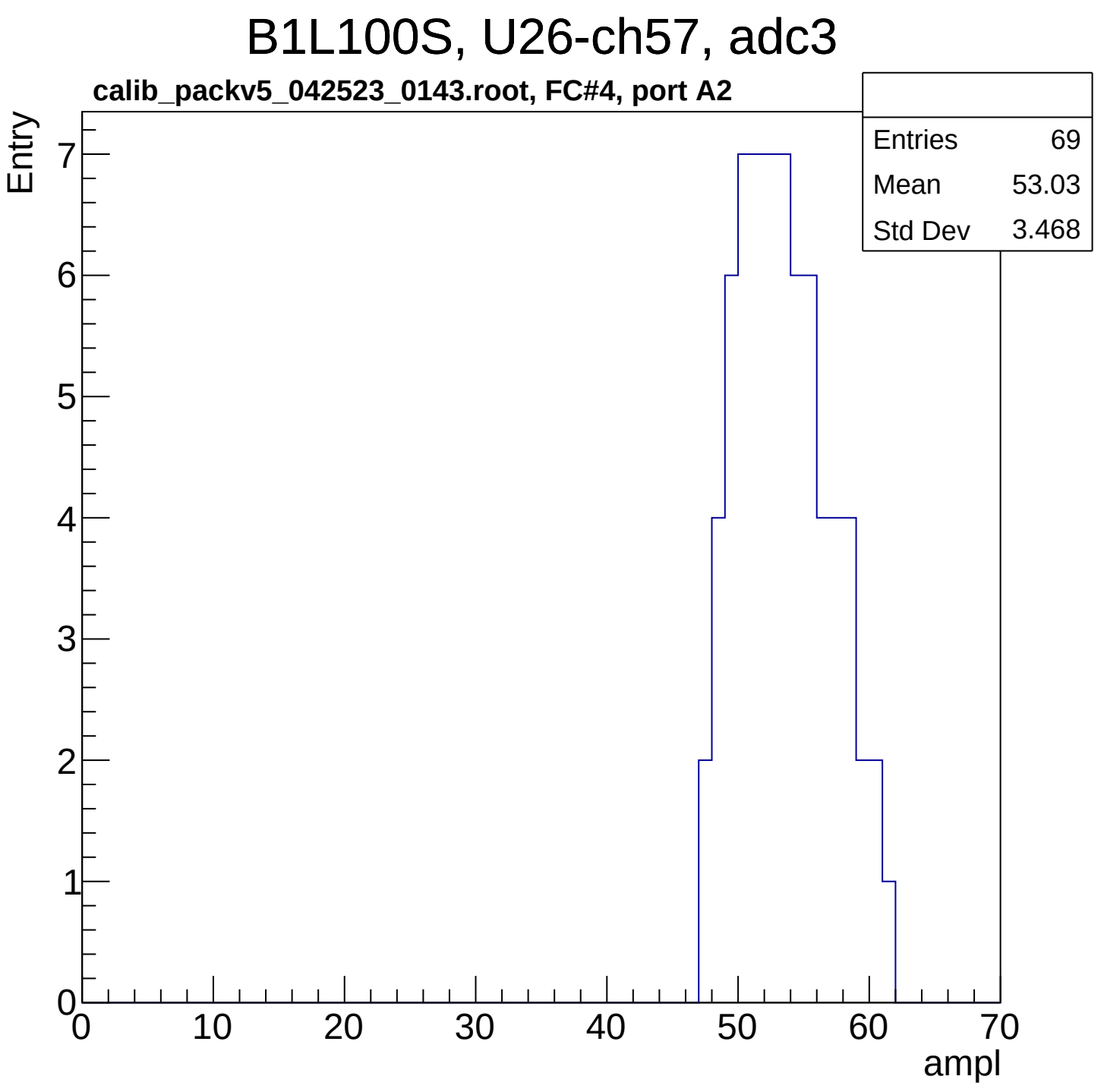
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.73
Std Dev 3.551
Gaus mean : 38.9591
Gaus Width: 4.6557
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 45.42
Std Dev 3.657
Gaus mean : 45.6941
Gaus Width: 3.8606
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch57, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 53.03
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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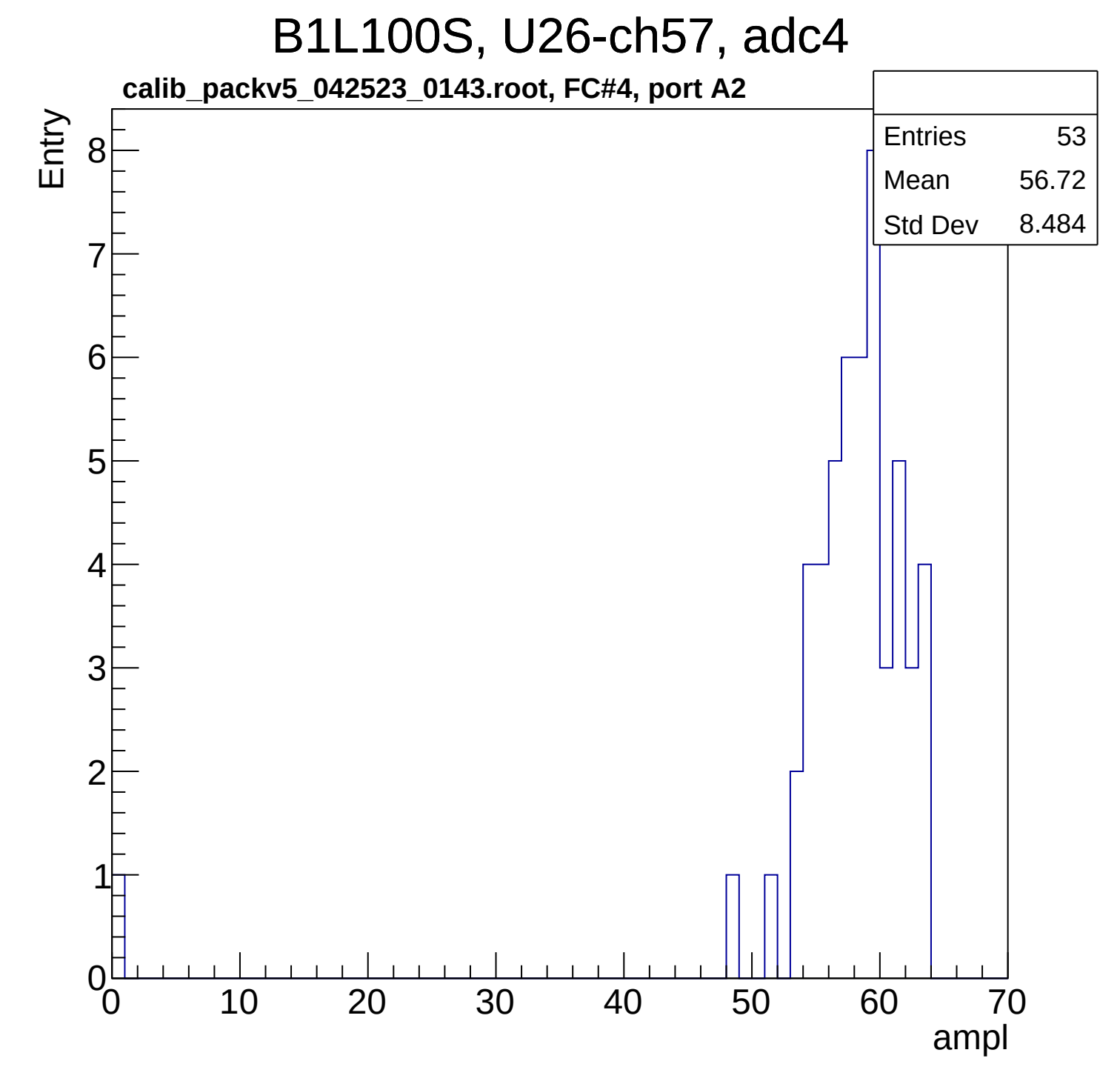
B1L100S, U26-ch57, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.72
Std Dev 8.484
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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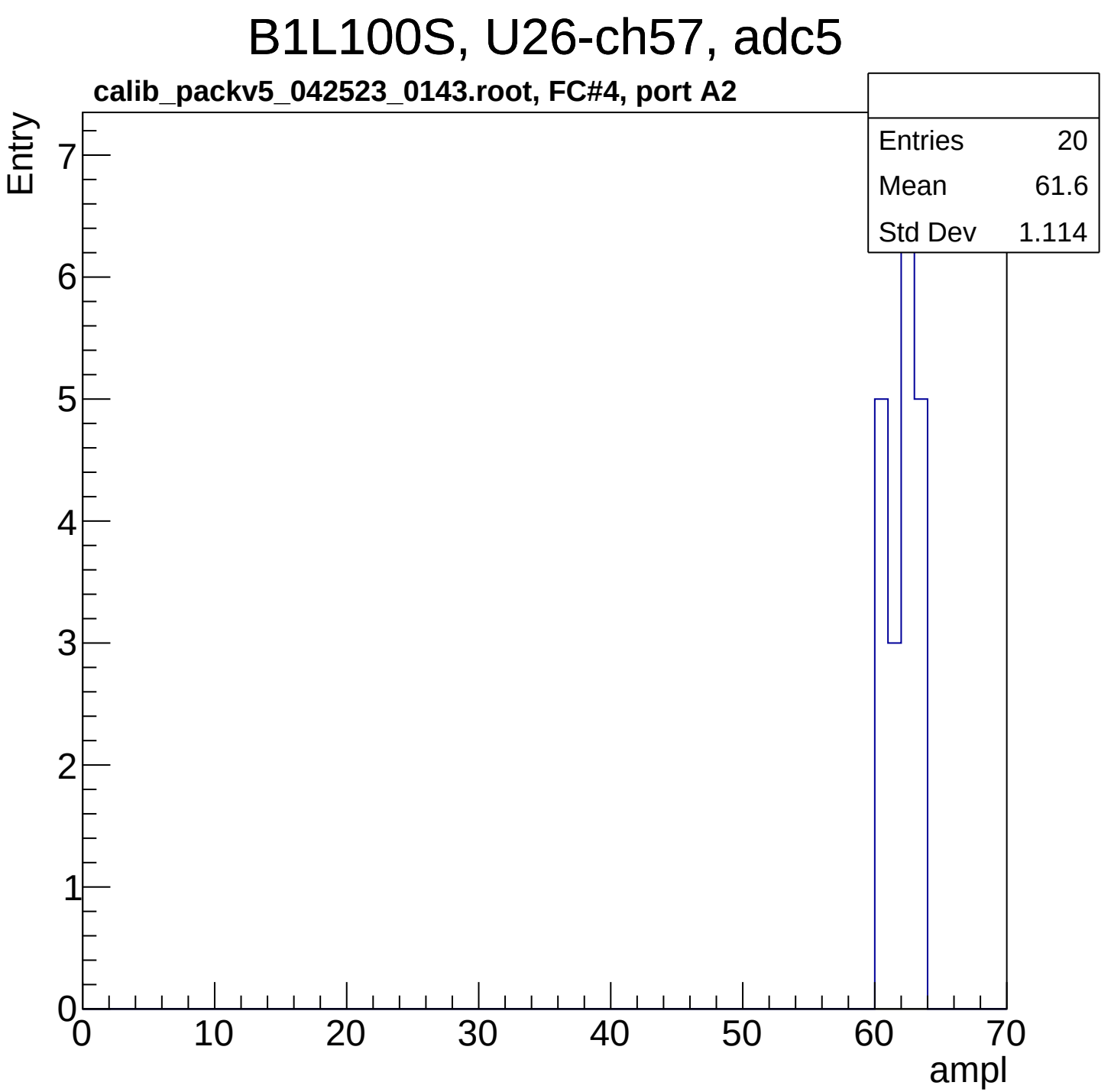
B1L100S, U26-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 20
Mean 61.6
Std Dev 1.114
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
30
40
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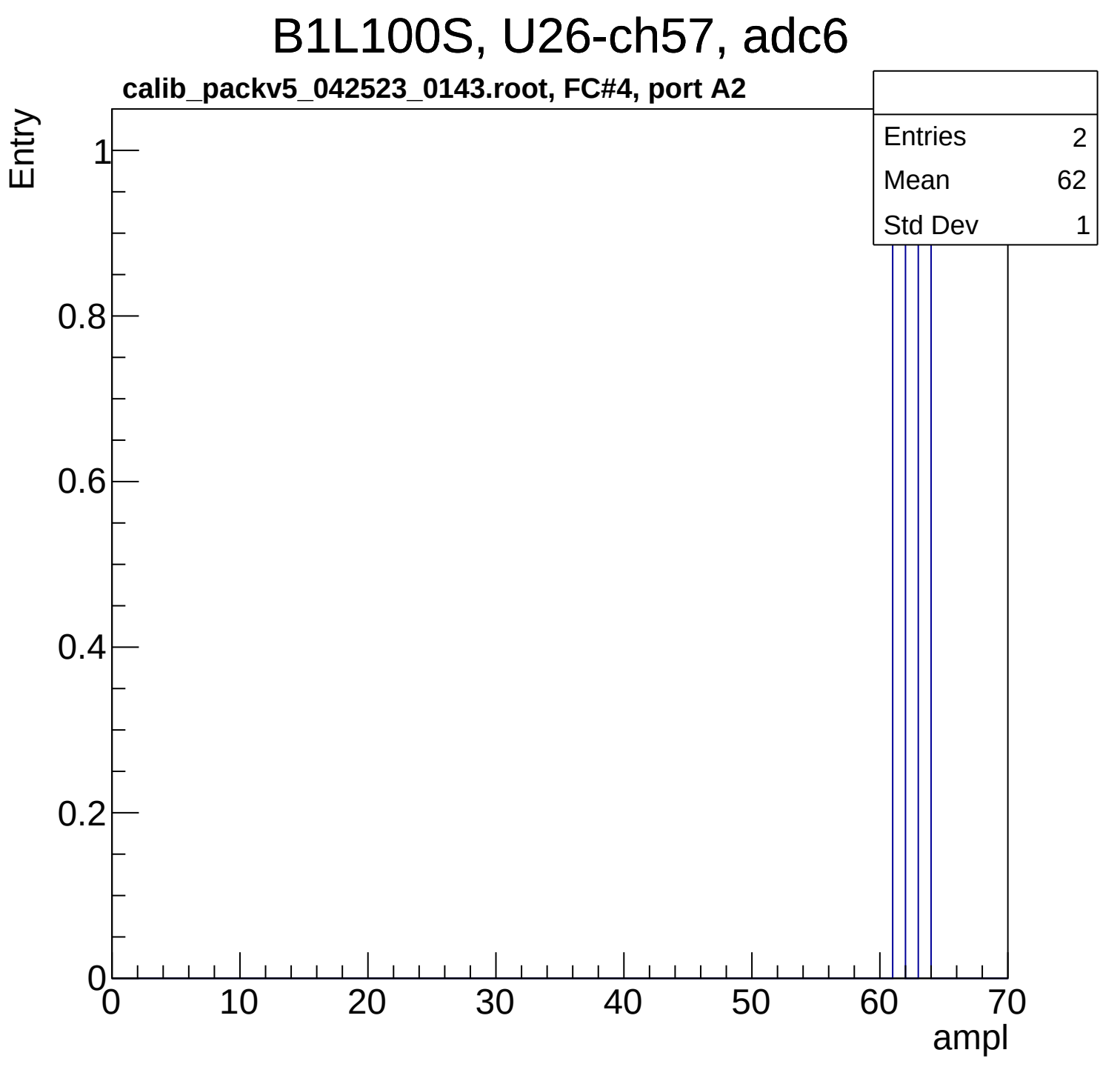
B1L100S, U26-ch57, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
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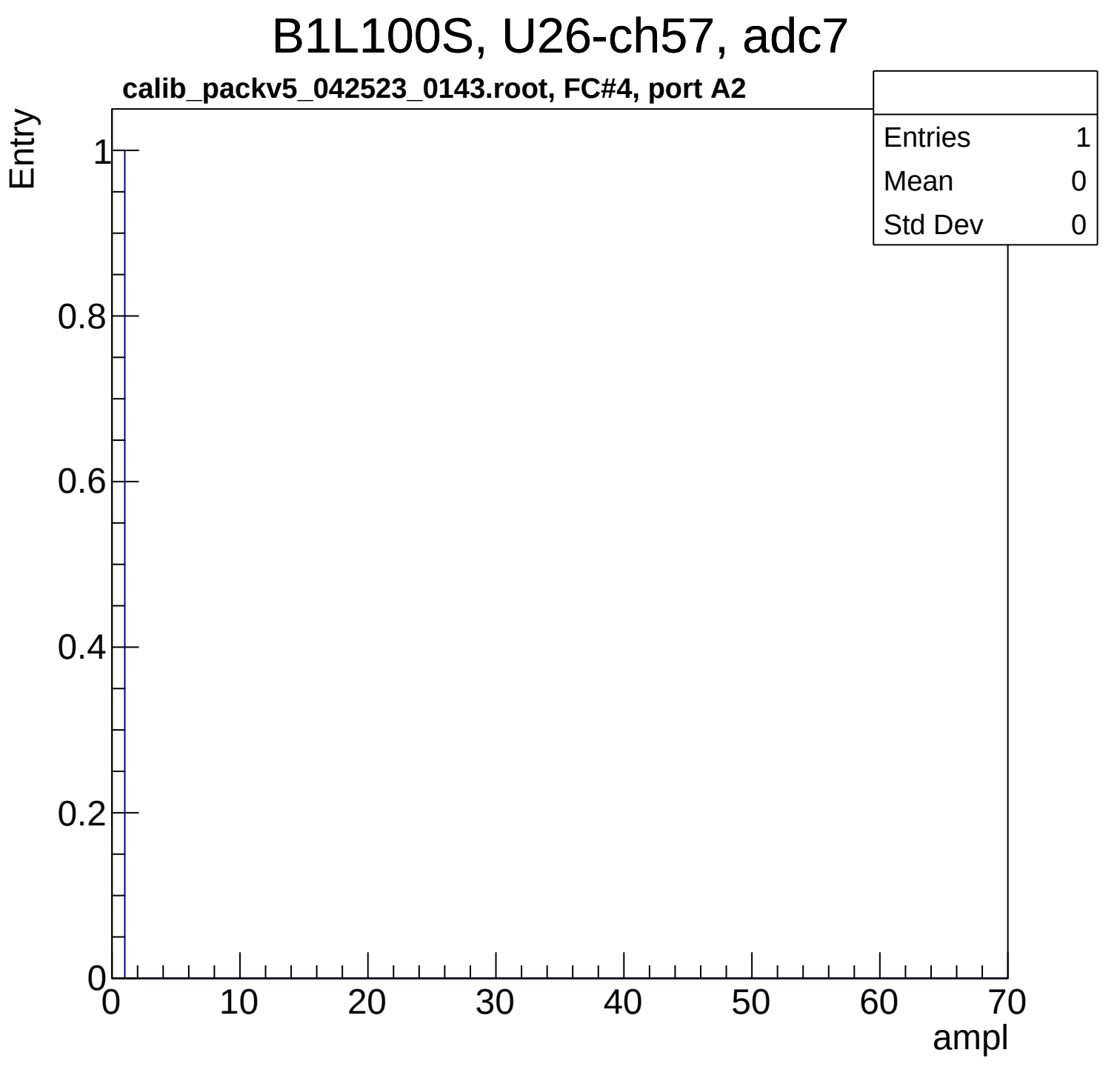
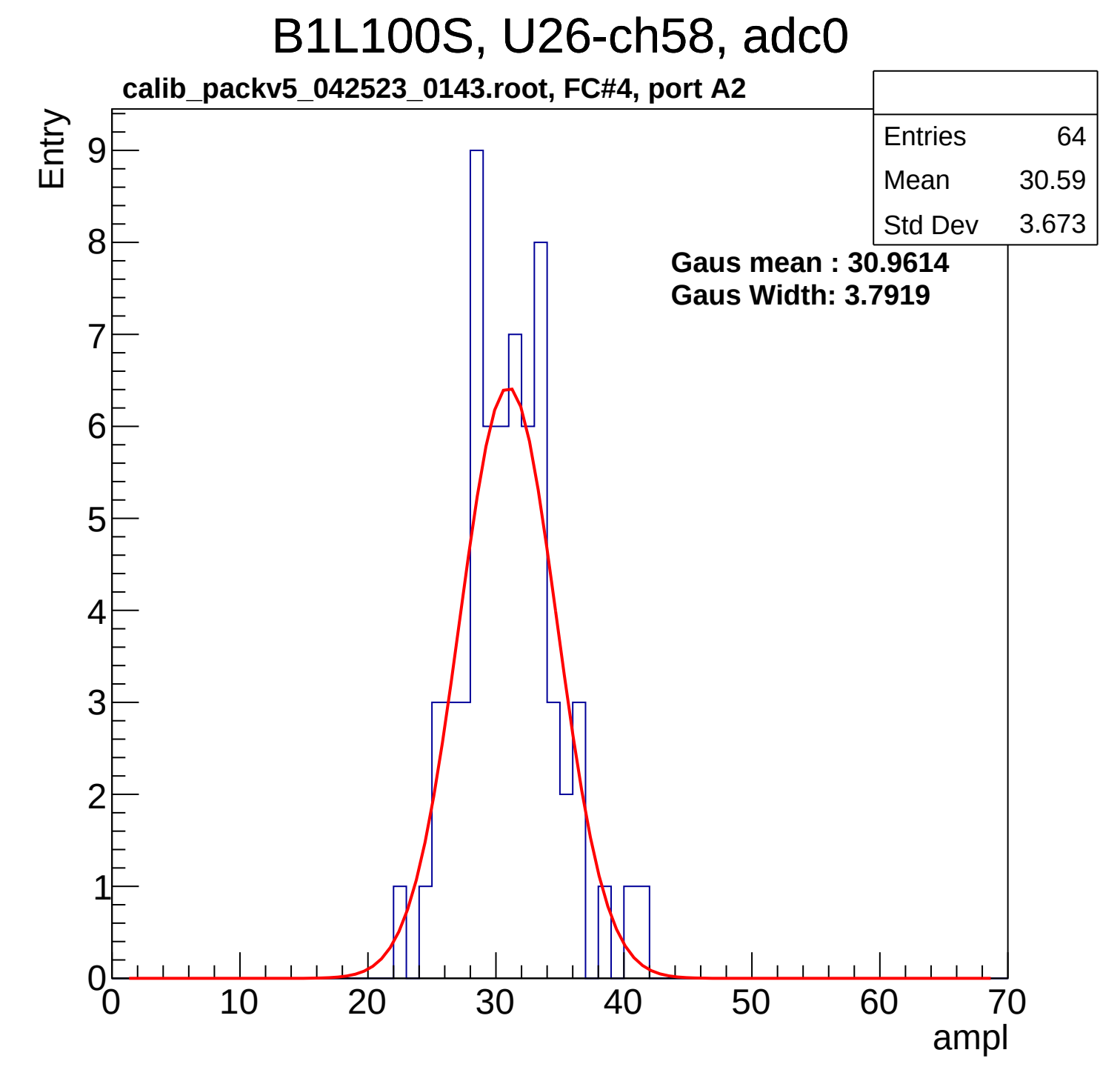
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.59
Std Dev 3.673
Gaus mean : 30.9614
Gaus Width: 3.7919
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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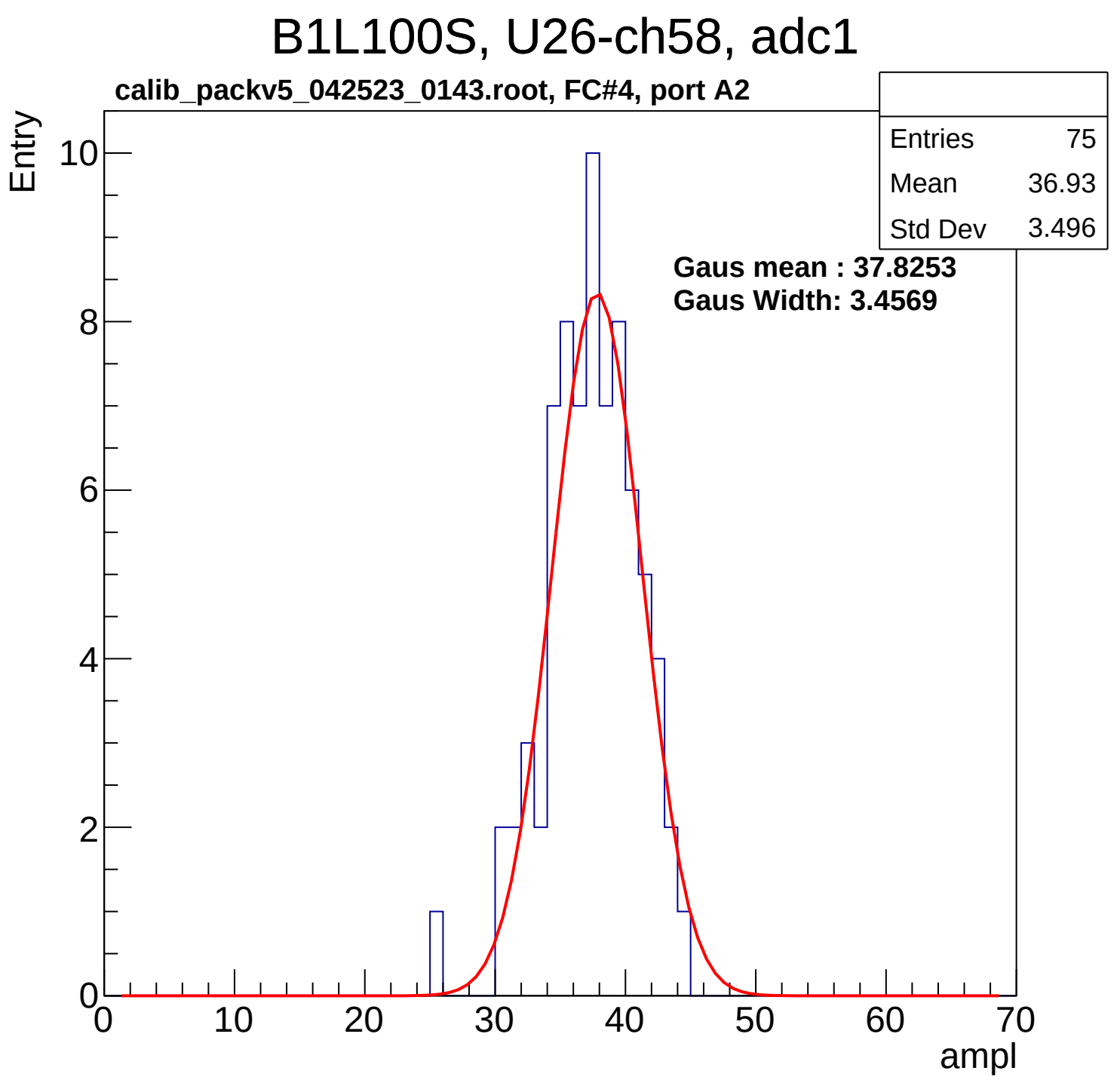
B1L100S, U26-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.93
Std Dev 3.496
Gaus mean : 37.8253
Gaus Width: 3.4569
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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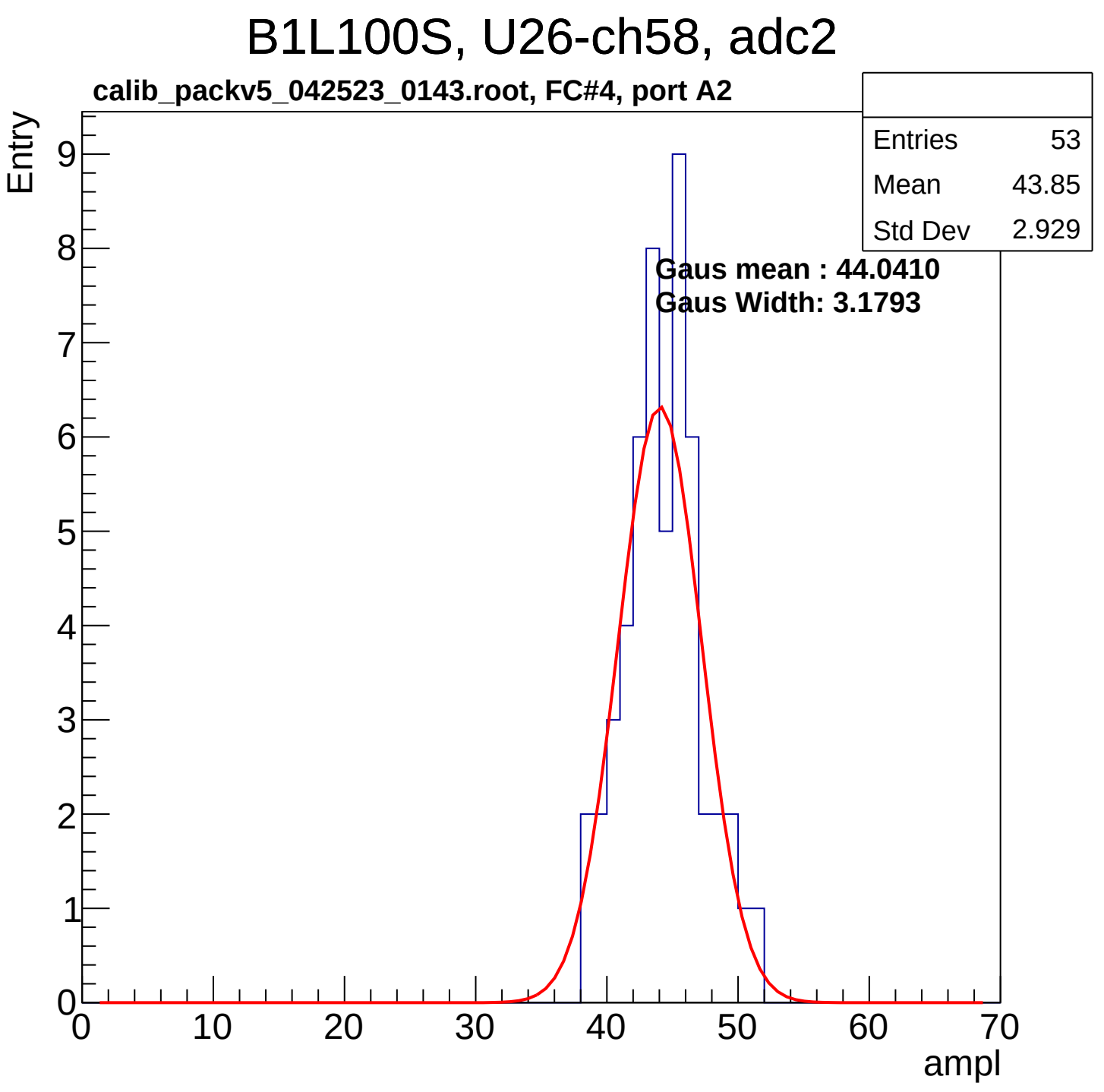
B1L100S, U26-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 43.85
Std Dev 2.929
Gaus mean : 44.0410
Gaus Width: 3.1793
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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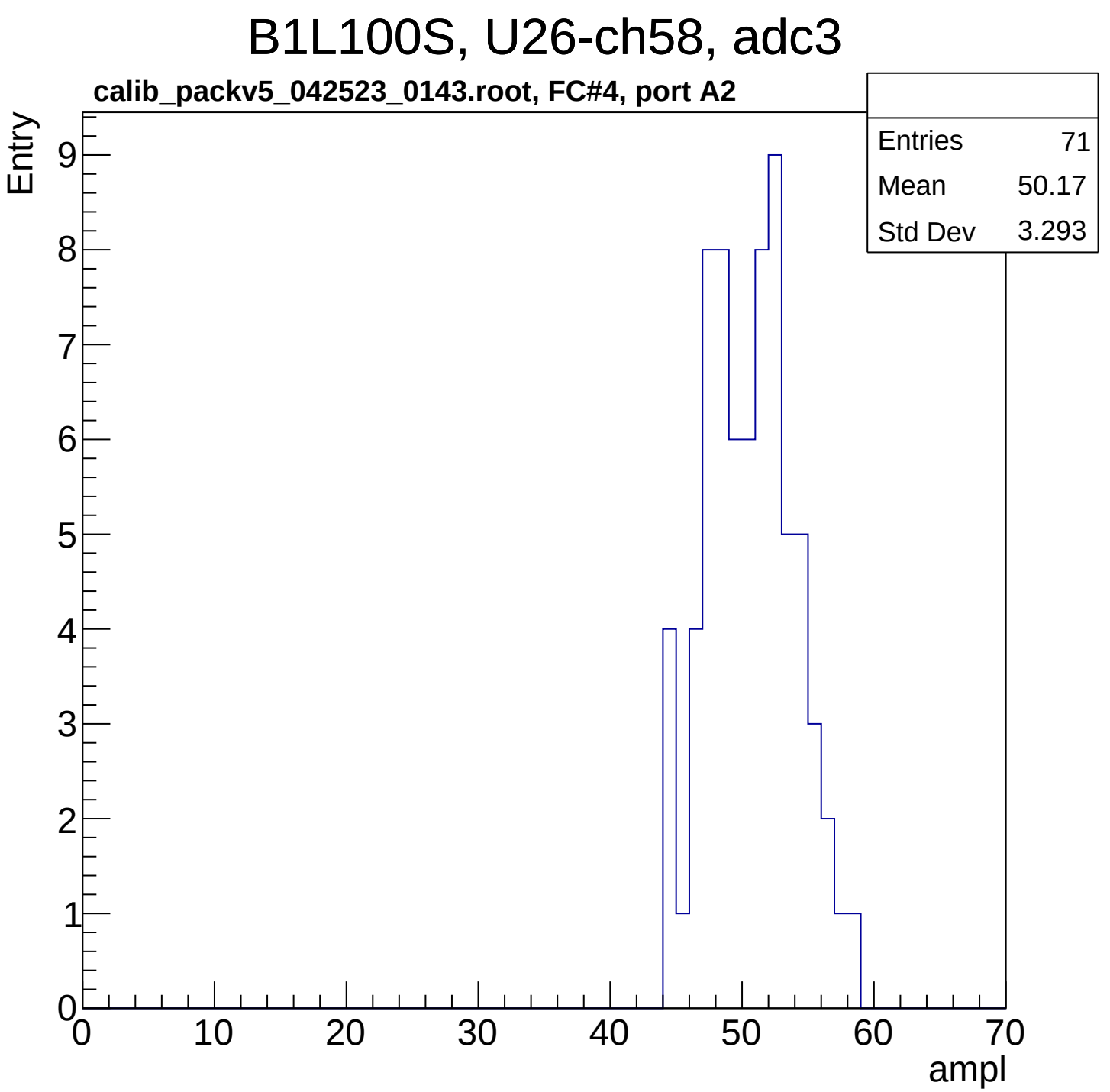
B1L100S, U26-ch58, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.17
Std Dev 3.293
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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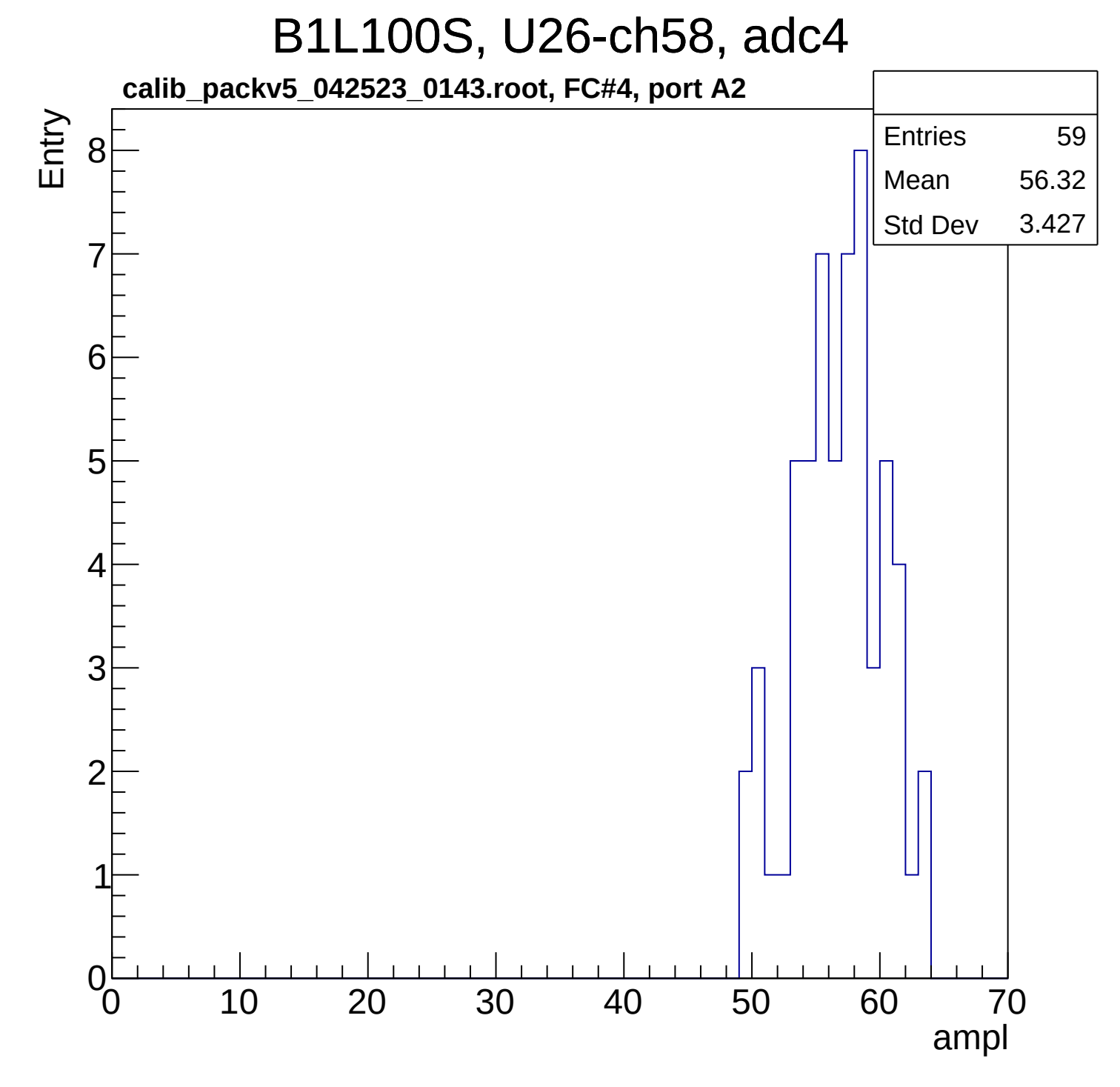
B1L100S, U26-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 56.32
Std Dev 3.427
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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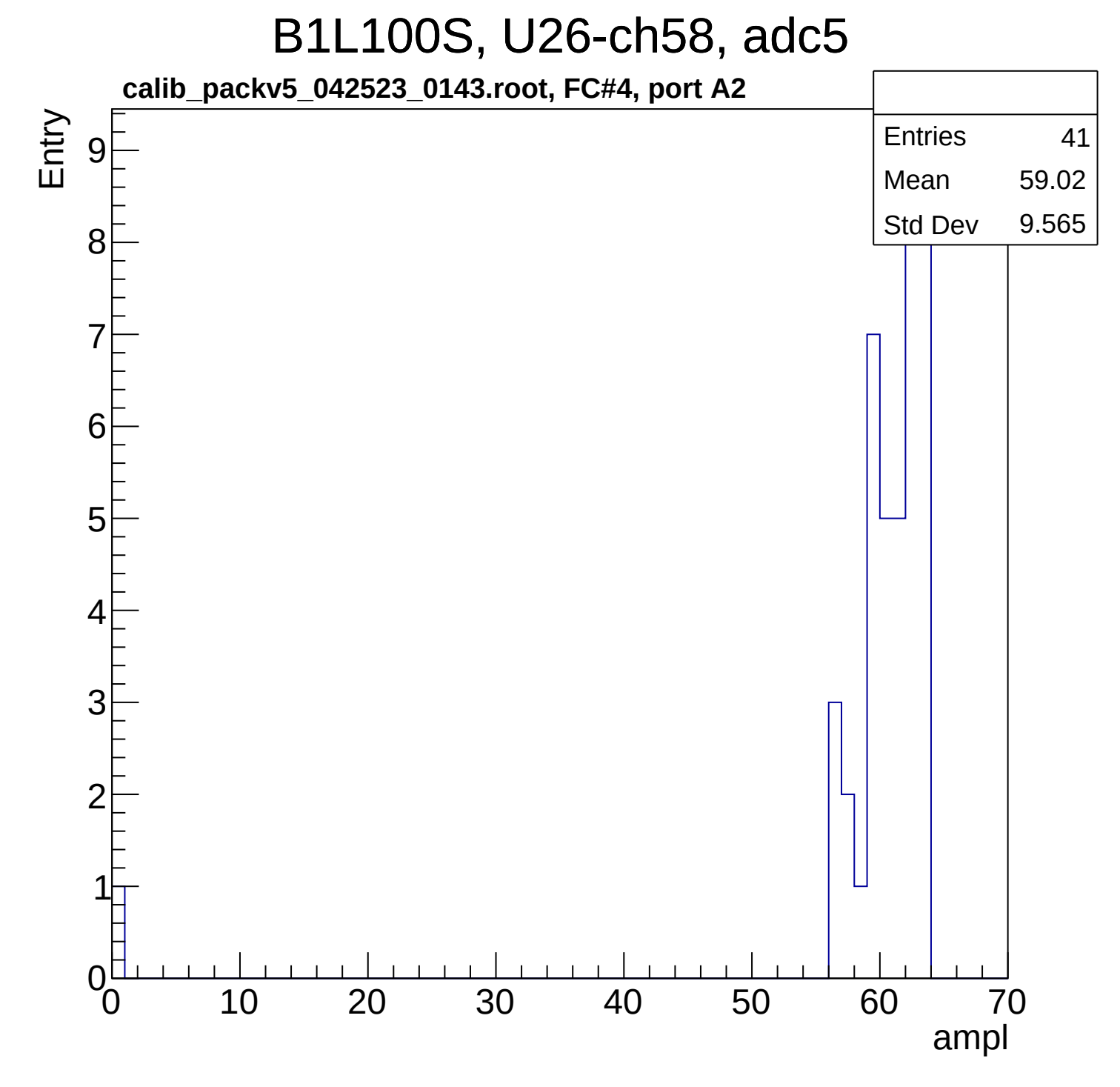
B1L100S, U26-ch58, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.02
Std Dev 9.565
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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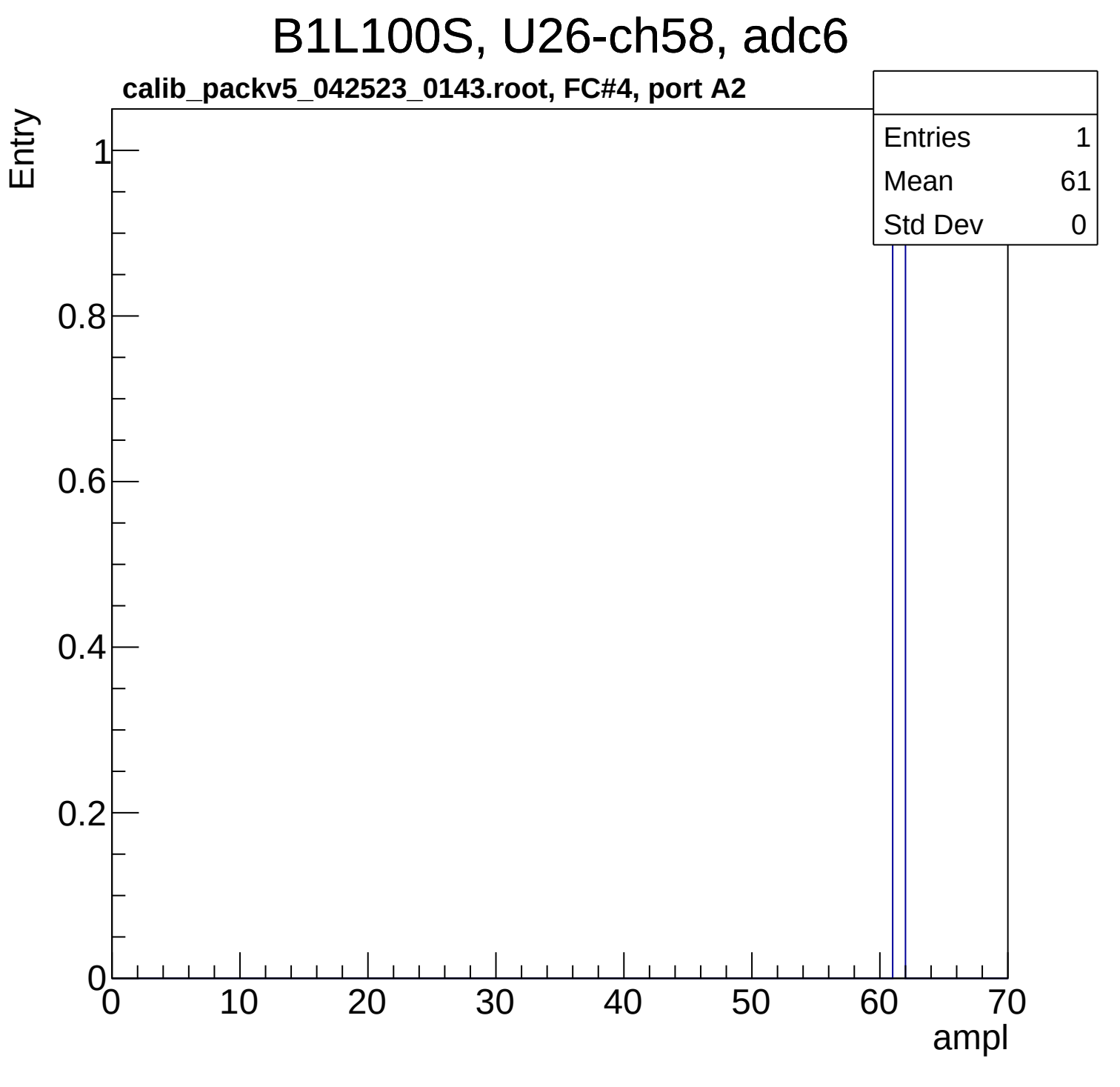
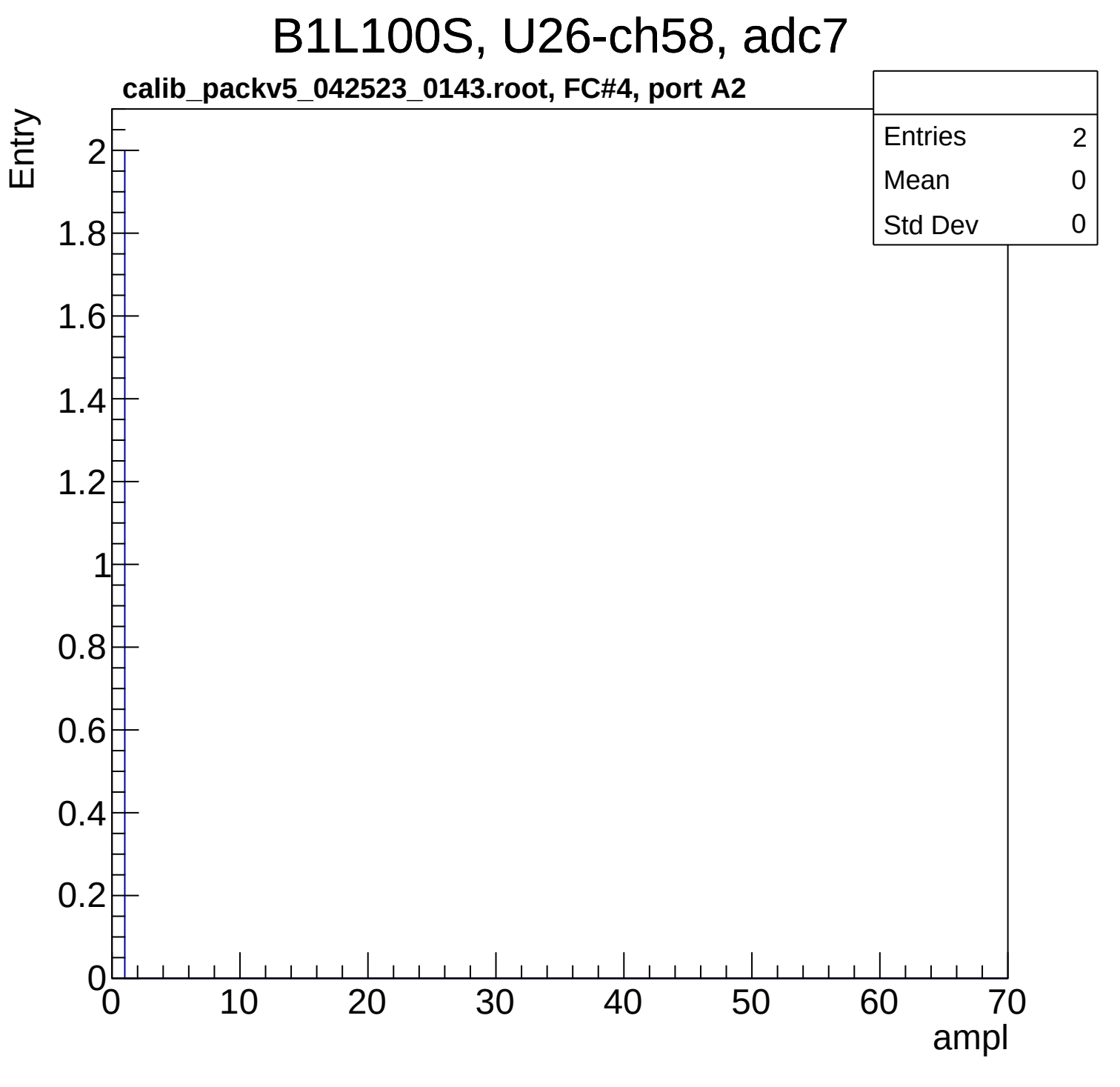
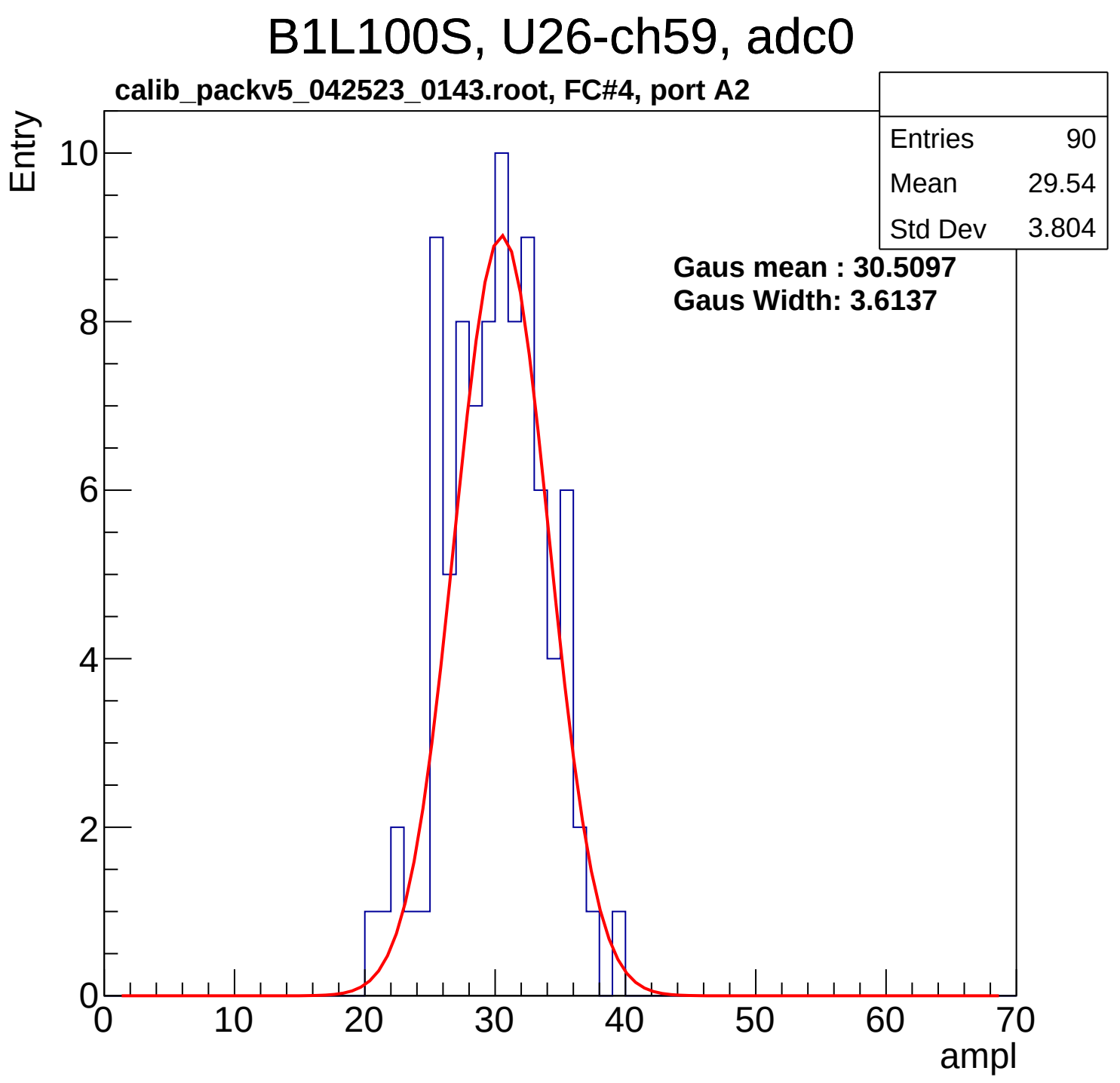
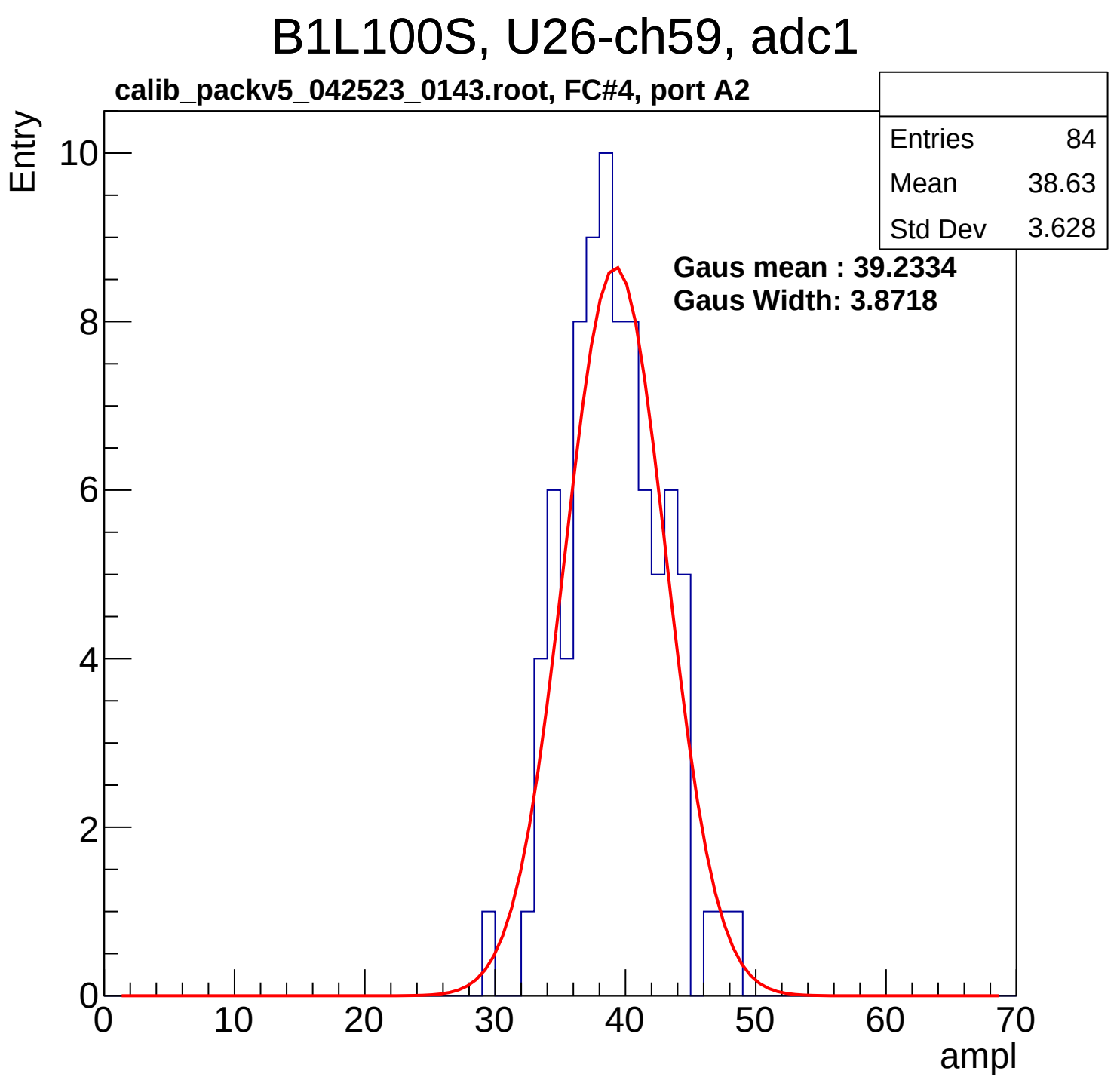
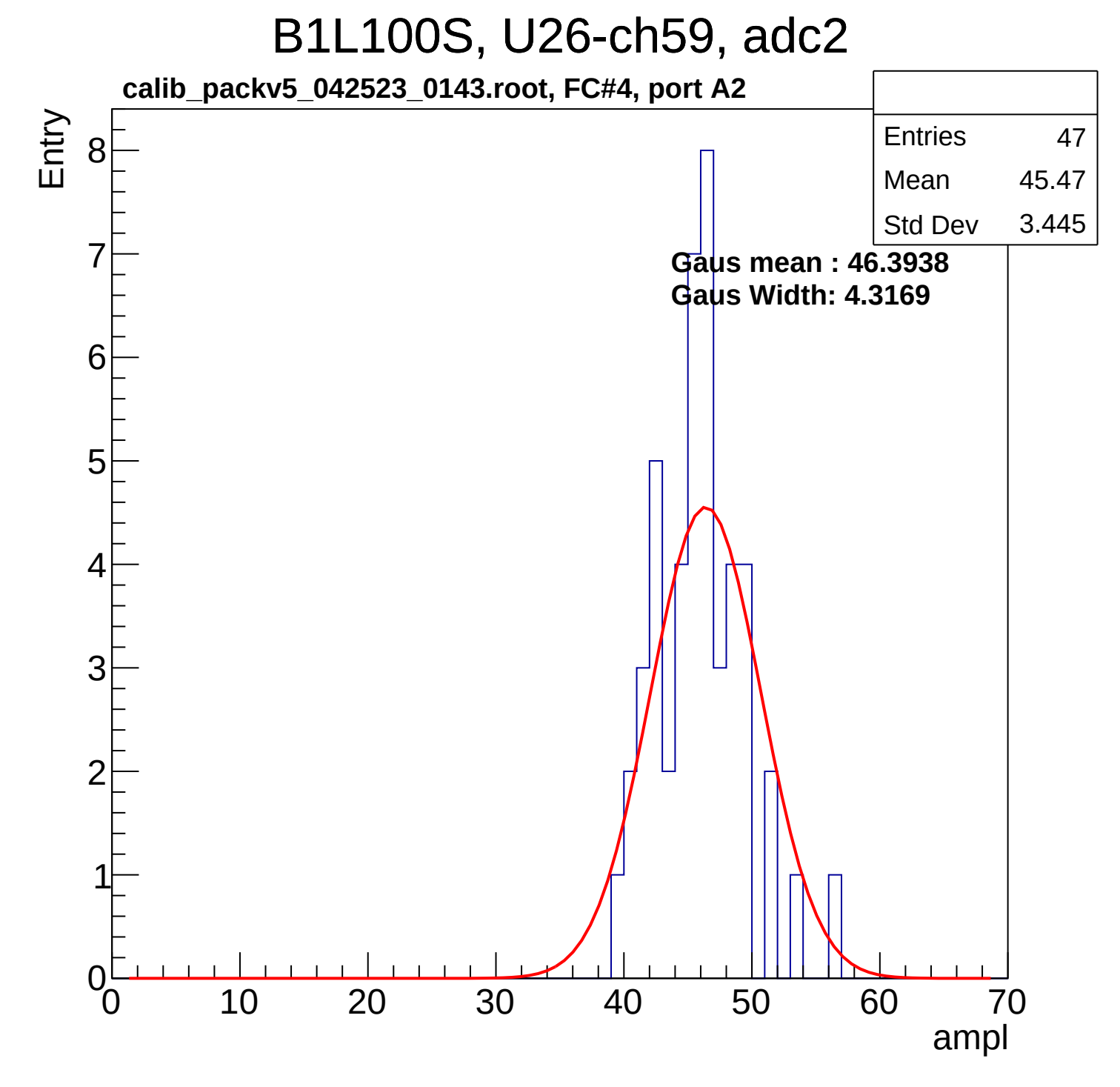

B1L100S, U26-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch58, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 29.54
Std Dev 3.804
Gaus mean : 30.5097
Gaus Width: 3.6137
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 38.63
Std Dev 3.628
Gaus mean : 39.2334
Gaus Width: 3.8718
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 45.47
Std Dev 3.445
Gaus mean : 46.3938
Gaus Width: 4.3169
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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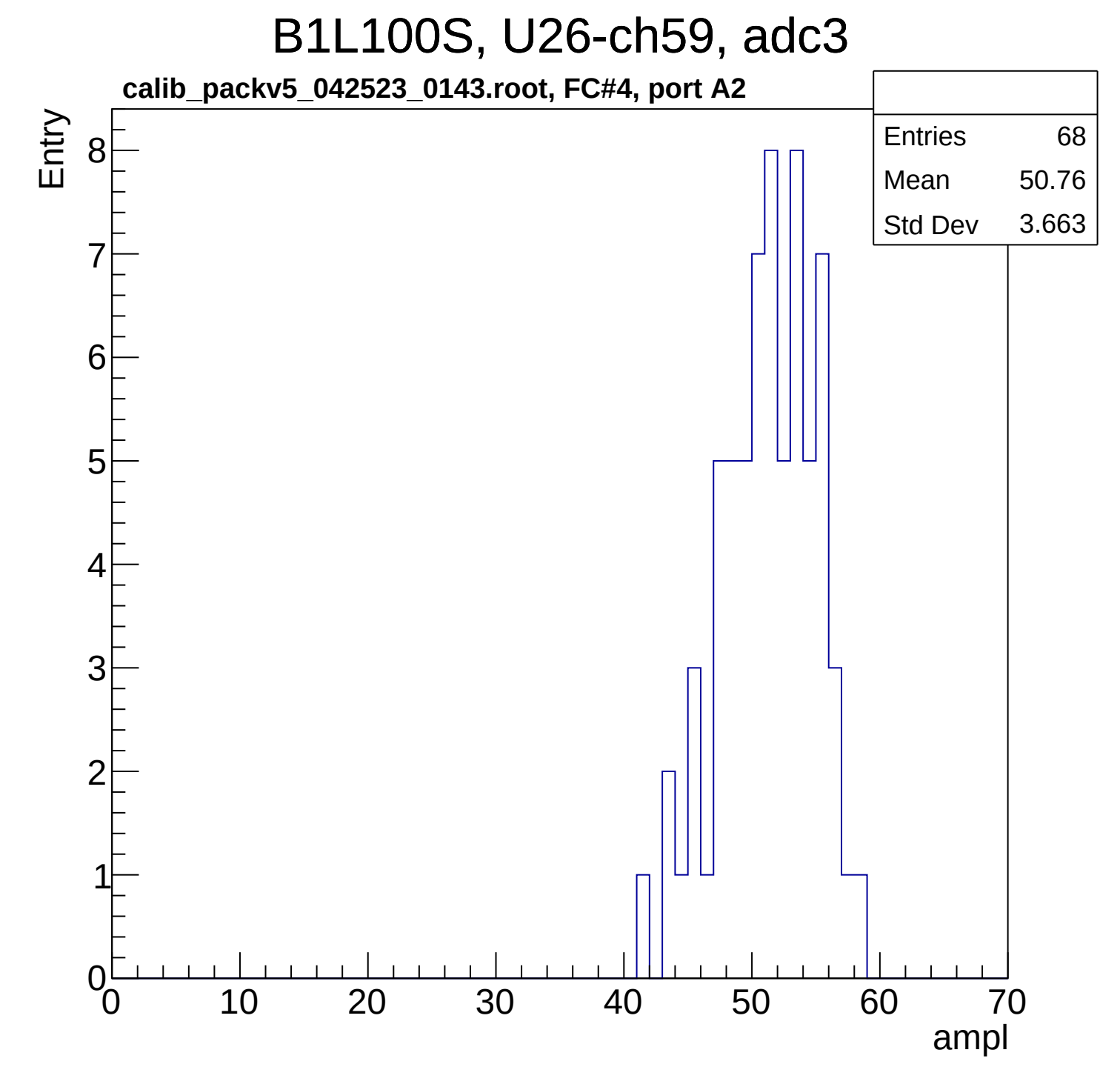
B1L100S, U26-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.76
Std Dev 3.663
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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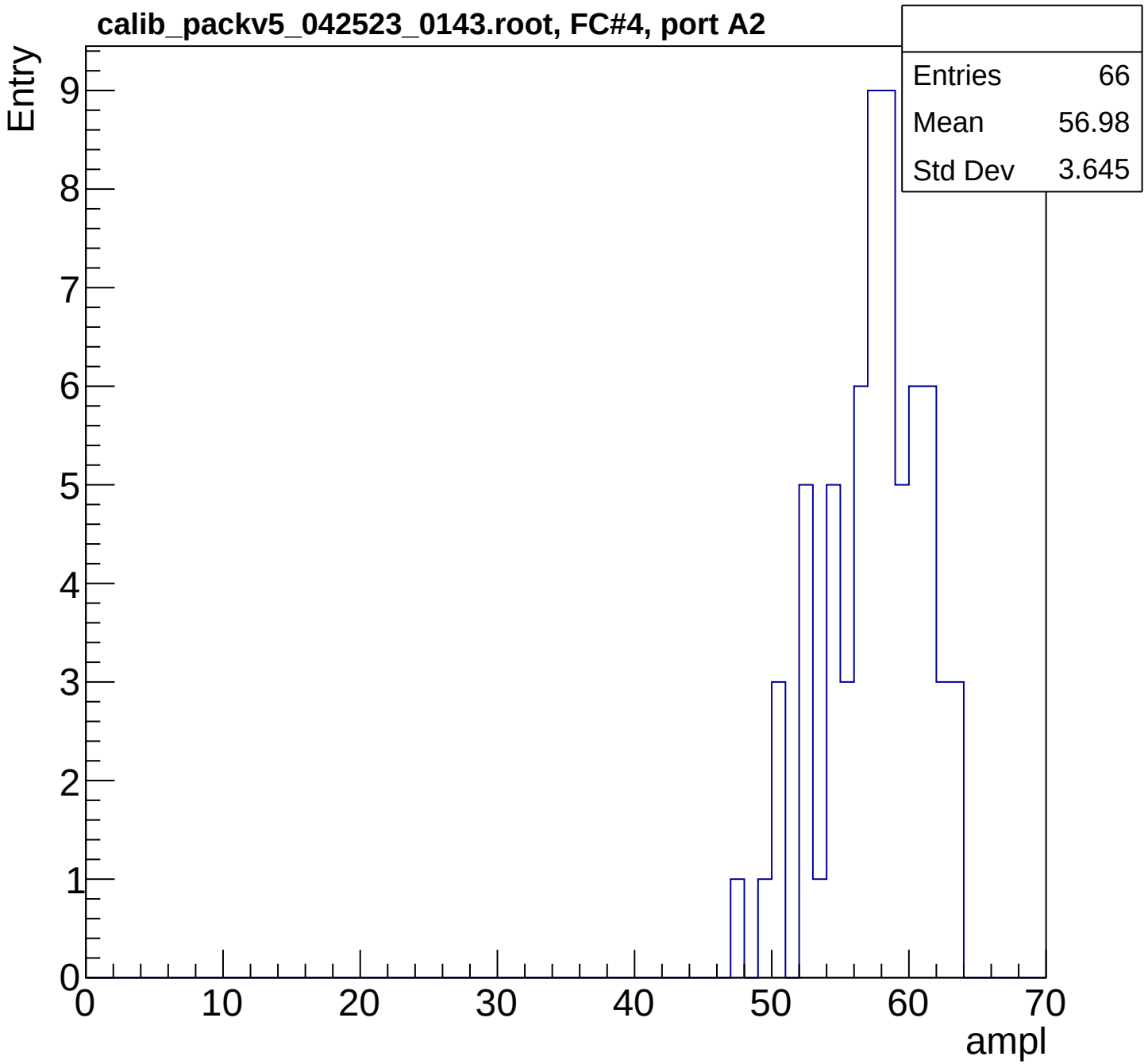
B1L100S, U26-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 56.98
Std Dev 3.645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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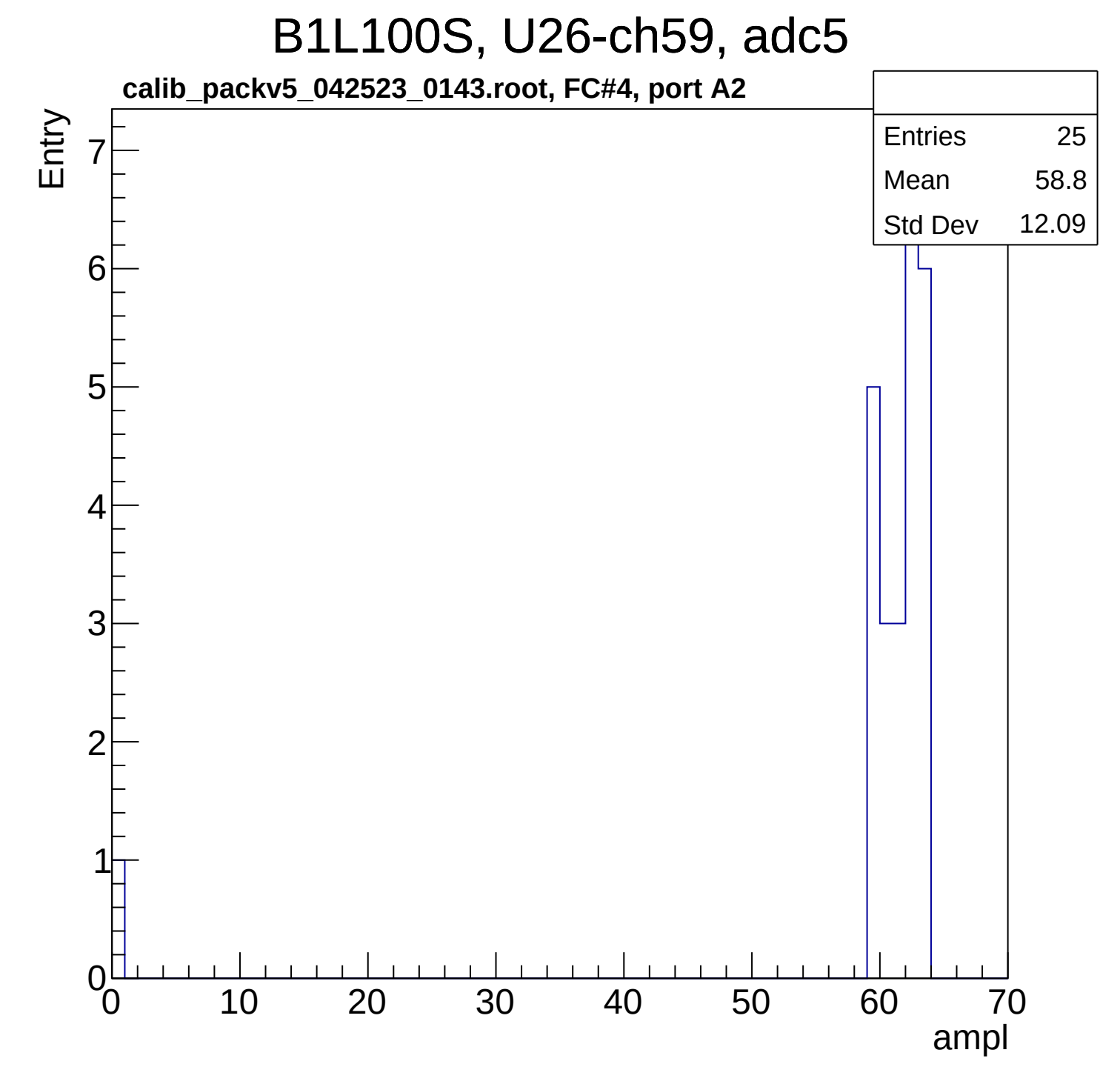
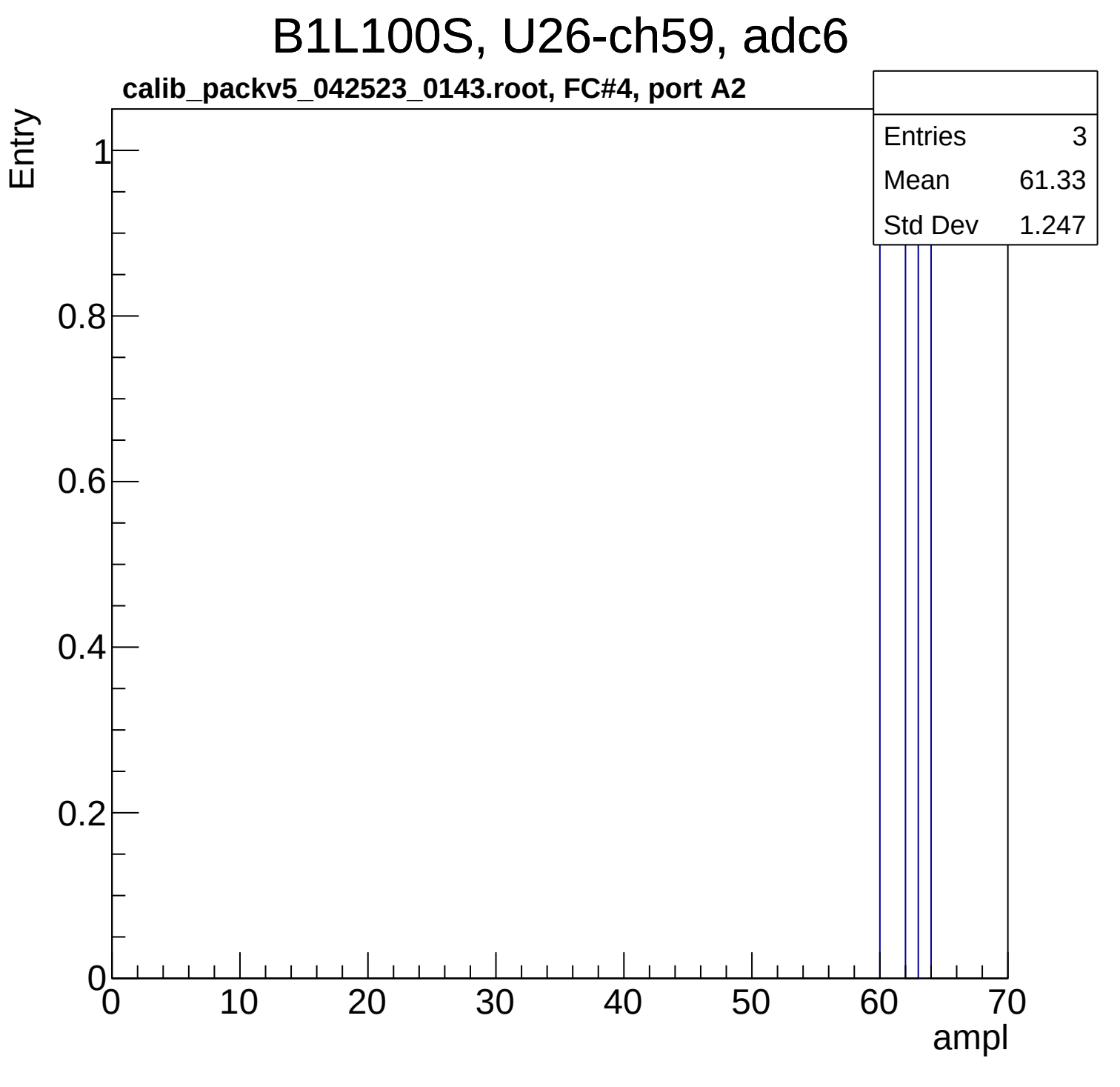
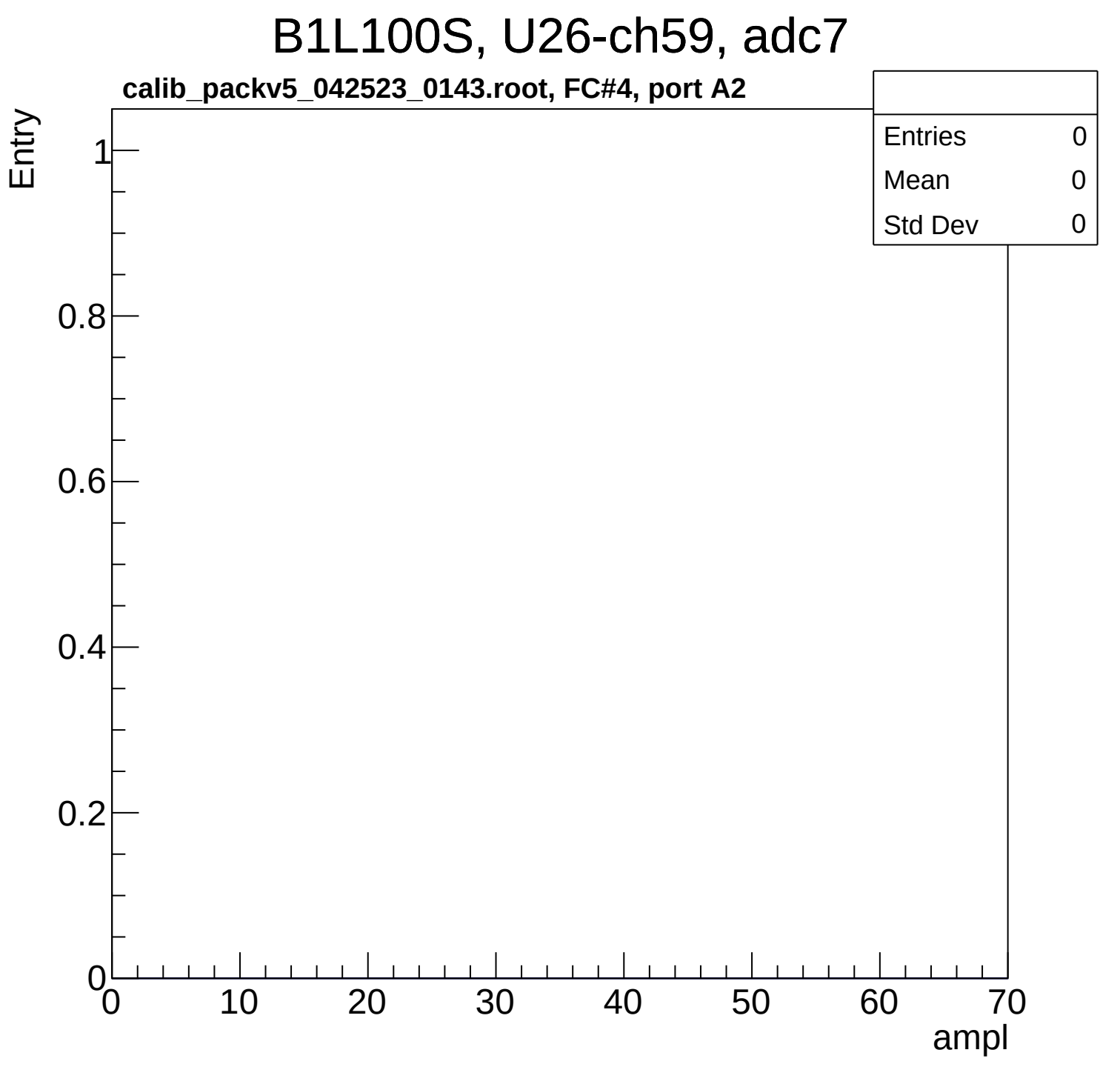
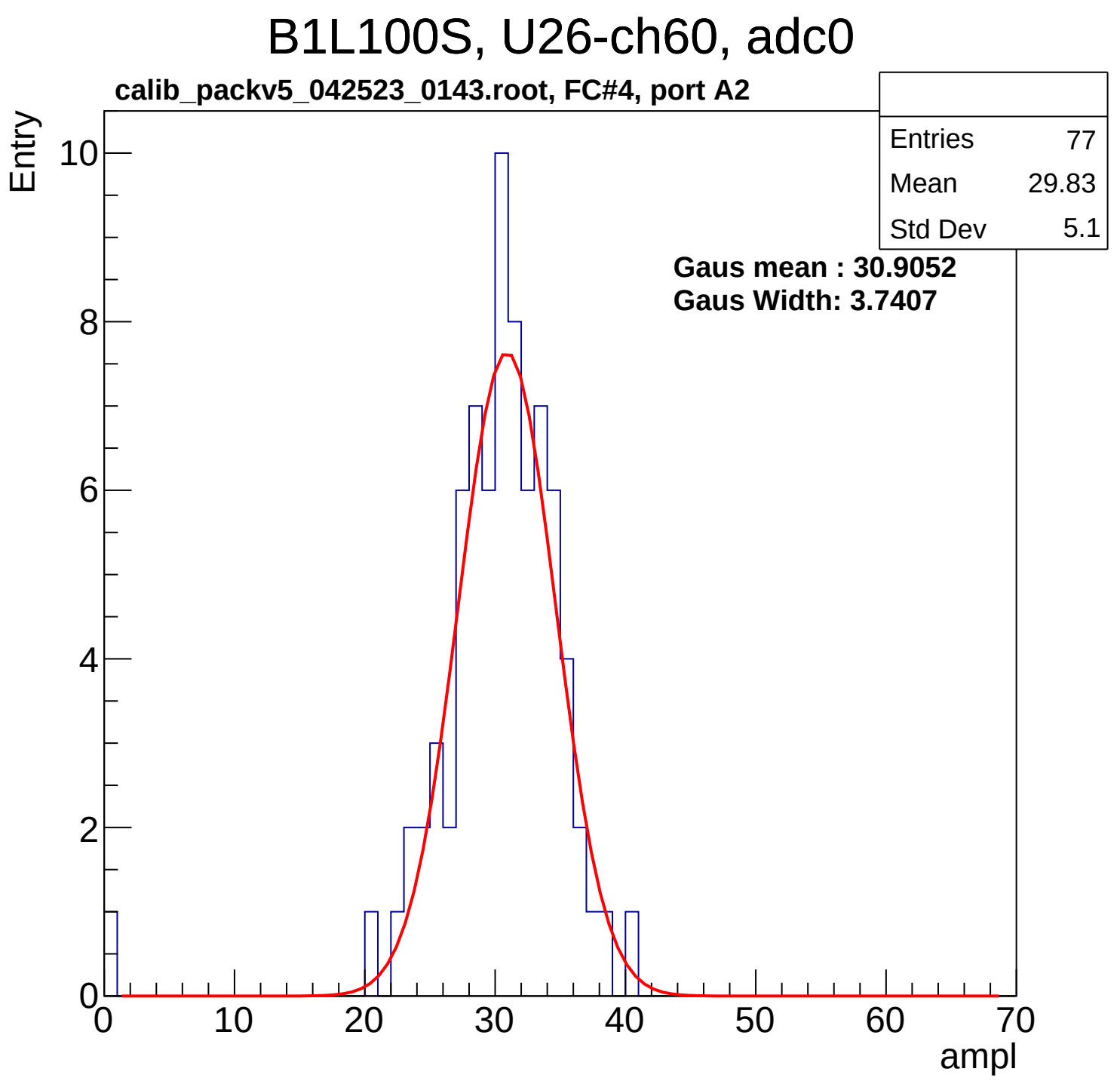
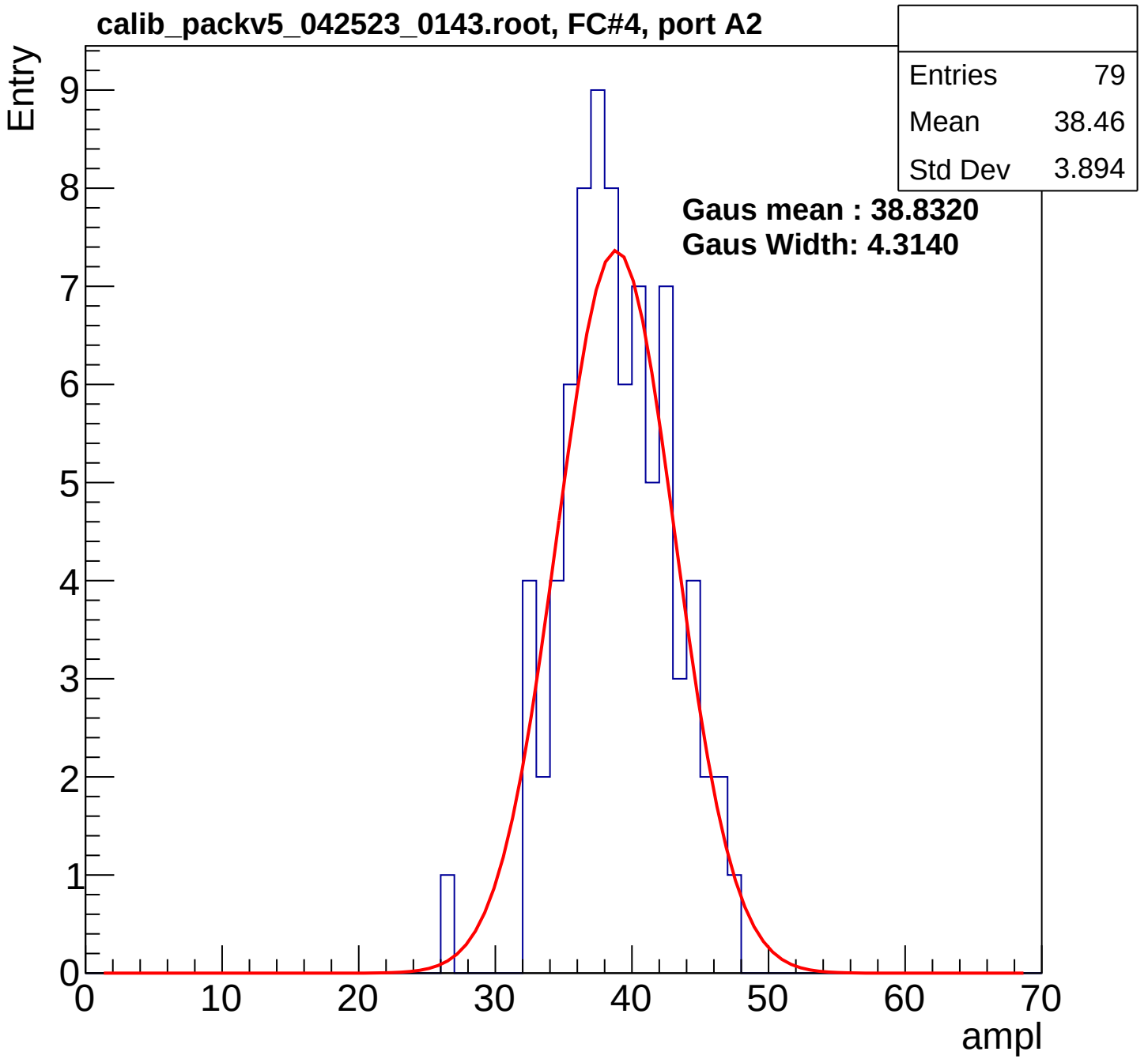
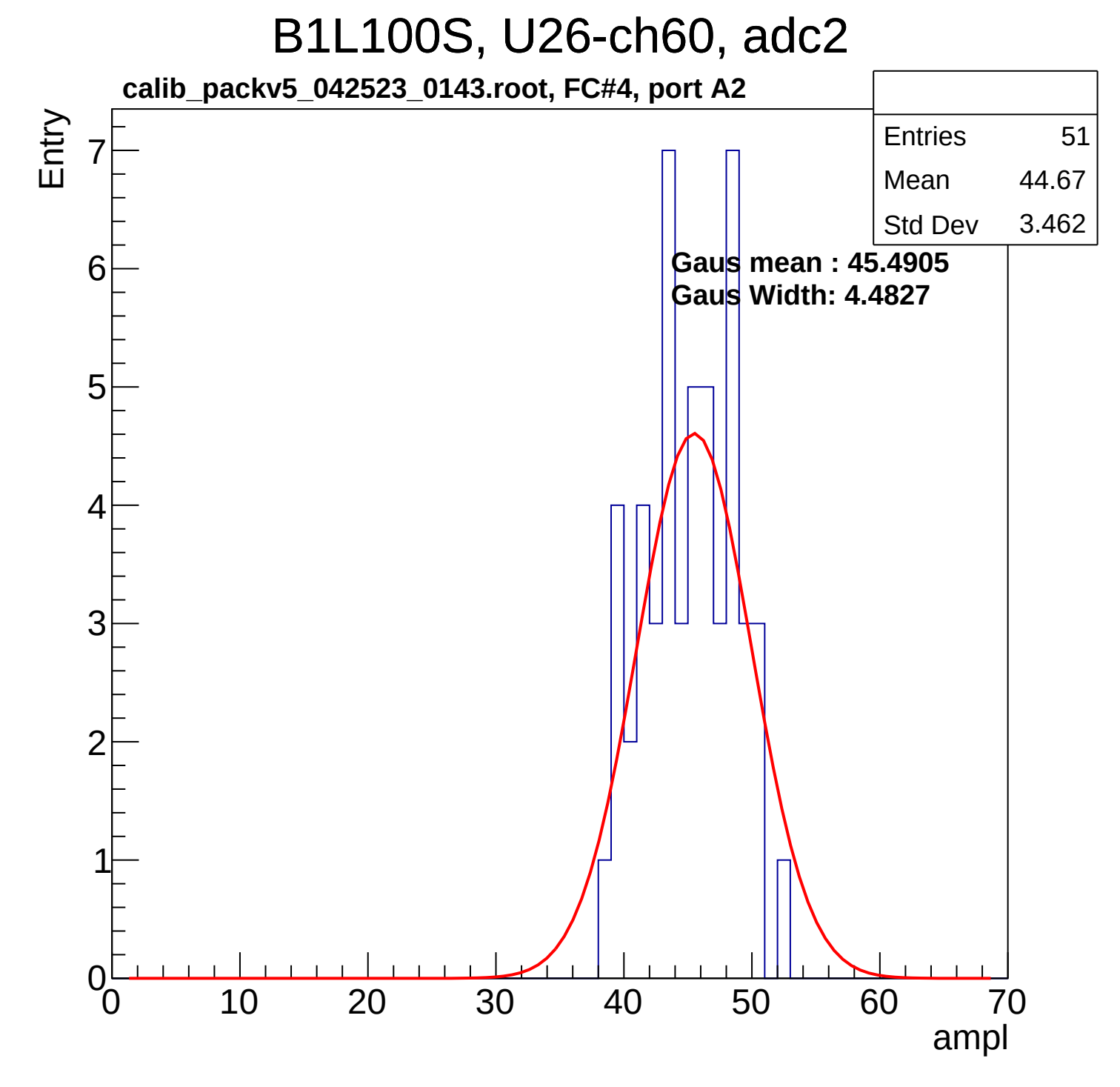

B1L100S, U26-ch59, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.8
Std Dev 12.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch59, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.83
Std Dev 5.1
Gaus mean : 30.9052
Gaus Width: 3.7407
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 38.46
Std Dev 3.894
Gaus mean : 38.8320
Gaus Width: 4.3140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 44.67
Std Dev 3.462
Gaus mean : 45.4905
Gaus Width: 4.4827
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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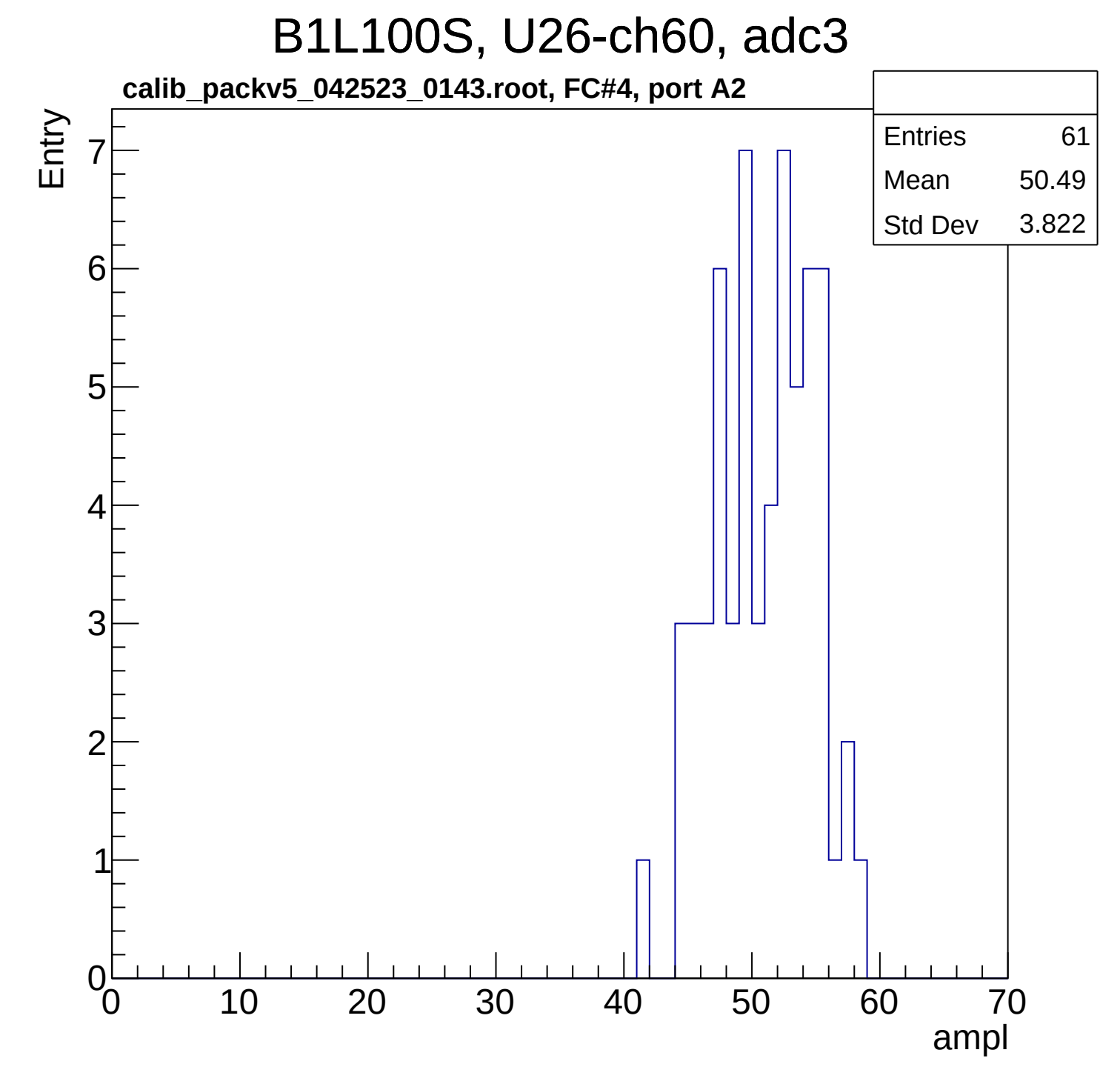
B1L100S, U26-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.49
Std Dev 3.822
Entry
ampl
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3
4
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6
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0
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20
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40
50
60
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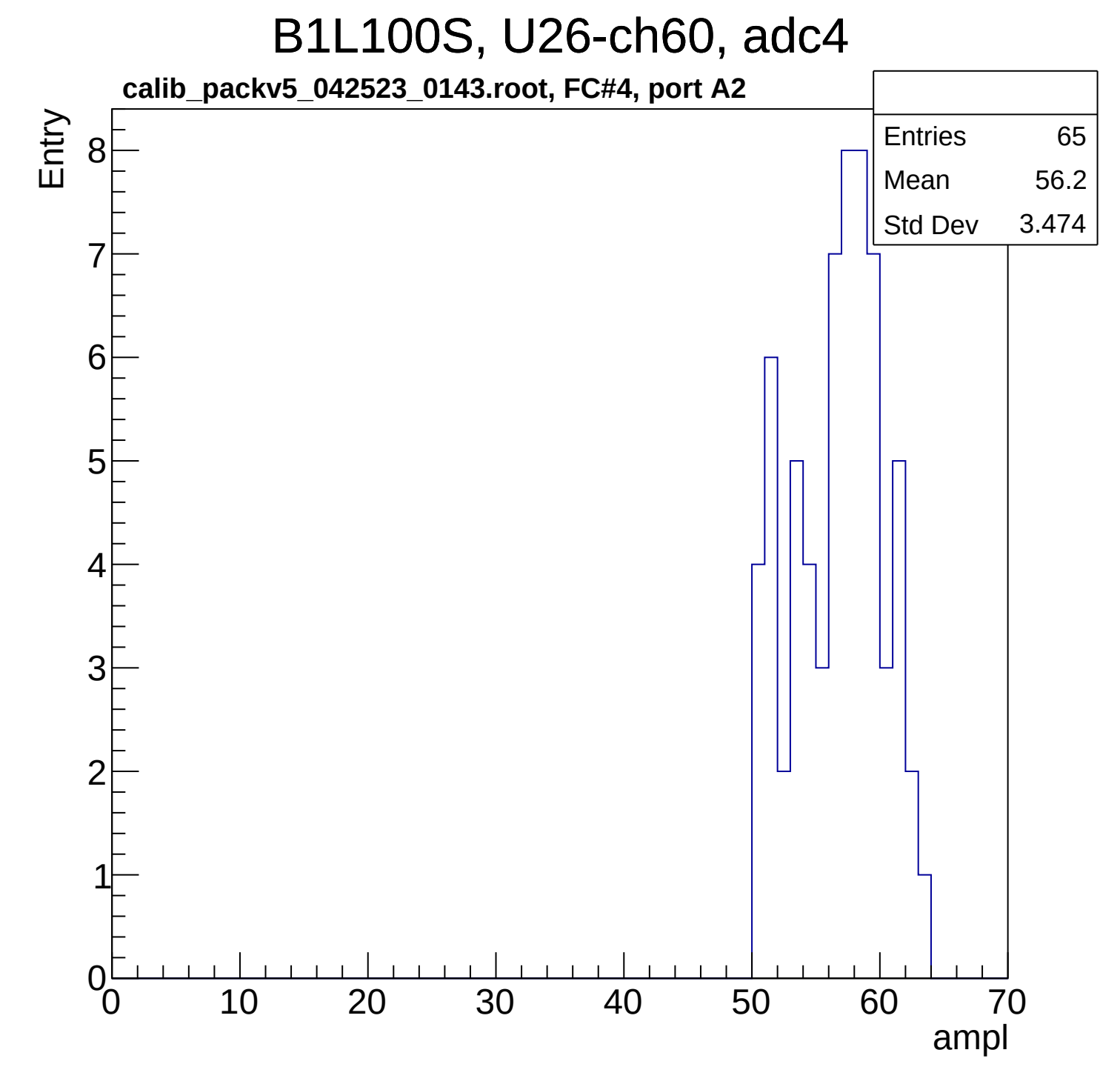
B1L100S, U26-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 56.2
Std Dev 3.474
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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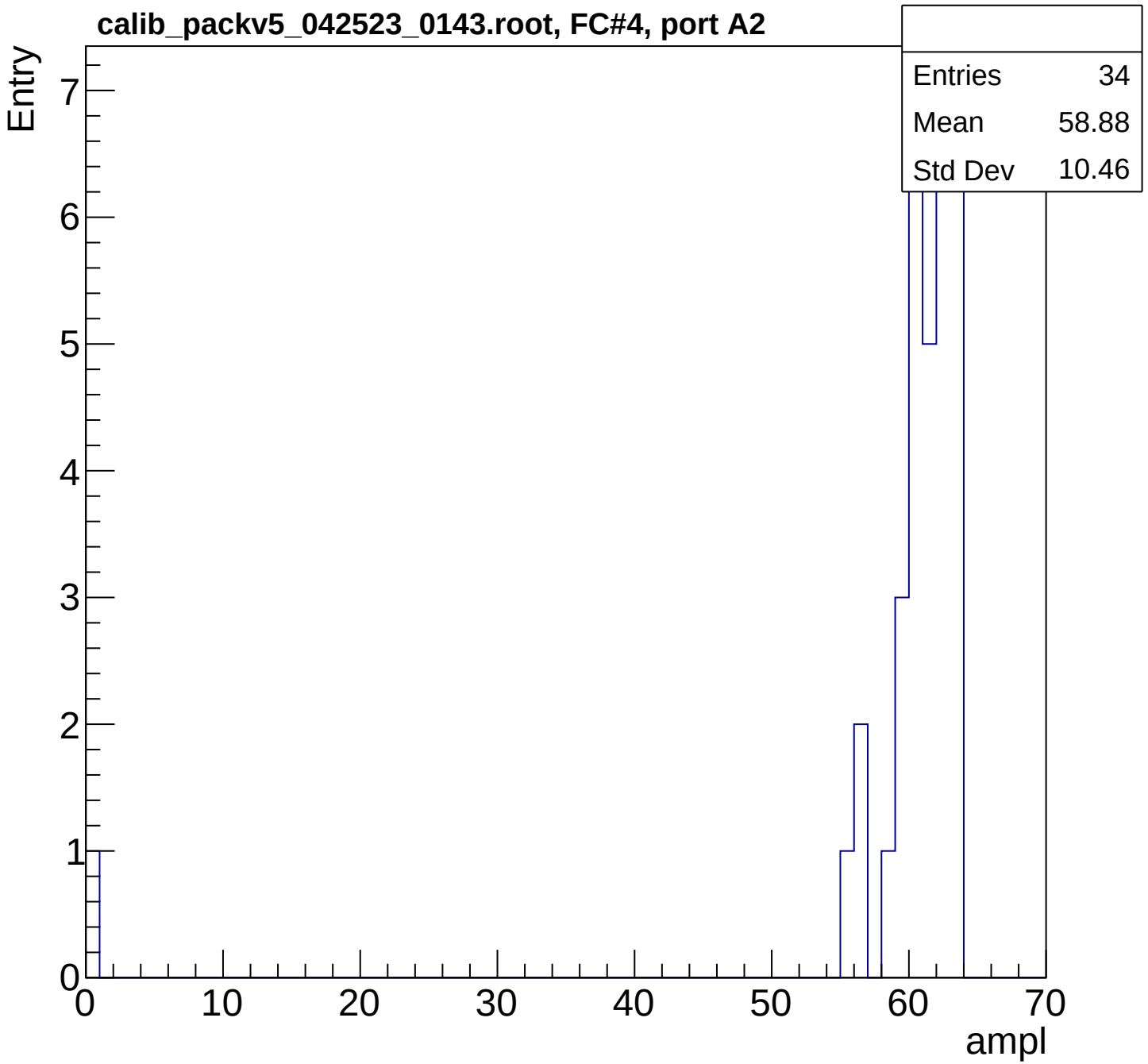
B1L100S, U26-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.88
Std Dev 10.46
Entry
ampl
0
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2
3
4
5
6
7
0
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20
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40
50
60
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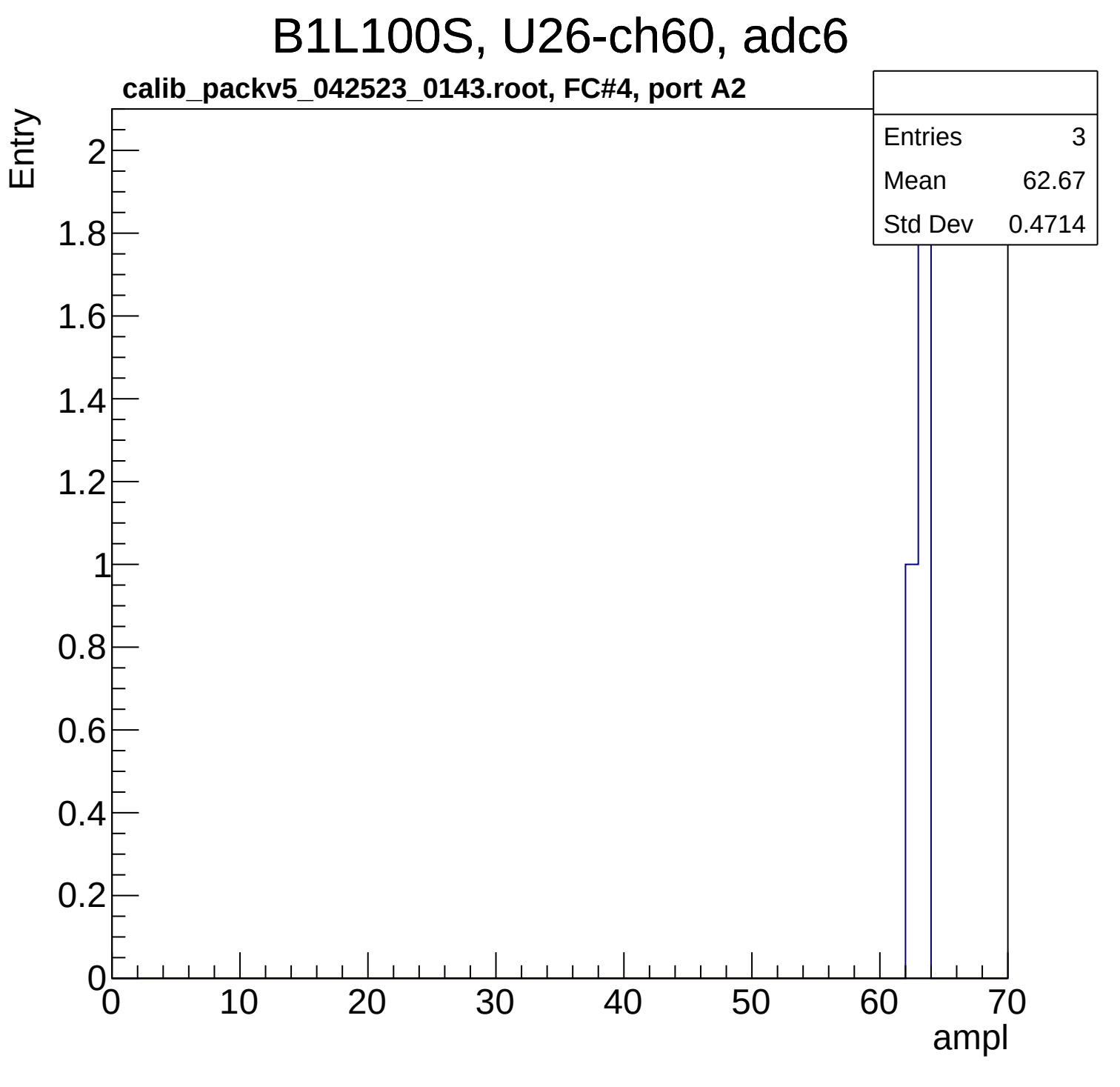
B1L100S, U26-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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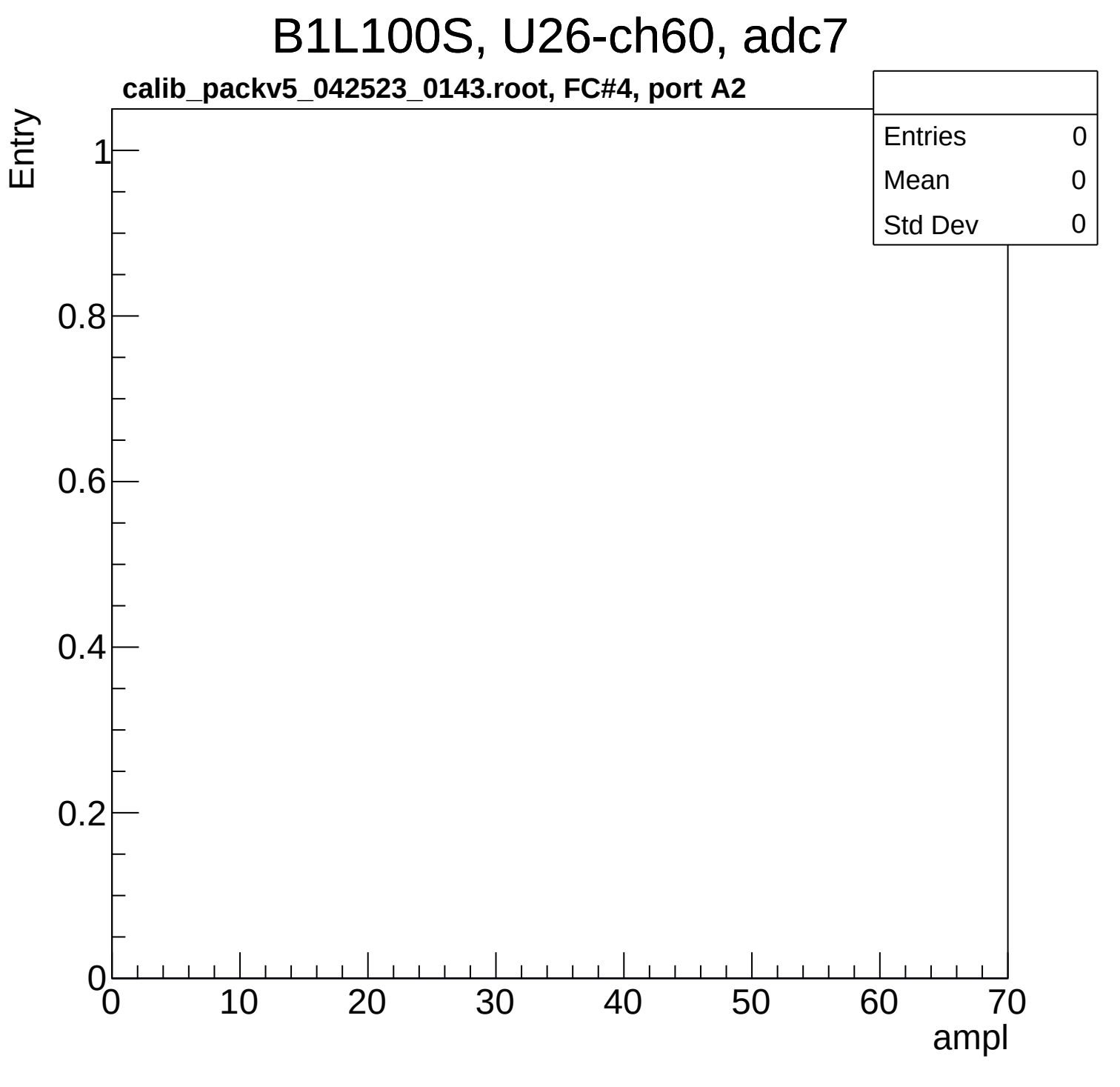
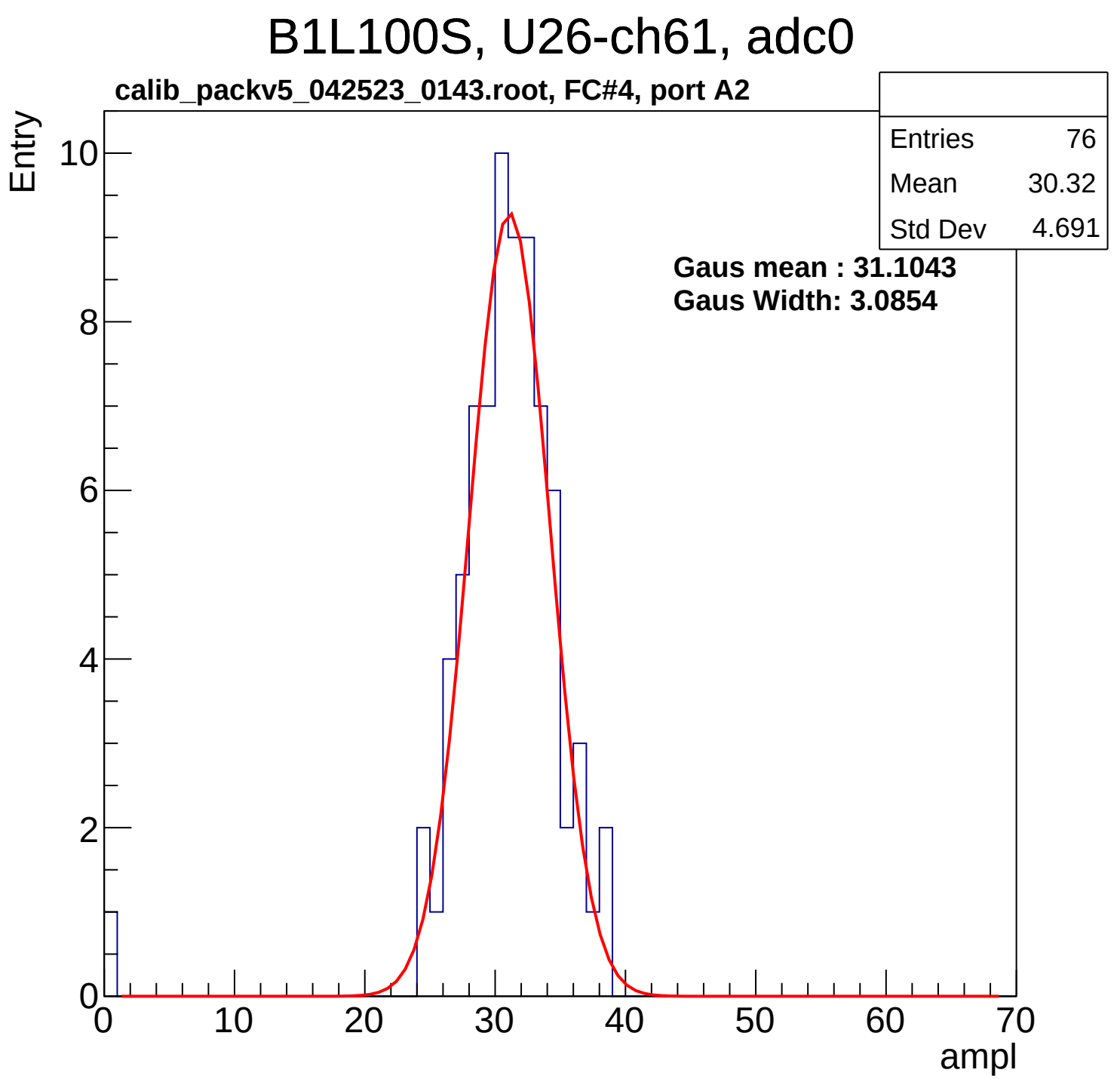

B1L100S, U26-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.32
Std Dev 4.691
Gaus mean : 31.1043
Gaus Width: 3.0854
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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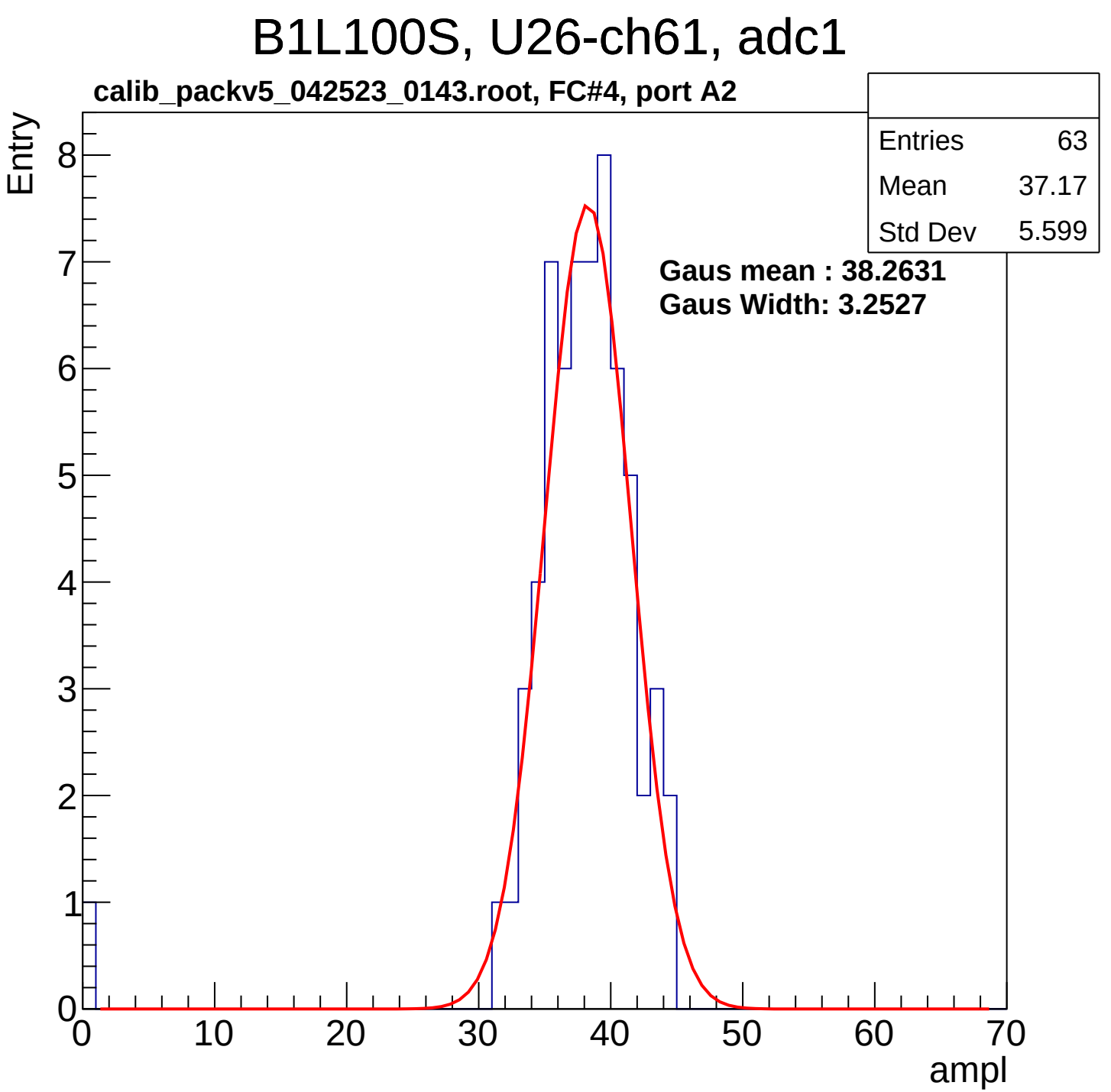
B1L100S, U26-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 37.17
Std Dev 5.599
Gaus mean : 38.2631
Gaus Width: 3.2527
Entry
ampl
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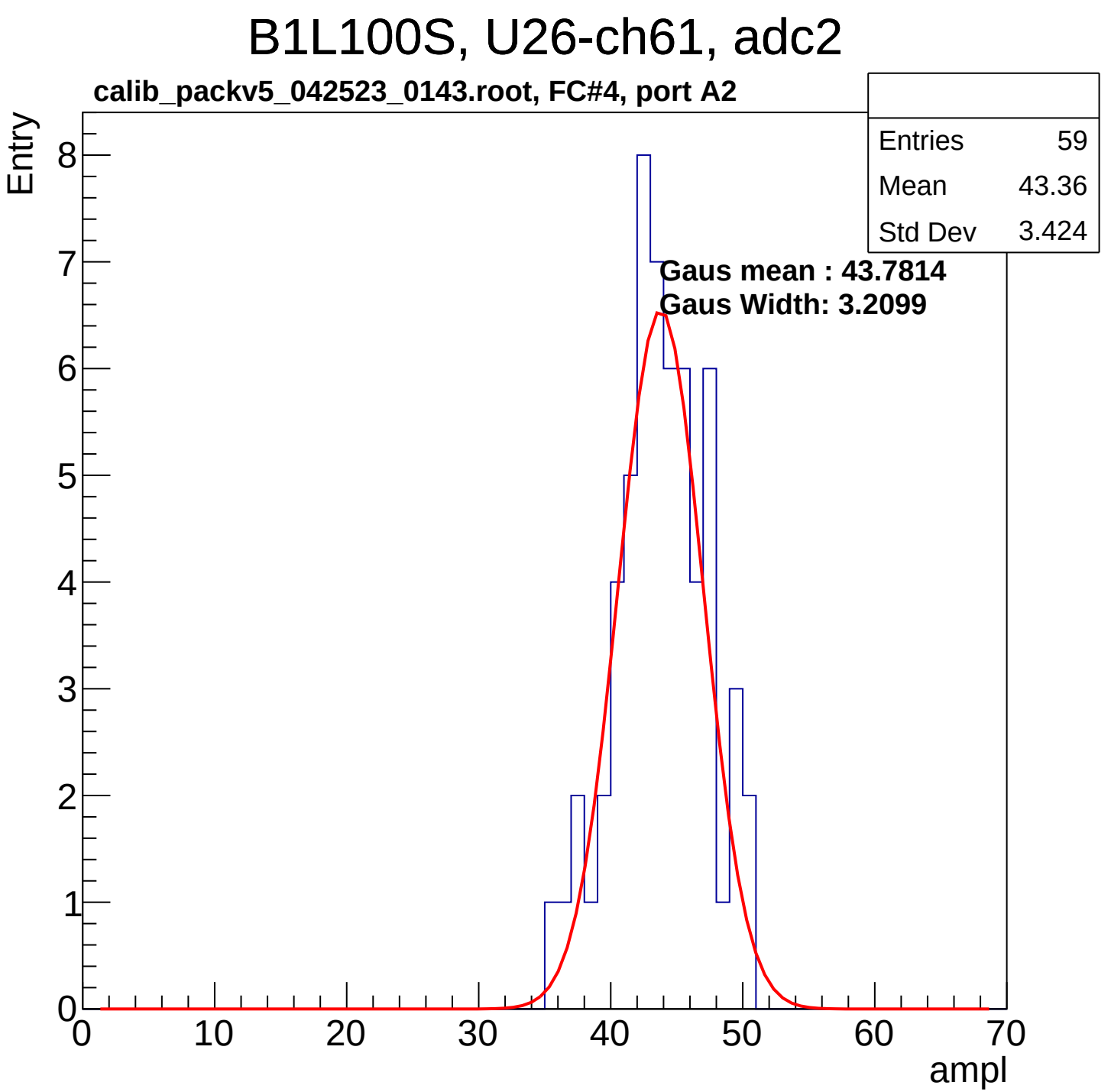
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch61, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.36
Std Dev 3.424
Gaus mean : 43.7814
Gaus Width: 3.2099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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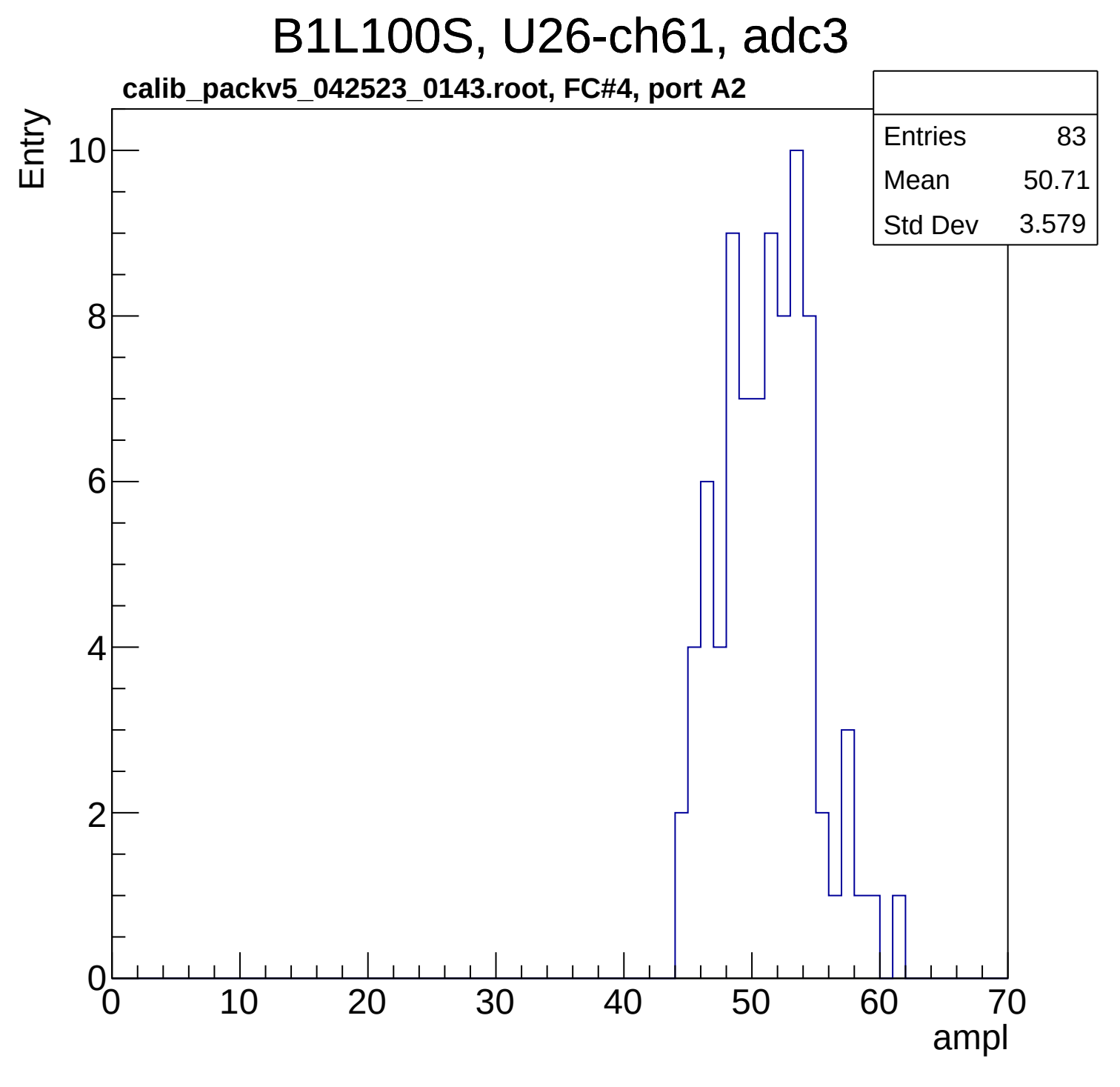
B1L100S, U26-ch61, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 50.71
Std Dev 3.579
Entry
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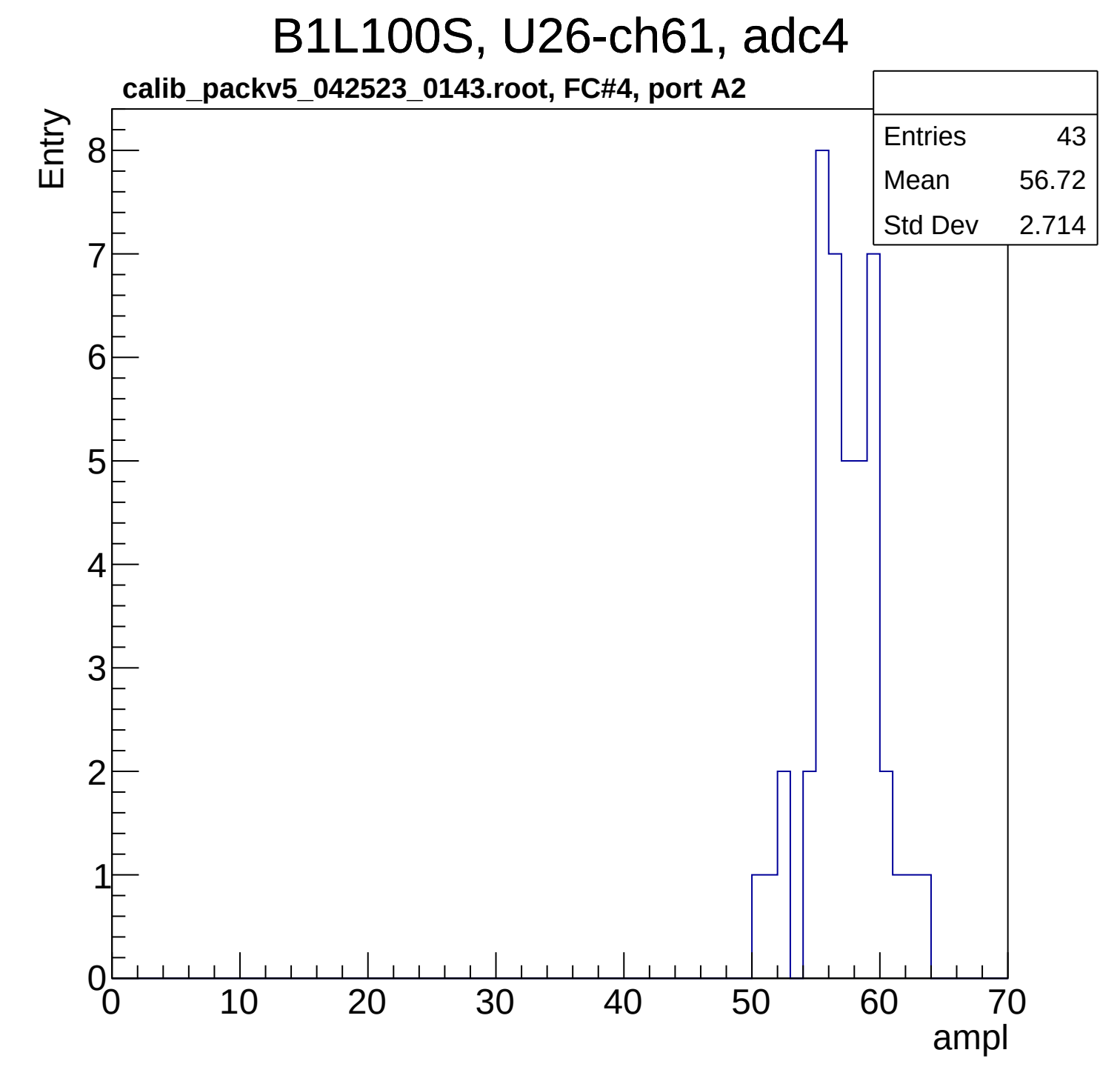
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 56.72
Std Dev 2.714
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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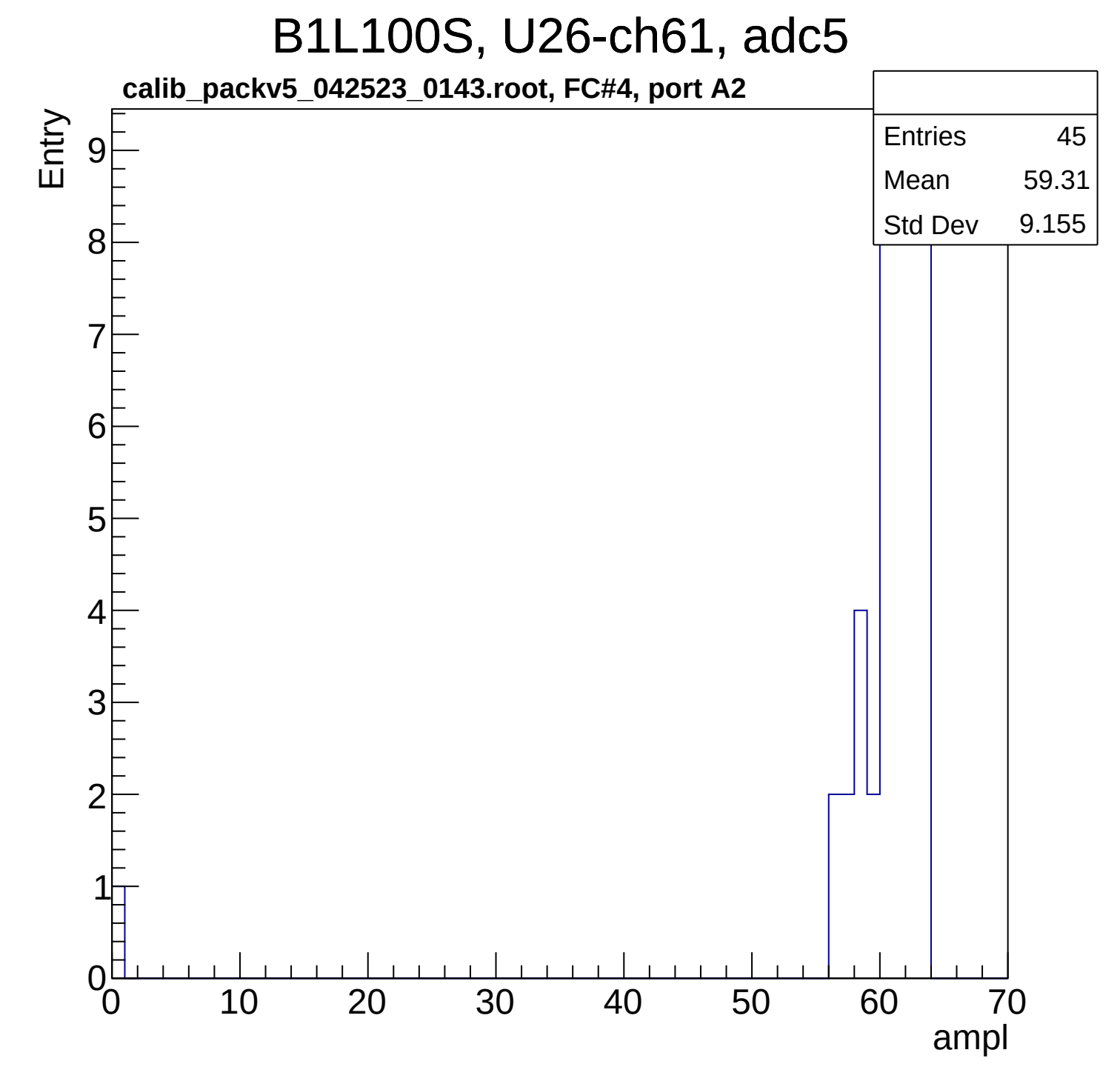
B1L100S, U26-ch61, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.31
Std Dev 9.155
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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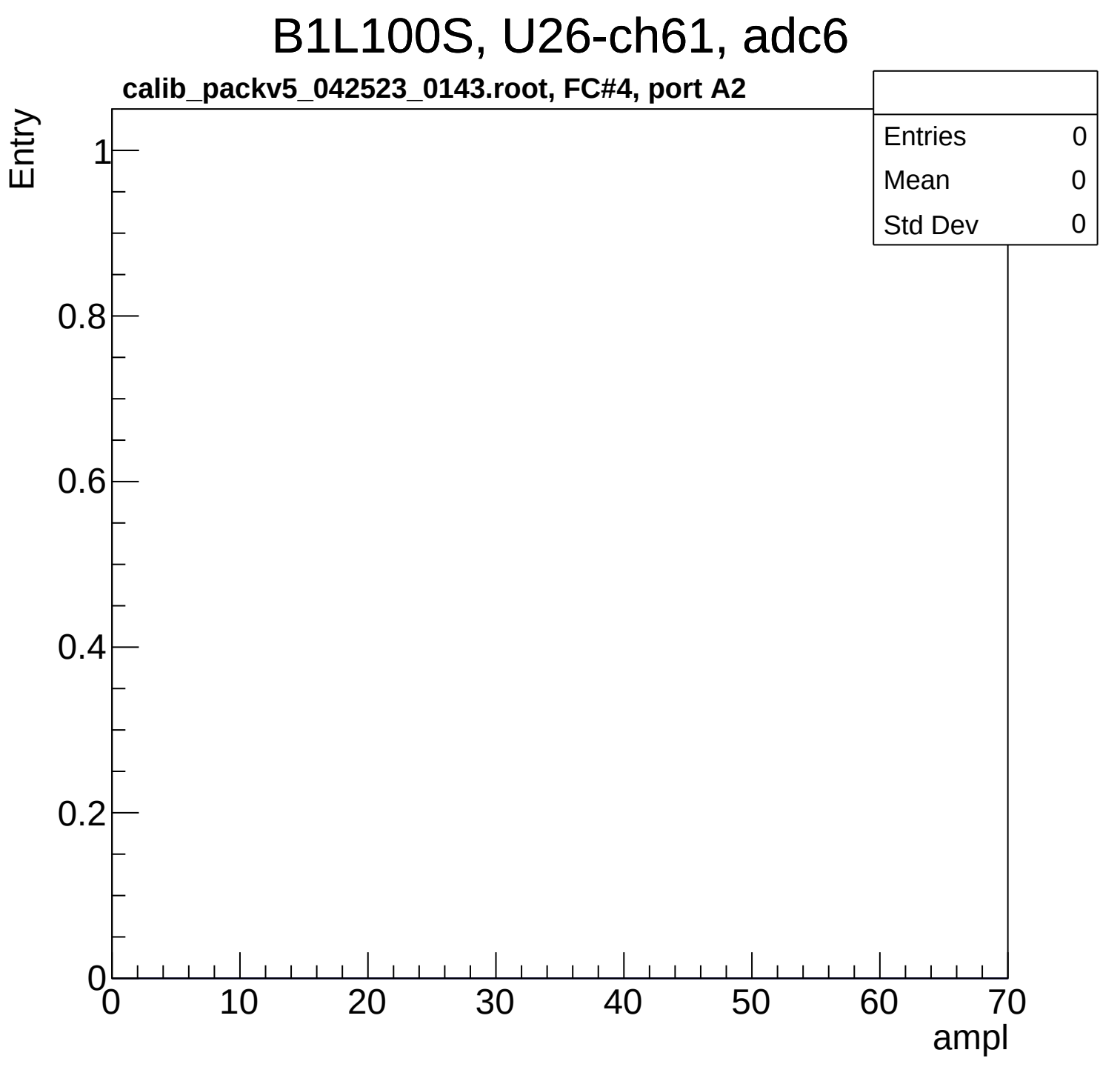
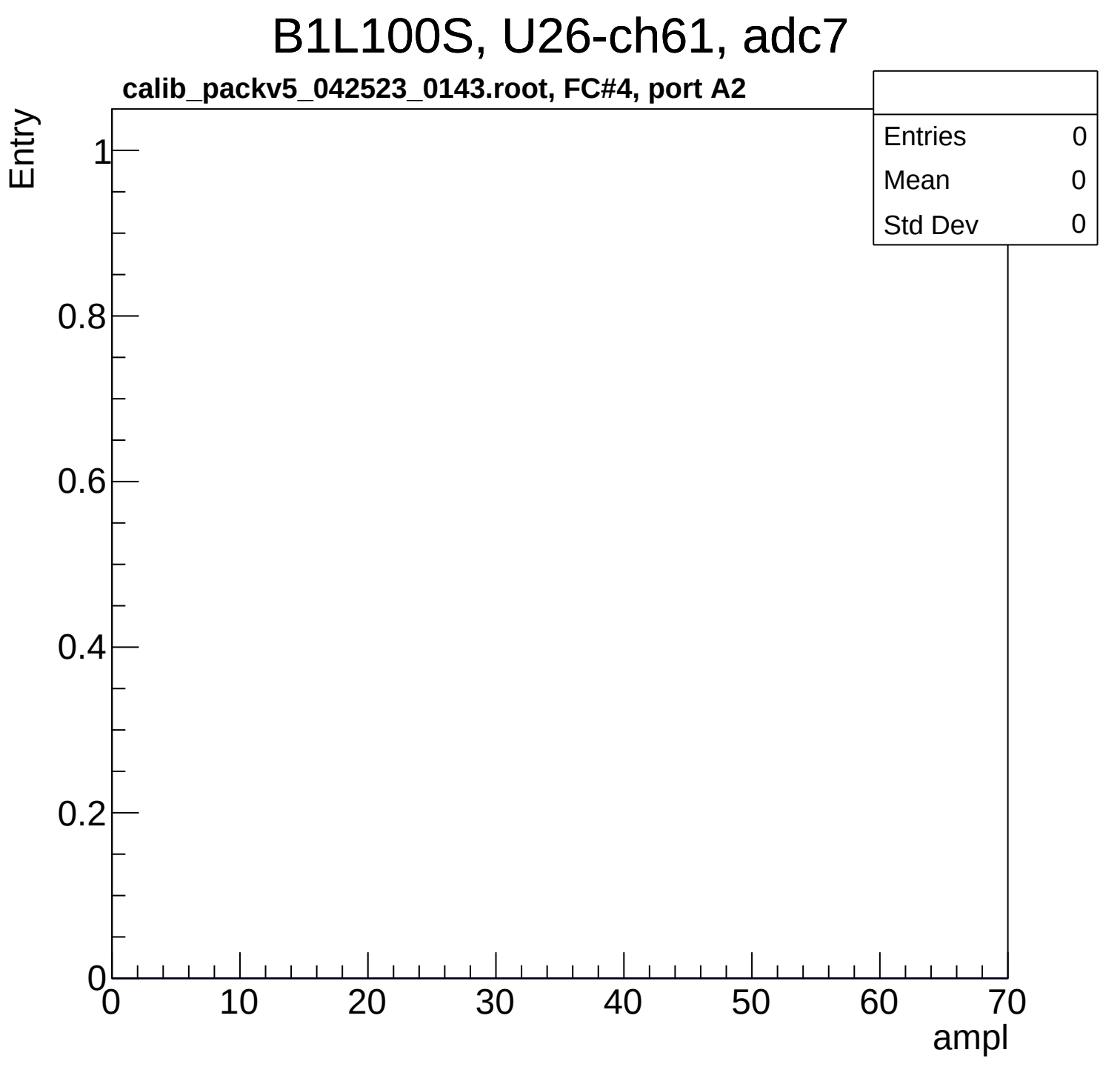

B1L100S, U26-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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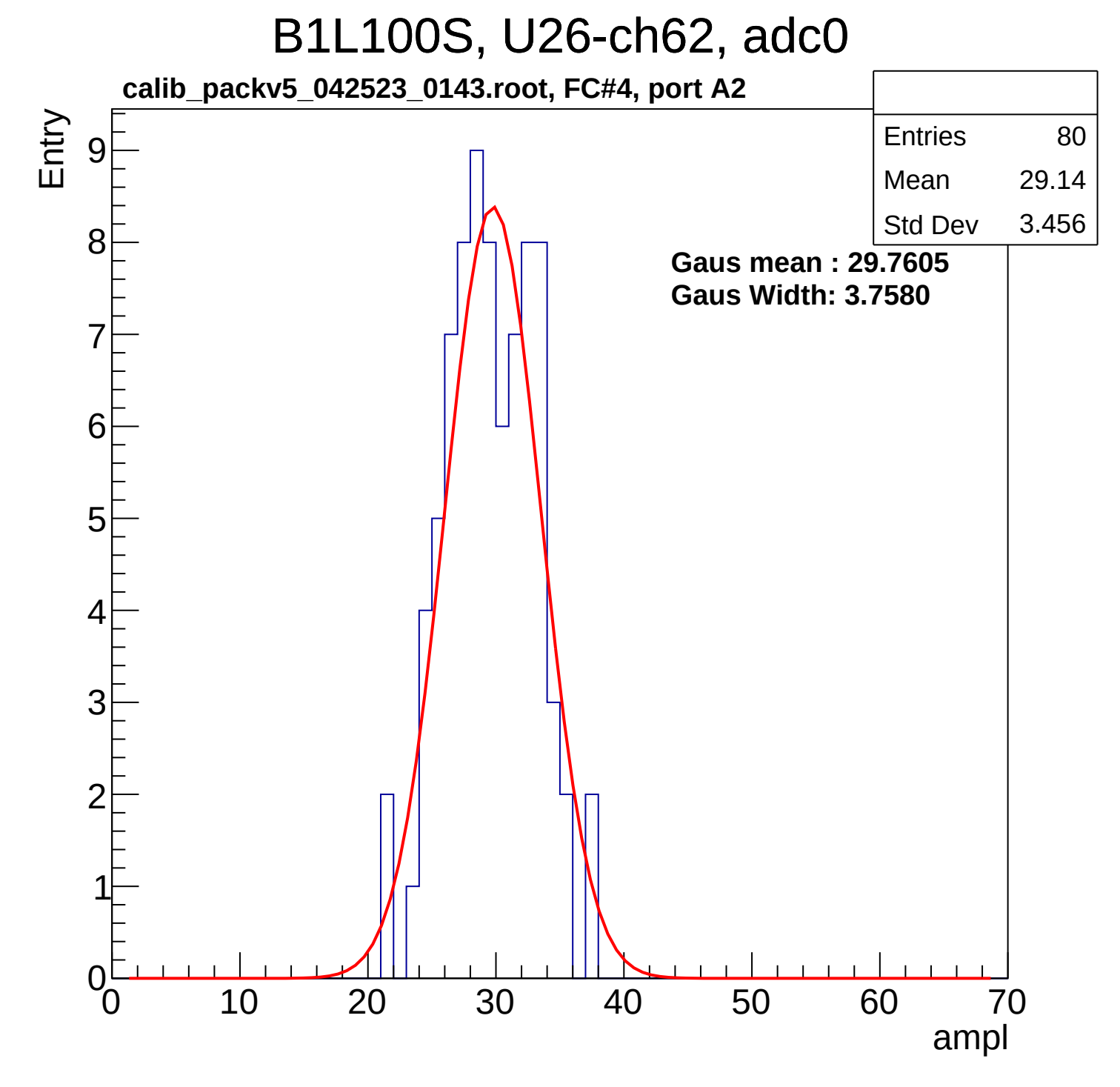
50
60
70
ampl


B1L100S, U26-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.14
Std Dev 3.456
Gaus mean : 29.7605
Gaus Width: 3.7580
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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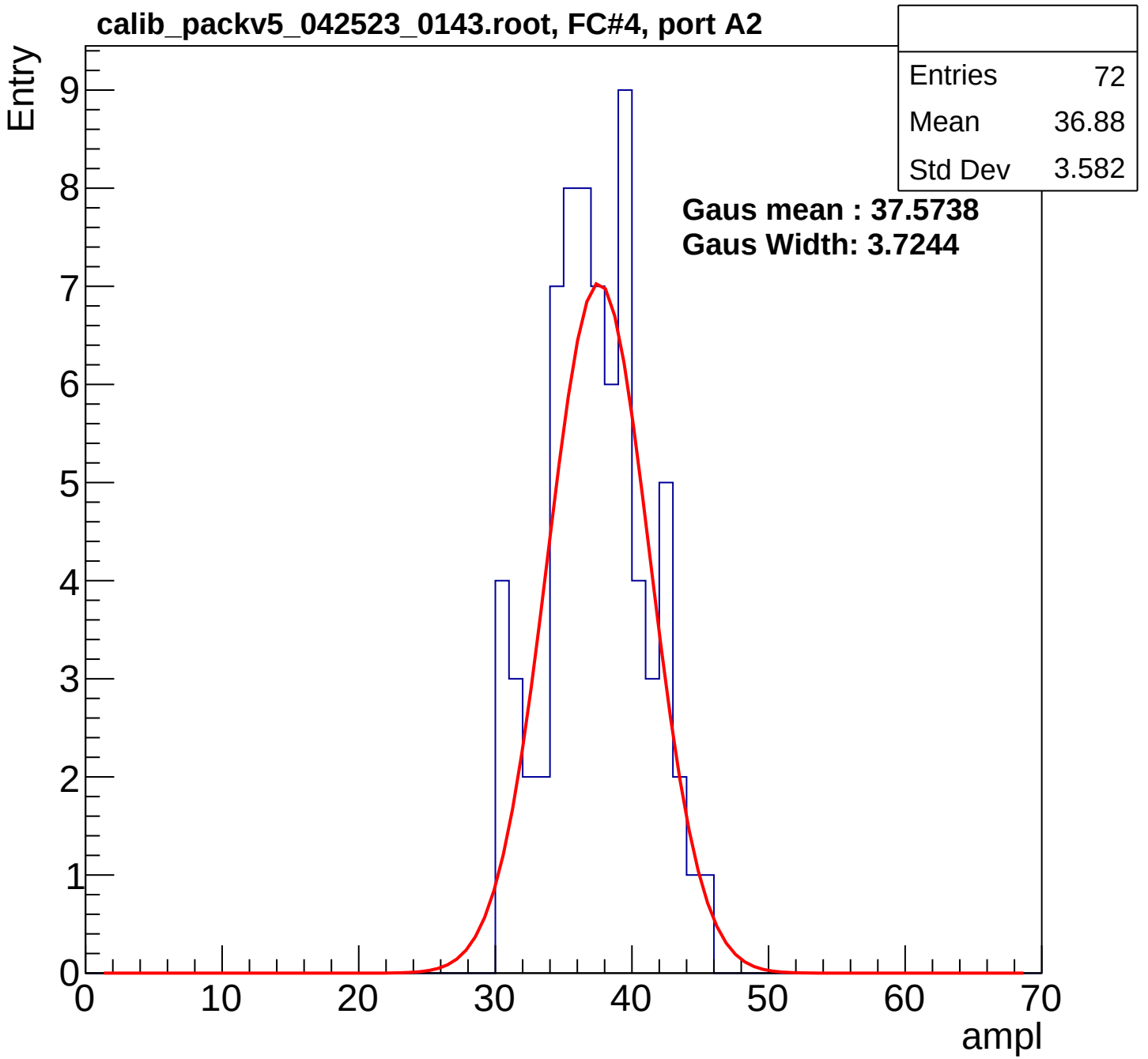
B1L100S, U26-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.88
Std Dev 3.582
Gaus mean : 37.5738
Gaus Width: 3.7244
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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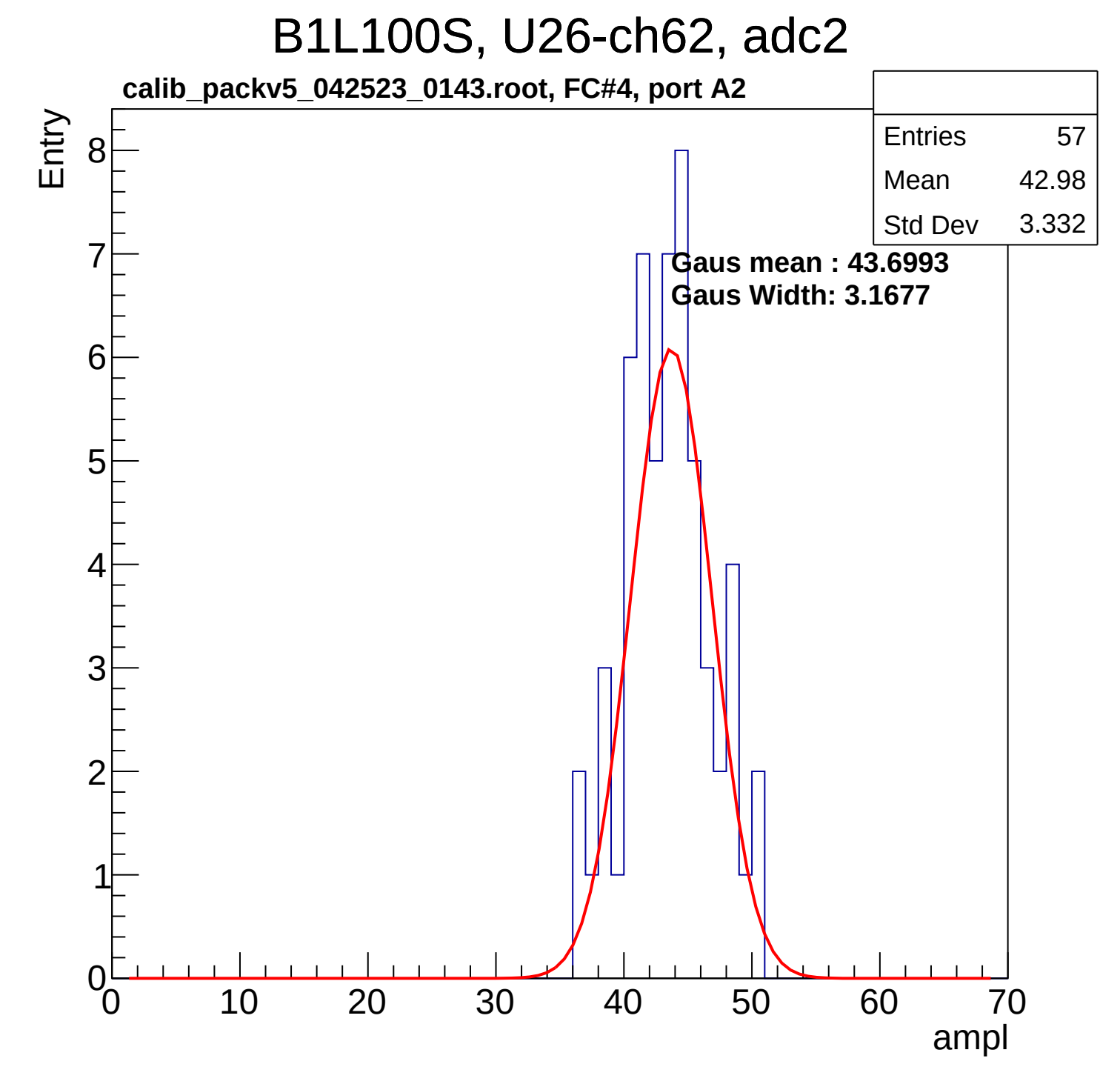
B1L100S, U26-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.98
Std Dev 3.332
Gaus mean : 43.6993
Gaus Width: 3.1677
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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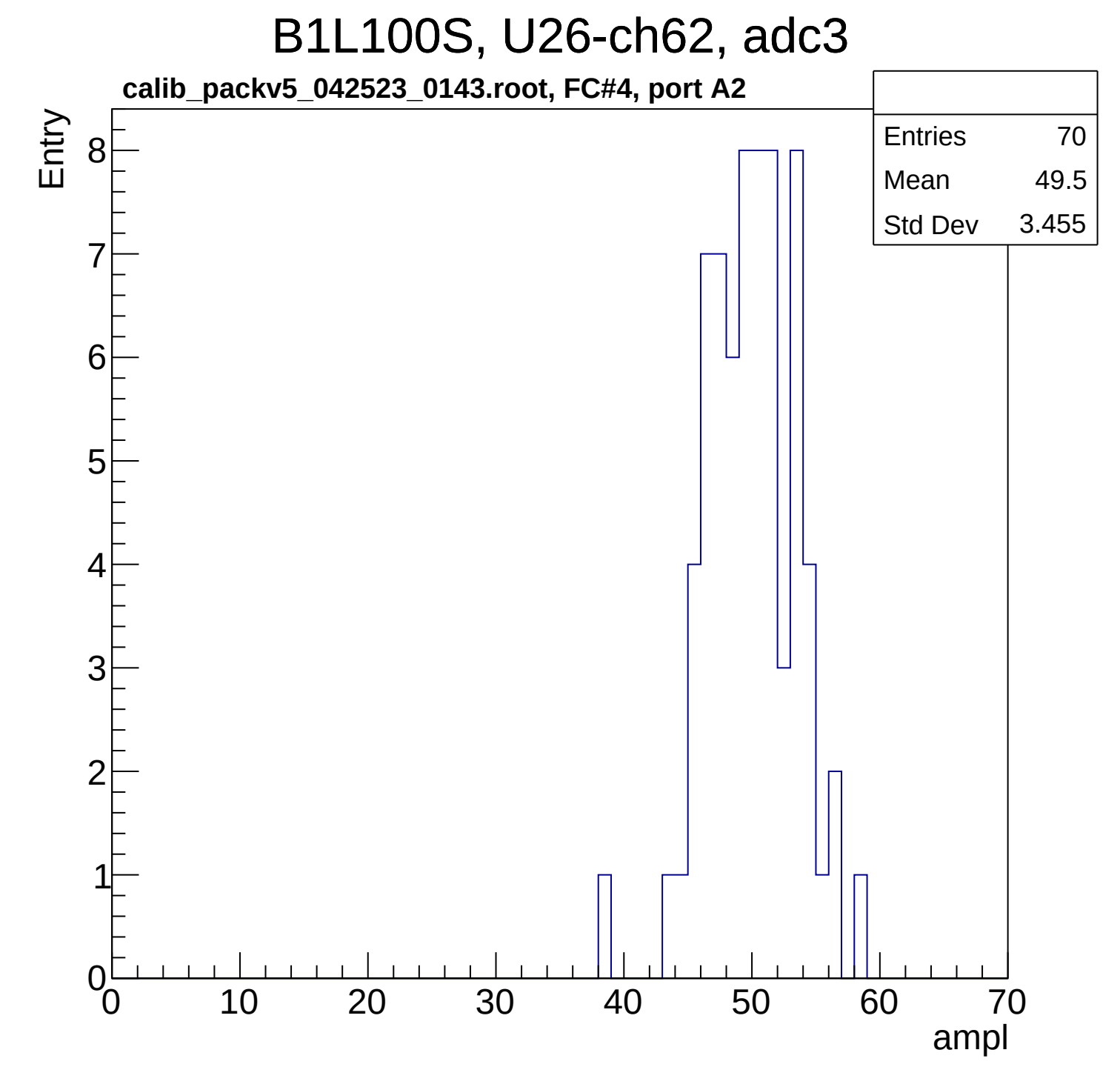
B1L100S, U26-ch62, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.5
Std Dev 3.455
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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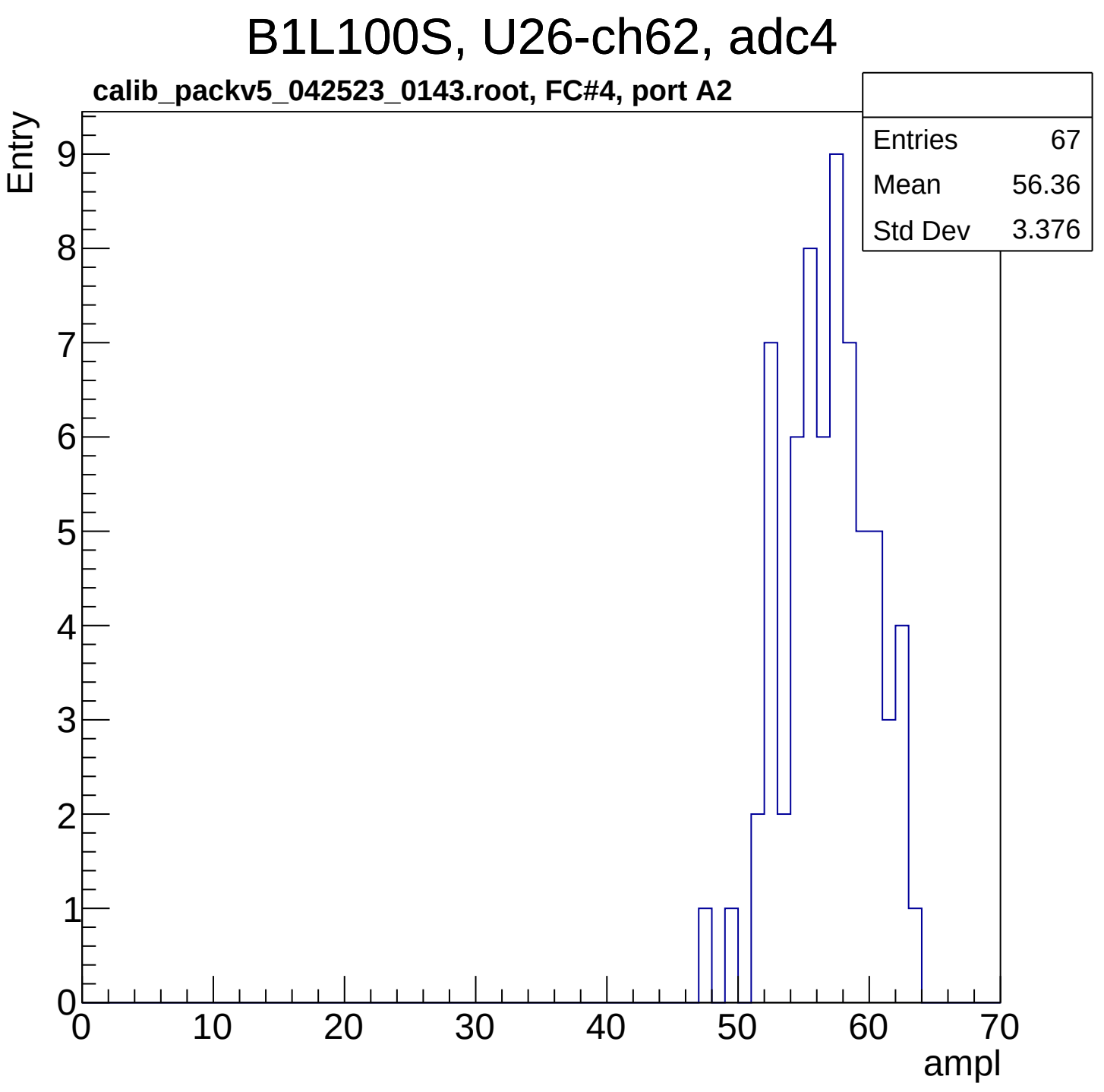
B1L100S, U26-ch62, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.36
Std Dev 3.376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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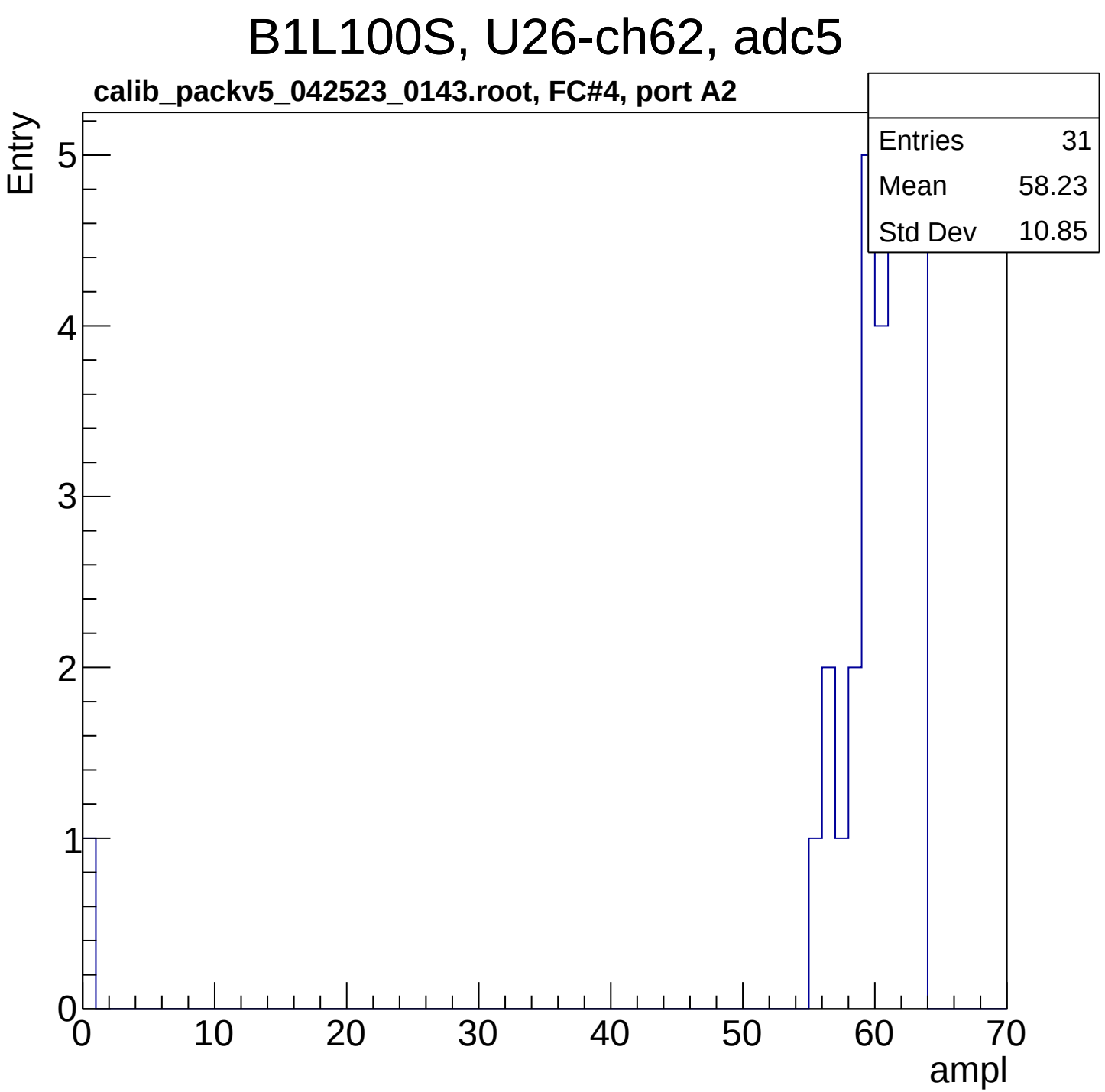
B1L100S, U26-ch62, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.23
Std Dev 10.85
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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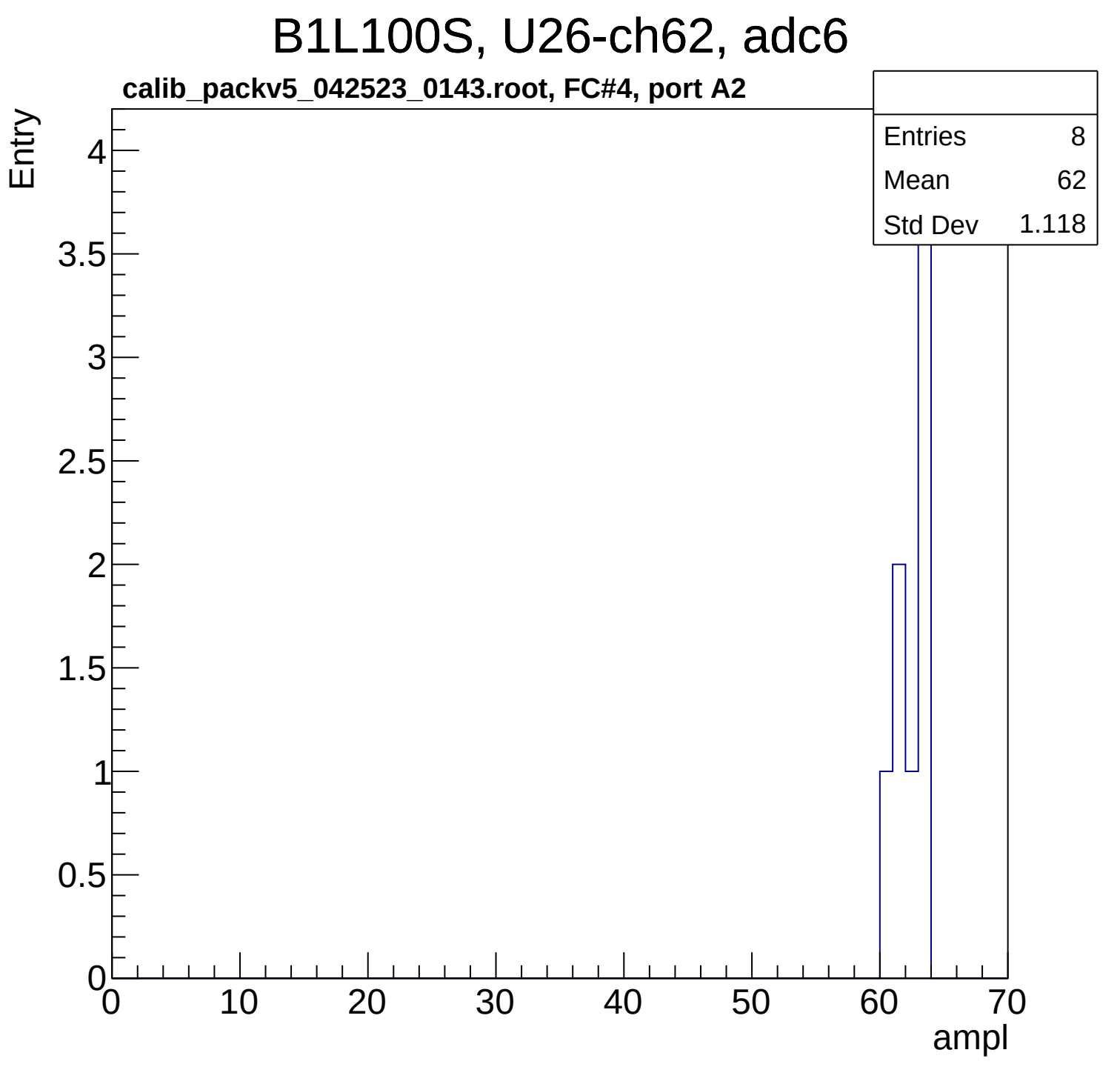
B1L100S, U26-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62
Std Dev 1.118
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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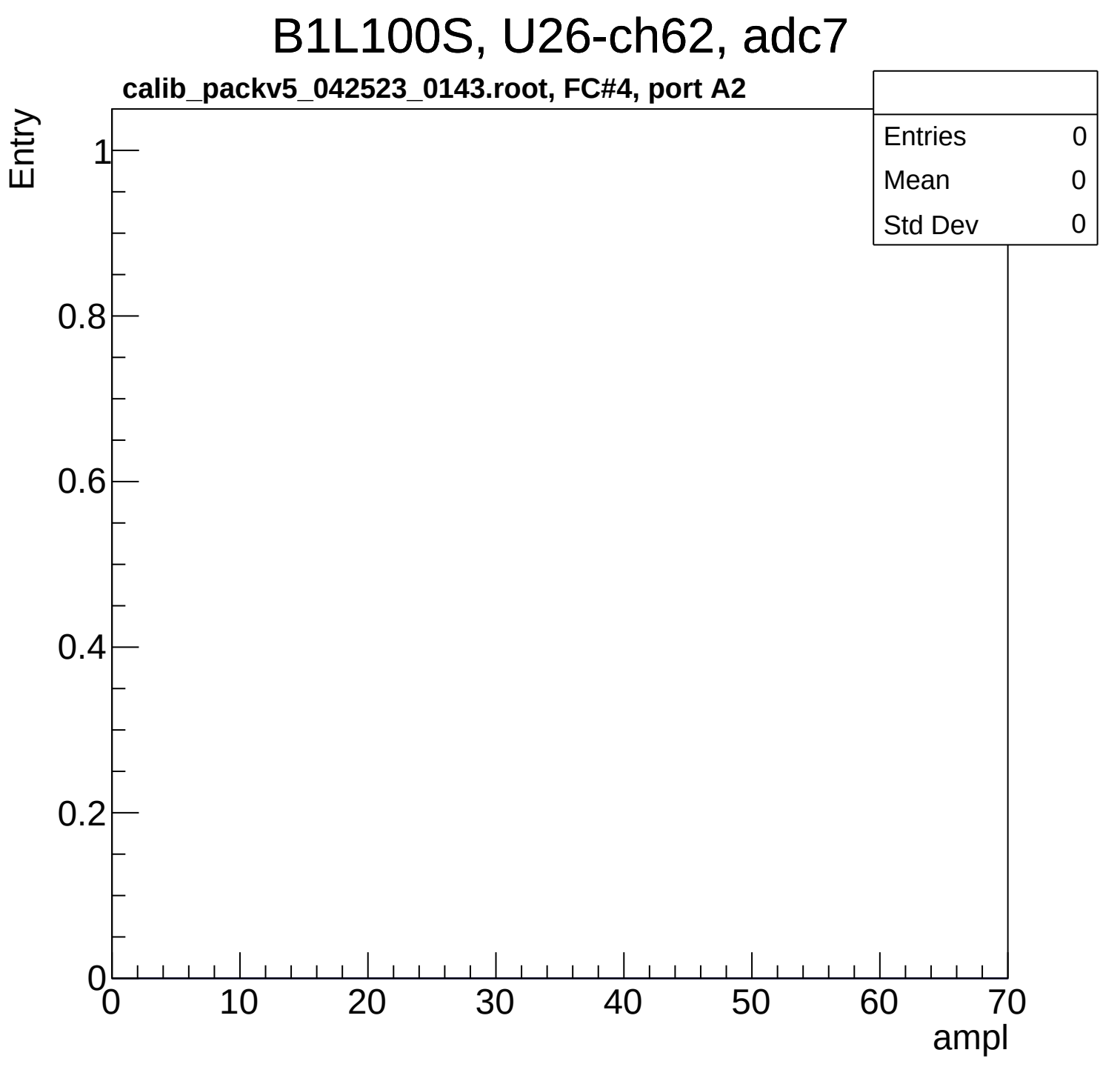
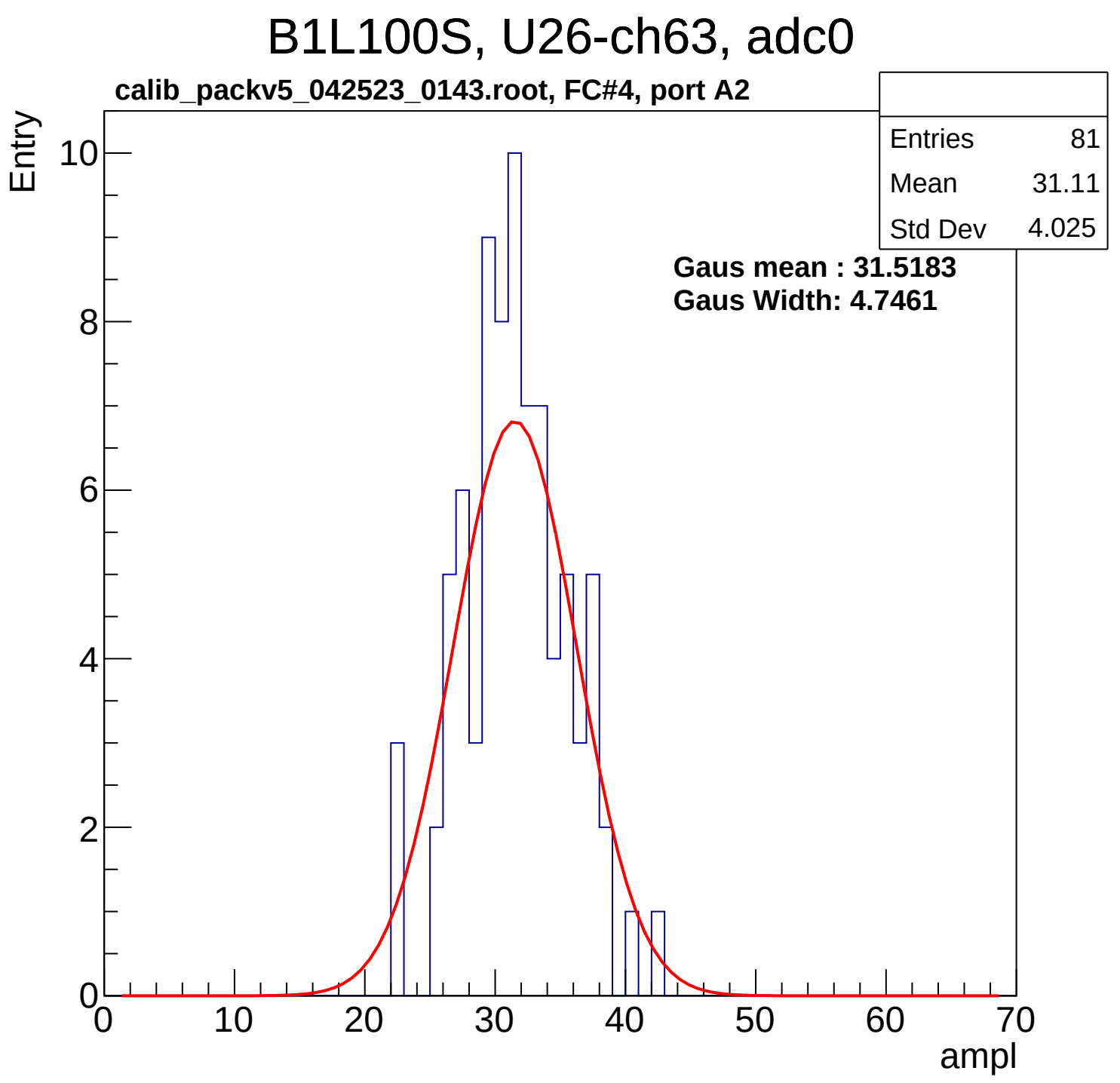
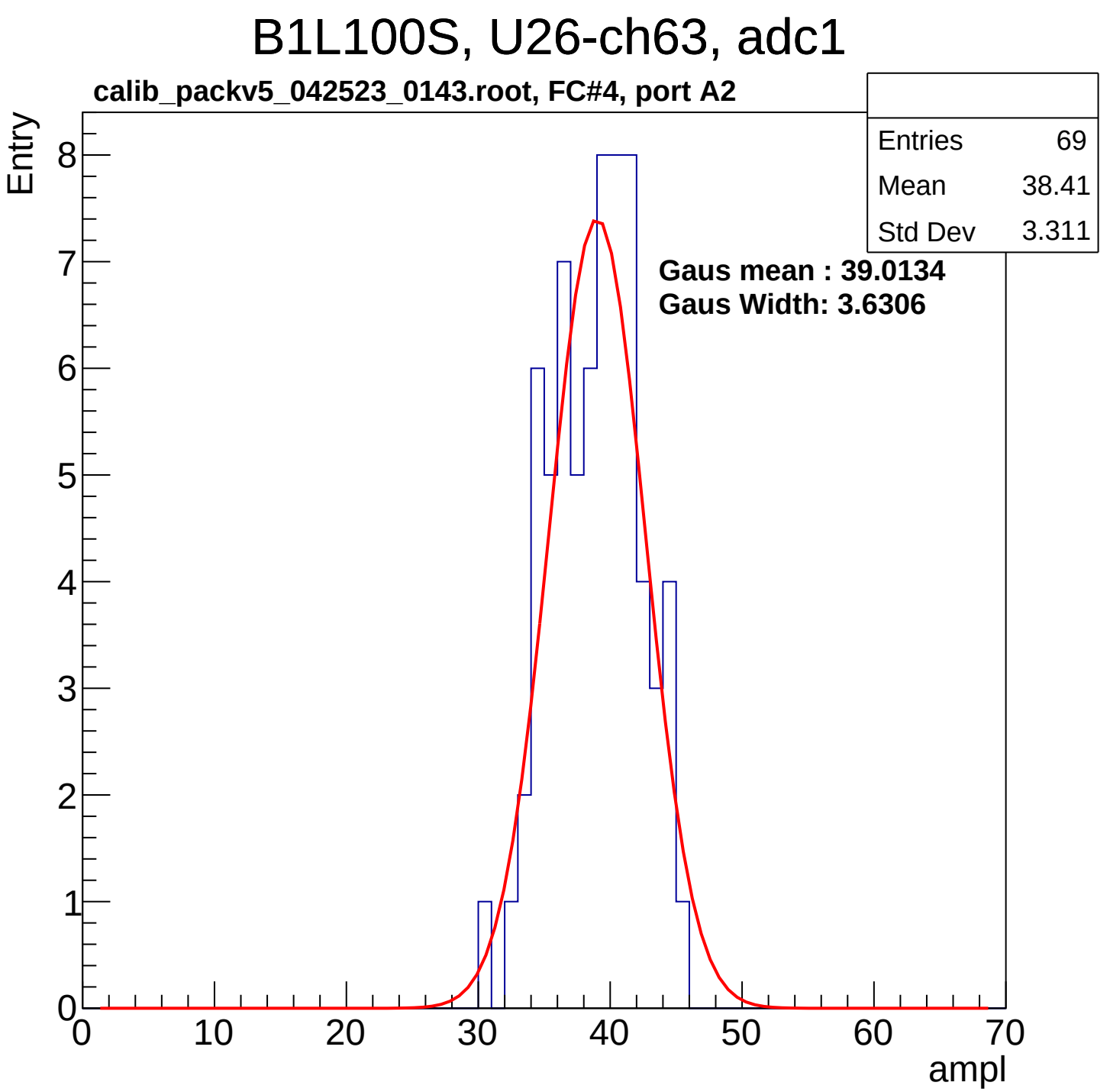

B1L100S, U26-ch62, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch63, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 31.11
Std Dev 4.025
Gaus mean : 31.5183
Gaus Width: 4.7461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 38.41
Std Dev 3.311
Gaus mean : 39.0134
Gaus Width: 3.6306
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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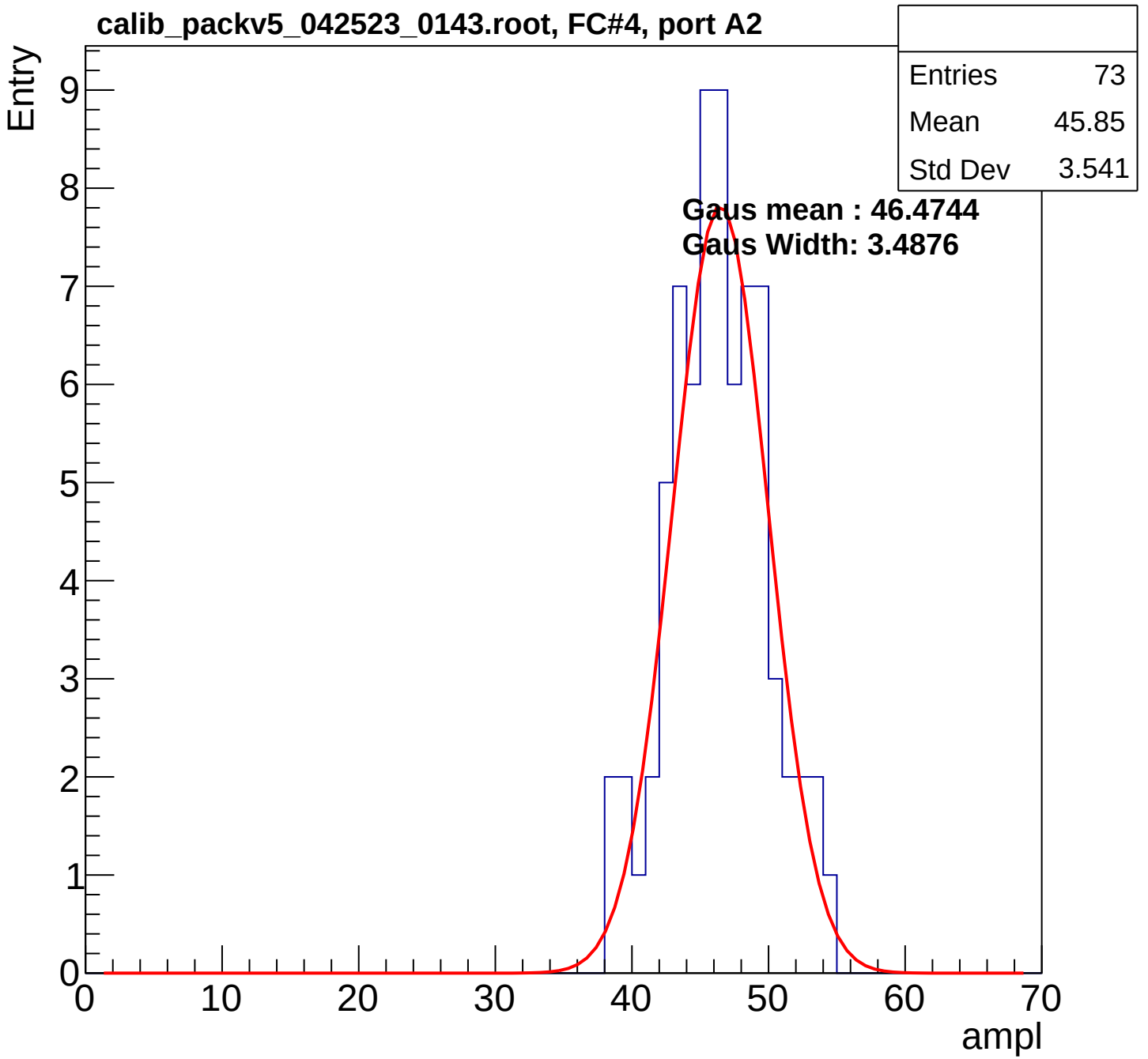
B1L100S, U26-ch63, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 45.85
Std Dev 3.541
Gaus mean : 46.4744
Gaus Width: 3.4876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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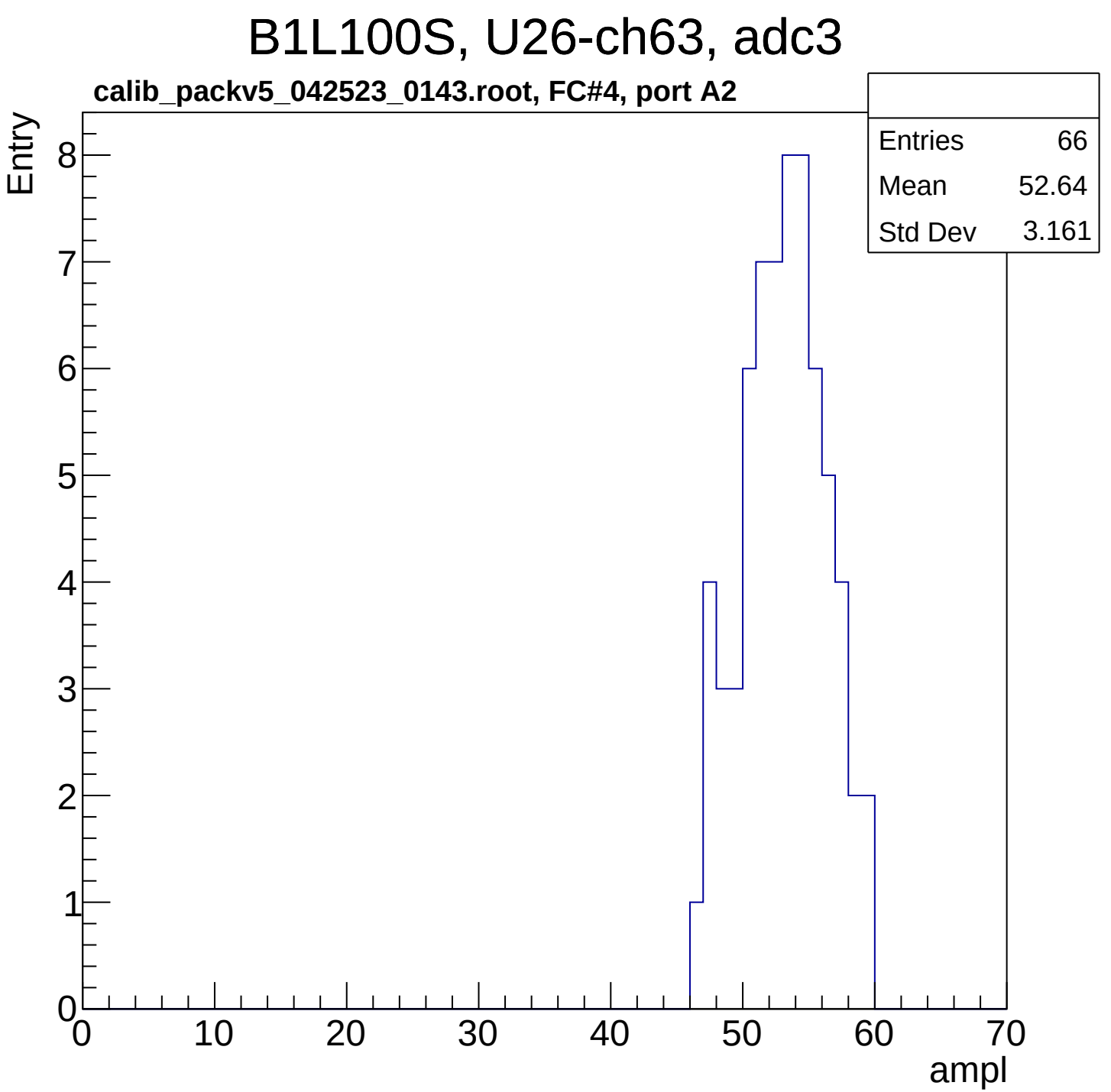
B1L100S, U26-ch63, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 52.64
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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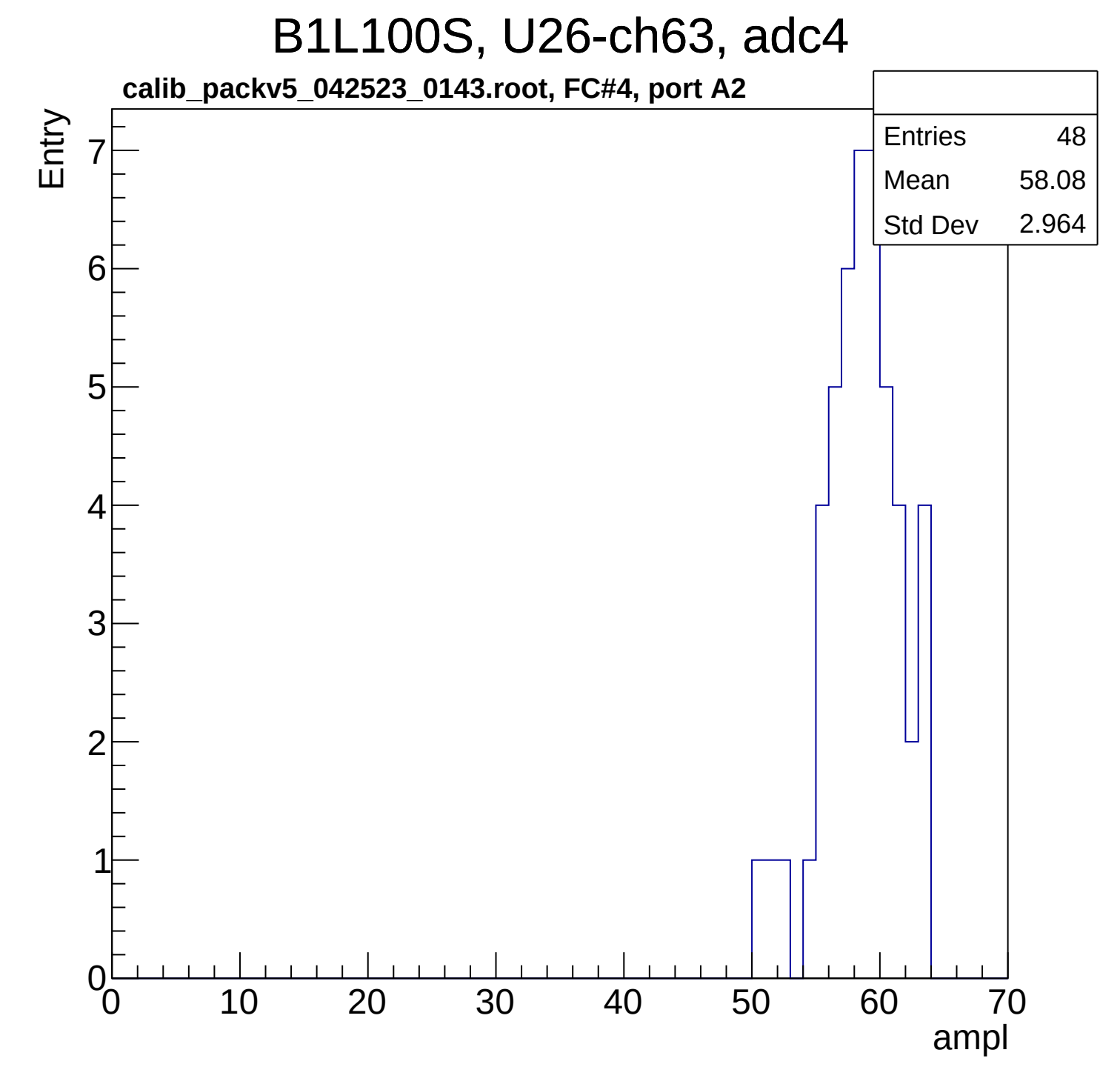
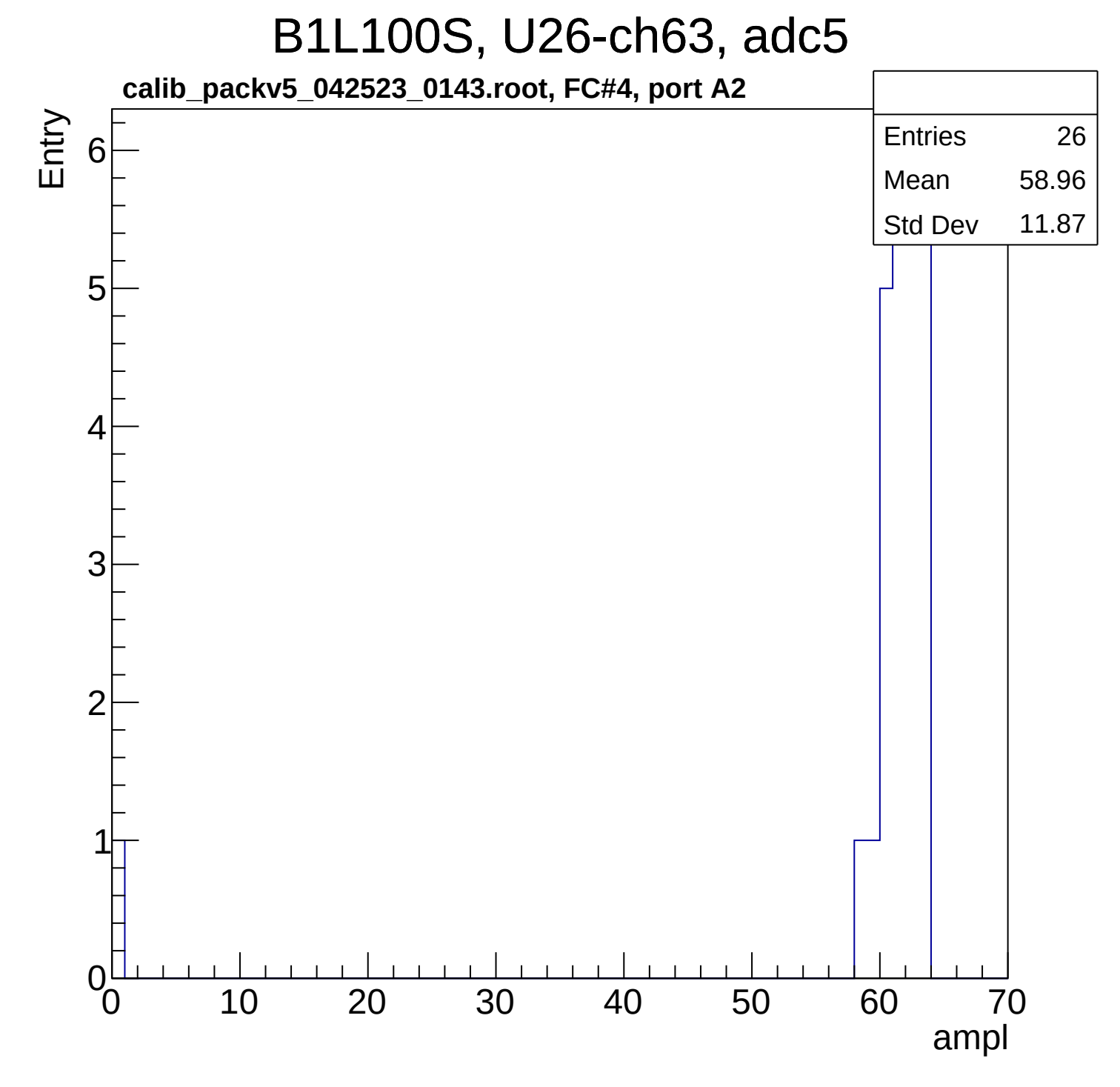
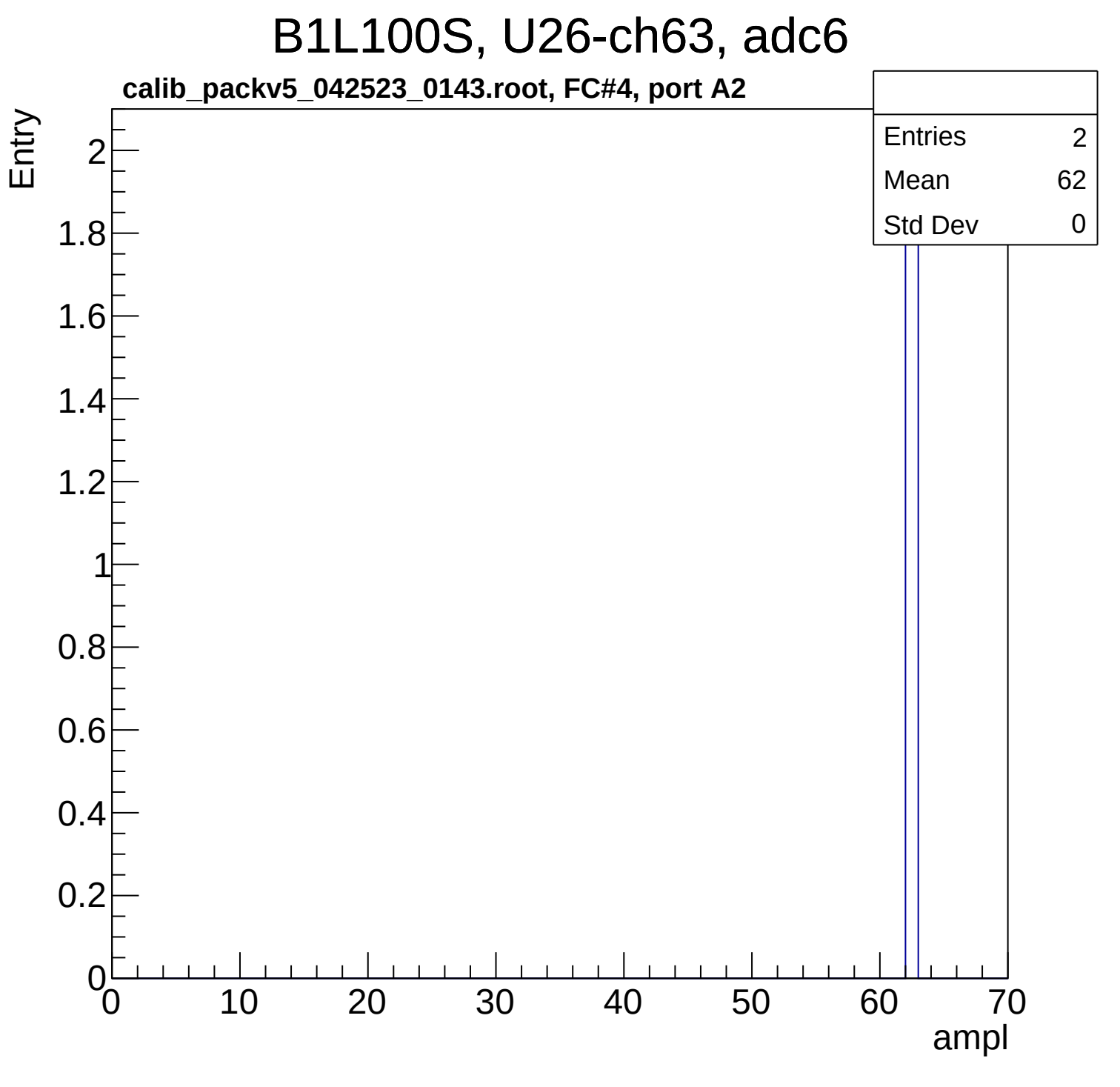
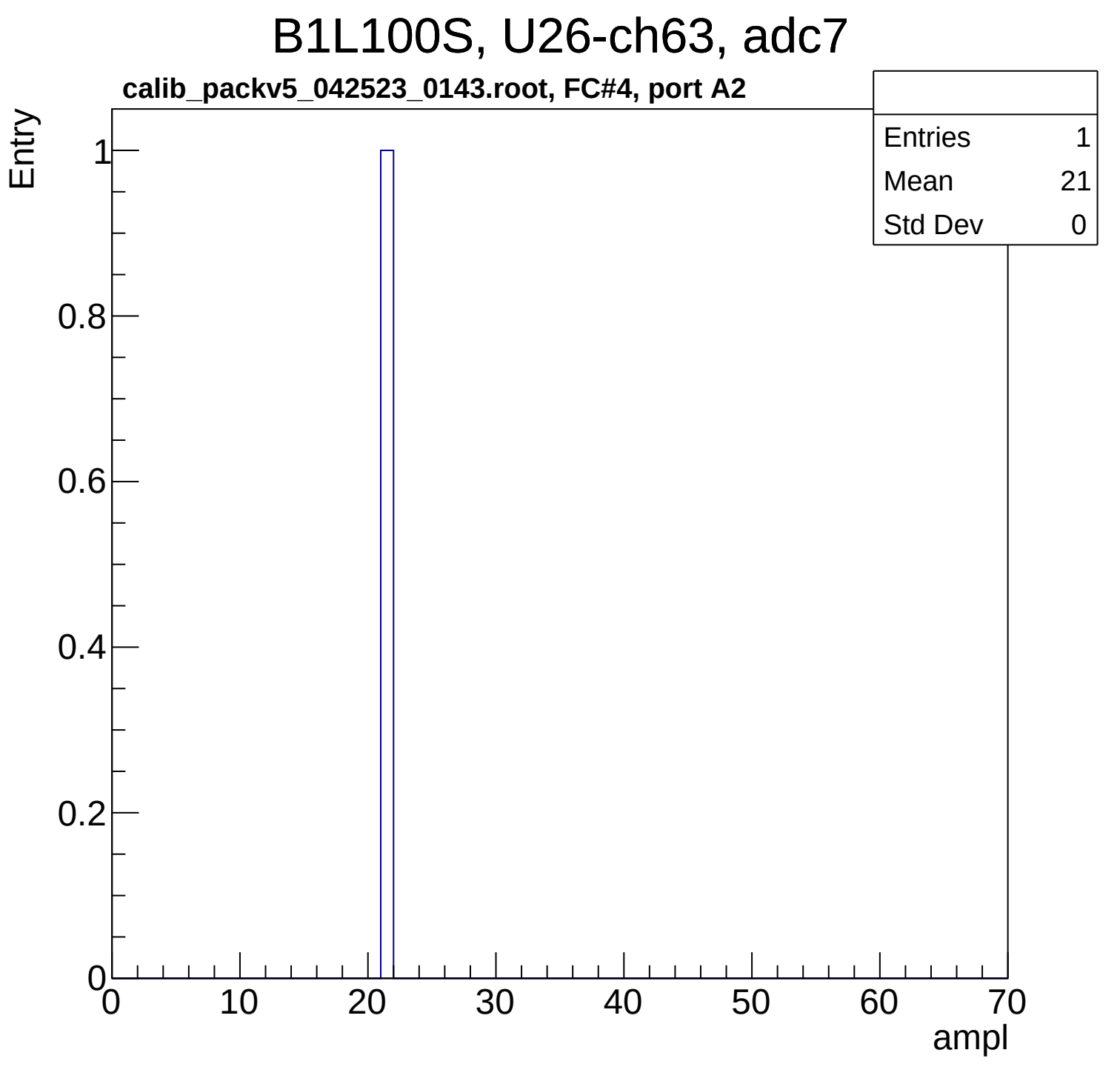

B1L100S, U26-ch63, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.08
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.96
Std Dev 11.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch63, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch63, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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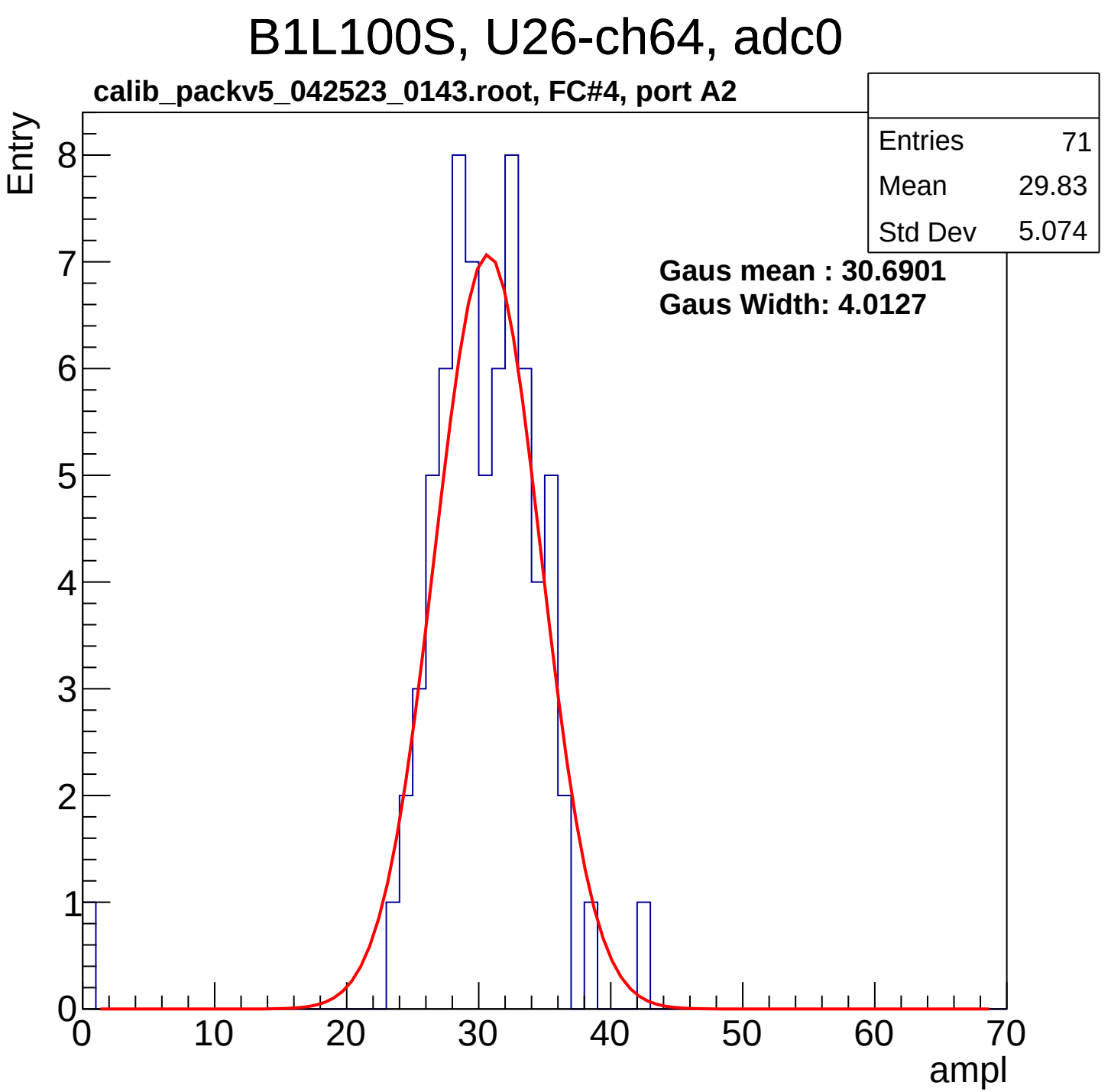
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.83
Std Dev 5.074
Gaus mean : 30.6901
Gaus Width: 4.0127
Entry
ampl
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1
2
3
4
5
6
7
8
0
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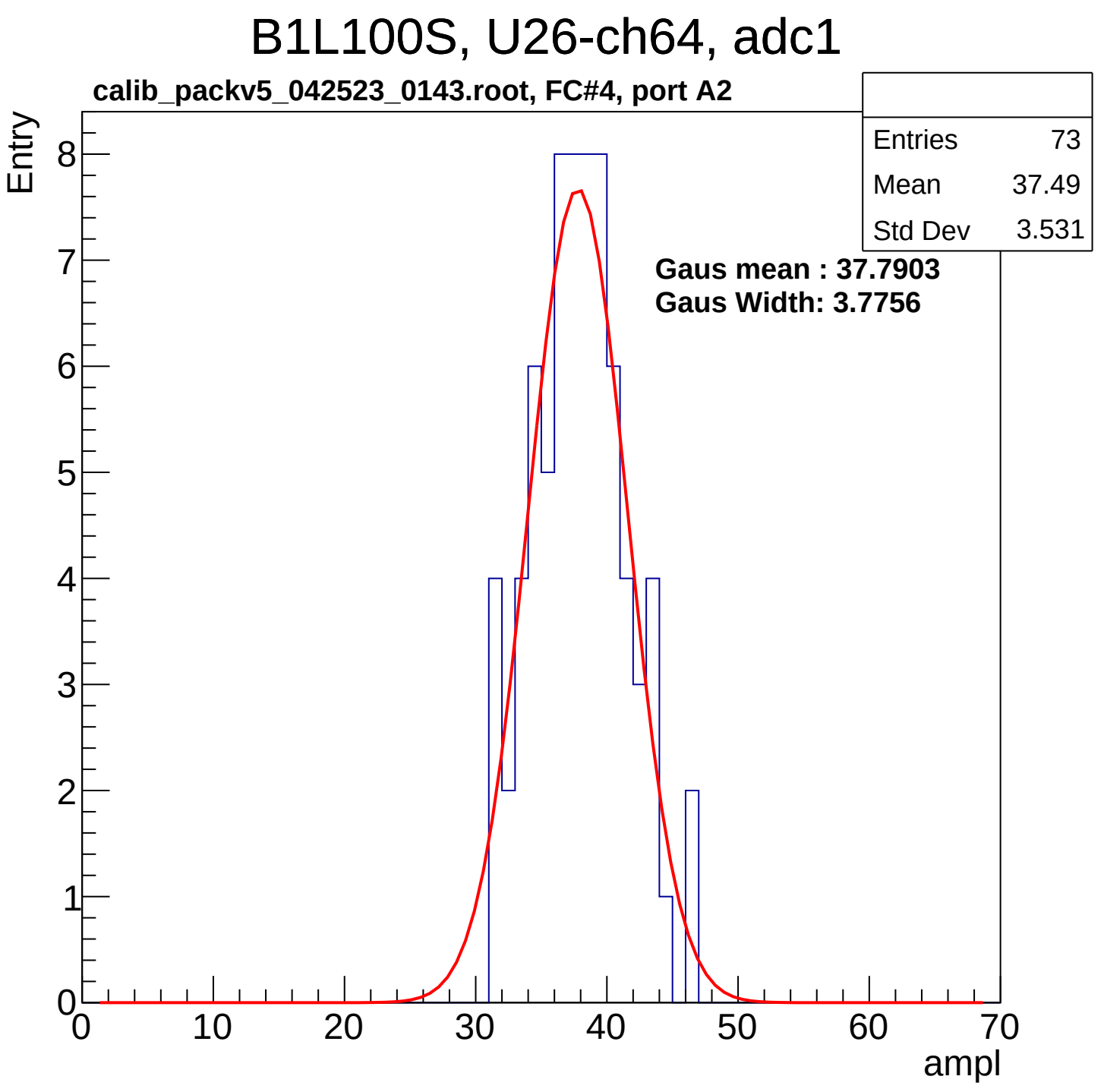
20
30
40
50
60
70


B1L100S, U26-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.49
Std Dev 3.531
Gaus mean : 37.7903
Gaus Width: 3.7756
Entry
ampl
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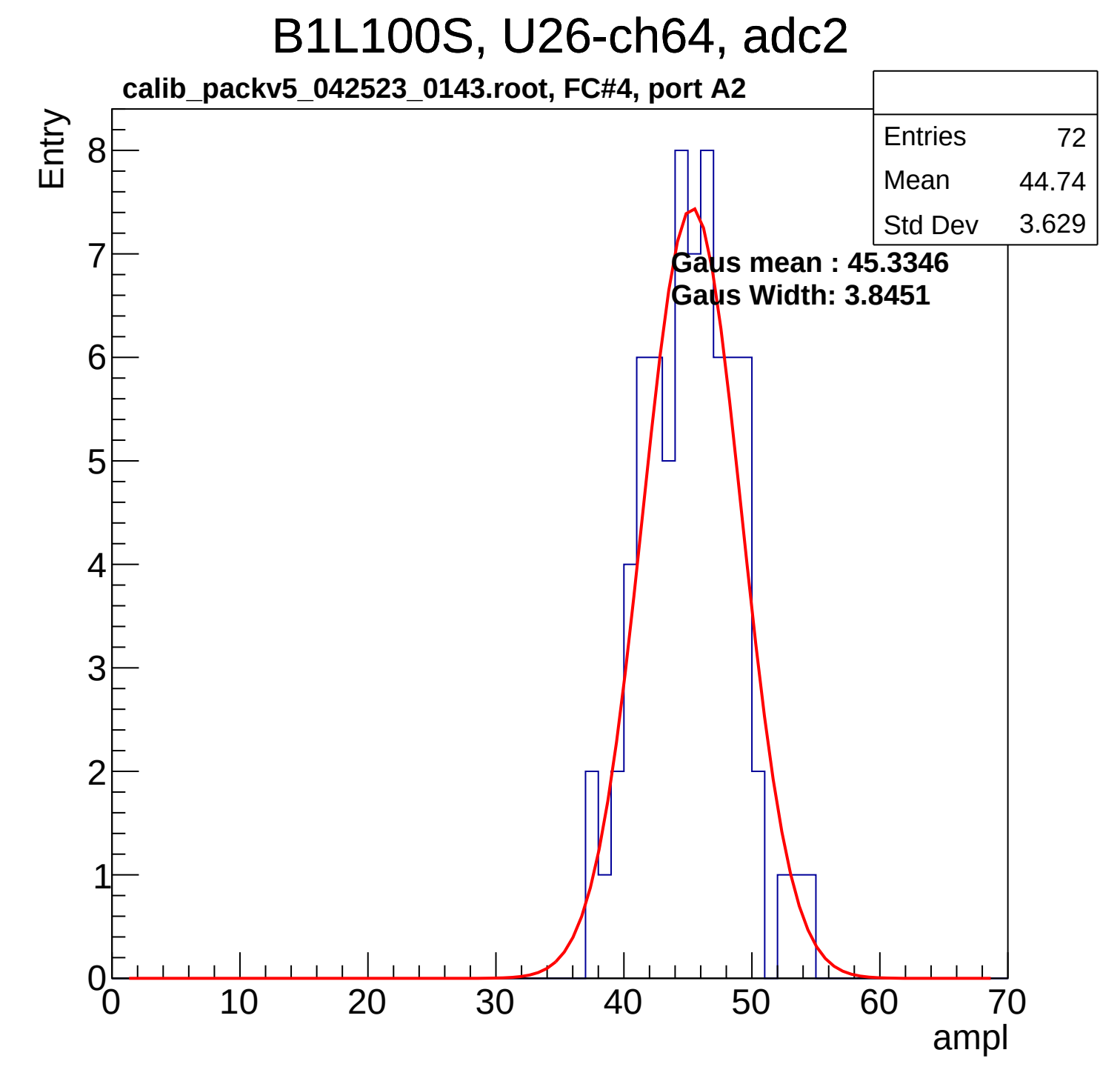
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.74
Std Dev 3.629
Gaus mean : 45.3346
Gaus Width: 3.8451
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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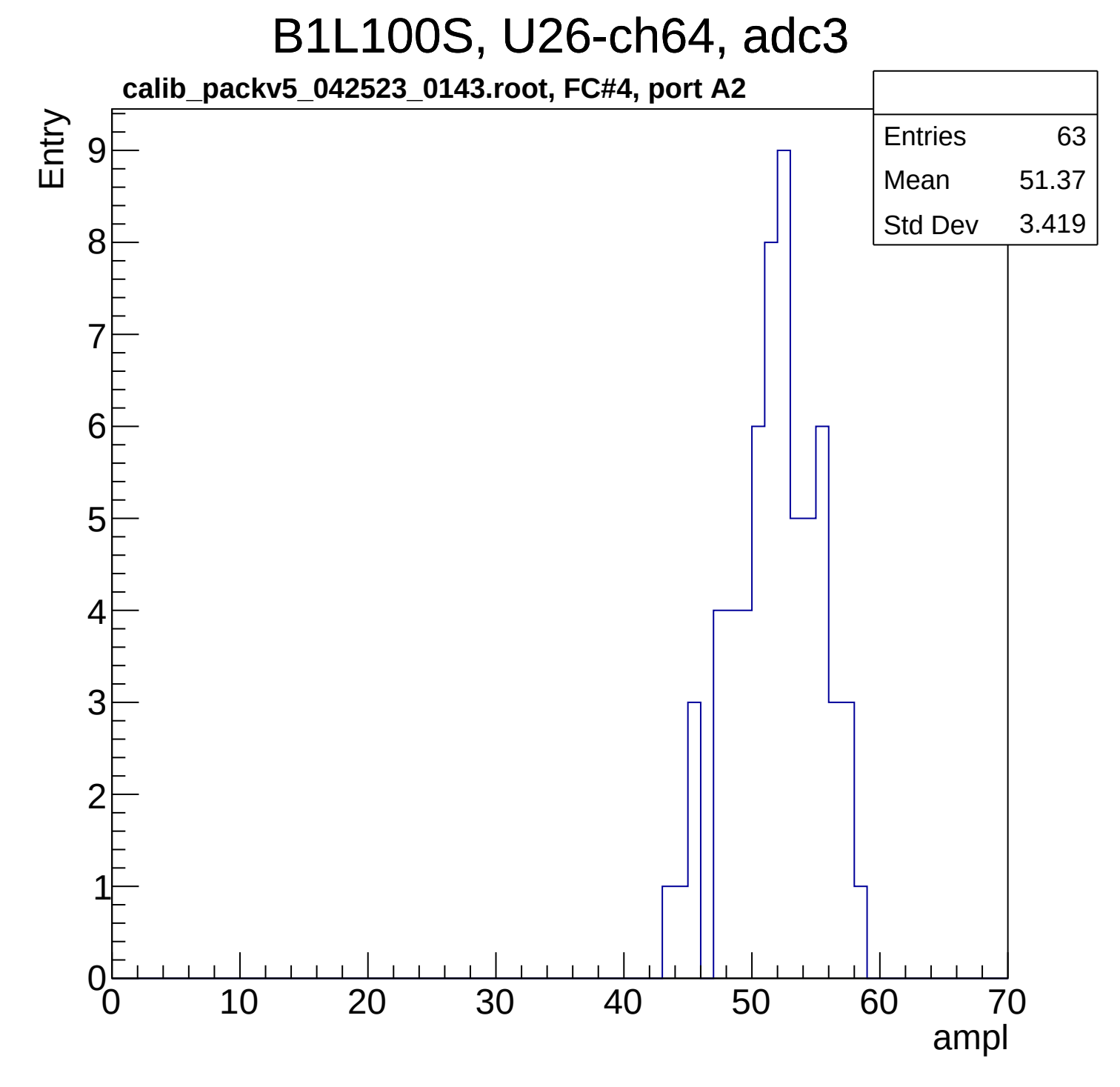
B1L100S, U26-ch64, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.37
Std Dev 3.419
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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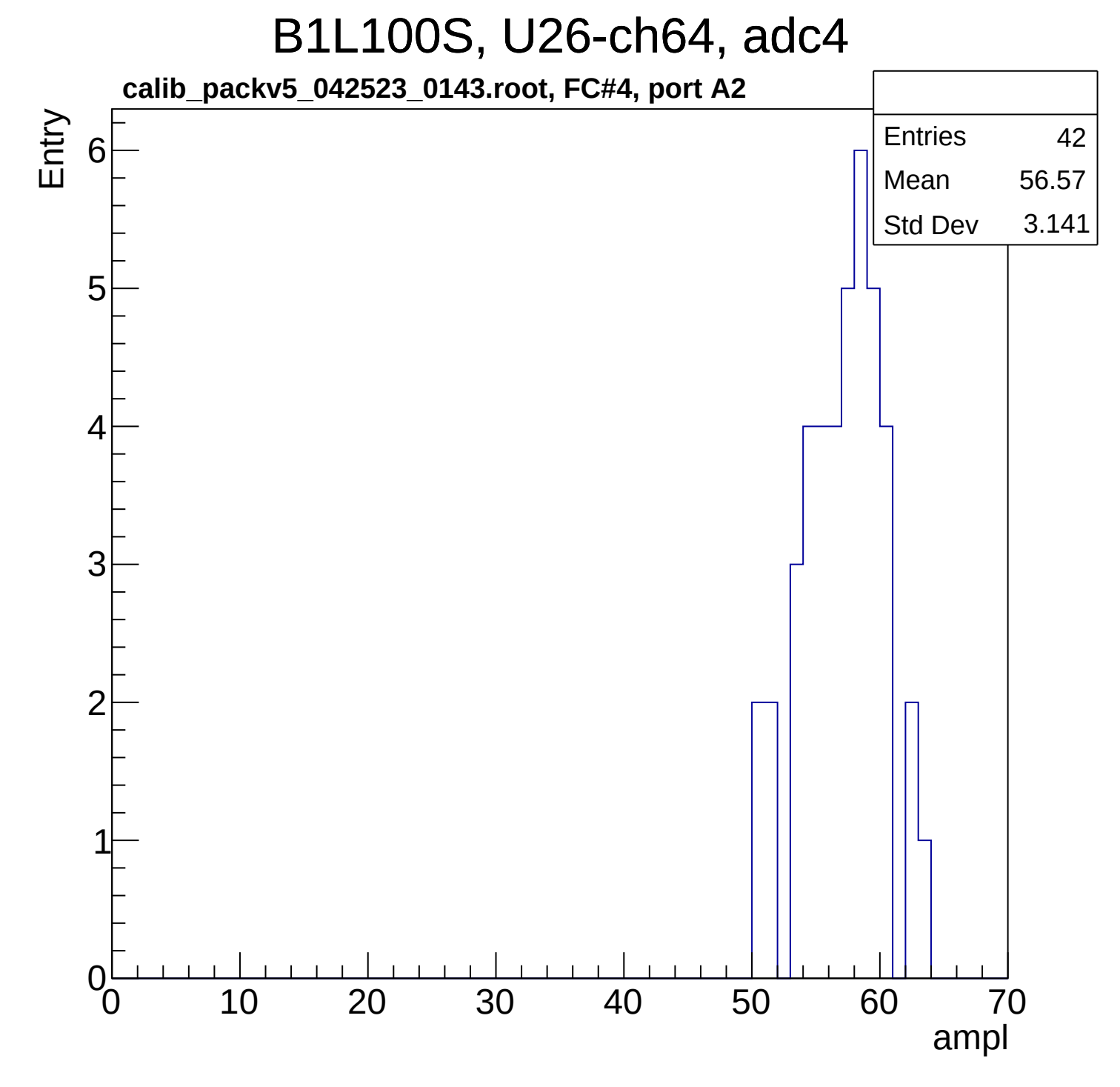
B1L100S, U26-ch64, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 56.57
Std Dev 3.141
Entry
ampl
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0
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60
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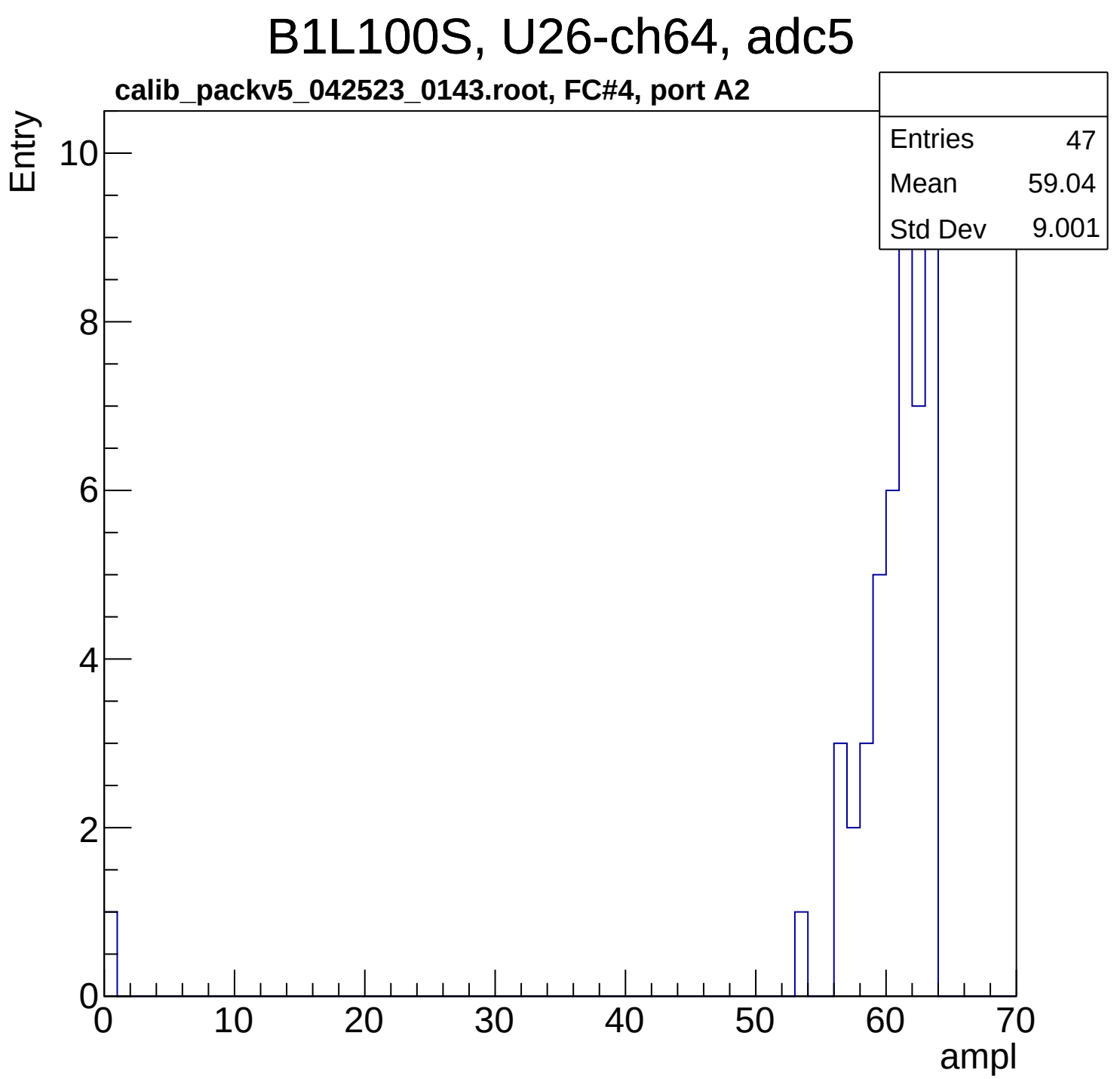
B1L100S, U26-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.04
Std Dev 9.001
Entry
ampl
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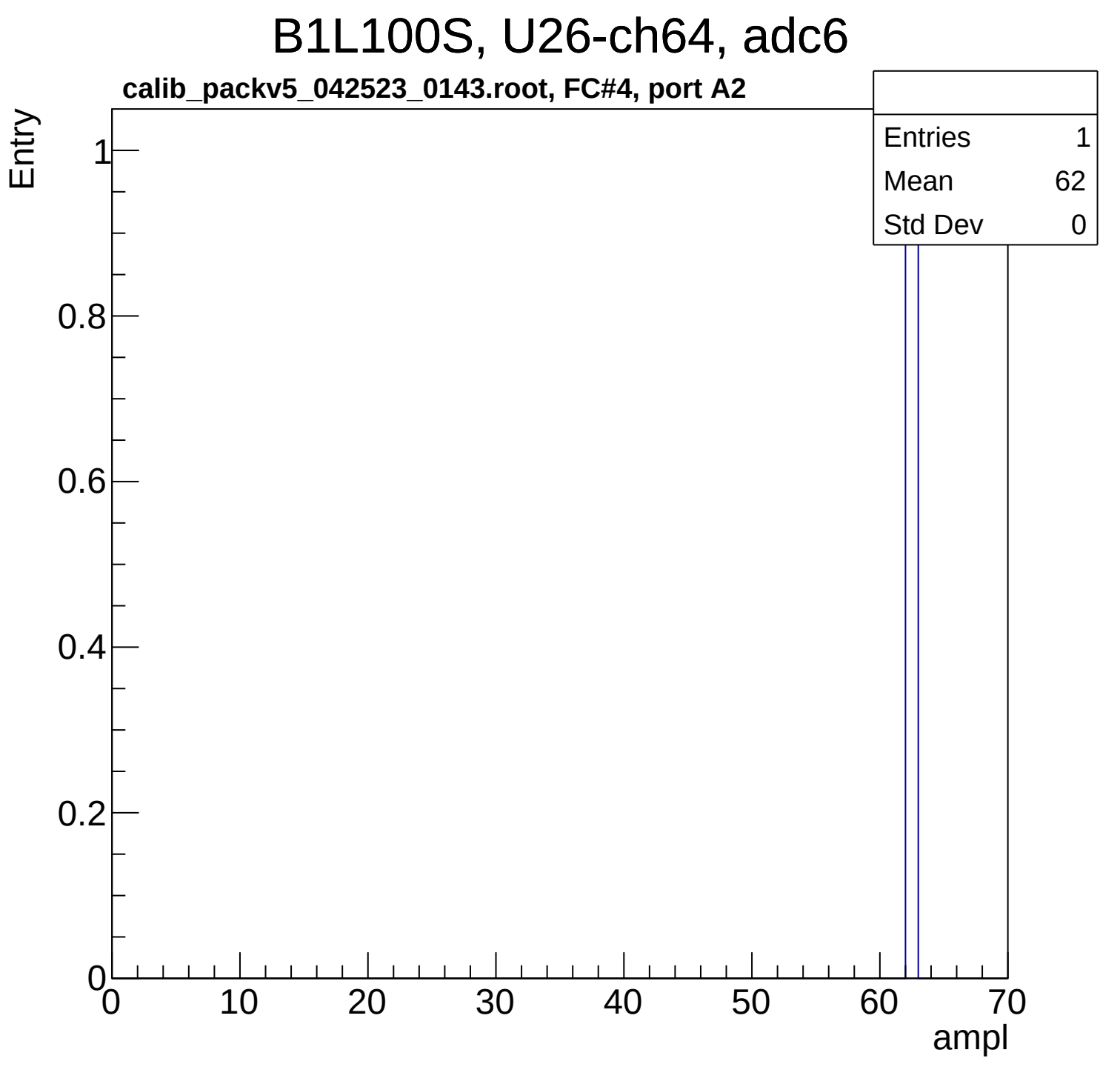
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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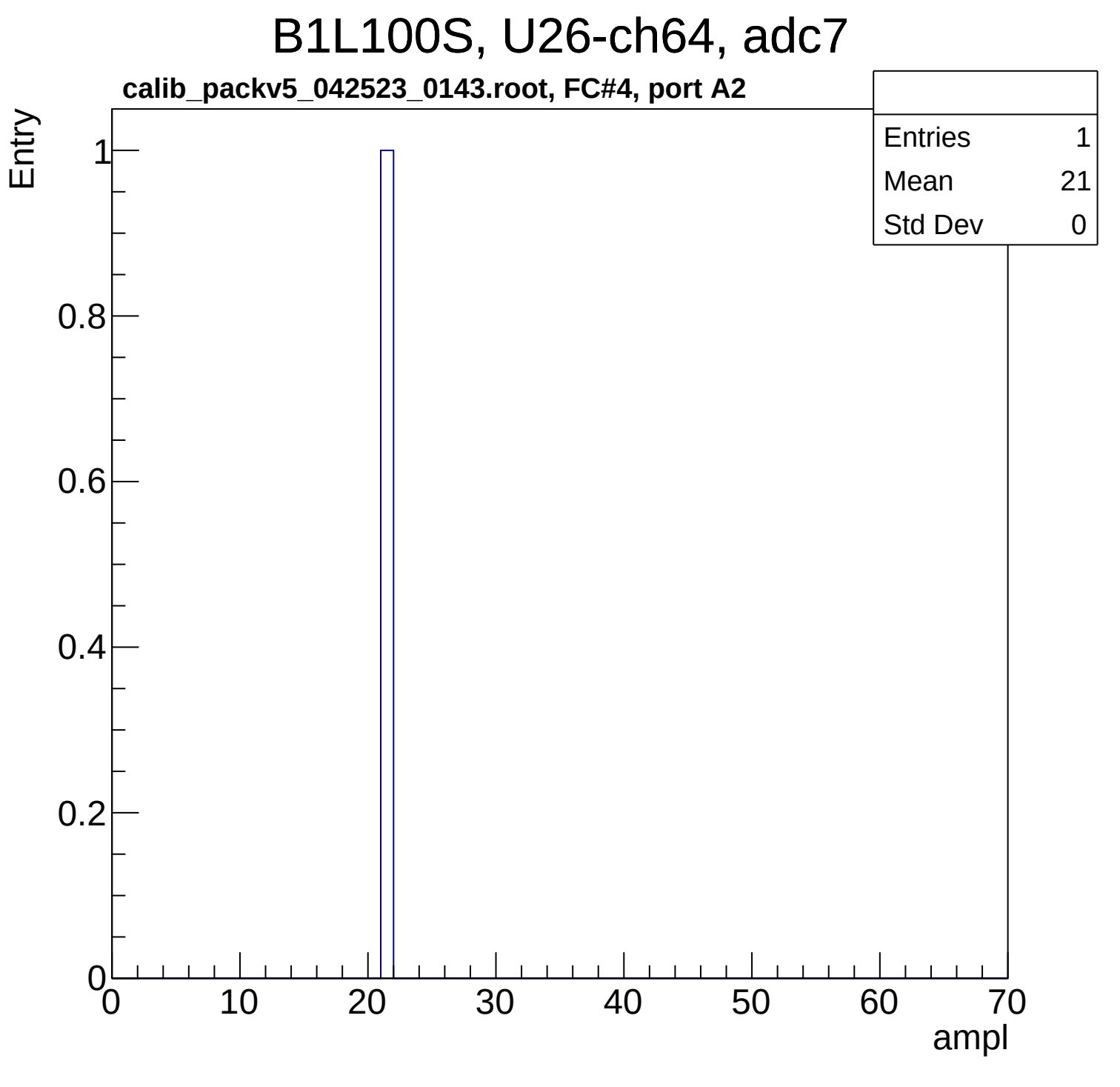
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch64, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
0.8
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0
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40
50
60
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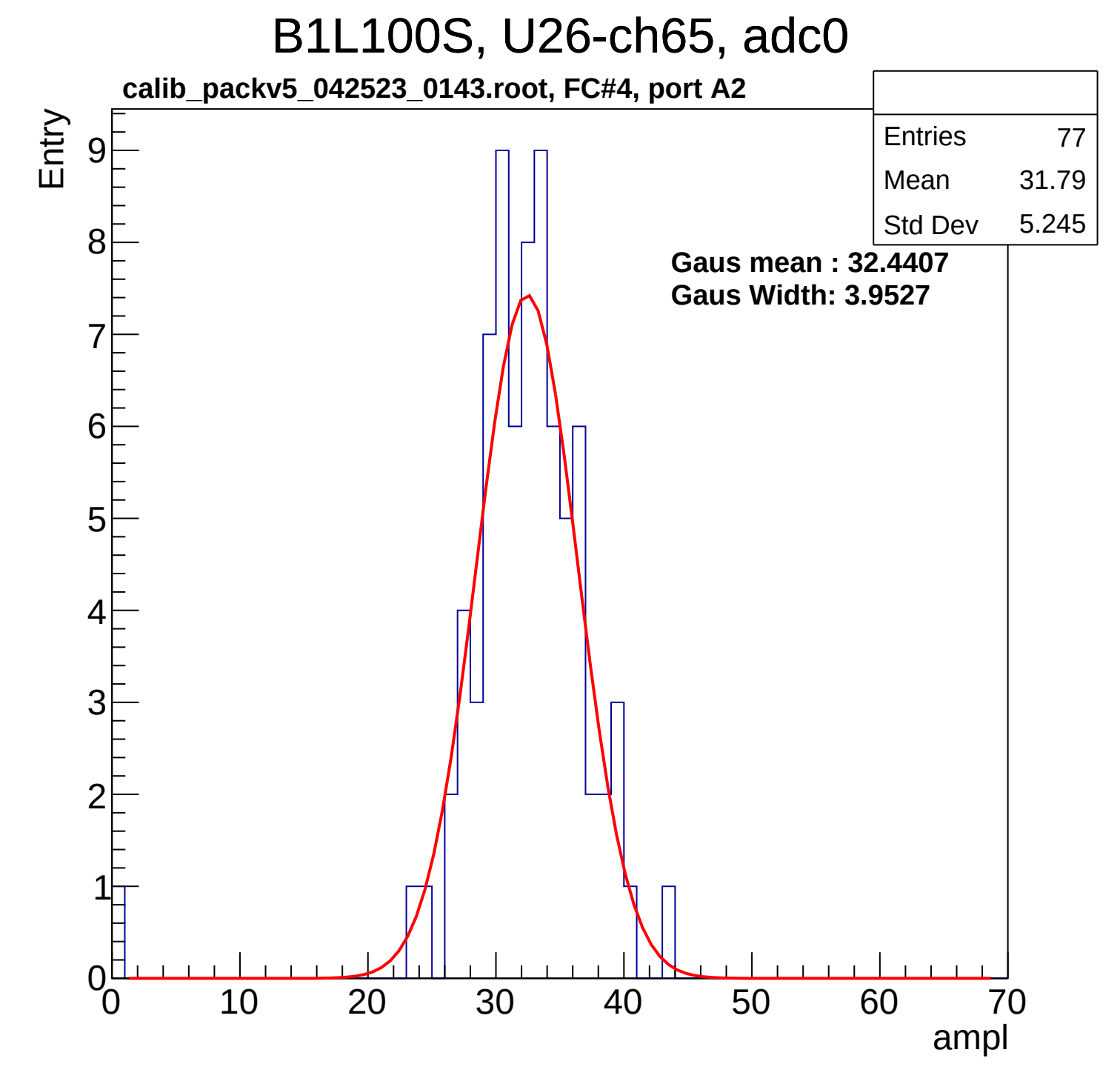
B1L100S, U26-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 31.79
Std Dev 5.245
Gaus mean : 32.4407
Gaus Width: 3.9527
Entry
ampl
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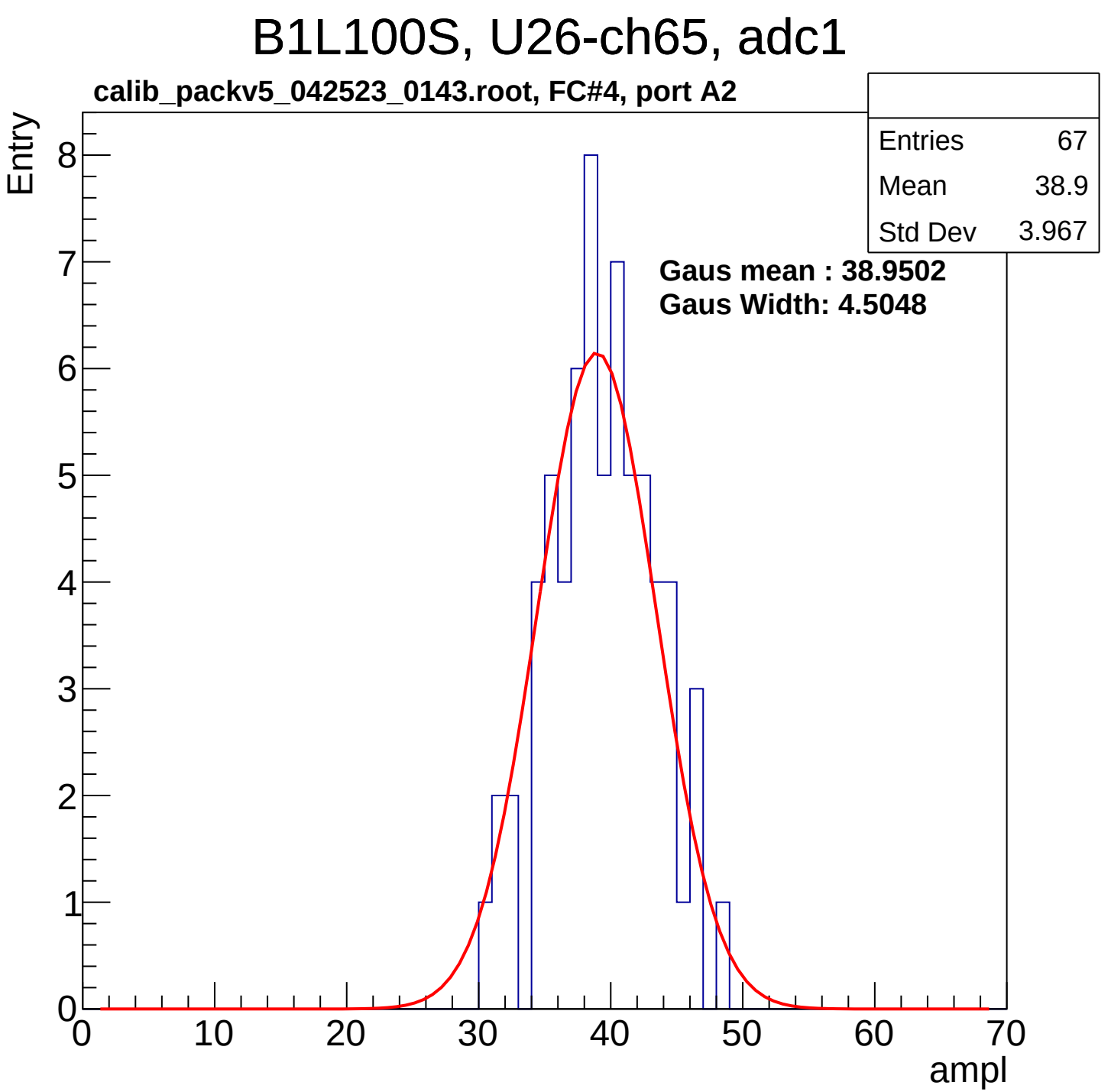
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 38.9
Std Dev 3.967
Gaus mean : 38.9502
Gaus Width: 4.5048
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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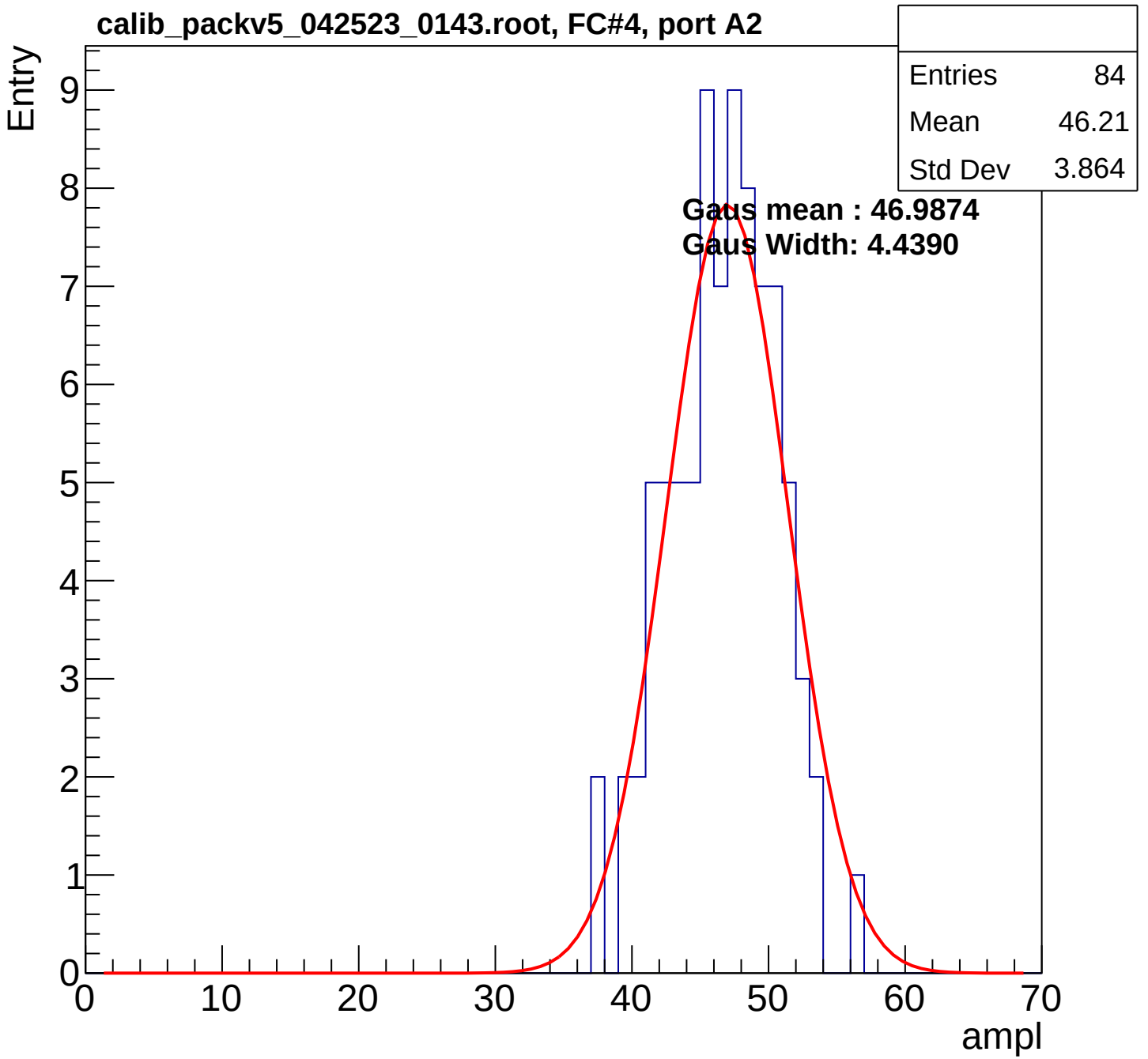
B1L100S, U26-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 46.21
Std Dev 3.864
Gaus mean : 46.9874
Gaus Width: 4.4390
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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50
60
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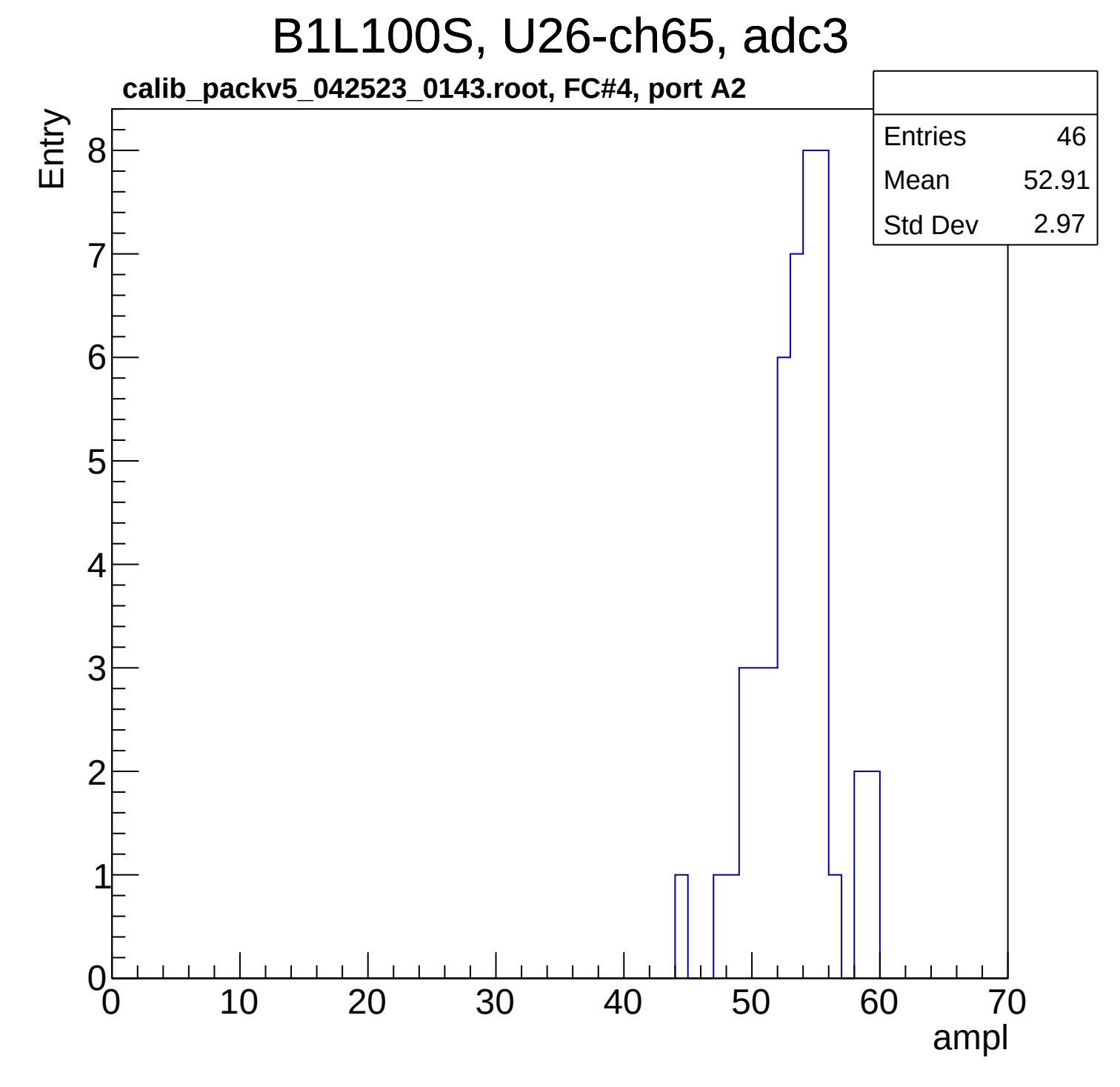
B1L100S, U26-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 52.91
Std Dev 2.97
Entry
ampl
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4
5
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0
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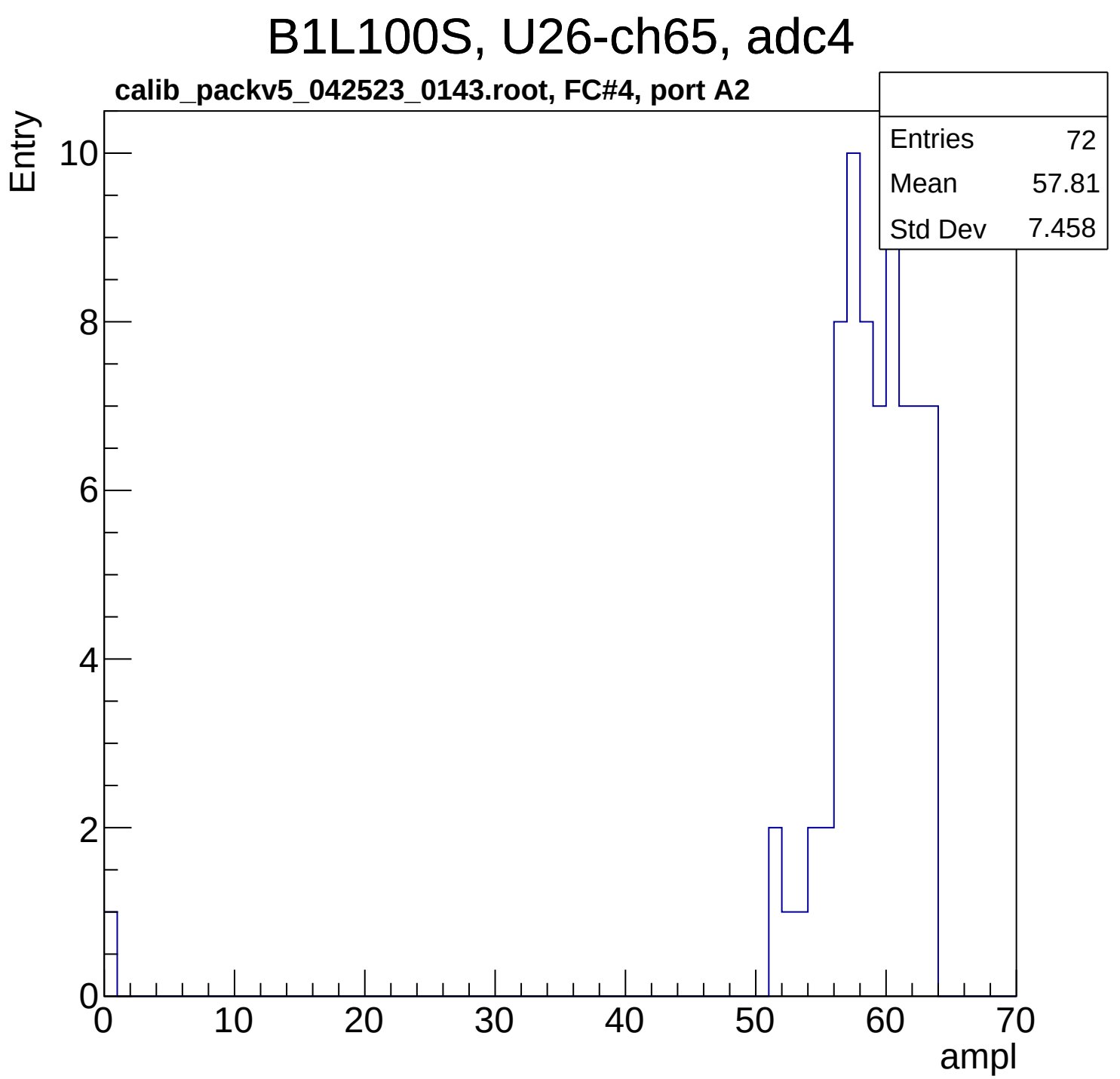
B1L100S, U26-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 57.81
Std Dev 7.458
Entry
ampl
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6
8
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0
10
20
30
40
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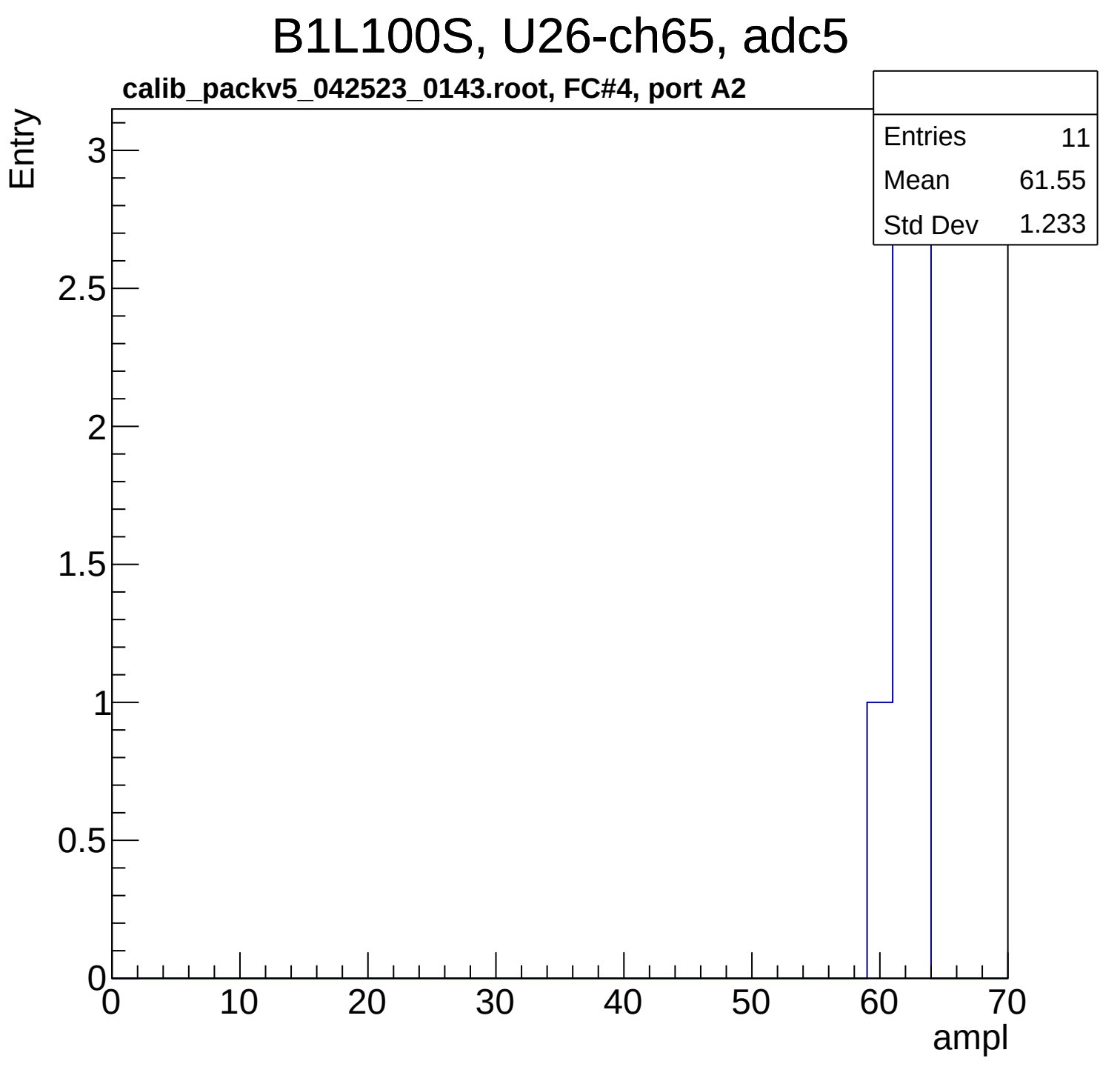
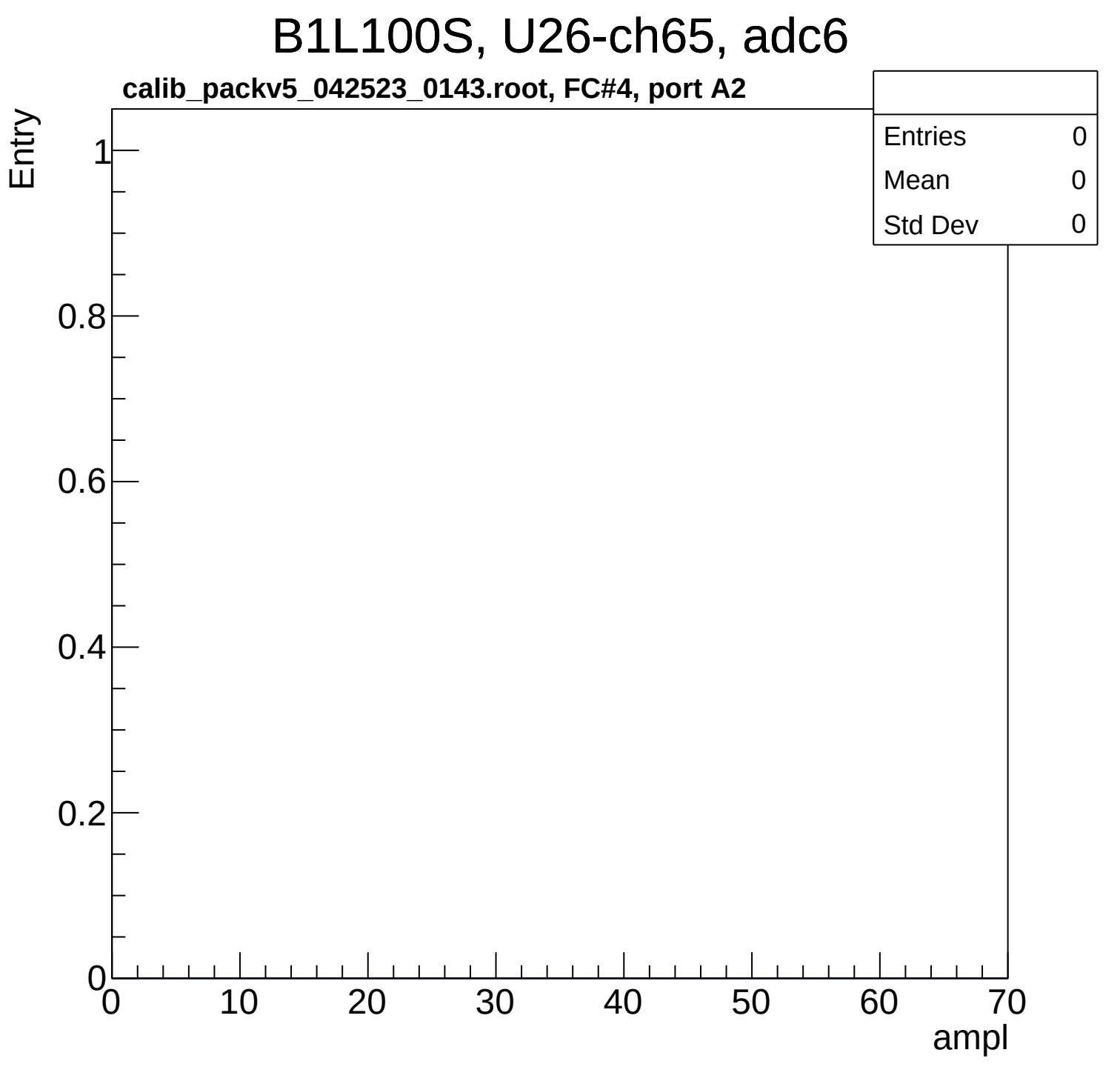
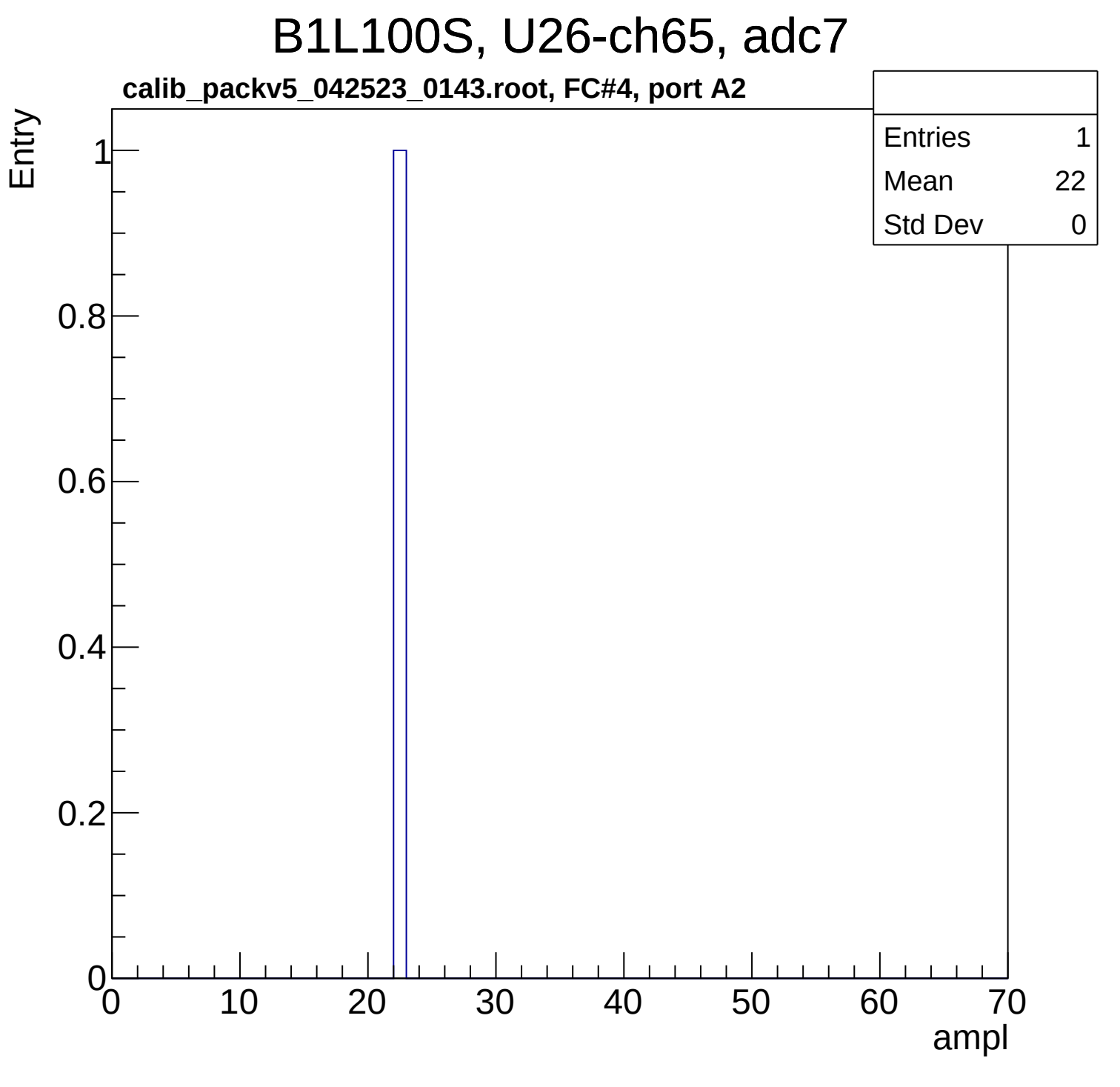
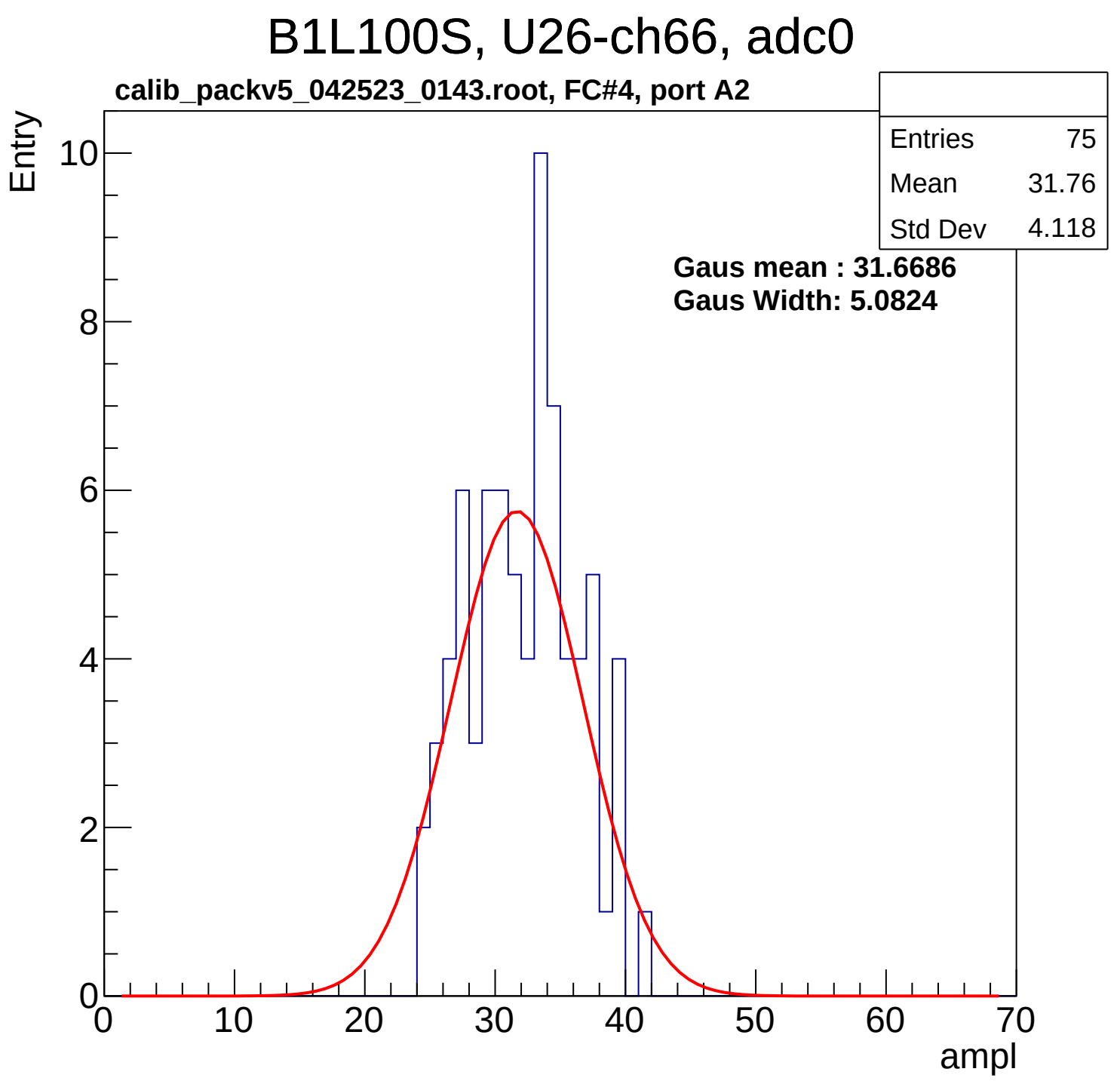
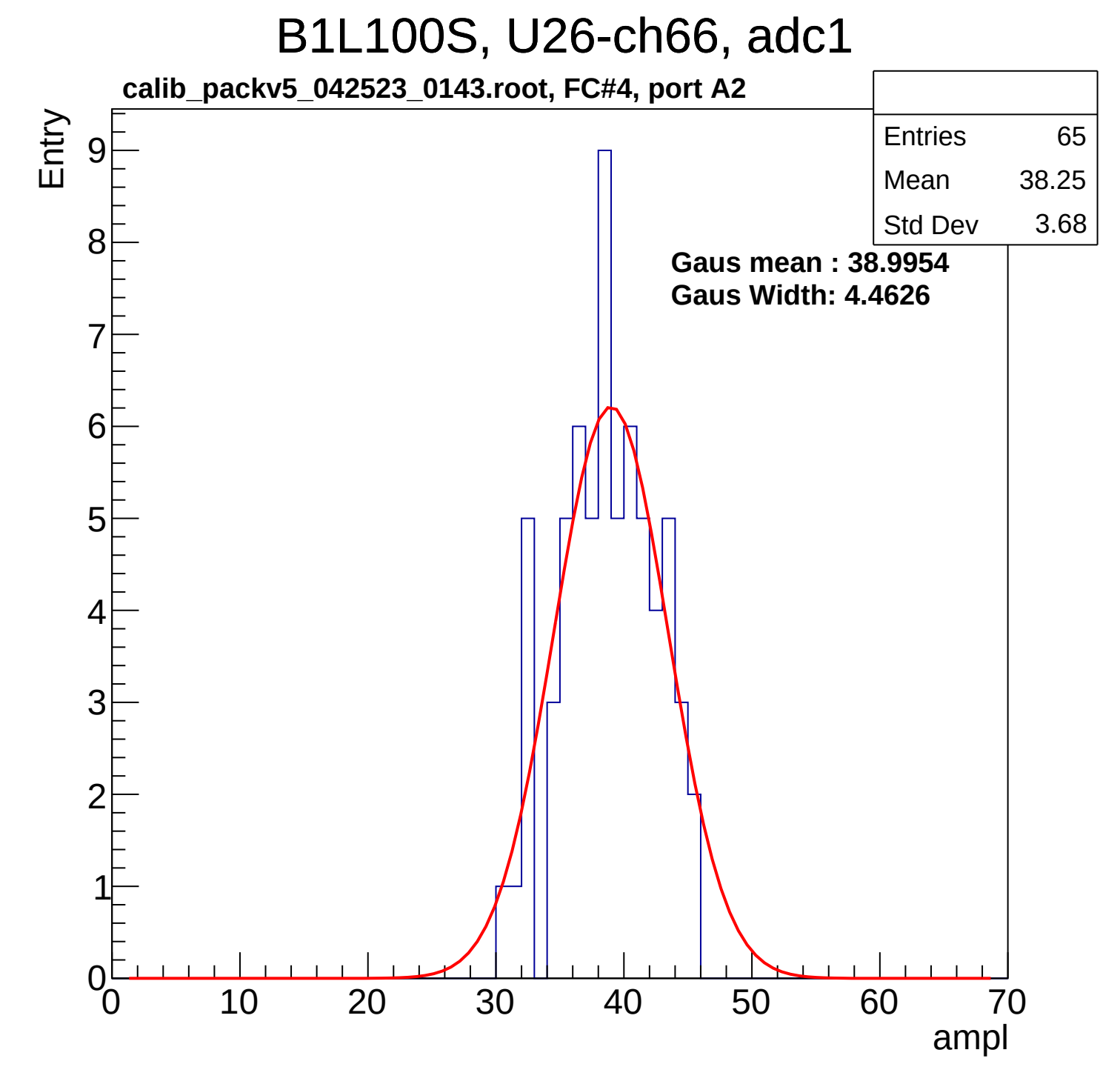
60
70


B1L100S, U26-ch65, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.55
Std Dev 1.233
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch65, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 31.76
Std Dev 4.118
Gaus mean : 31.6686
Gaus Width: 5.0824
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch66, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 38.25
Std Dev 3.68
Gaus mean : 38.9954
Gaus Width: 4.4626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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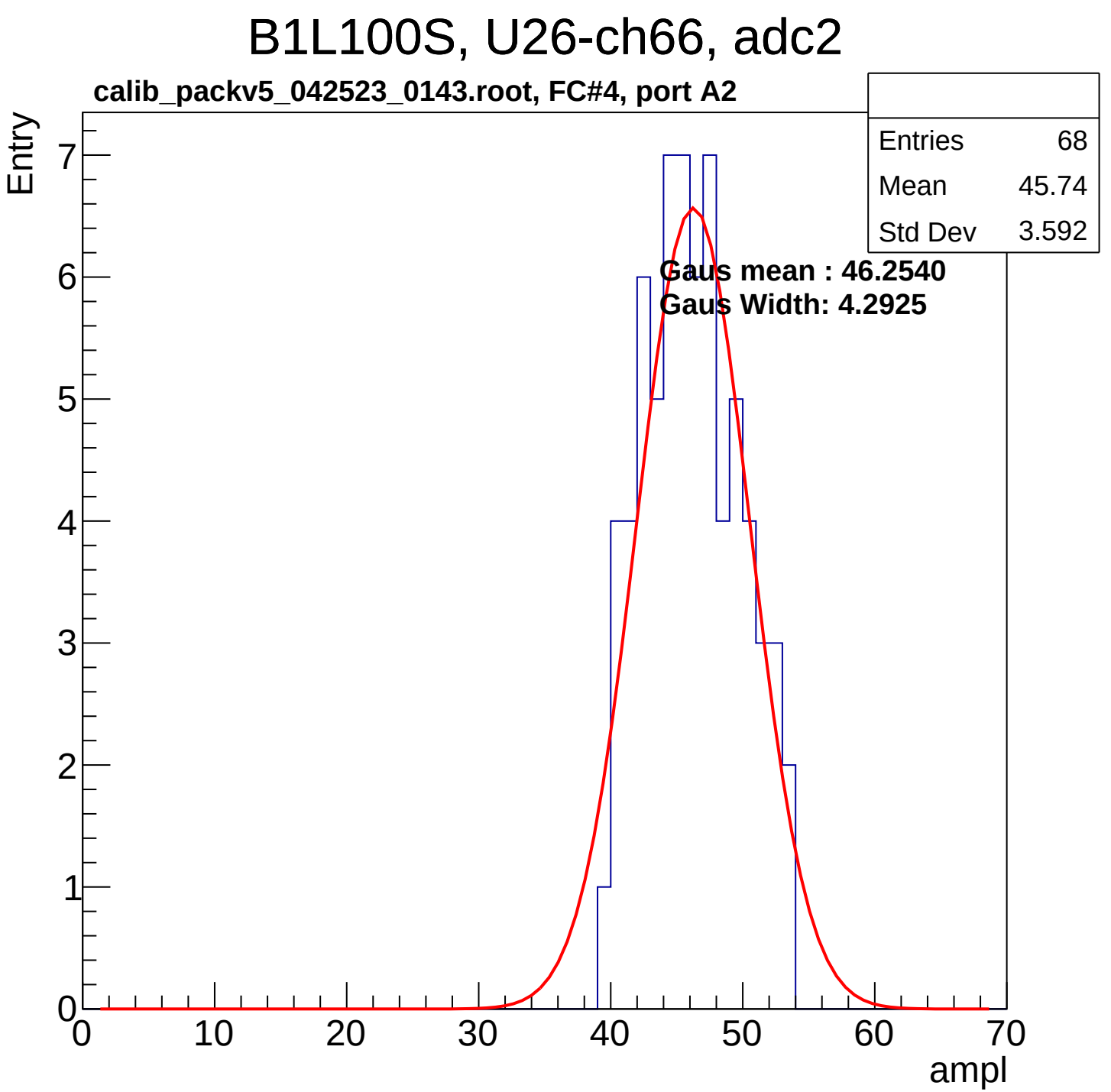
B1L100S, U26-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 45.74
Std Dev 3.592
Gaus mean : 46.2540
Gaus Width: 4.2925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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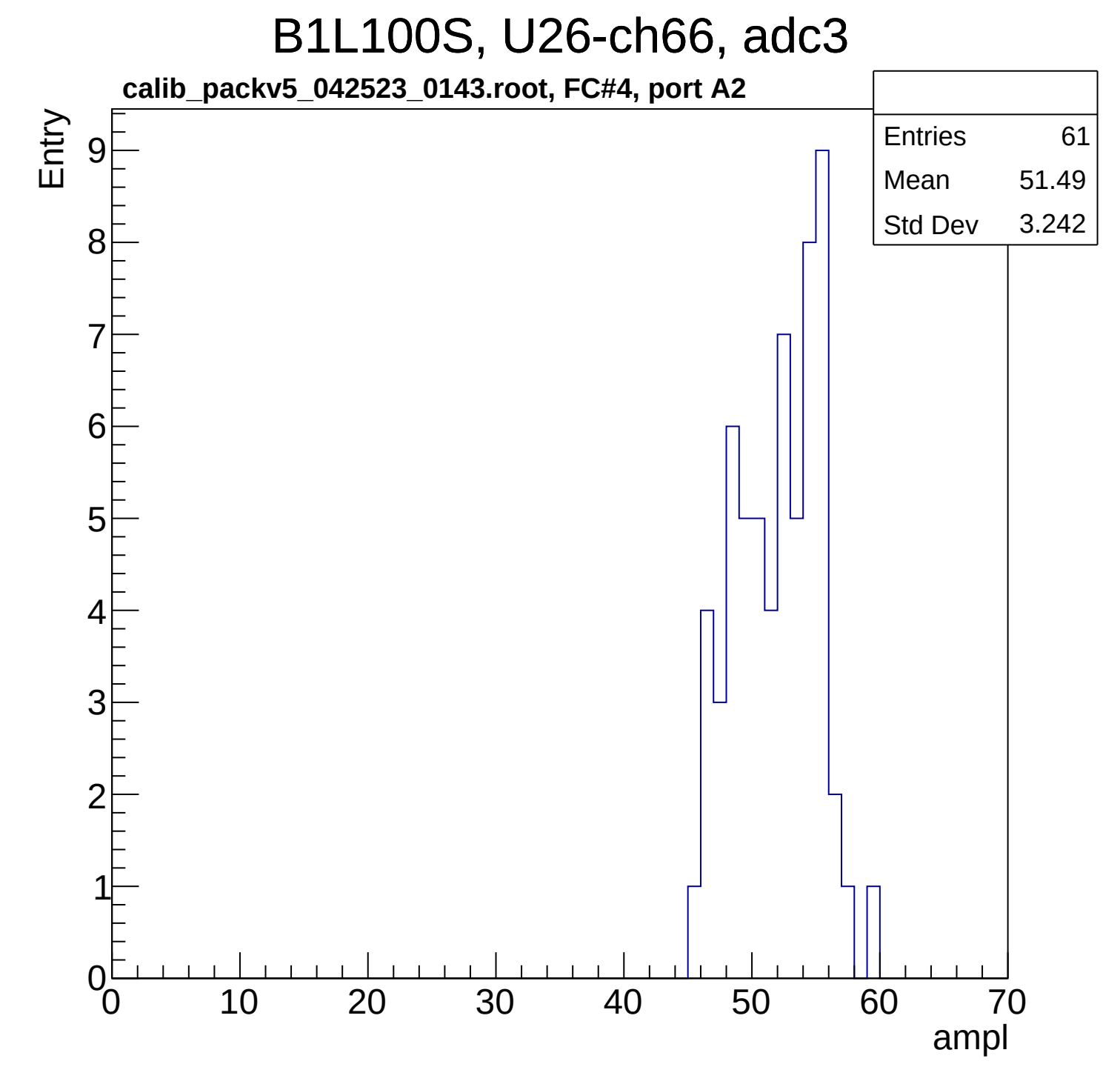
B1L100S, U26-ch66, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 51.49
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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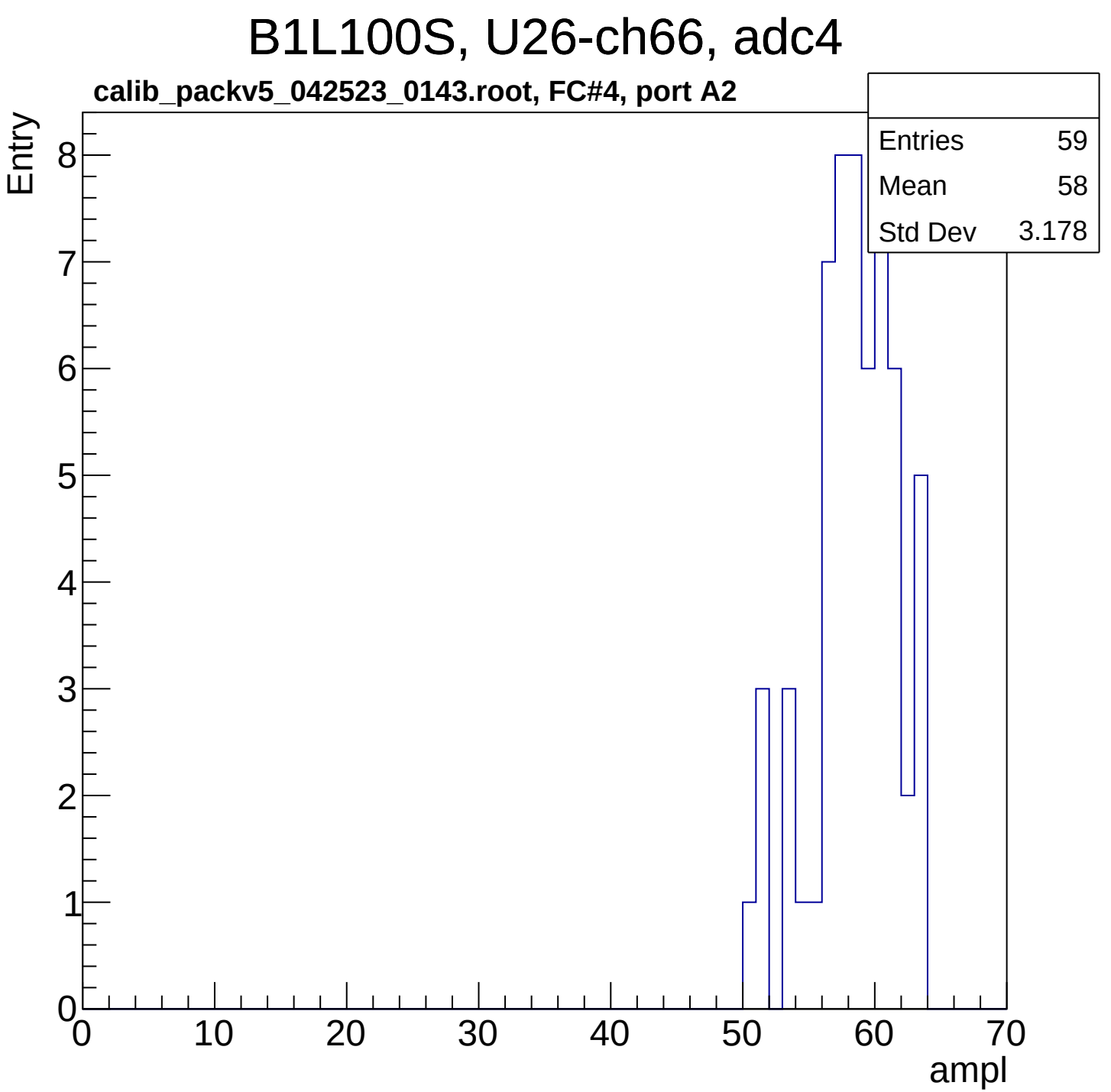
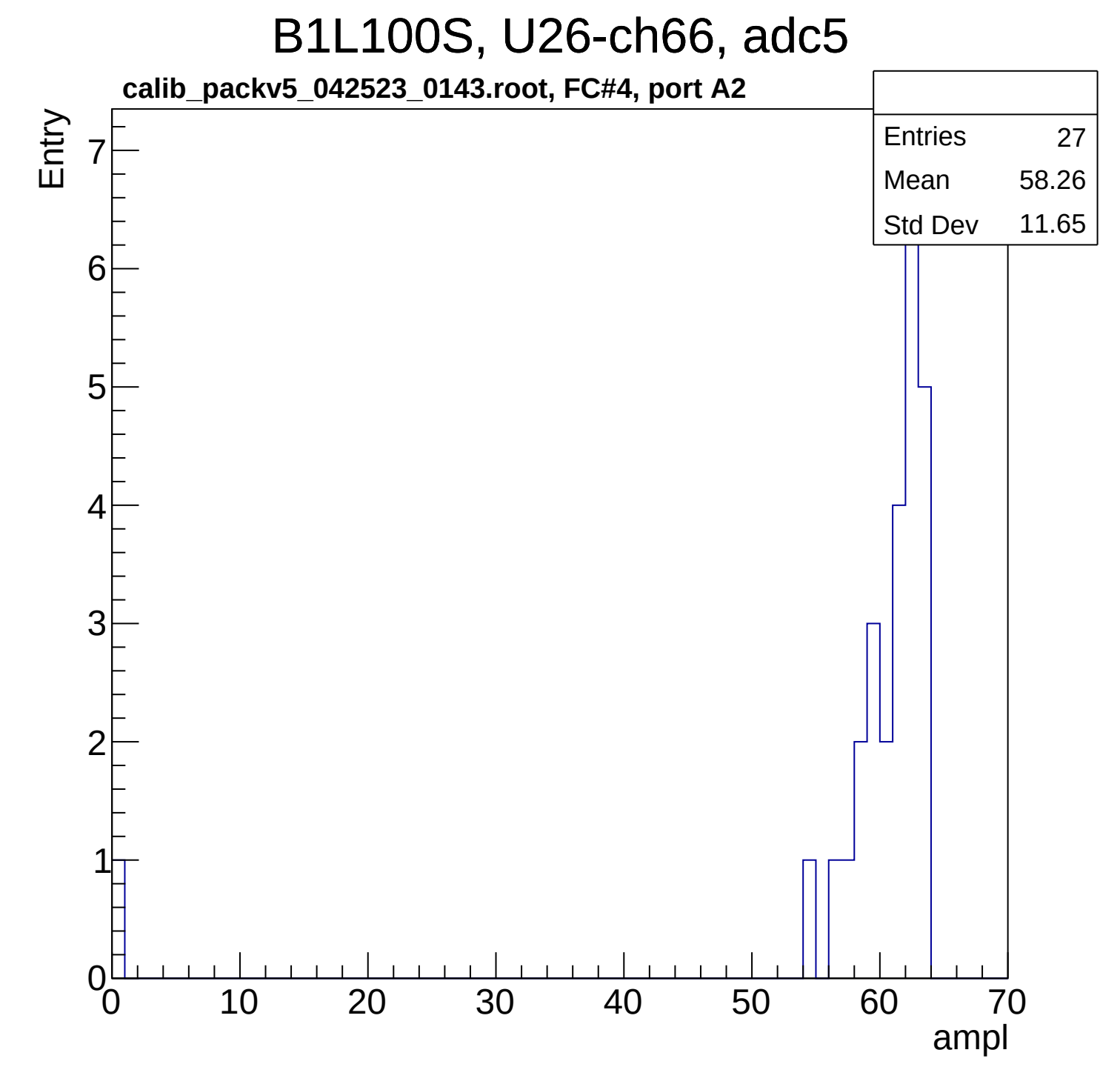
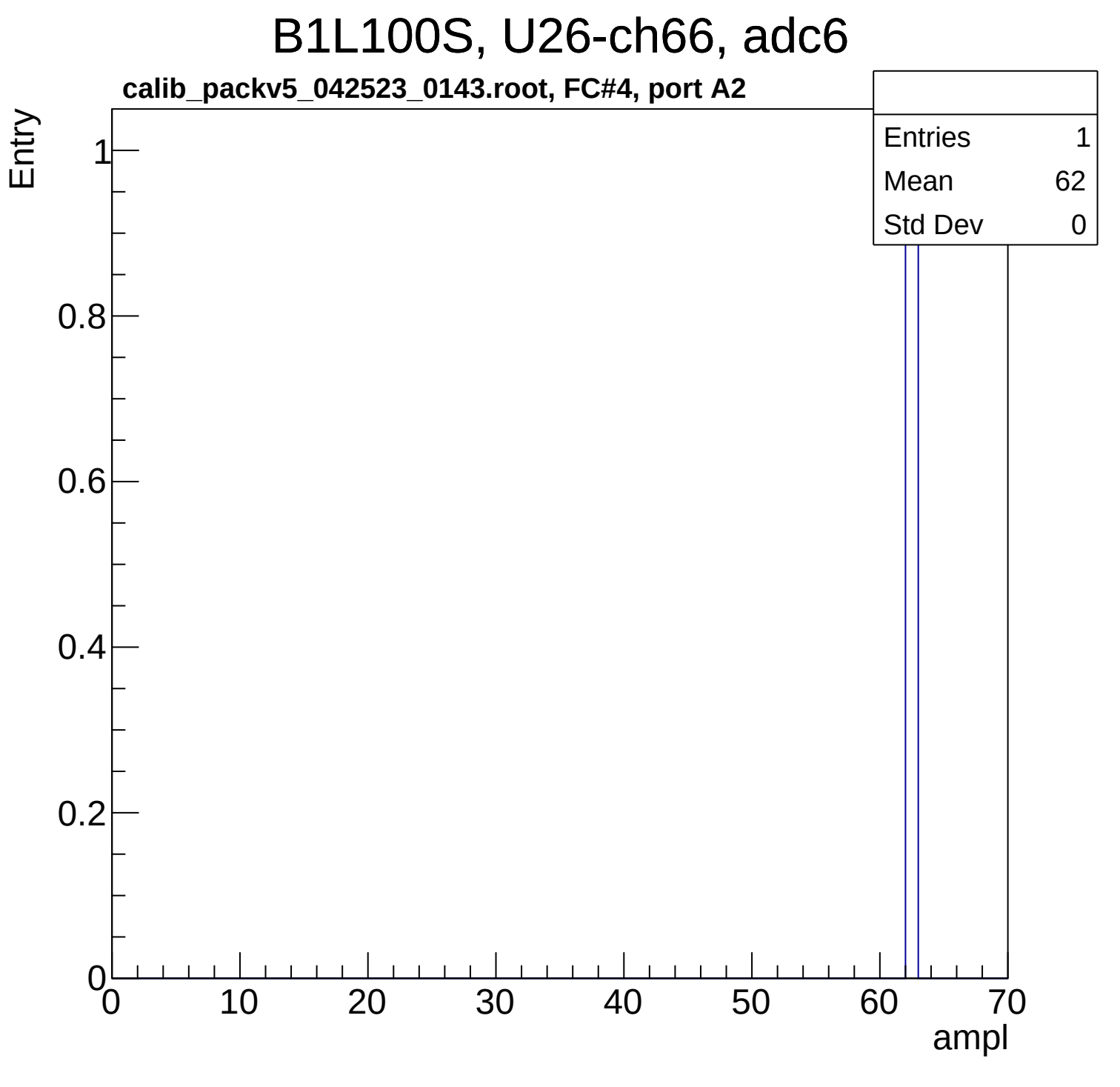

B1L100S, U26-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch66, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.26
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch66, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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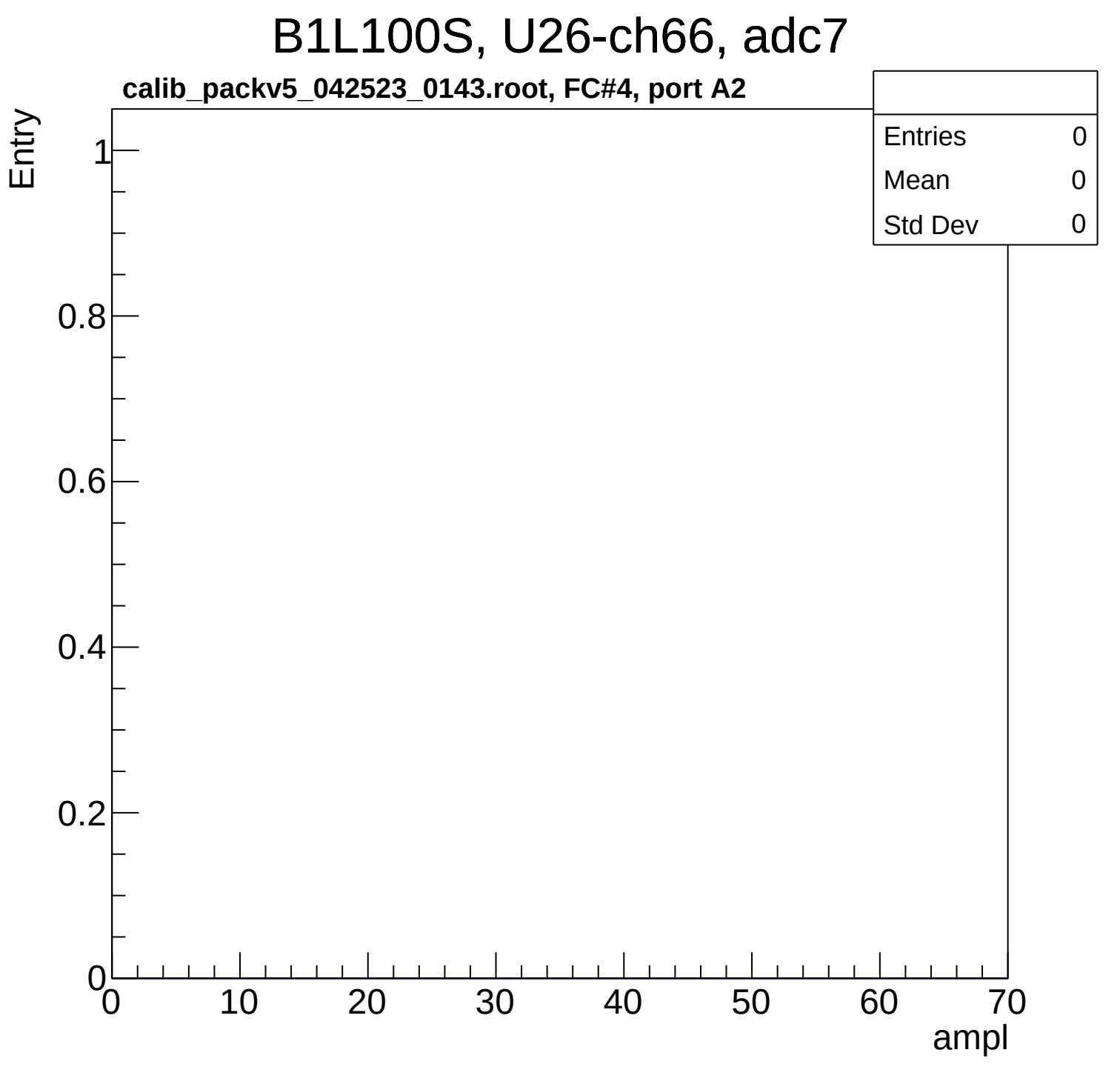
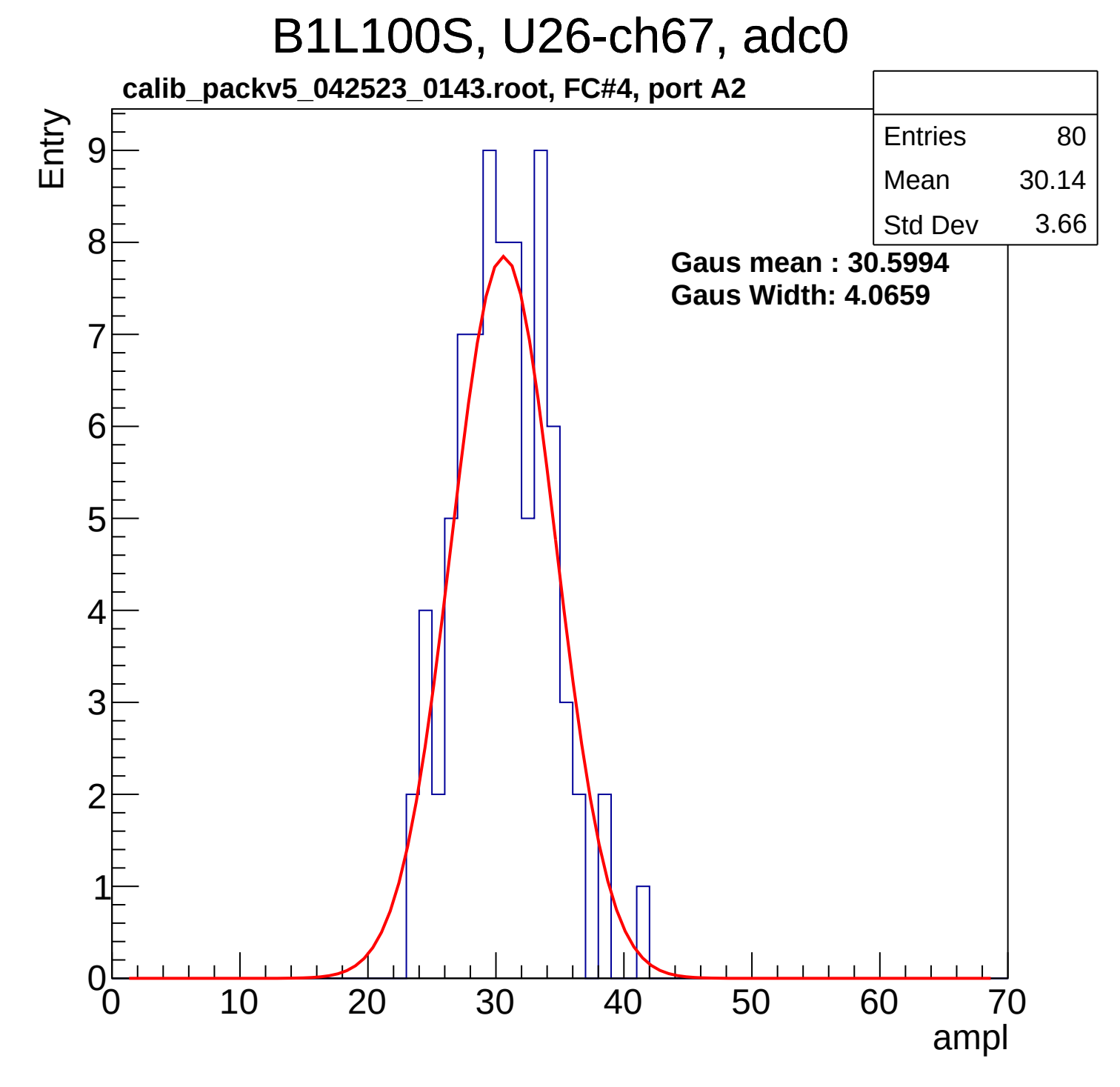
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 30.14
Std Dev 3.66
Gaus mean : 30.5994
Gaus Width: 4.0659
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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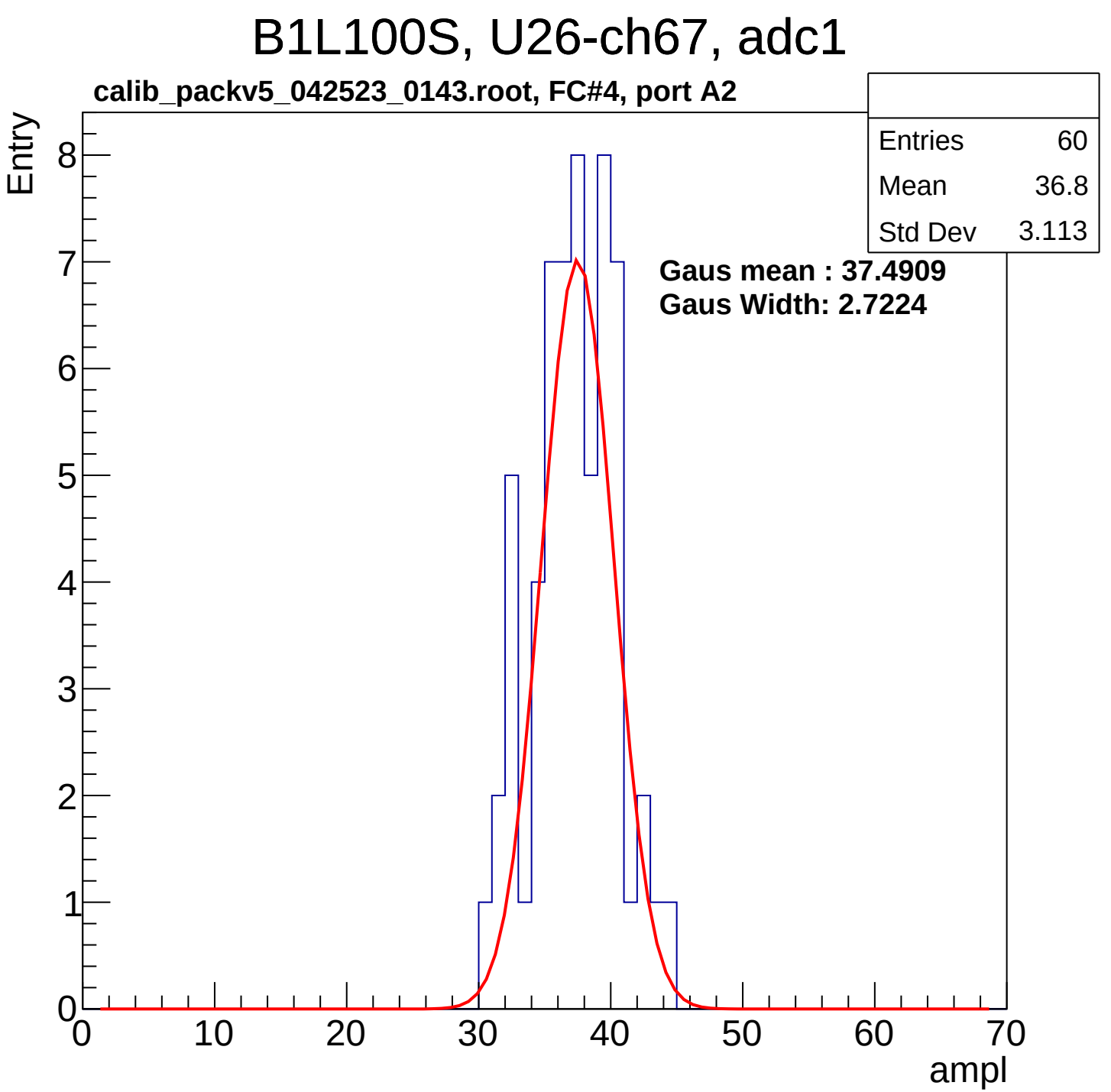
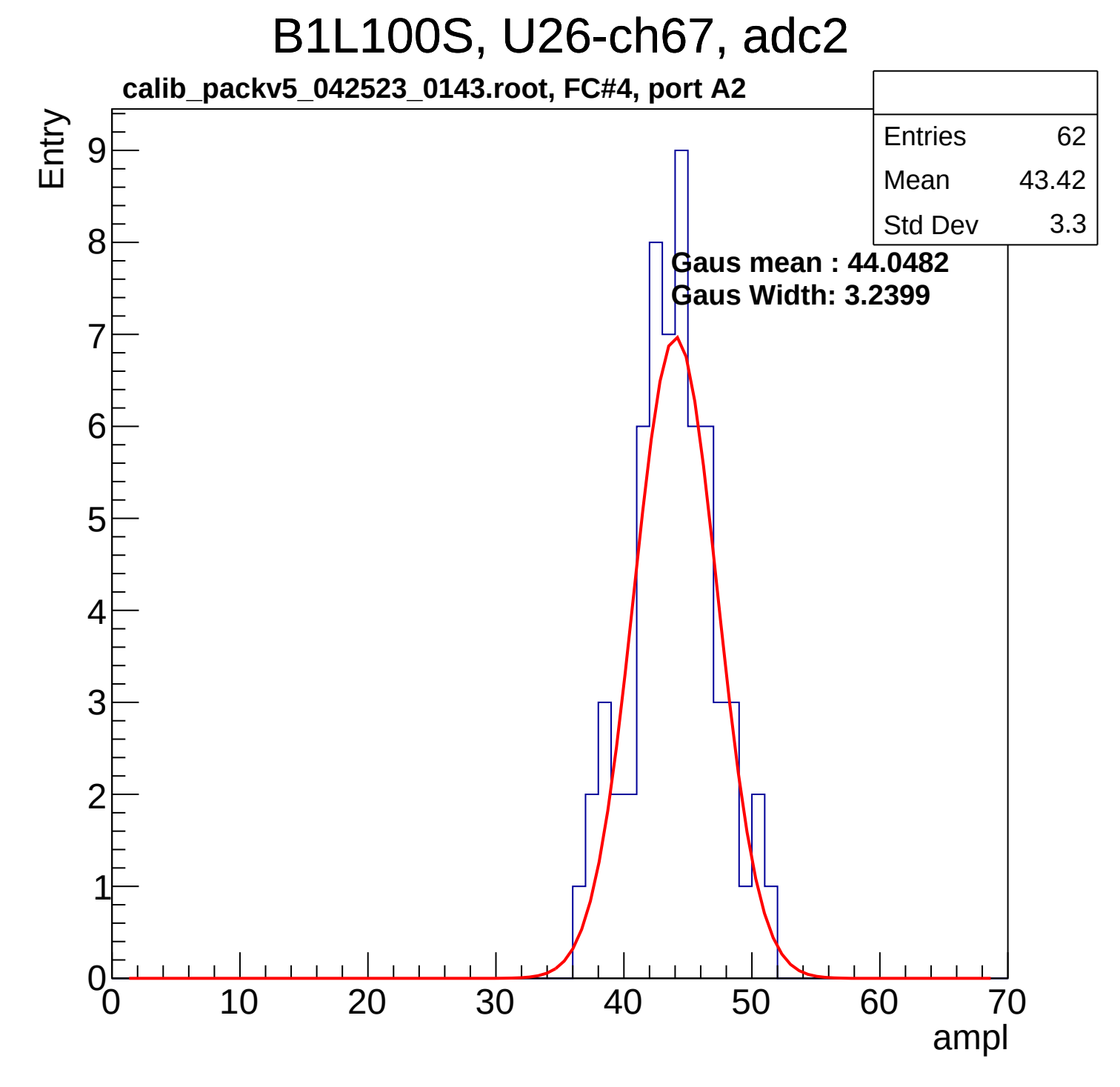
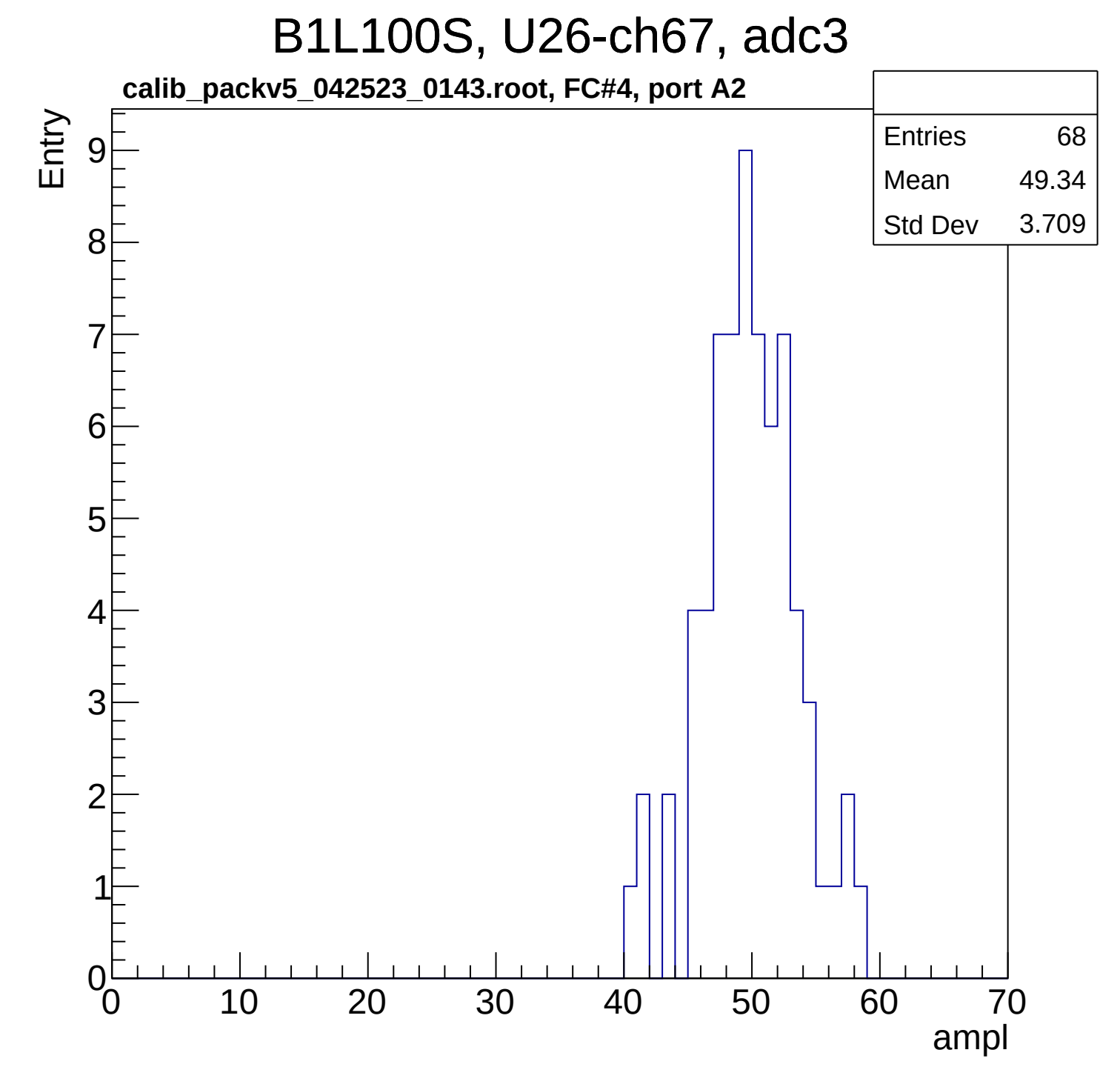

B1L100S, U26-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.8
Std Dev 3.113
Gaus mean : 37.4909
Gaus Width: 2.7224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.42
Std Dev 3.3
Gaus mean : 44.0482
Gaus Width: 3.2399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.34
Std Dev 3.709
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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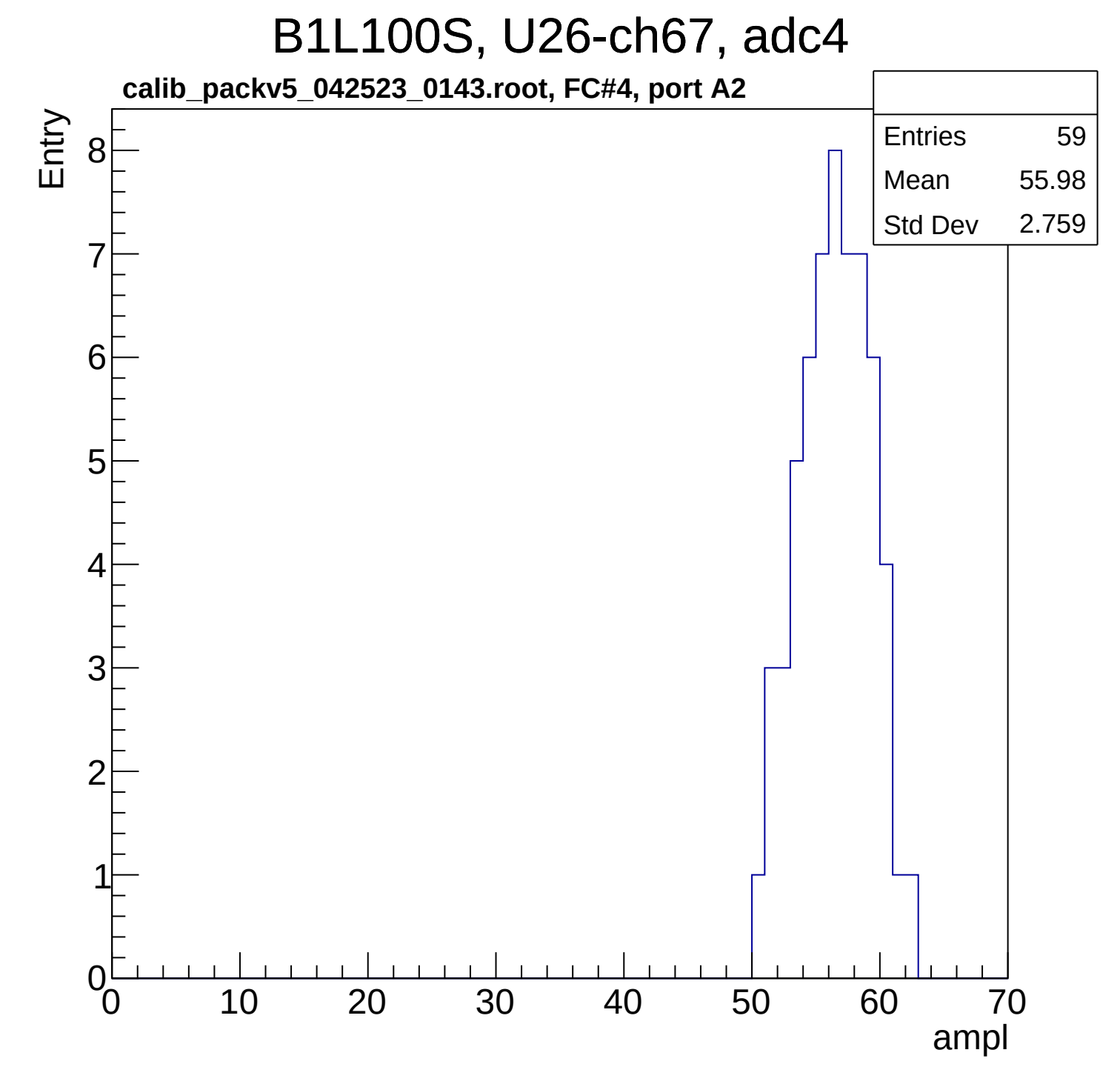
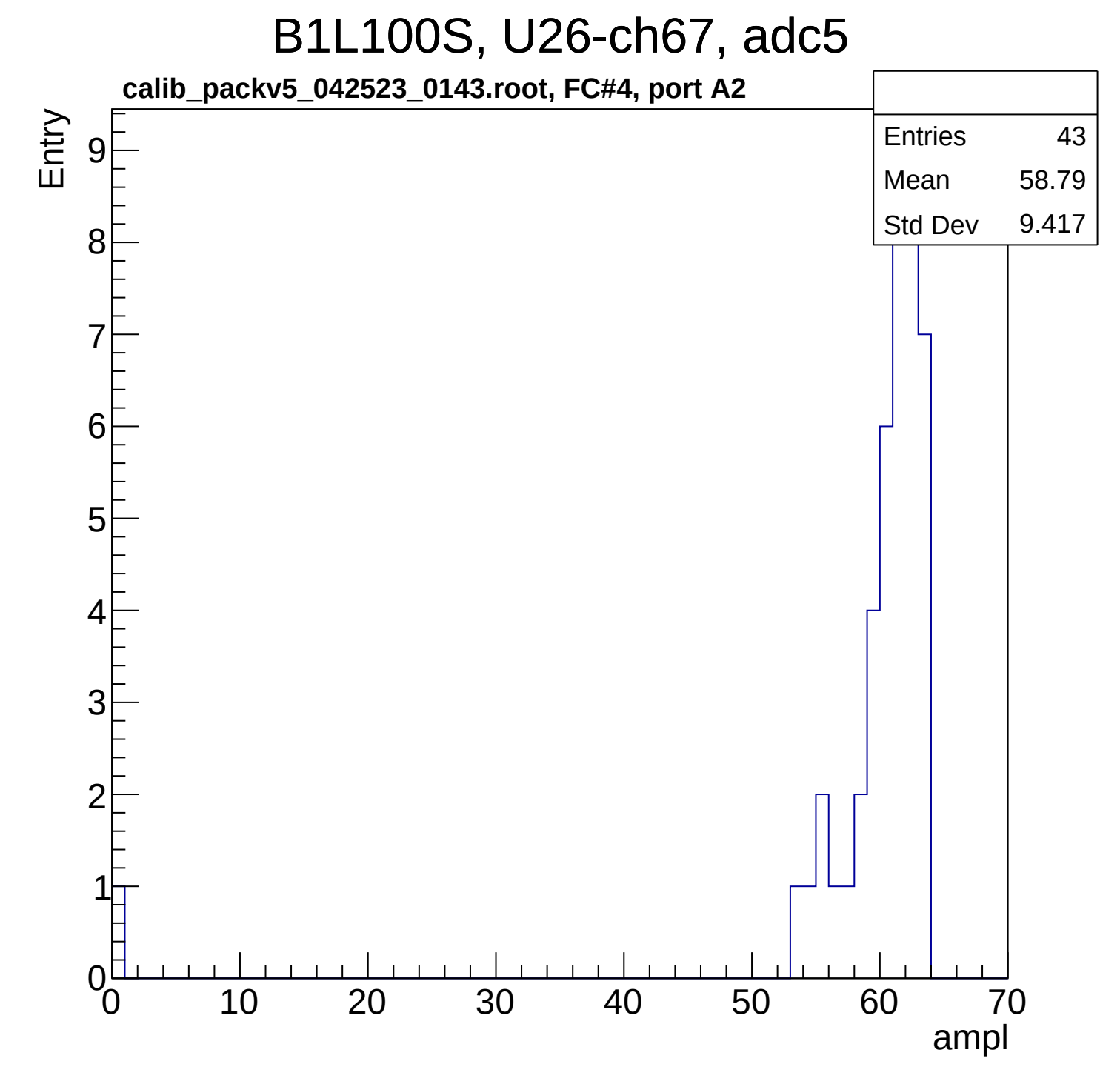
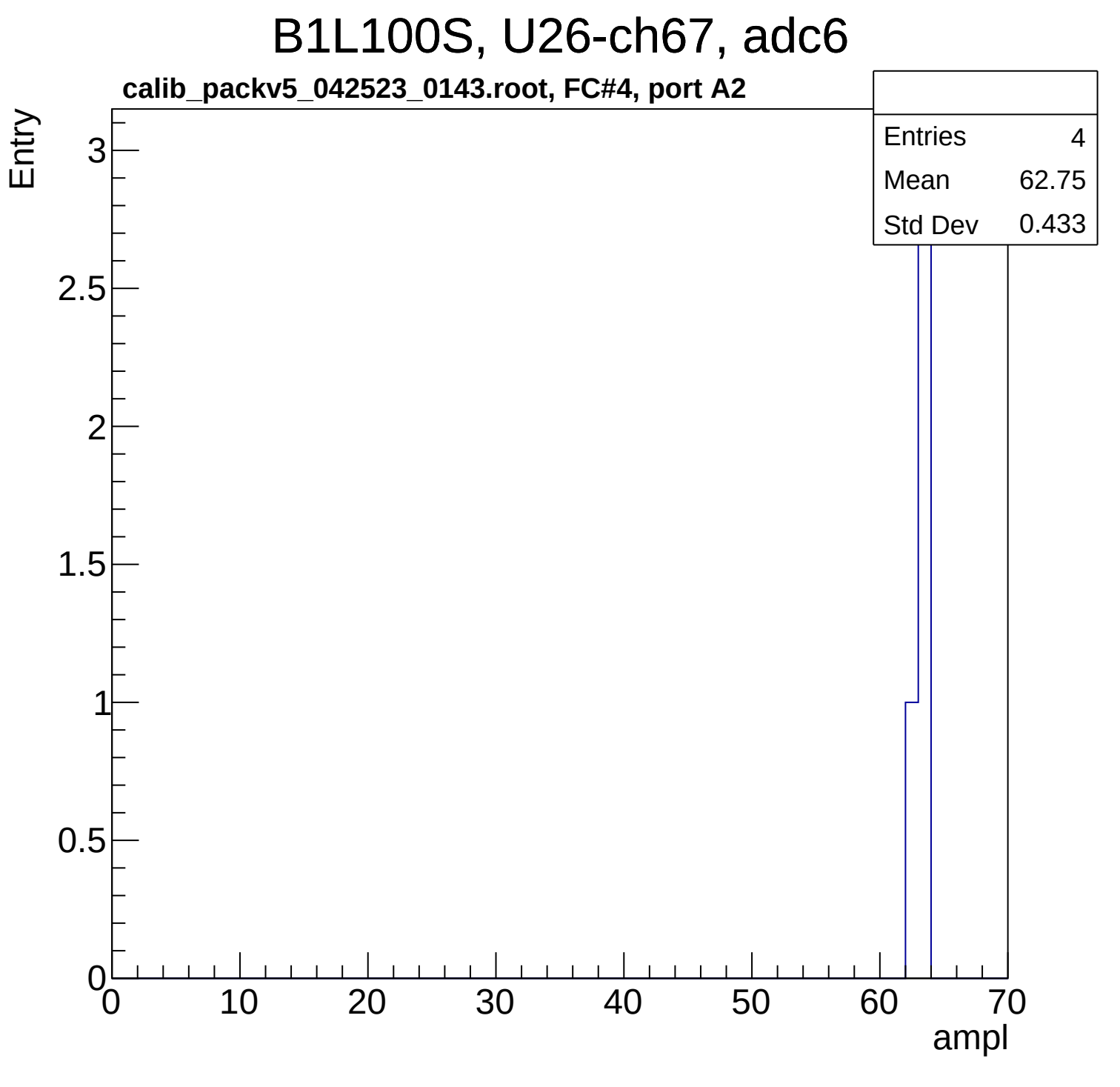
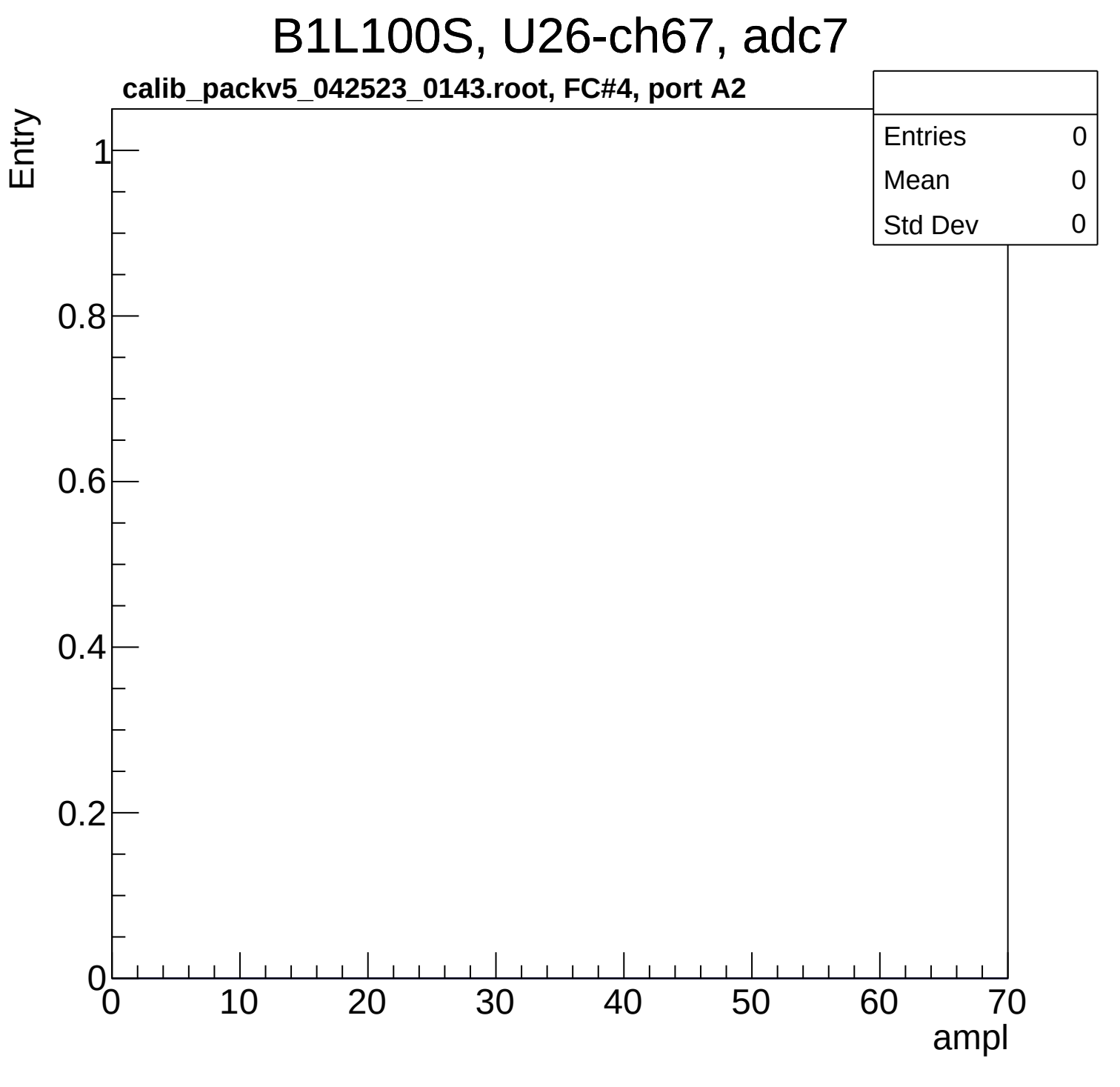
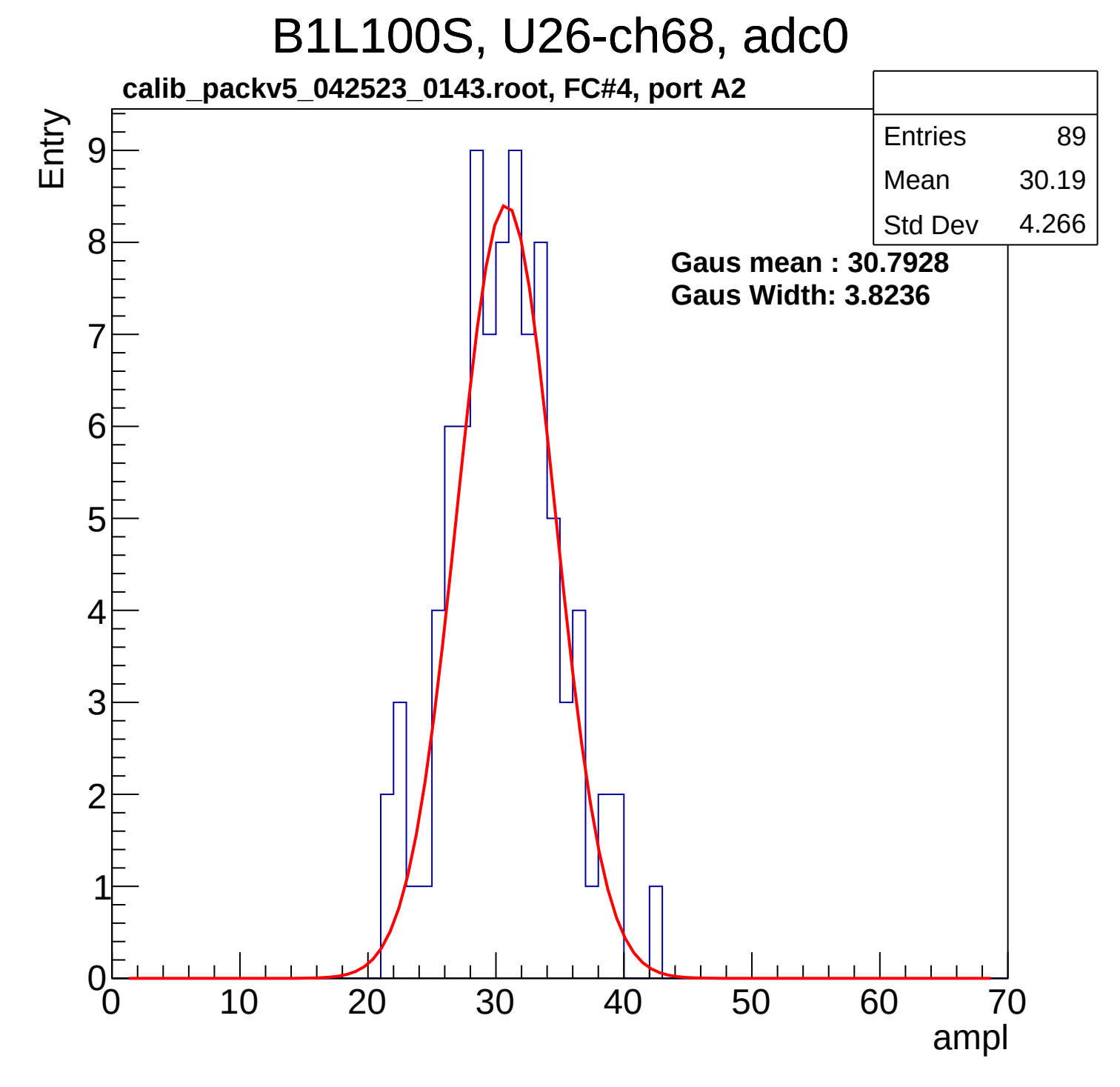
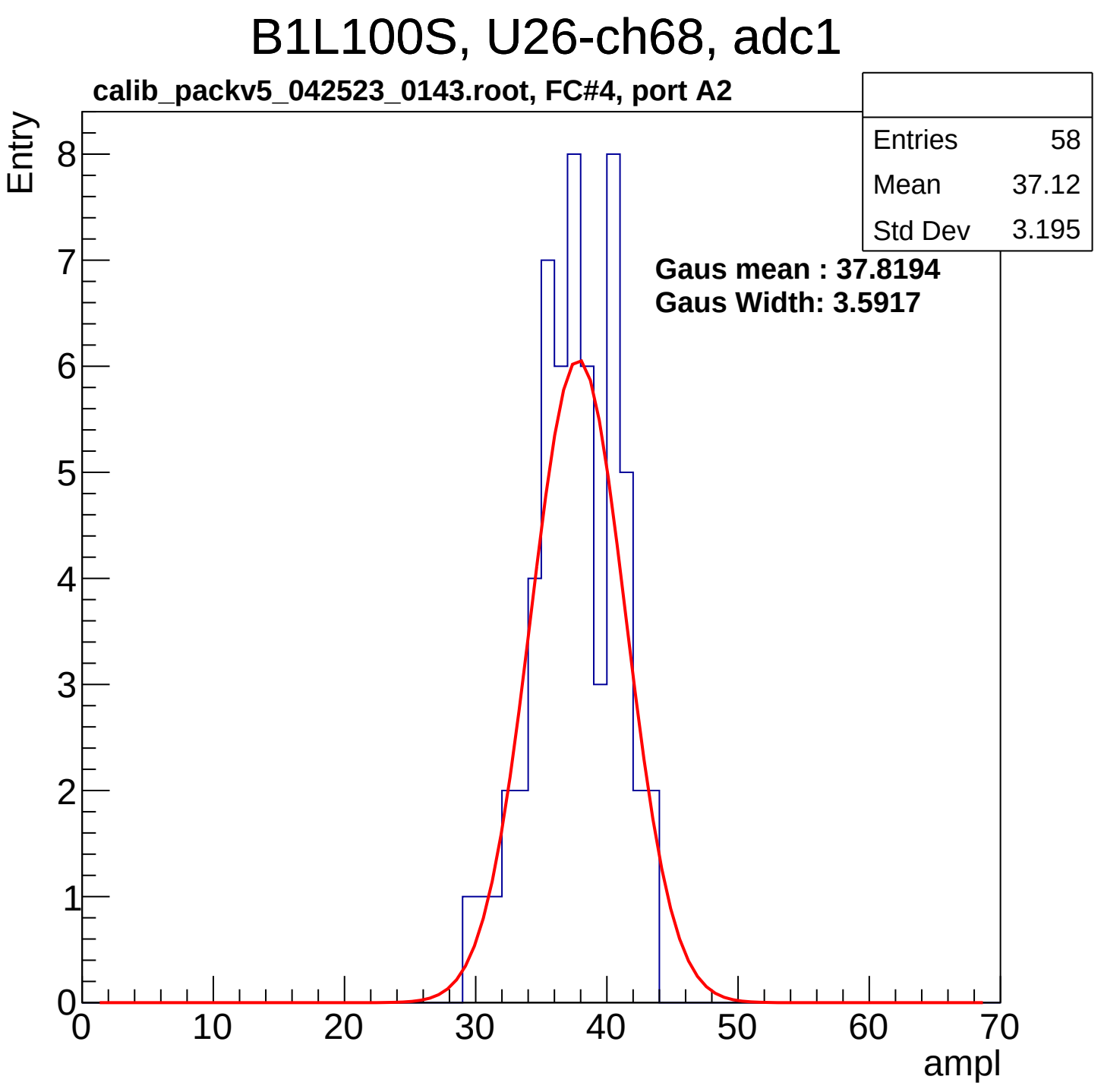

B1L100S, U26-ch67, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.98
Std Dev 2.759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.79
Std Dev 9.417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch67, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch67, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 30.19
Std Dev 4.266
Gaus mean : 30.7928
Gaus Width: 3.8236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 37.12
Std Dev 3.195
Gaus mean : 37.8194
Gaus Width: 3.5917
Entry
ampl
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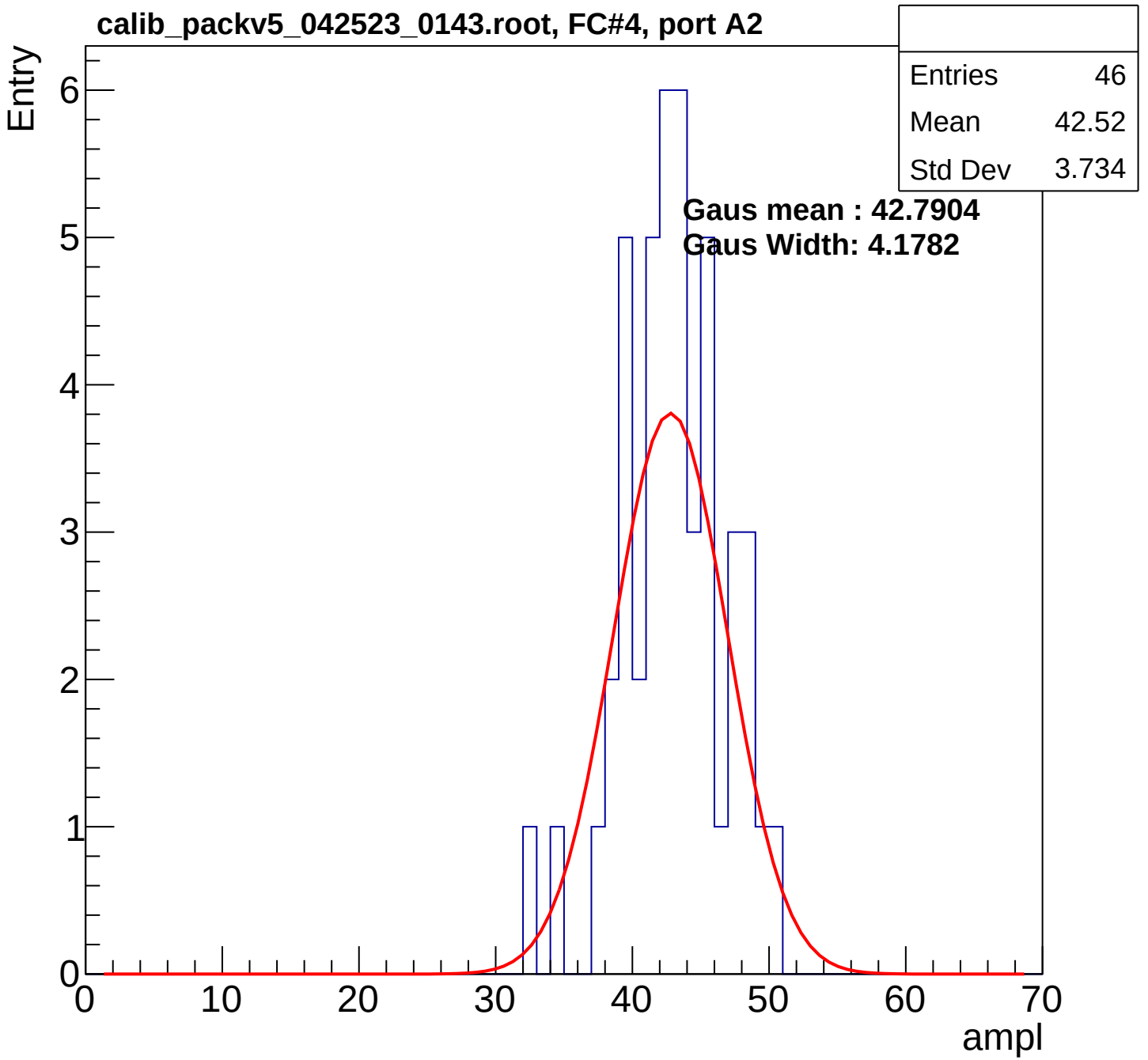
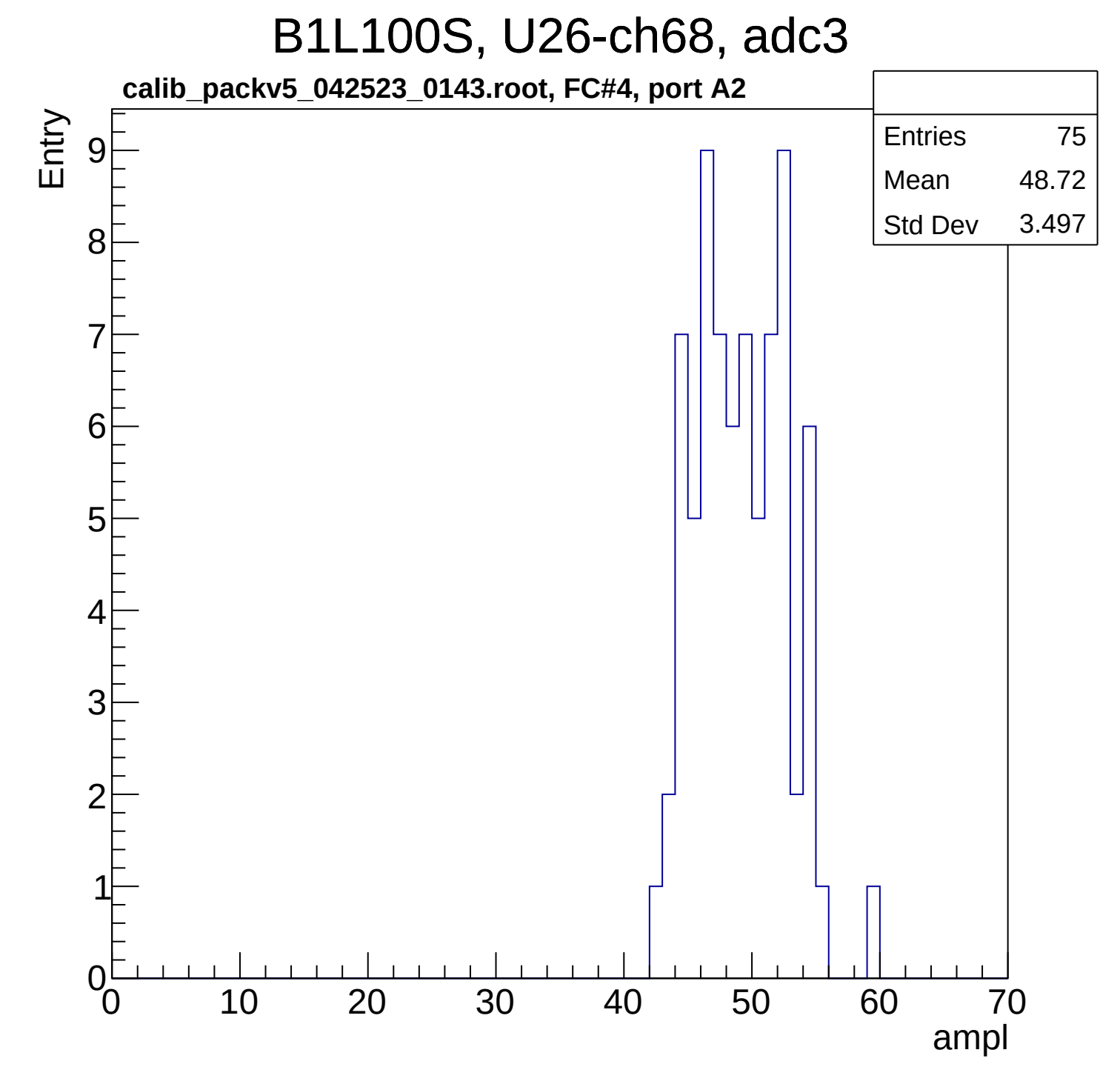
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 42.52
Std Dev 3.734
Gaus mean : 42.7904
Gaus Width: 4.1782
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U26-ch68, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 48.72
Std Dev 3.497
Entry
ampl
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3
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0
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60
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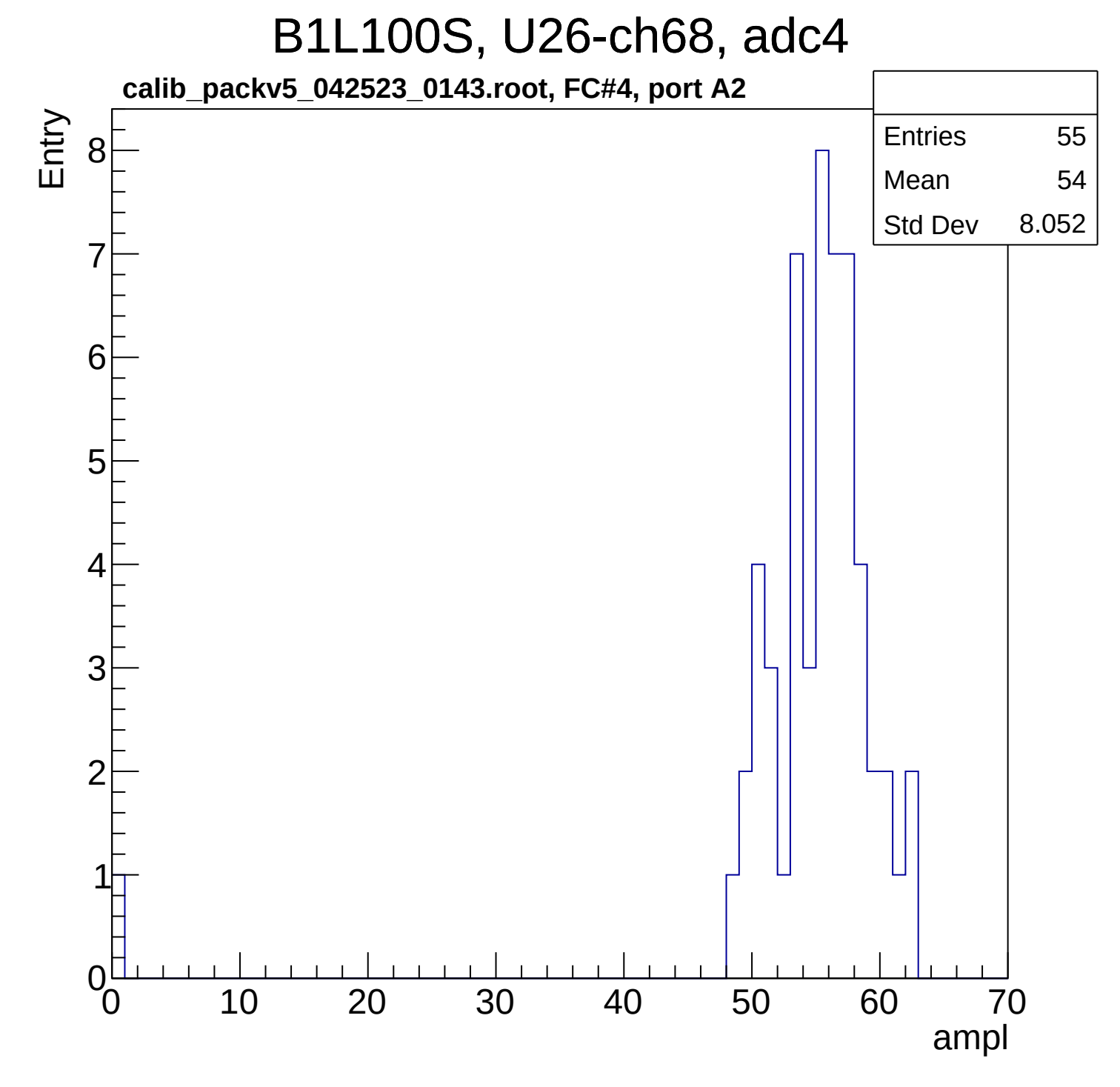
B1L100S, U26-ch68, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 54
Std Dev 8.052
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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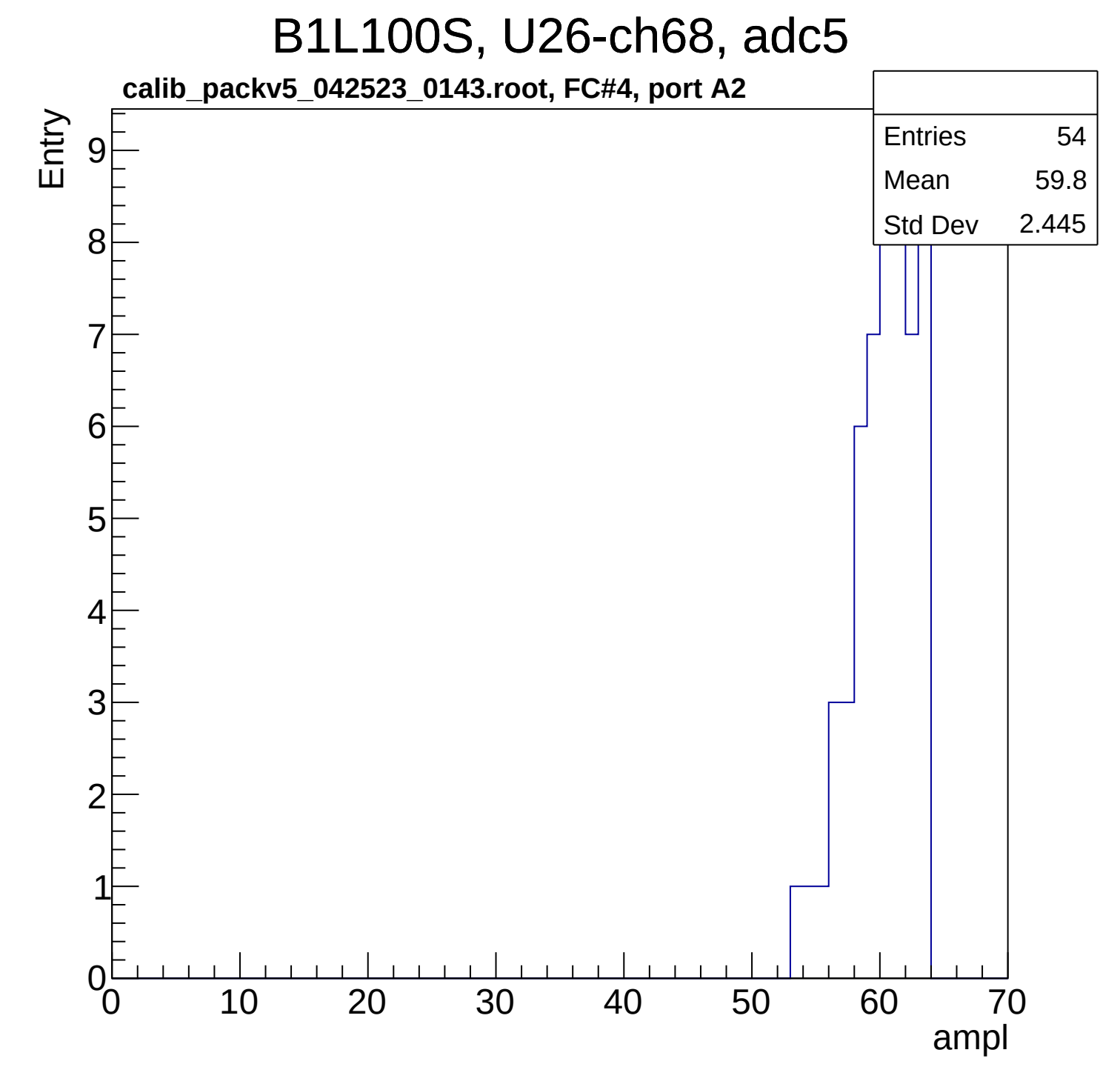
40
50
60
70


B1L100S, U26-ch68, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 59.8
Std Dev 2.445
Entry
ampl
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2
3
4
5
6
7
8
9
0
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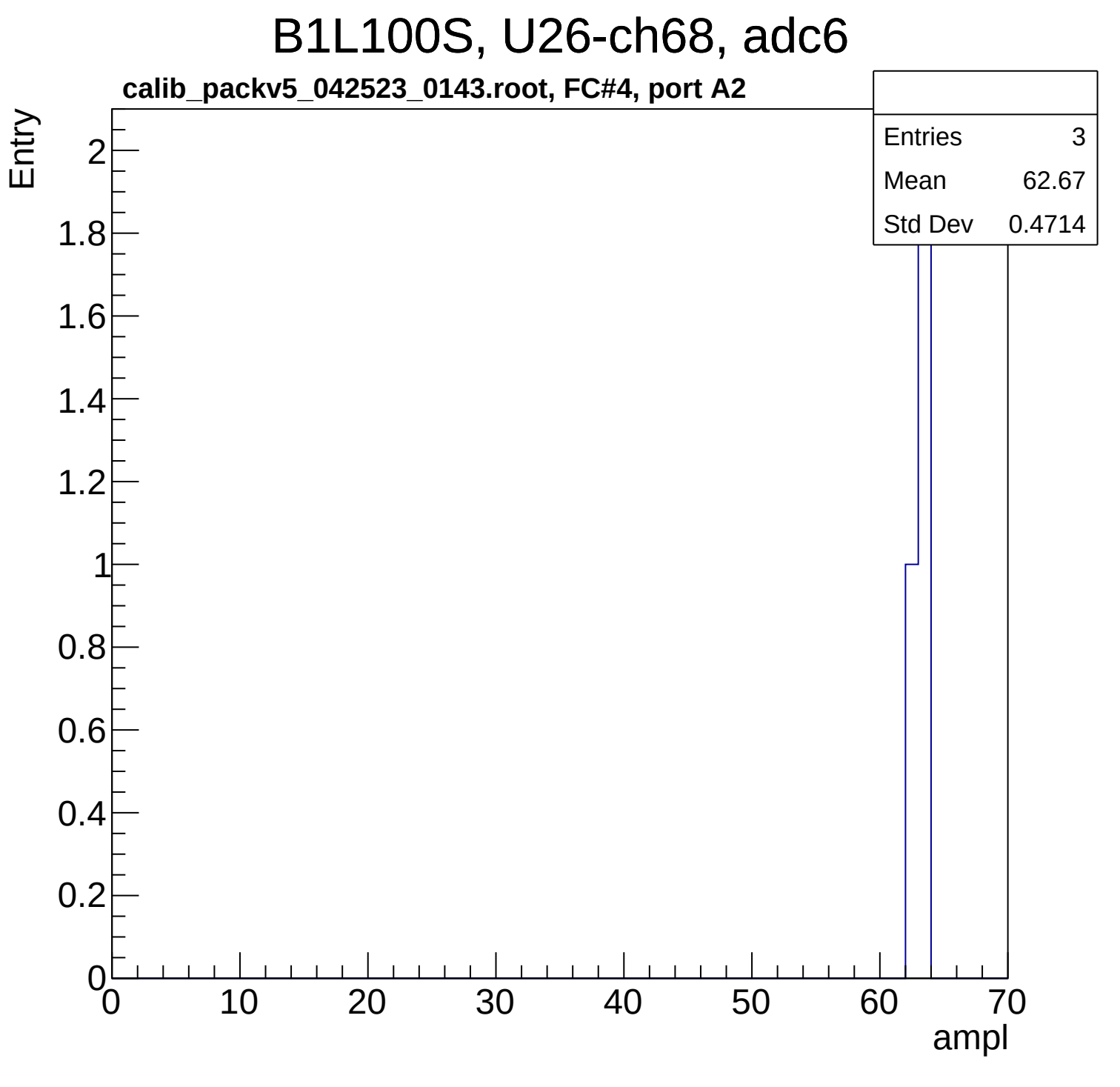
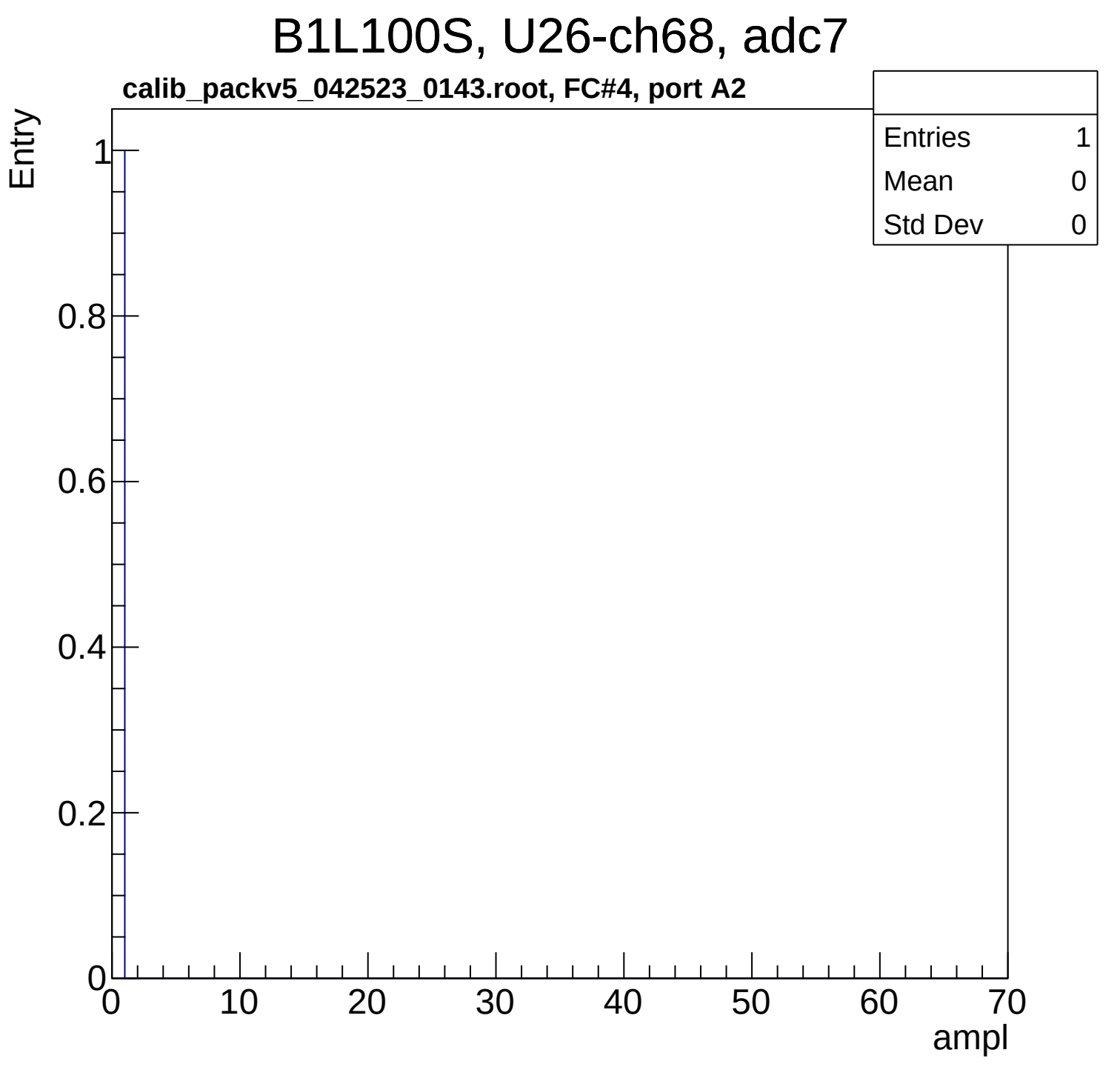
60
70


B1L100S, U26-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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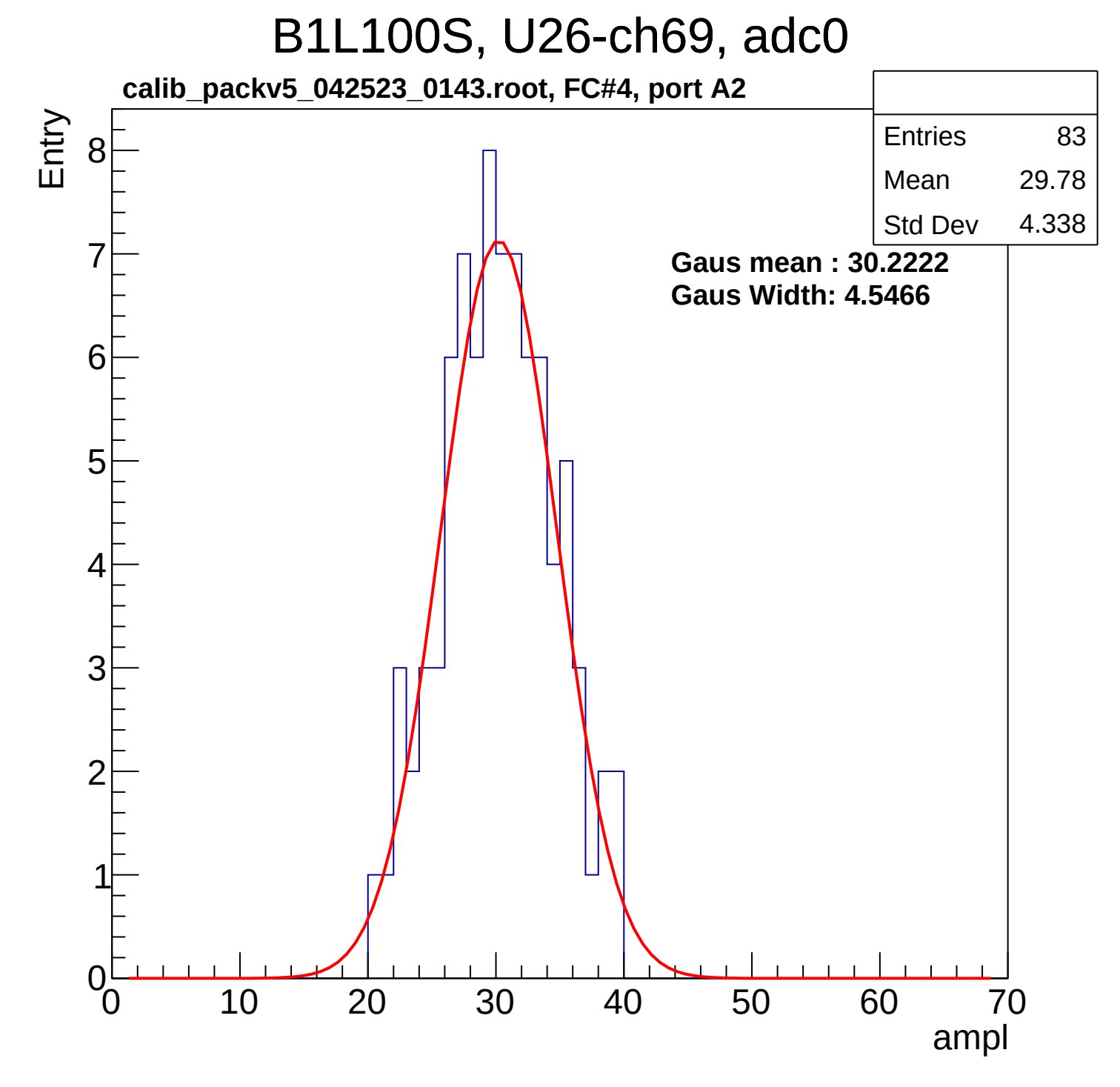
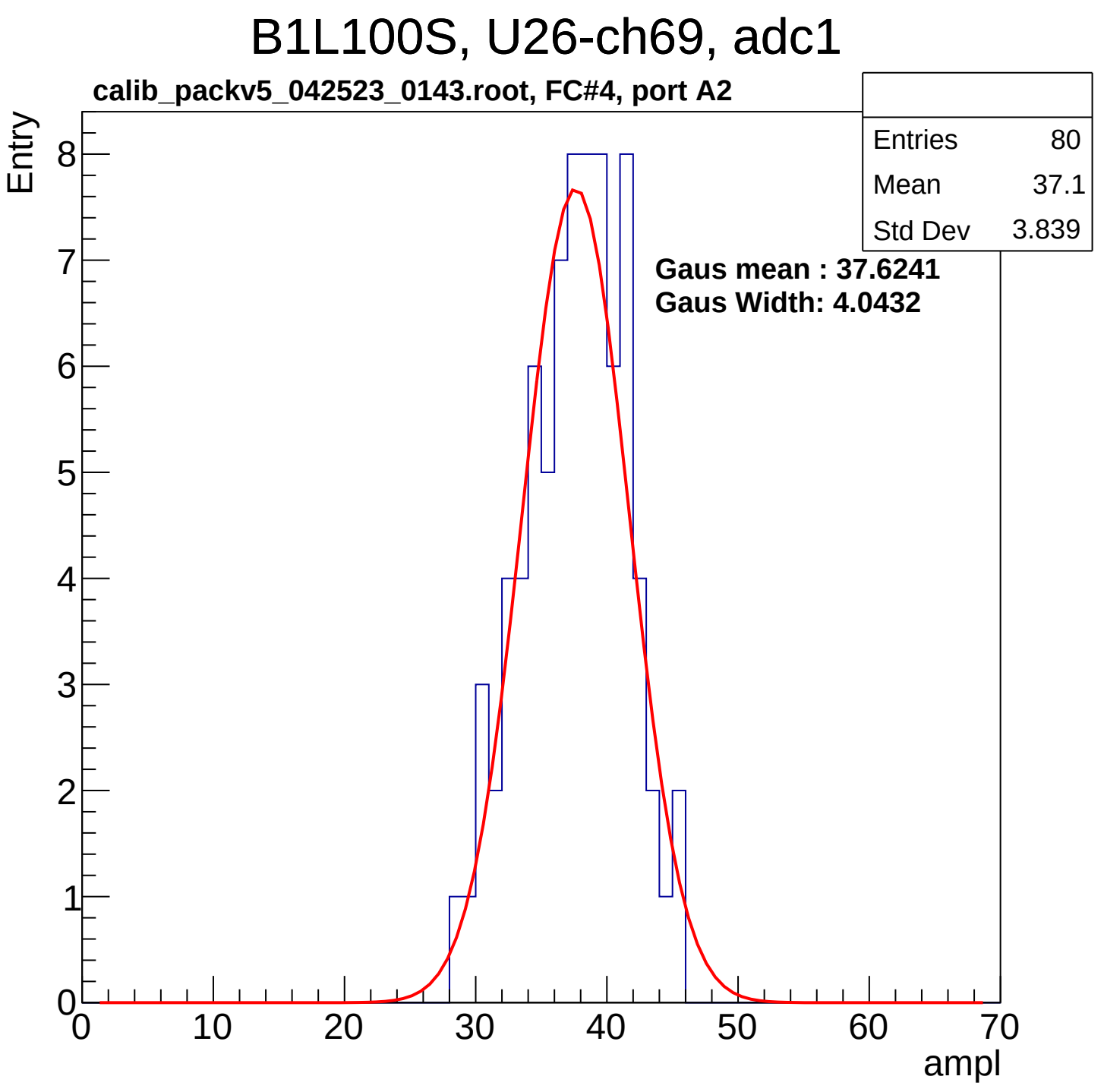
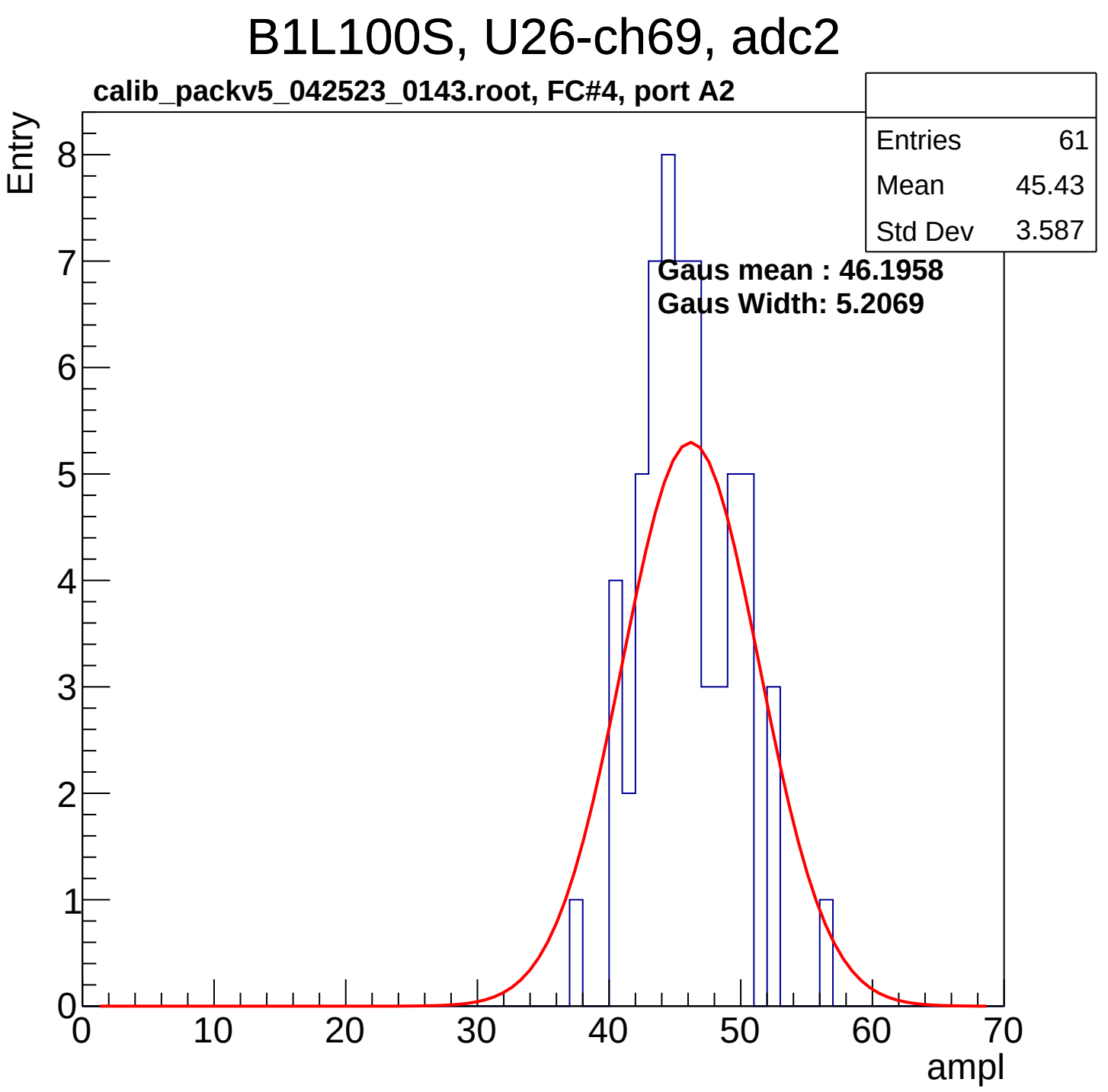
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 29.78
Std Dev 4.338
Gaus mean : 30.2222
Gaus Width: 4.5466
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.1
Std Dev 3.839
Gaus mean : 37.6241
Gaus Width: 4.0432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 45.43
Std Dev 3.587
Gaus mean : 46.1958
Gaus Width: 5.2069
Entry
ampl
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1
2
3
4
5
6
7
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0
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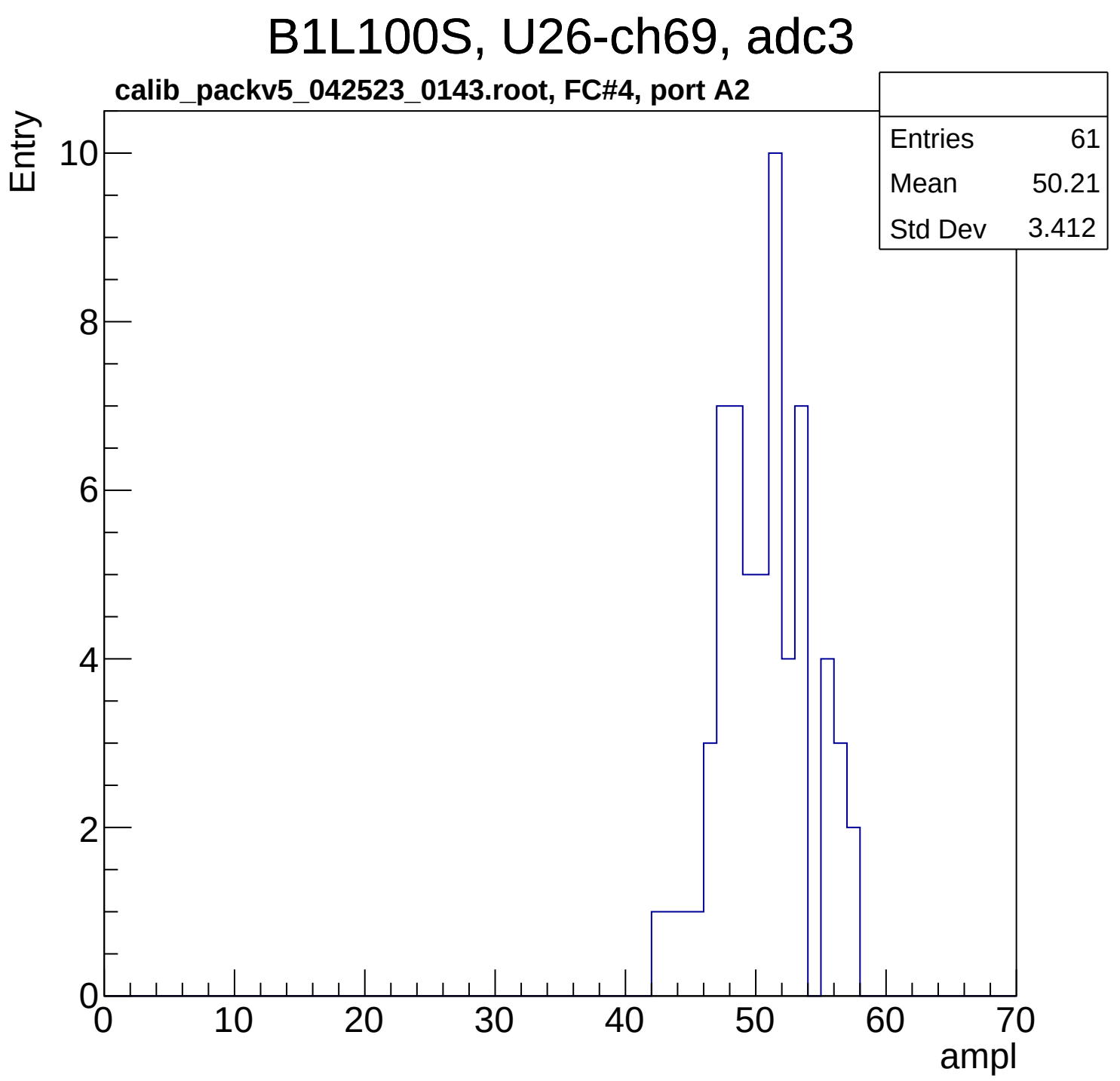
B1L100S, U26-ch69, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.21
Std Dev 3.412
Entry
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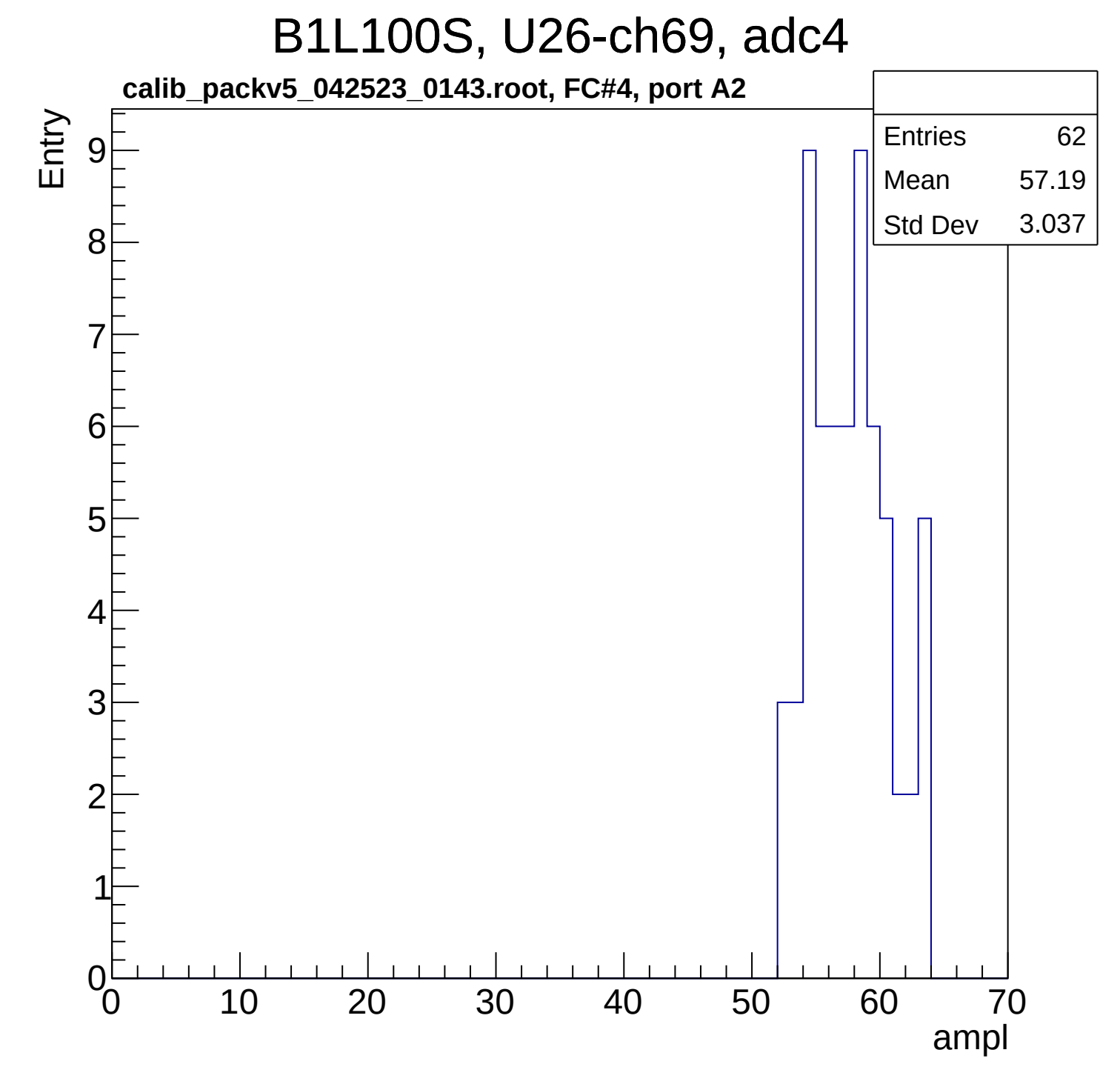
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch69, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.19
Std Dev 3.037
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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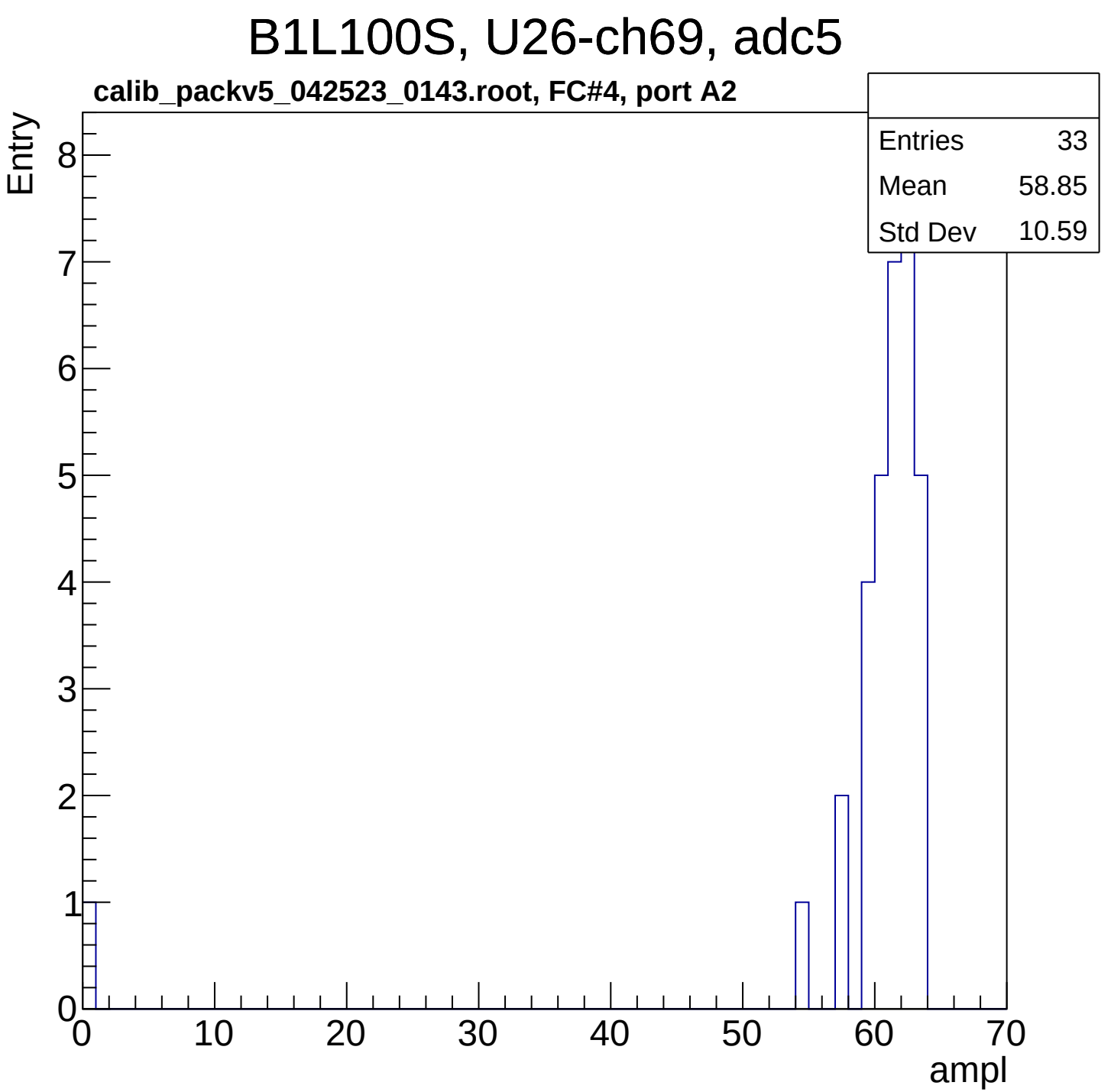
B1L100S, U26-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.85
Std Dev 10.59
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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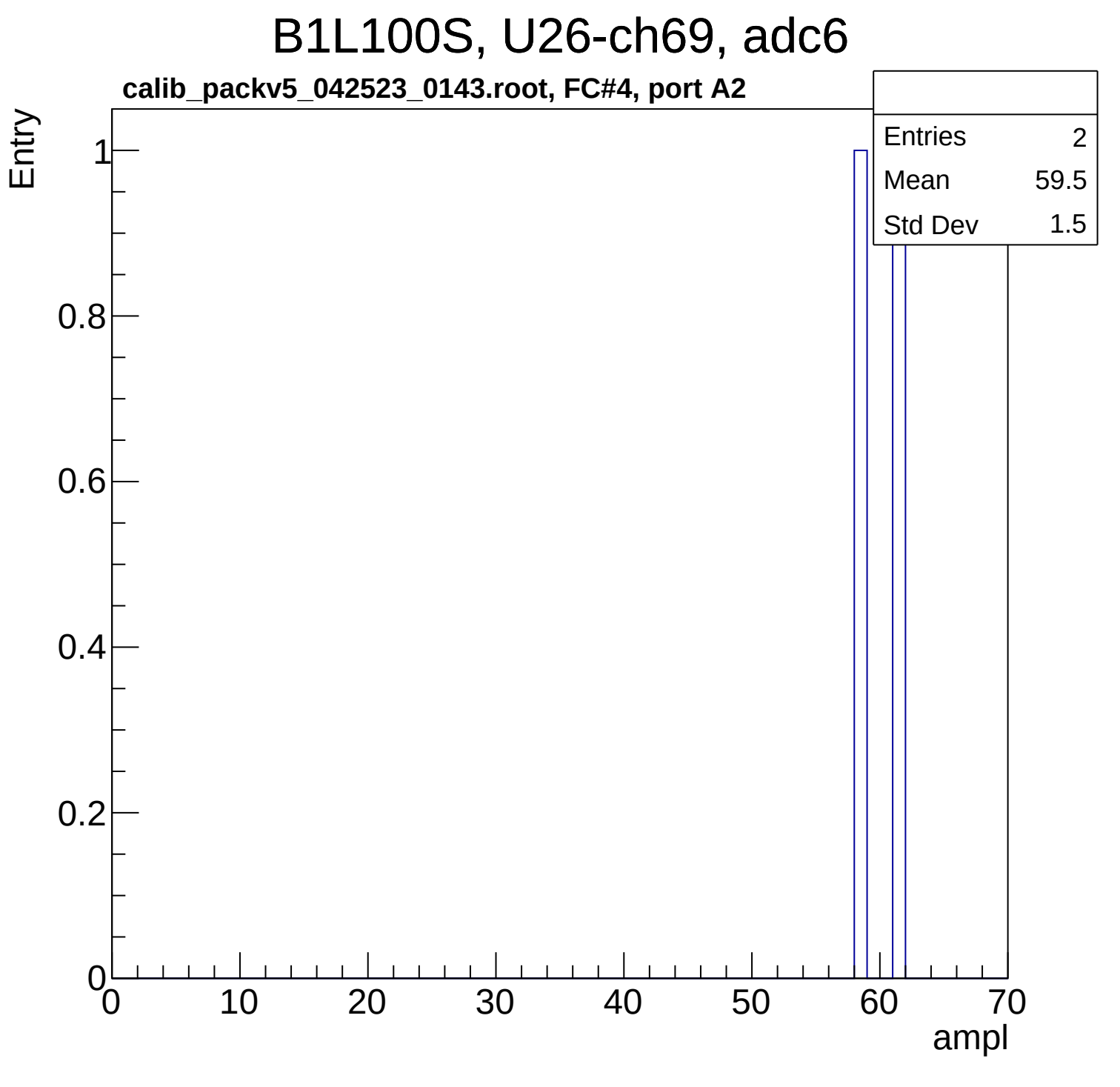
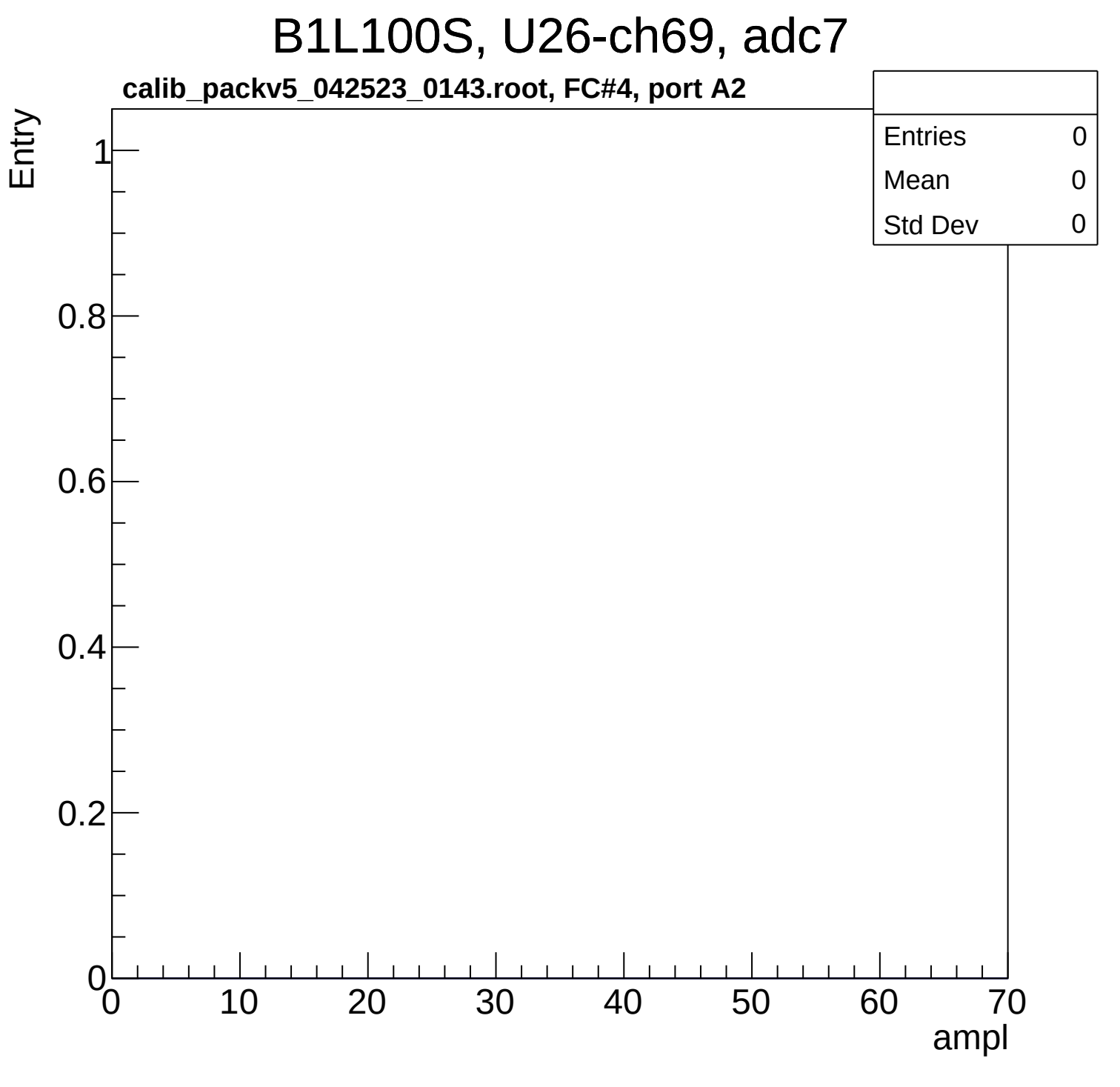
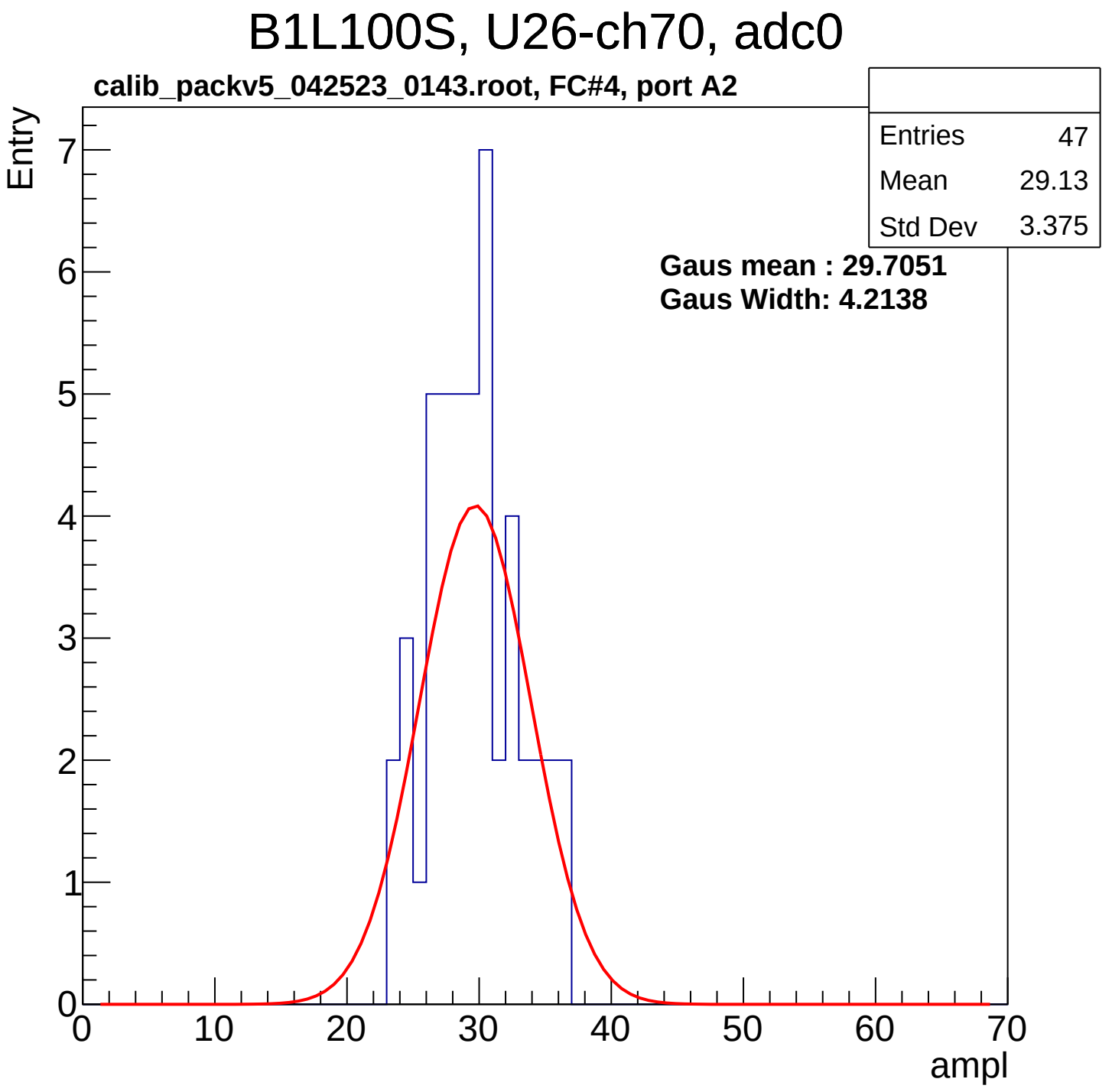
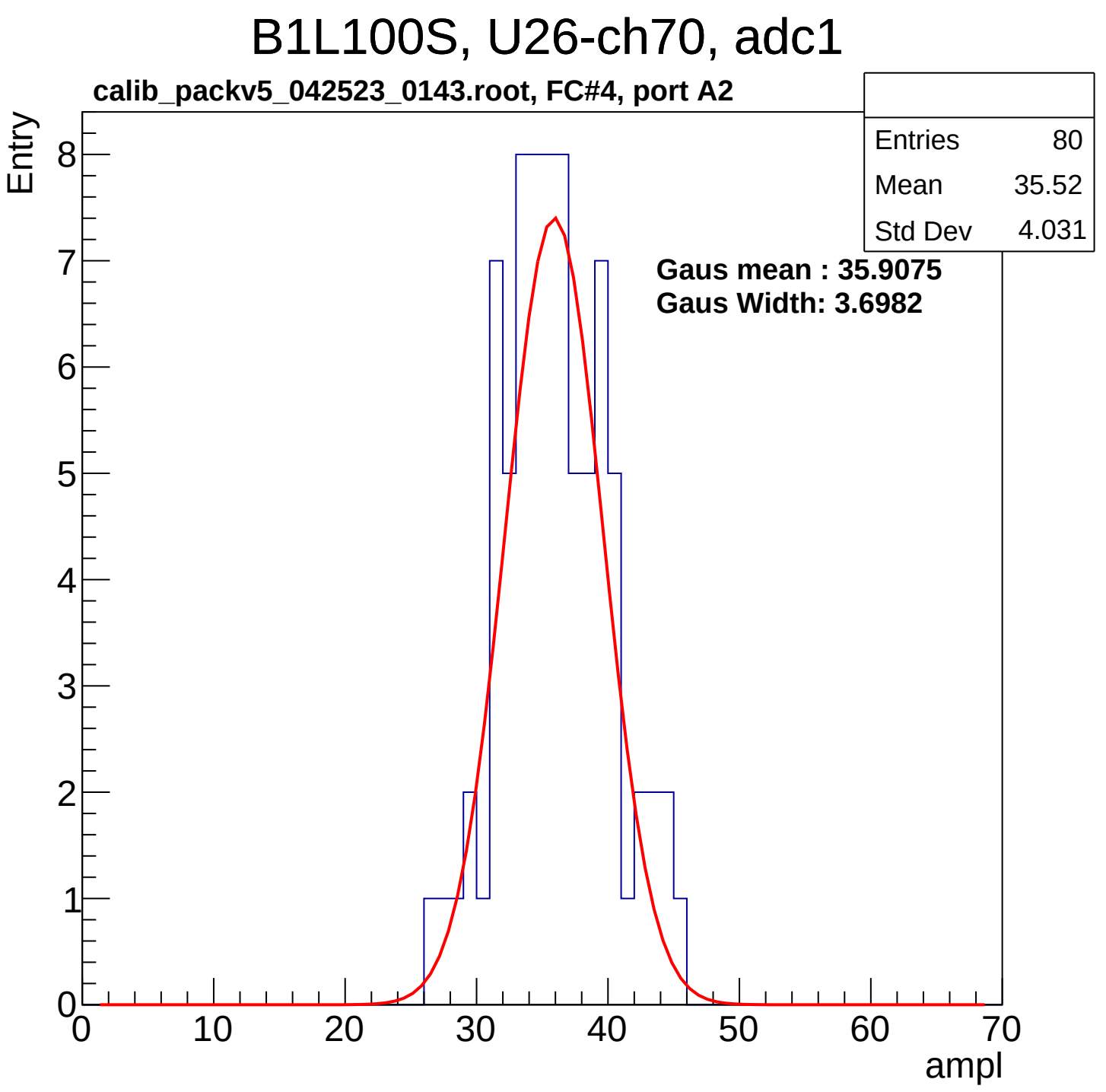

B1L100S, U26-ch69, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 59.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 29.13
Std Dev 3.375
Gaus mean : 29.7051
Gaus Width: 4.2138
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.52
Std Dev 4.031
Gaus mean : 35.9075
Gaus Width: 3.6982
Entry
ampl
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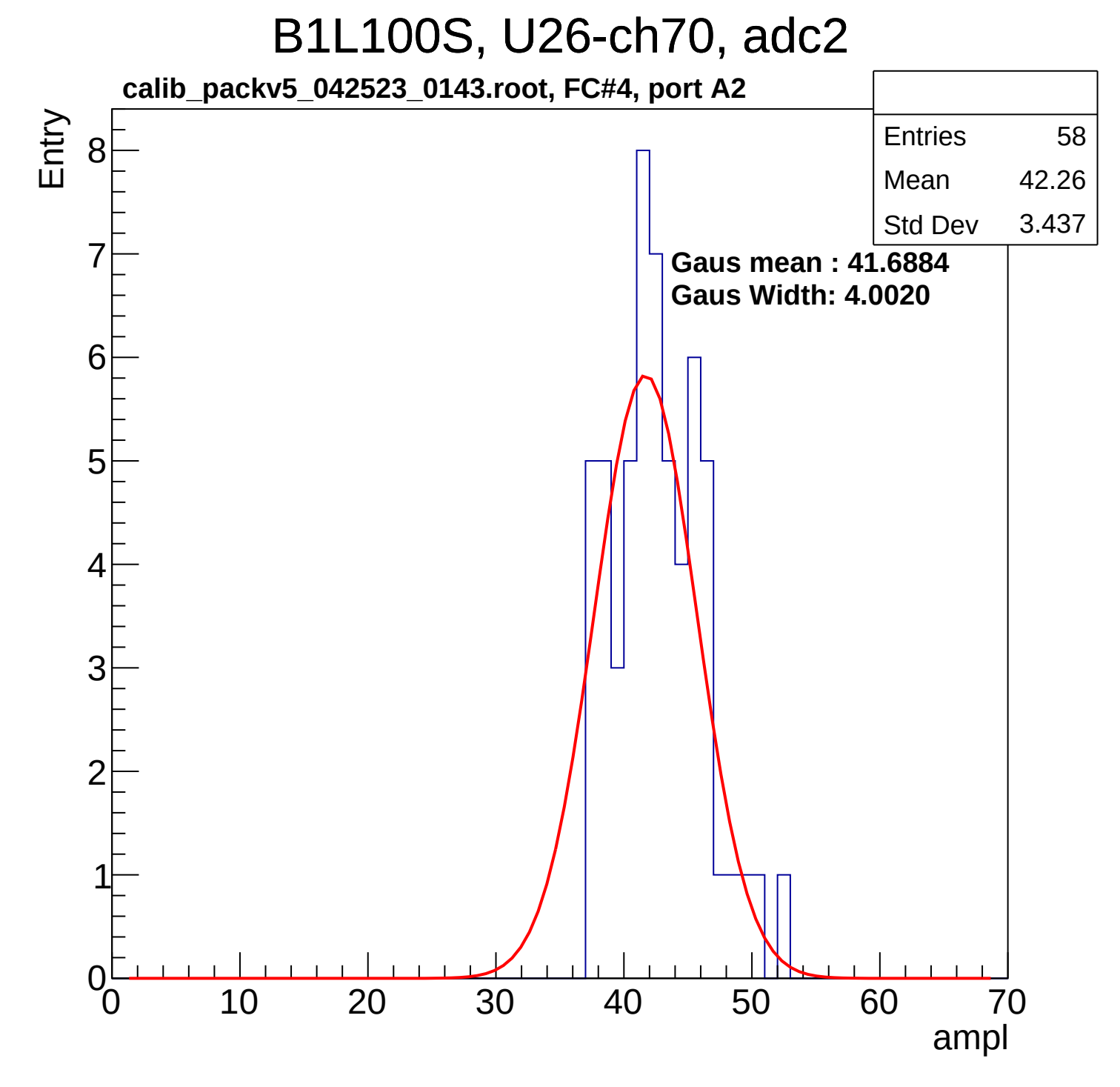
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 42.26
Std Dev 3.437
Gaus mean : 41.6884
Gaus Width: 4.0020
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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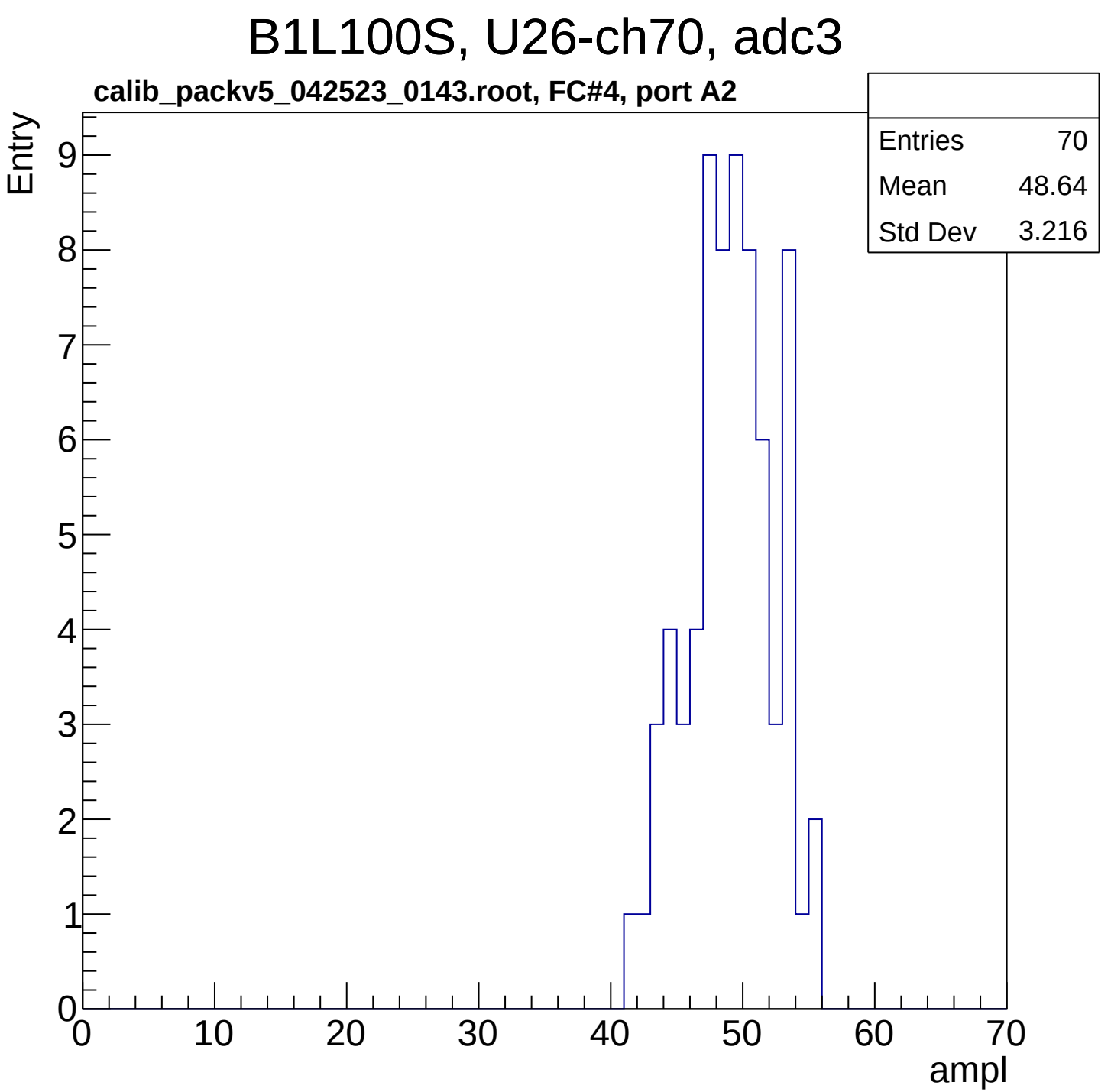
B1L100S, U26-ch70, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 48.64
Std Dev 3.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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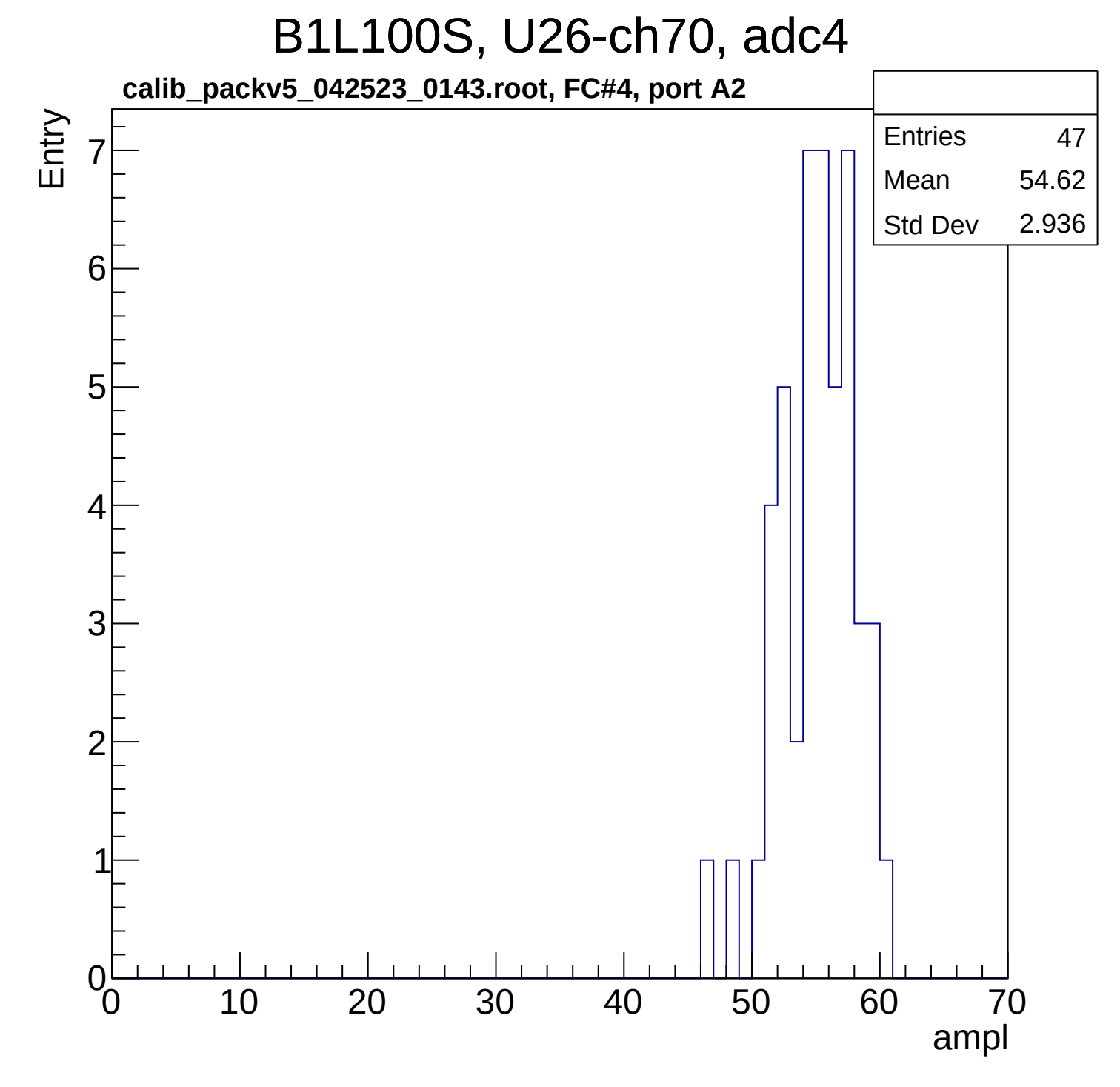
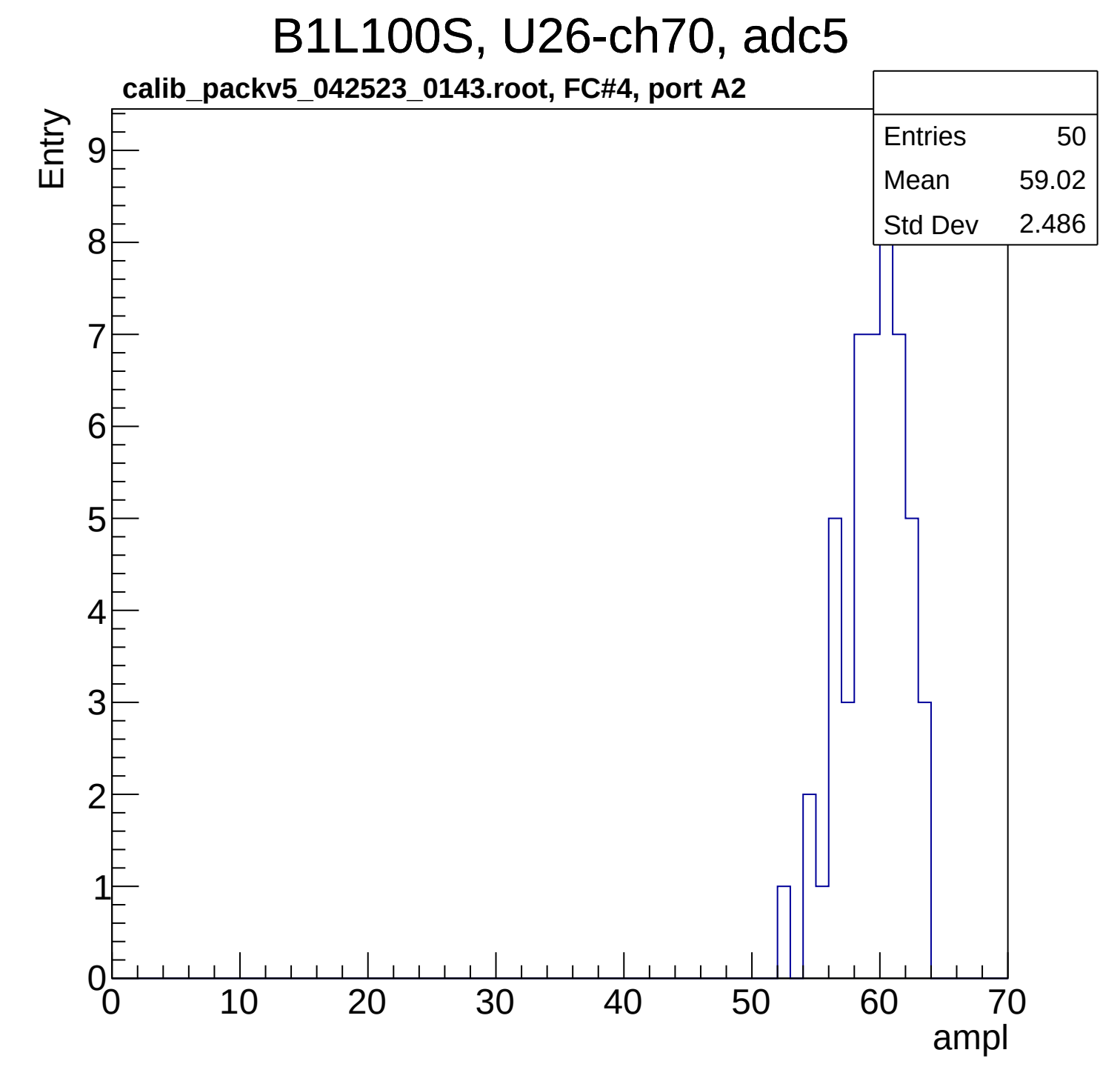
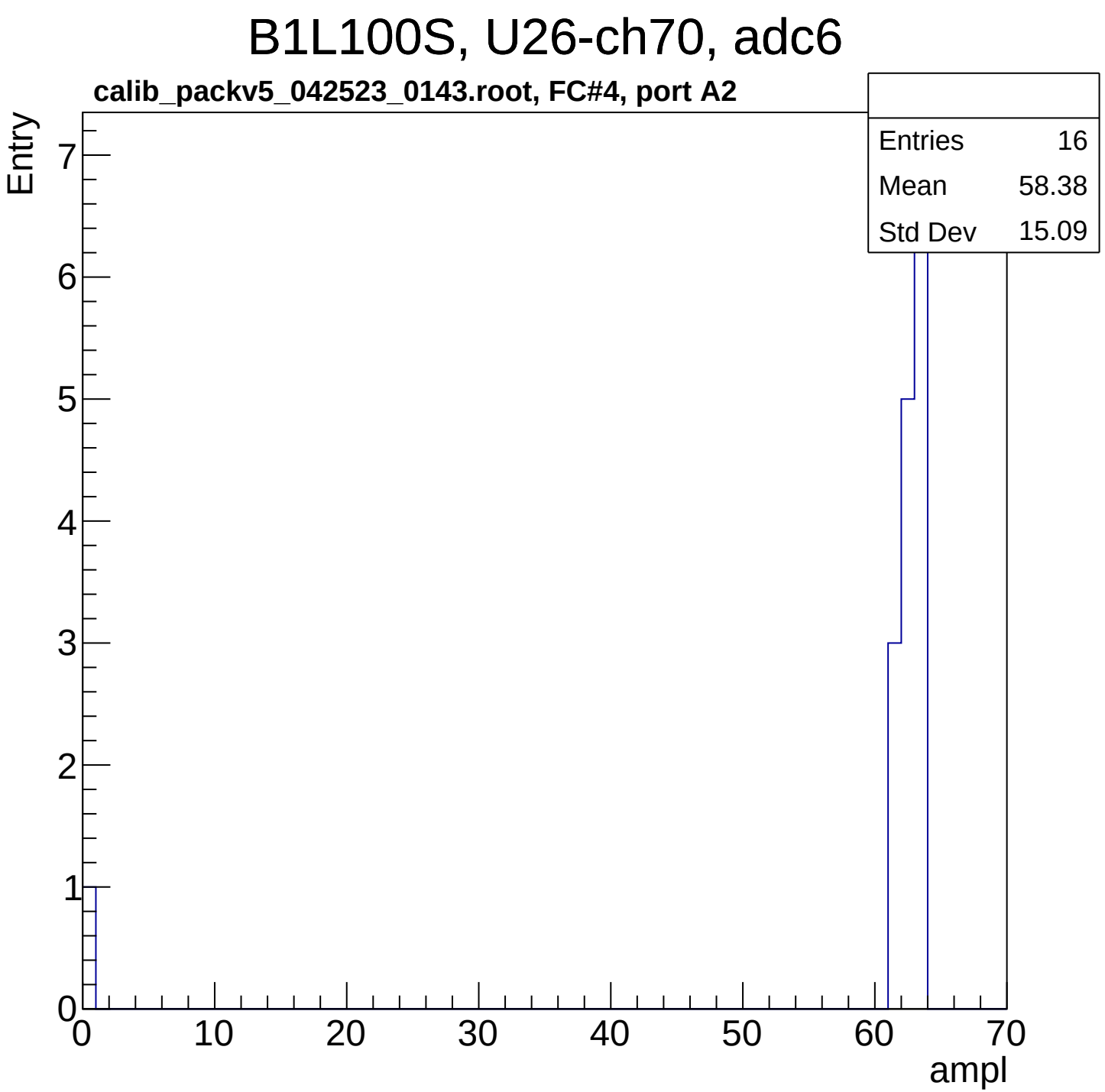

B1L100S, U26-ch70, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 54.62
Std Dev 2.936
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 59.02
Std Dev 2.486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch70, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 58.38
Std Dev 15.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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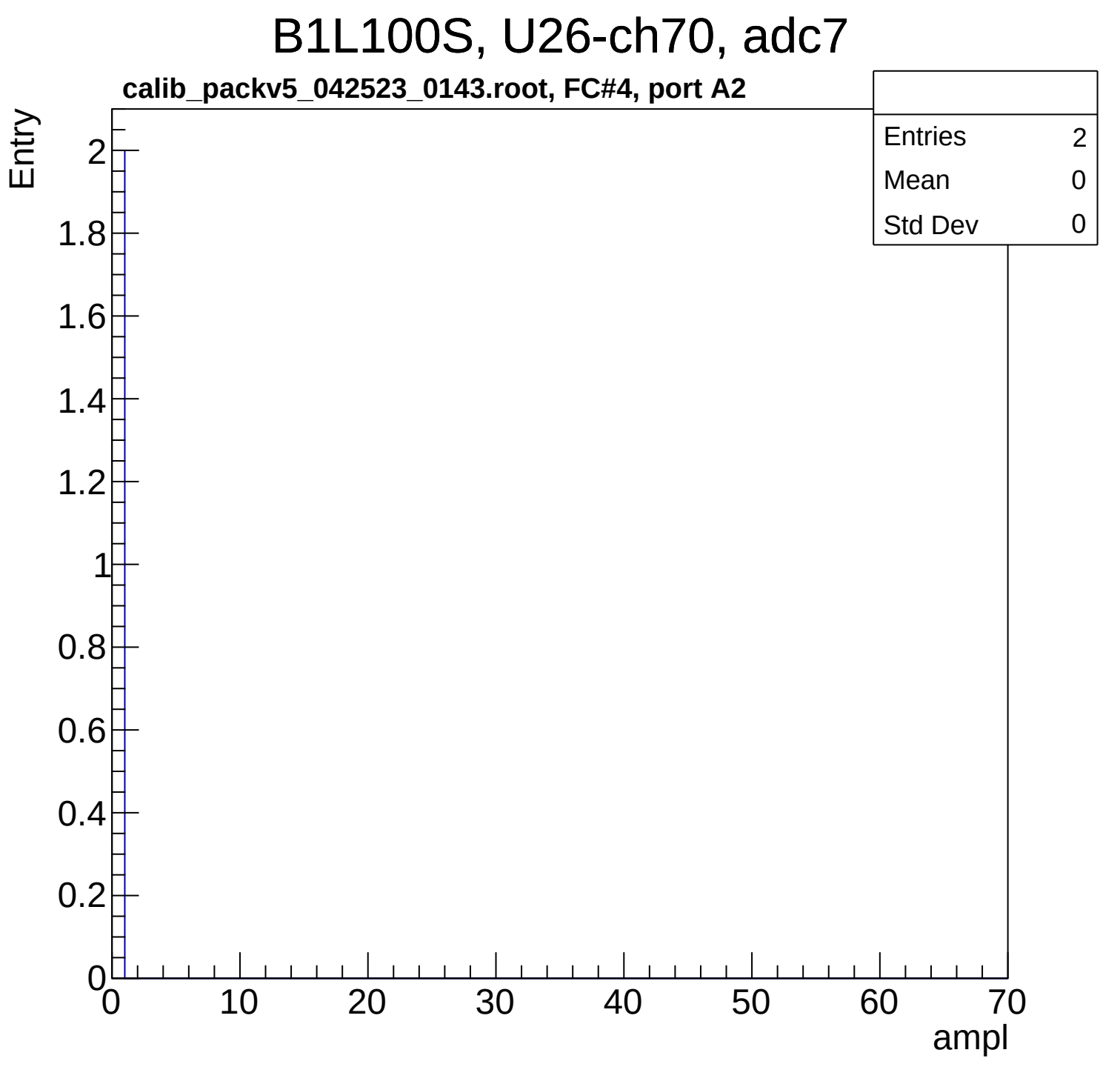
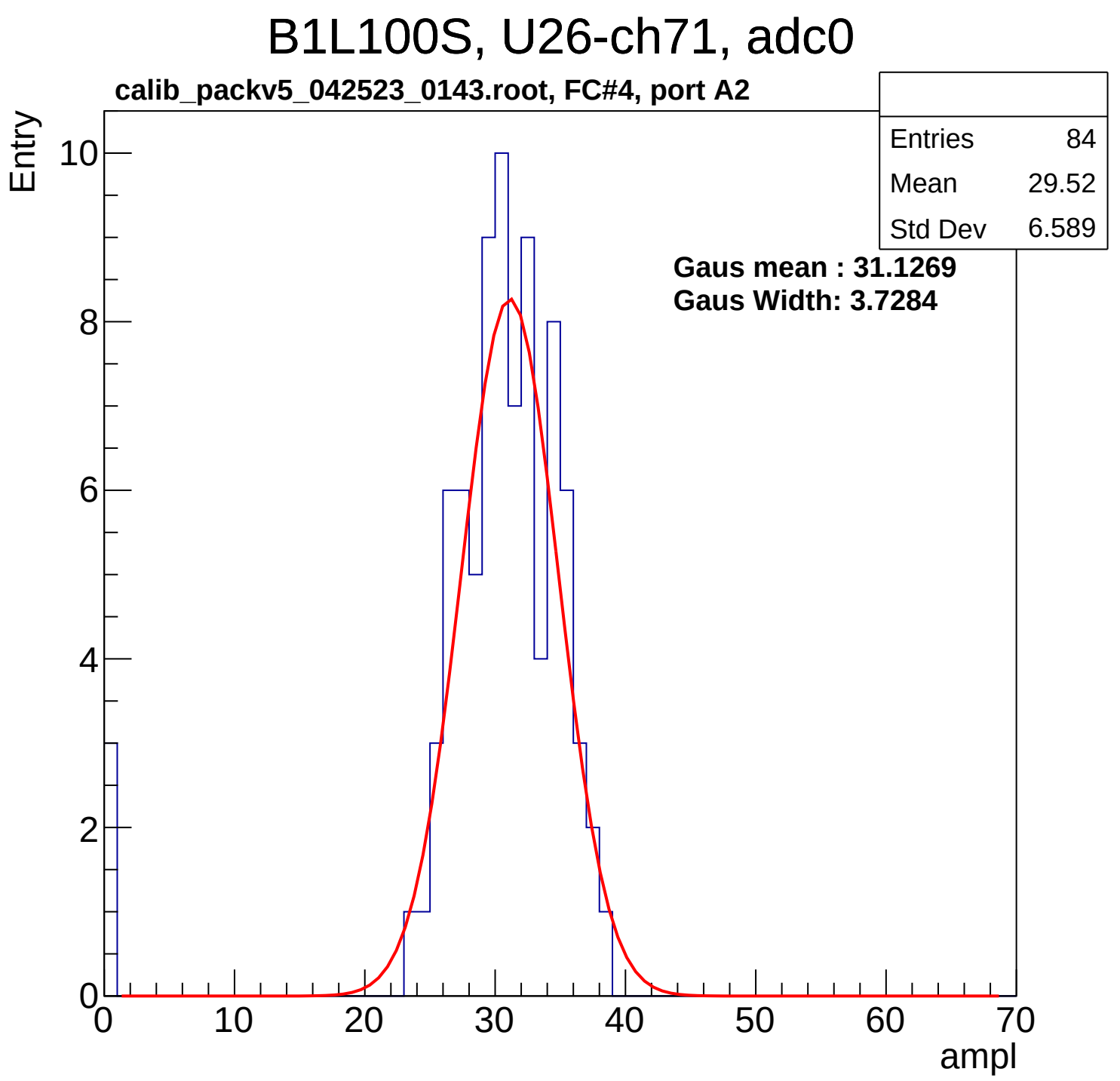
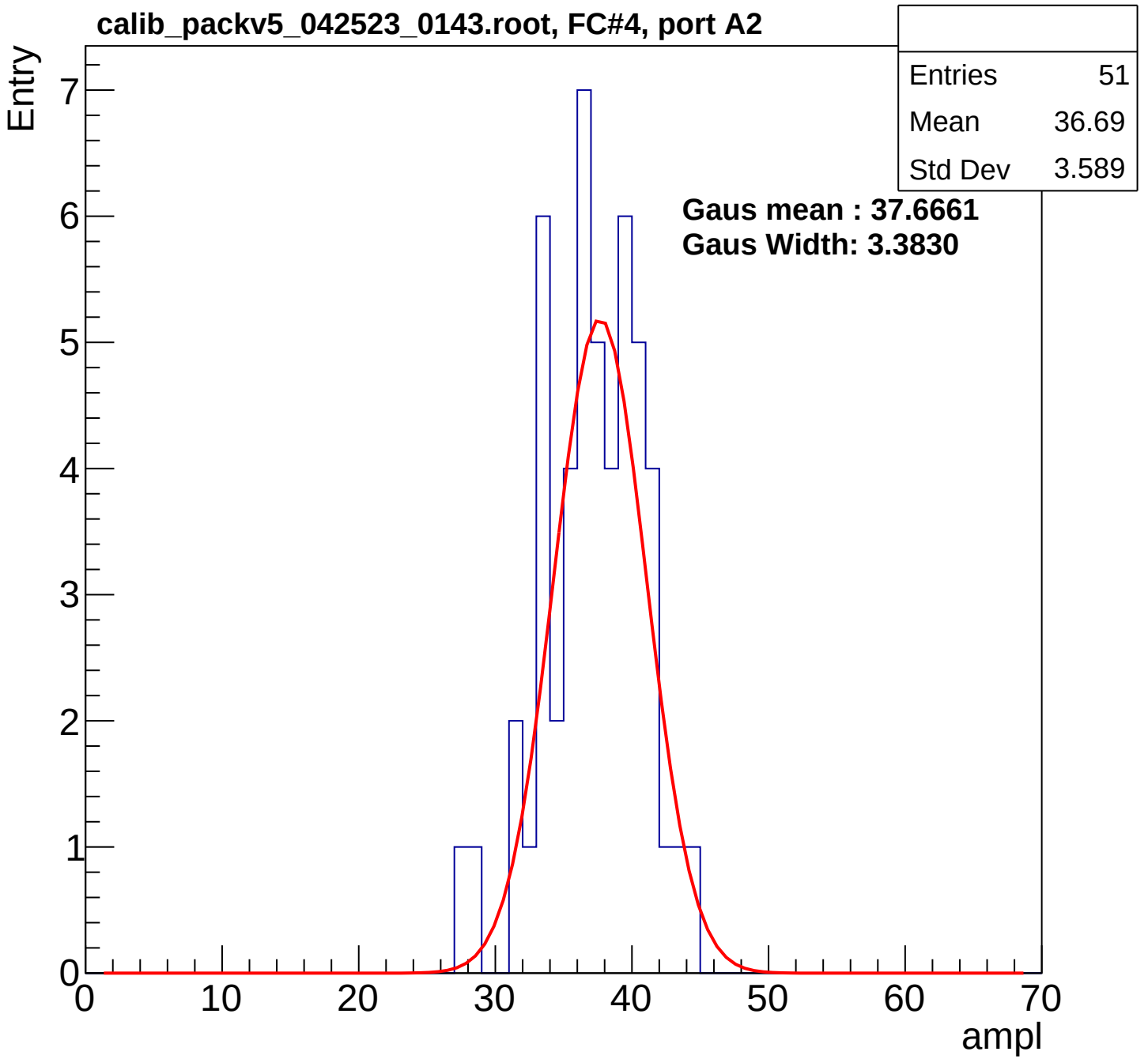
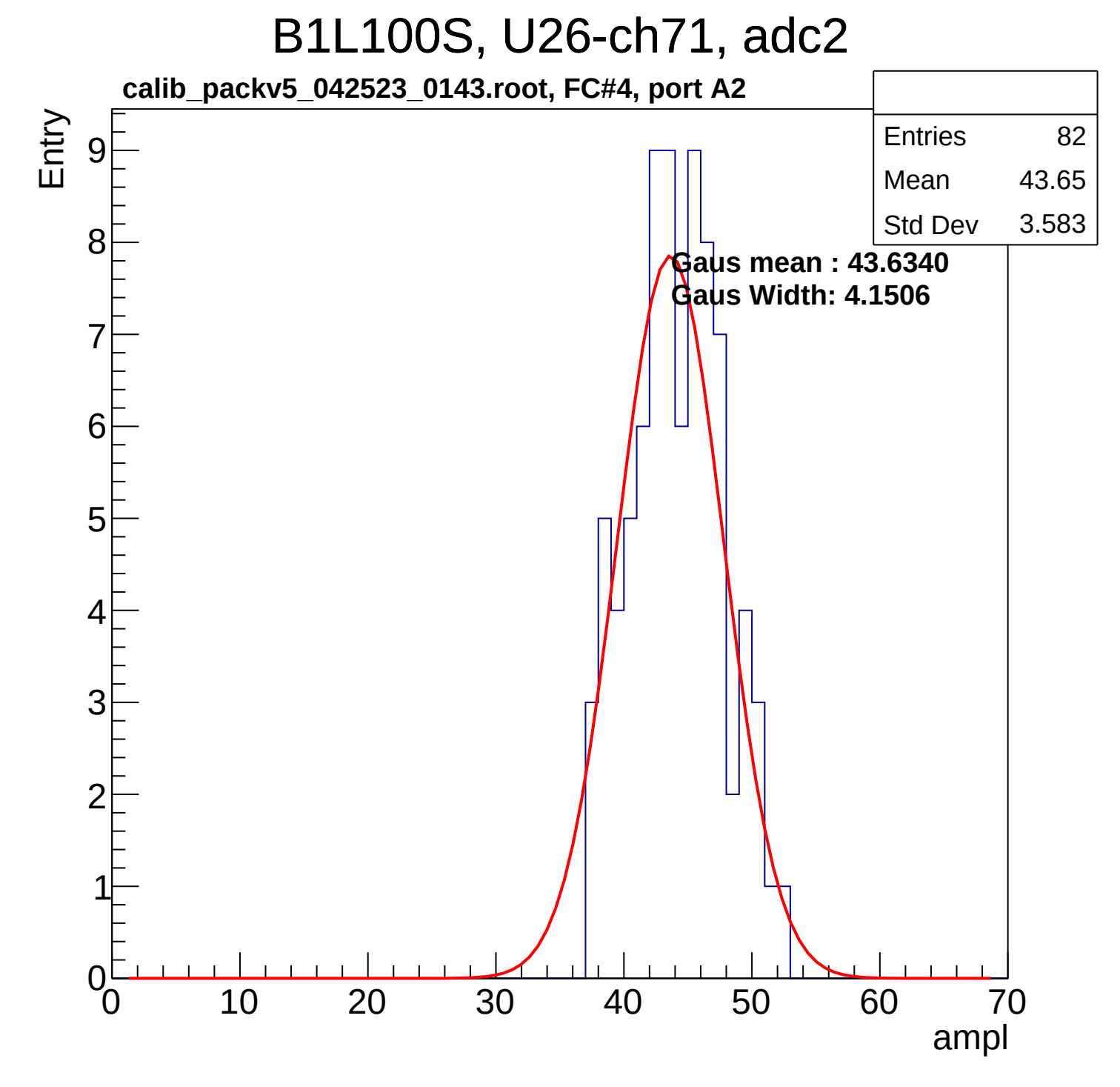

B1L100S, U26-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 29.52
Std Dev 6.589
Gaus mean : 31.1269
Gaus Width: 3.7284
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 36.69
Std Dev 3.589
Gaus mean : 37.6661
Gaus Width: 3.3830
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 43.65
Std Dev 3.583
Gaus mean : 43.6340
Gaus Width: 4.1506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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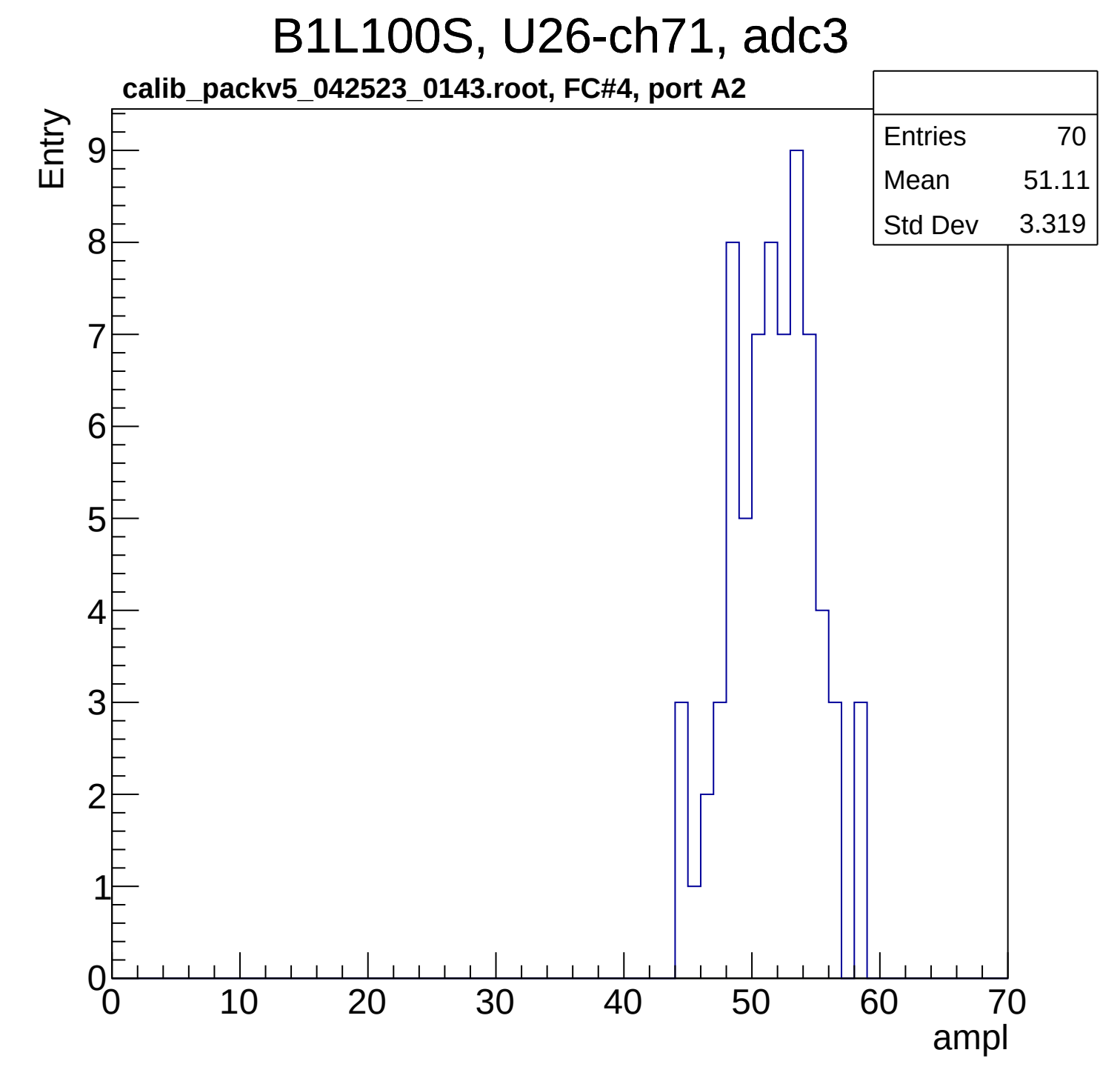
B1L100S, U26-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 51.11
Std Dev 3.319
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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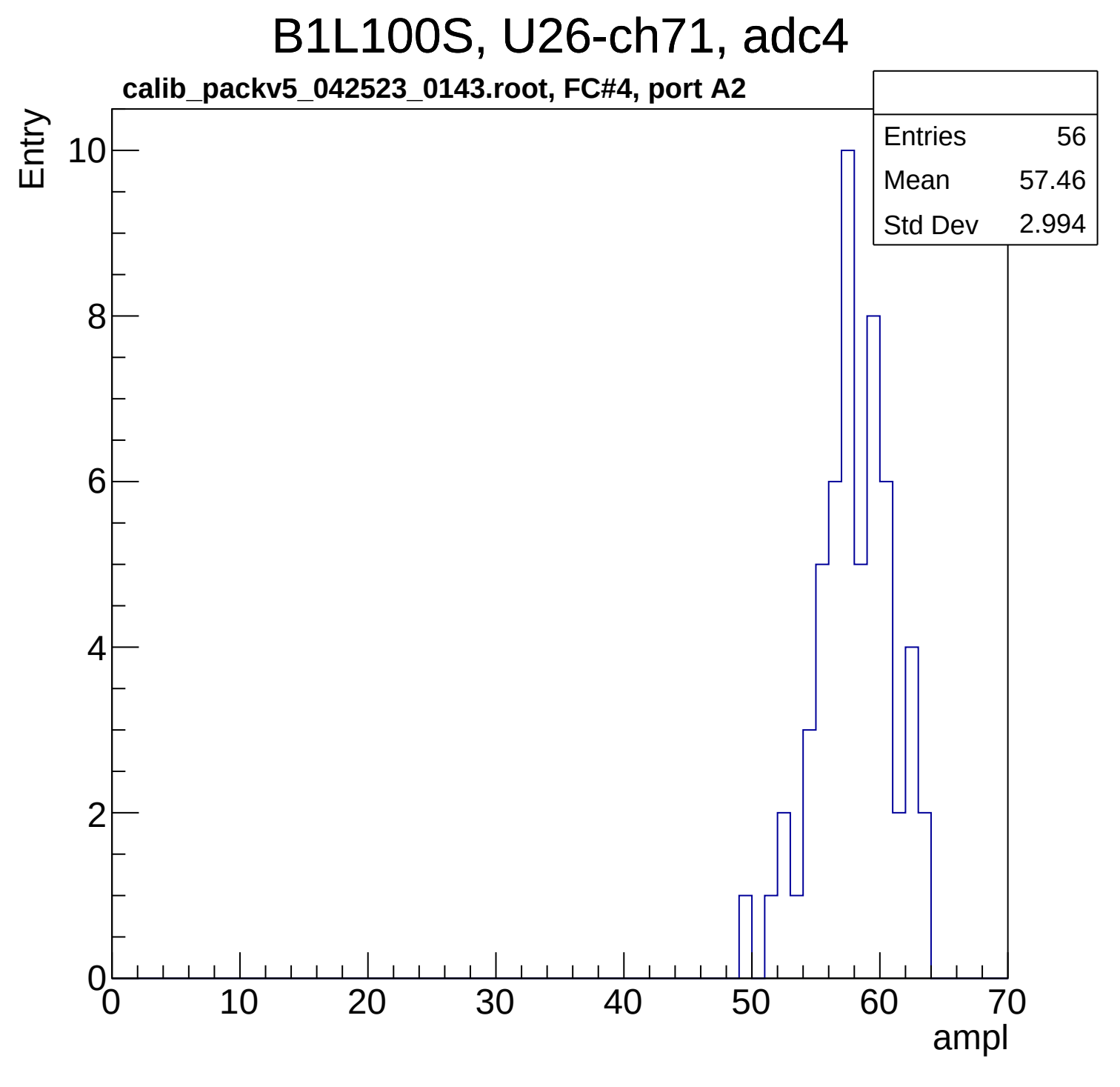
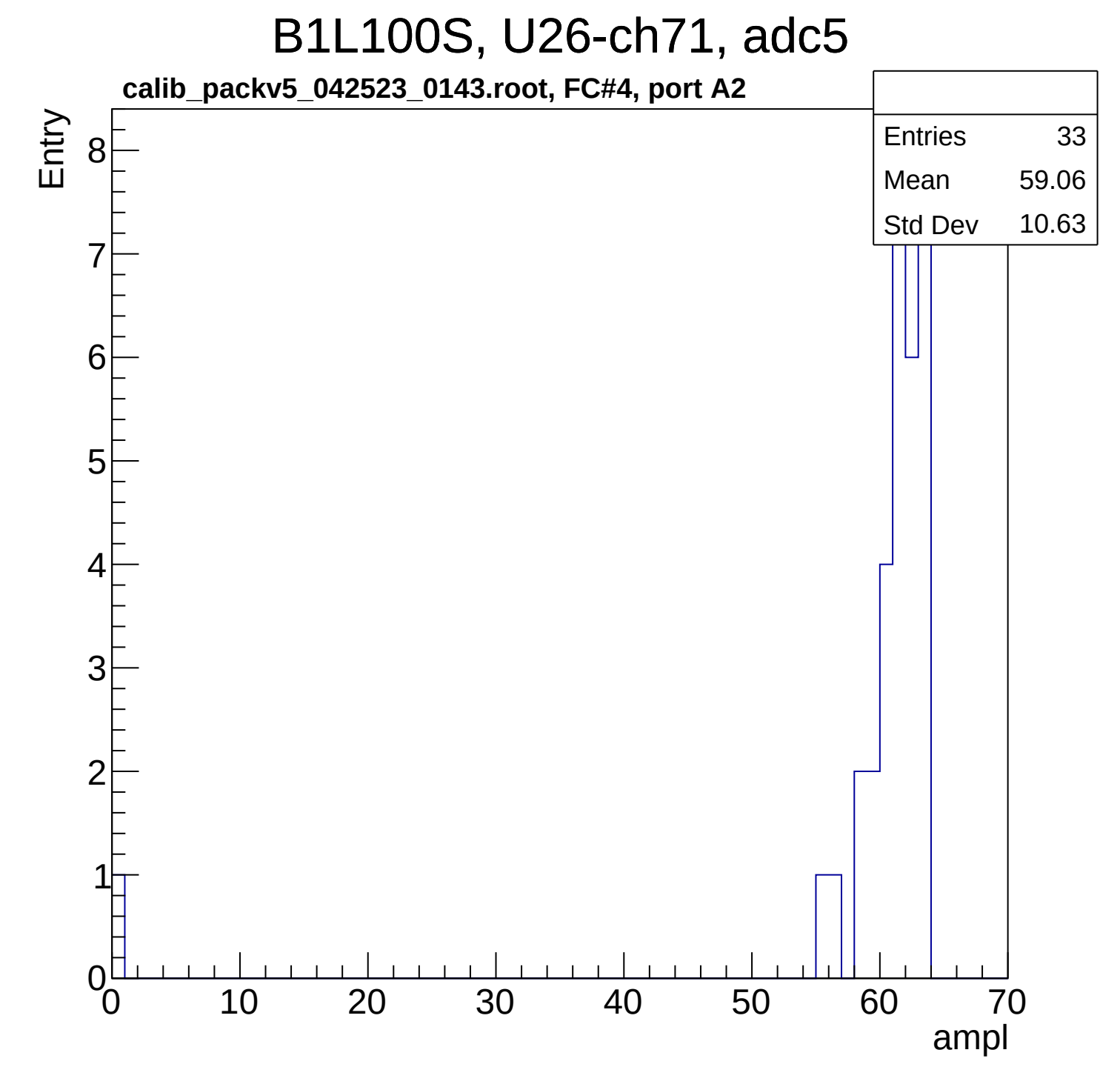
60
70


B1L100S, U26-ch71, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.46
Std Dev 2.994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.06
Std Dev 10.63
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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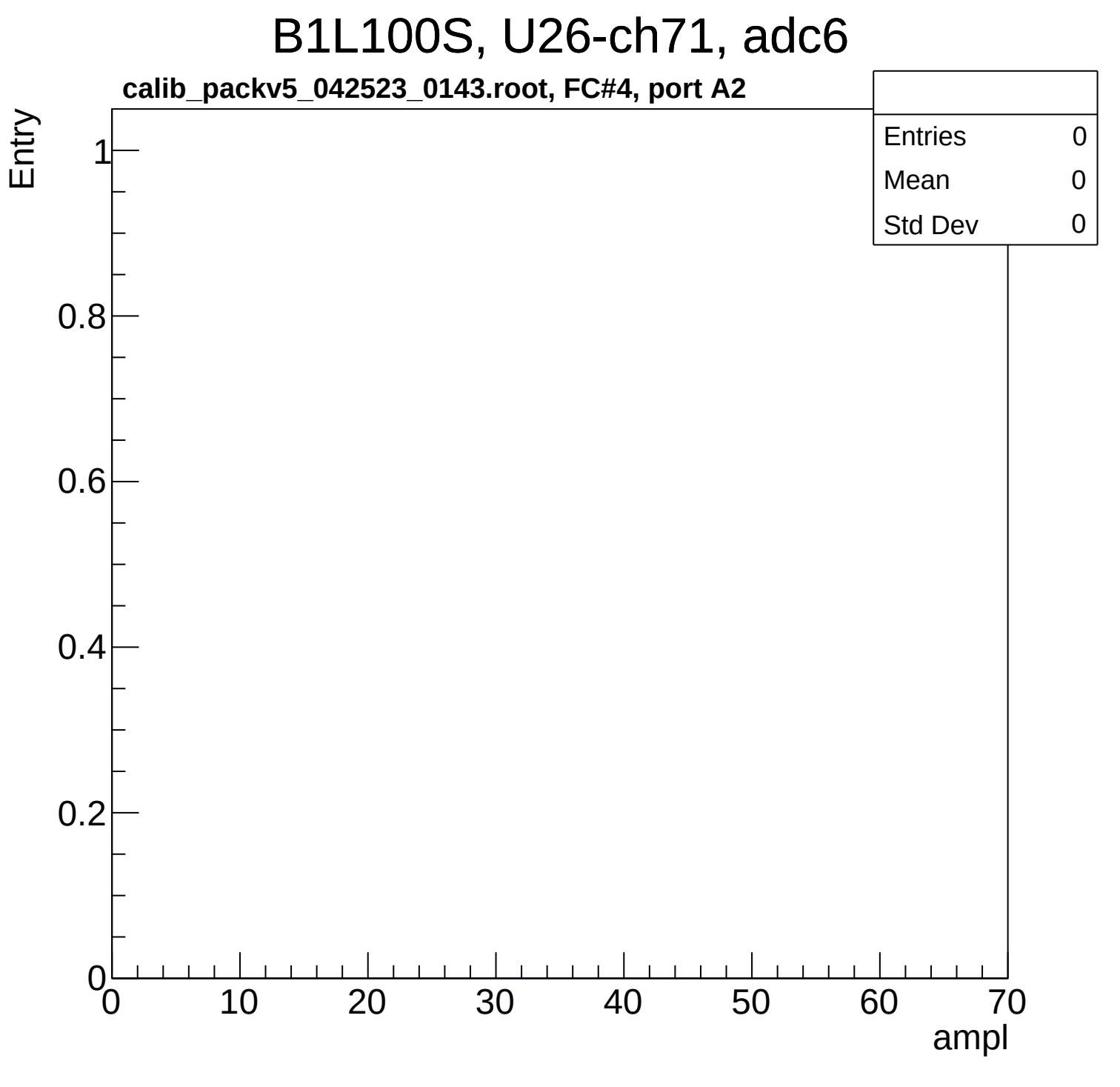
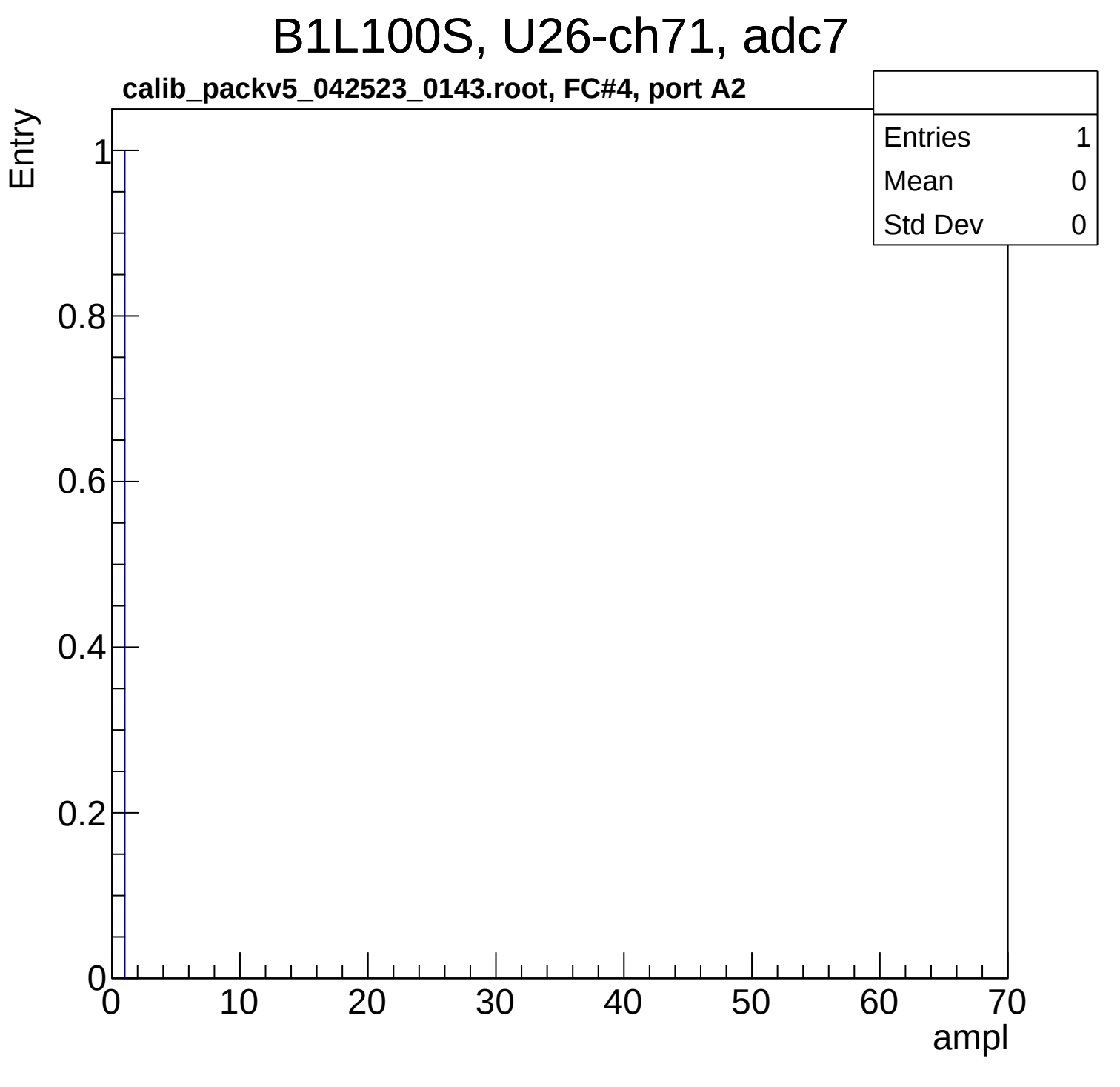
40
50
60
70


B1L100S, U26-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch71, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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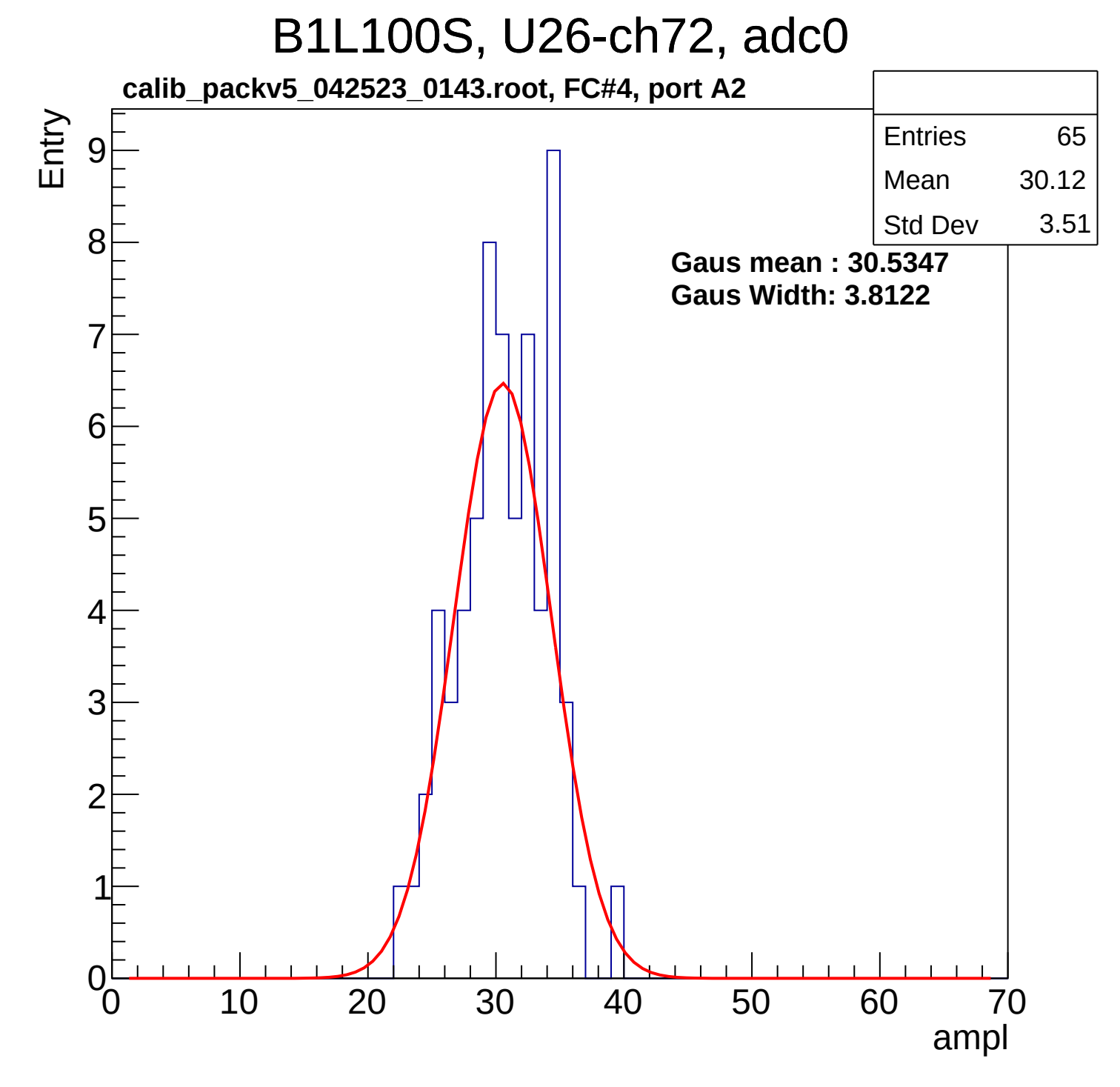
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 30.12
Std Dev 3.51
Gaus mean : 30.5347
Gaus Width: 3.8122
Entry
ampl
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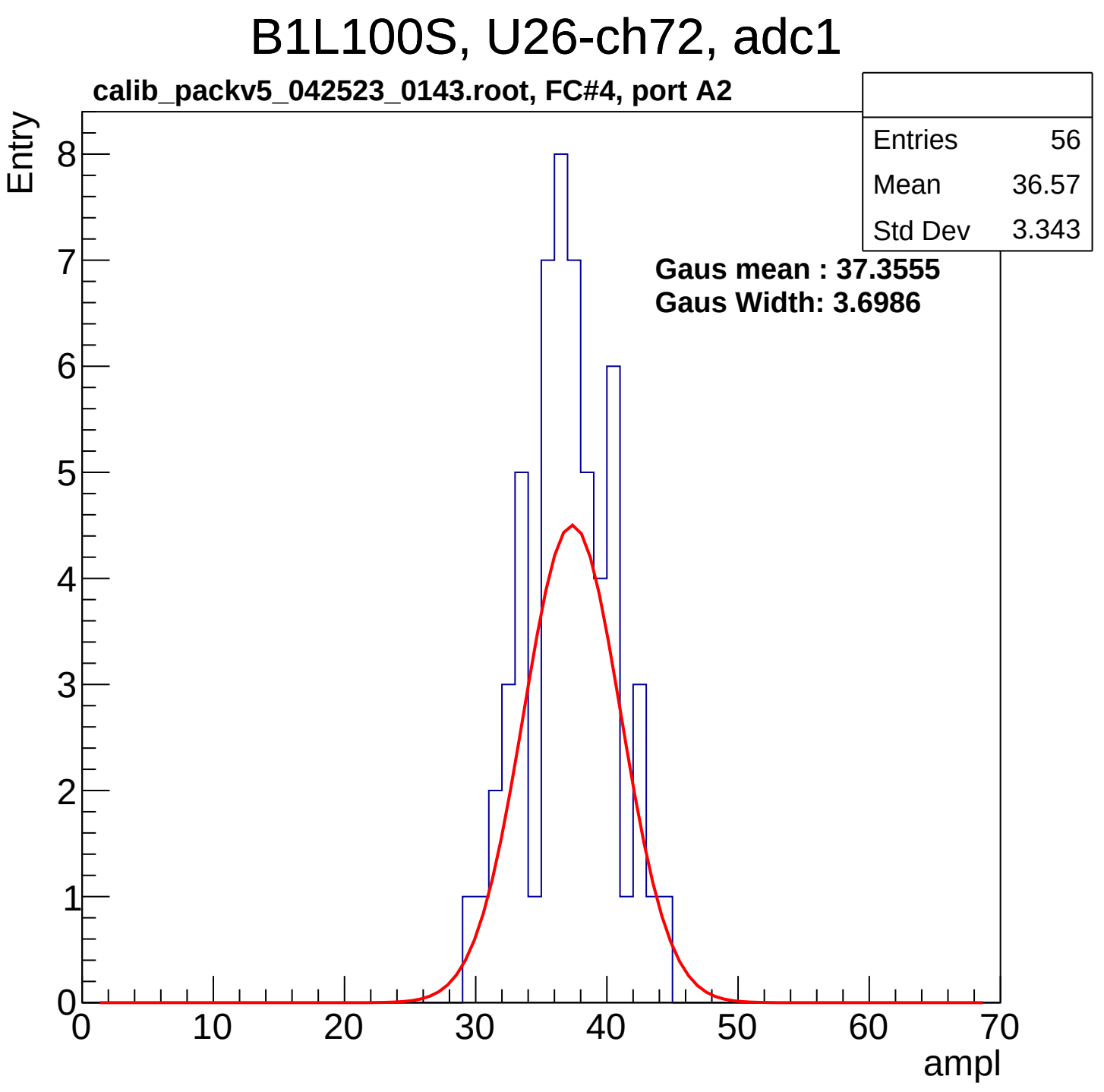
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 36.57
Std Dev 3.343
Gaus mean : 37.3555
Gaus Width: 3.6986
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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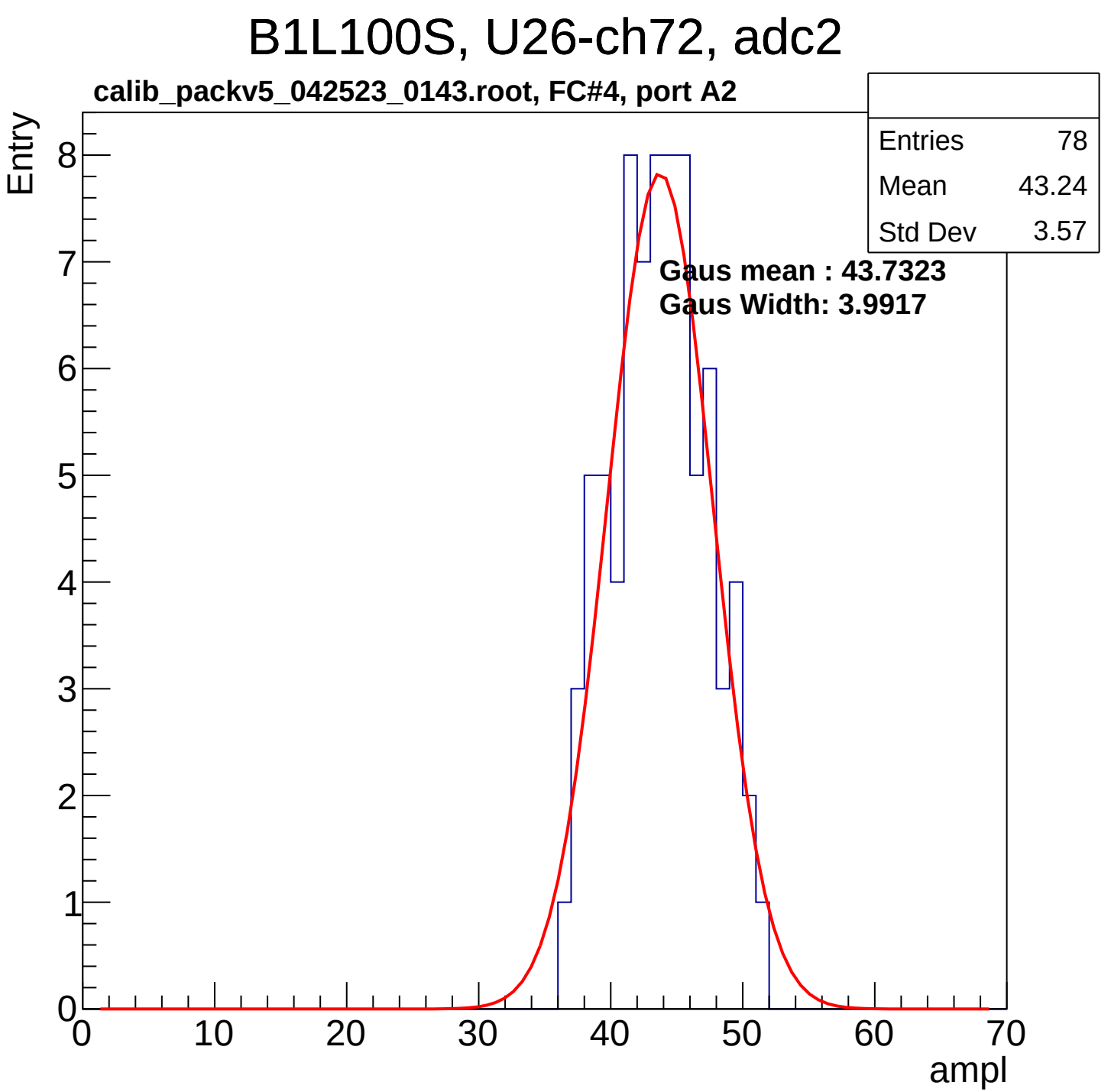
40
50
60
70


B1L100S, U26-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.24
Std Dev 3.57
Gaus mean : 43.7323
Gaus Width: 3.9917
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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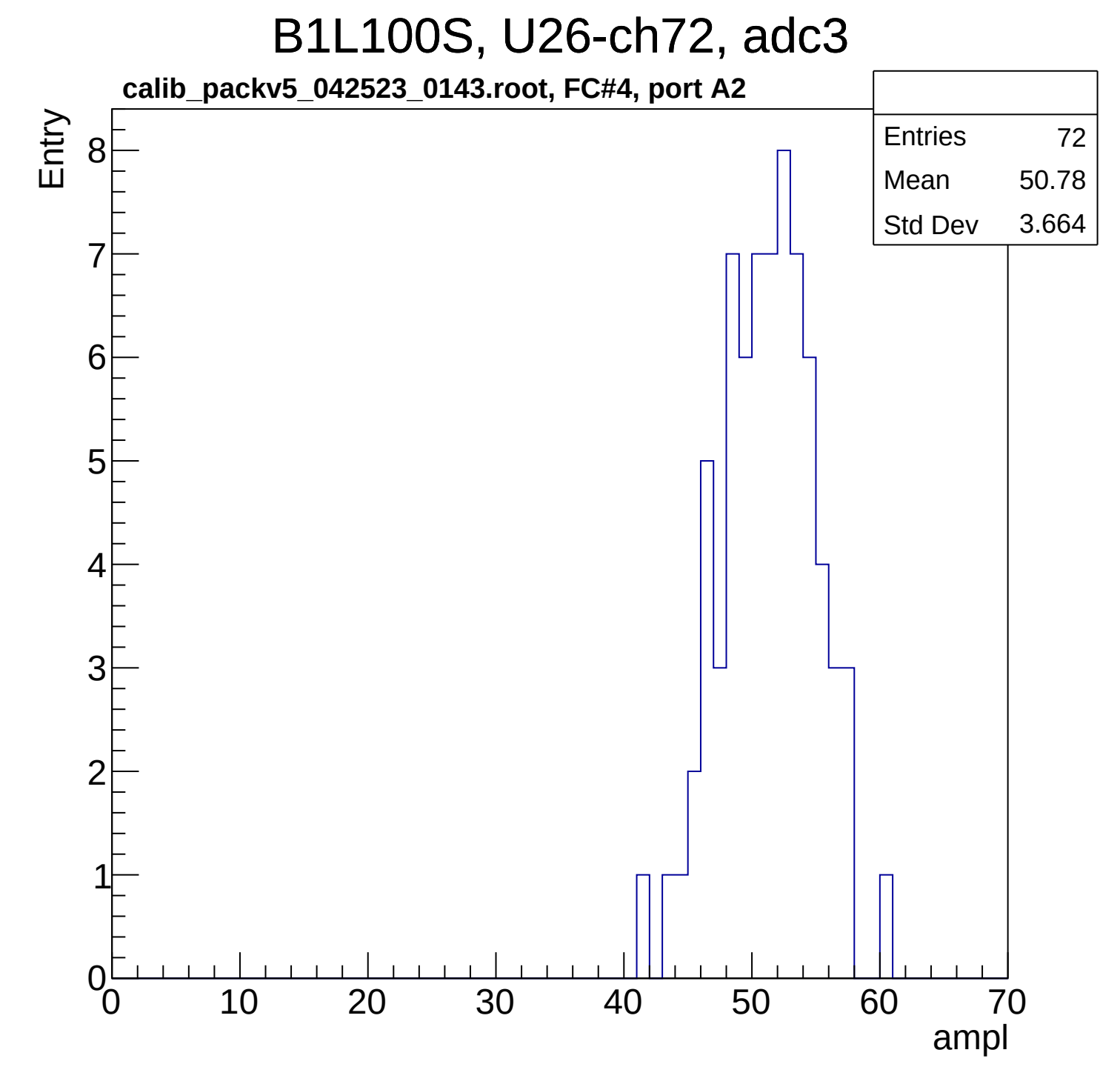
60
70


B1L100S, U26-ch72, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.78
Std Dev 3.664
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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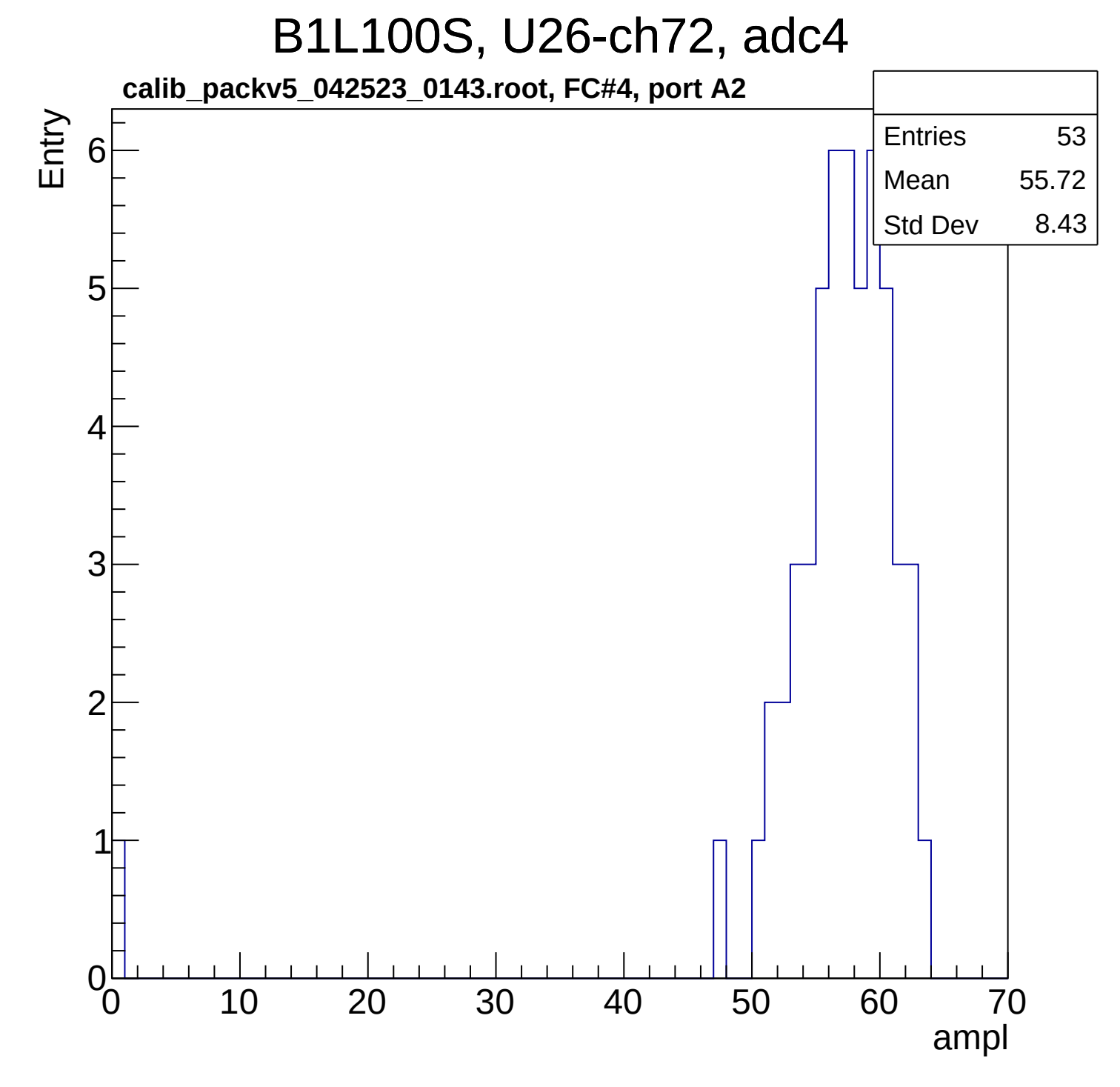
40
50
60
70


B1L100S, U26-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.72
Std Dev 8.43
Entry
ampl
0
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3
4
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0
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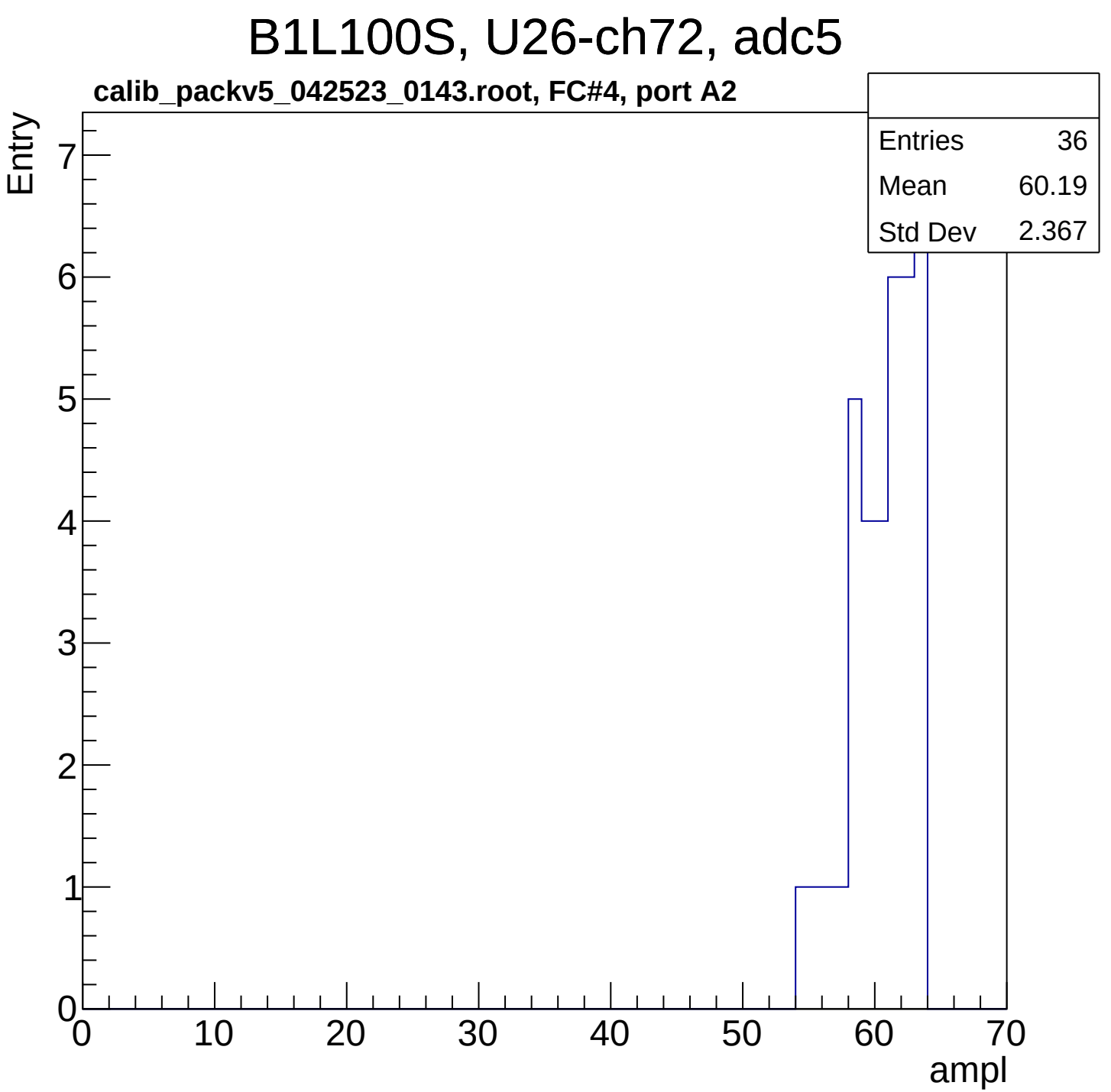
40
50
60
70


B1L100S, U26-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.19
Std Dev 2.367
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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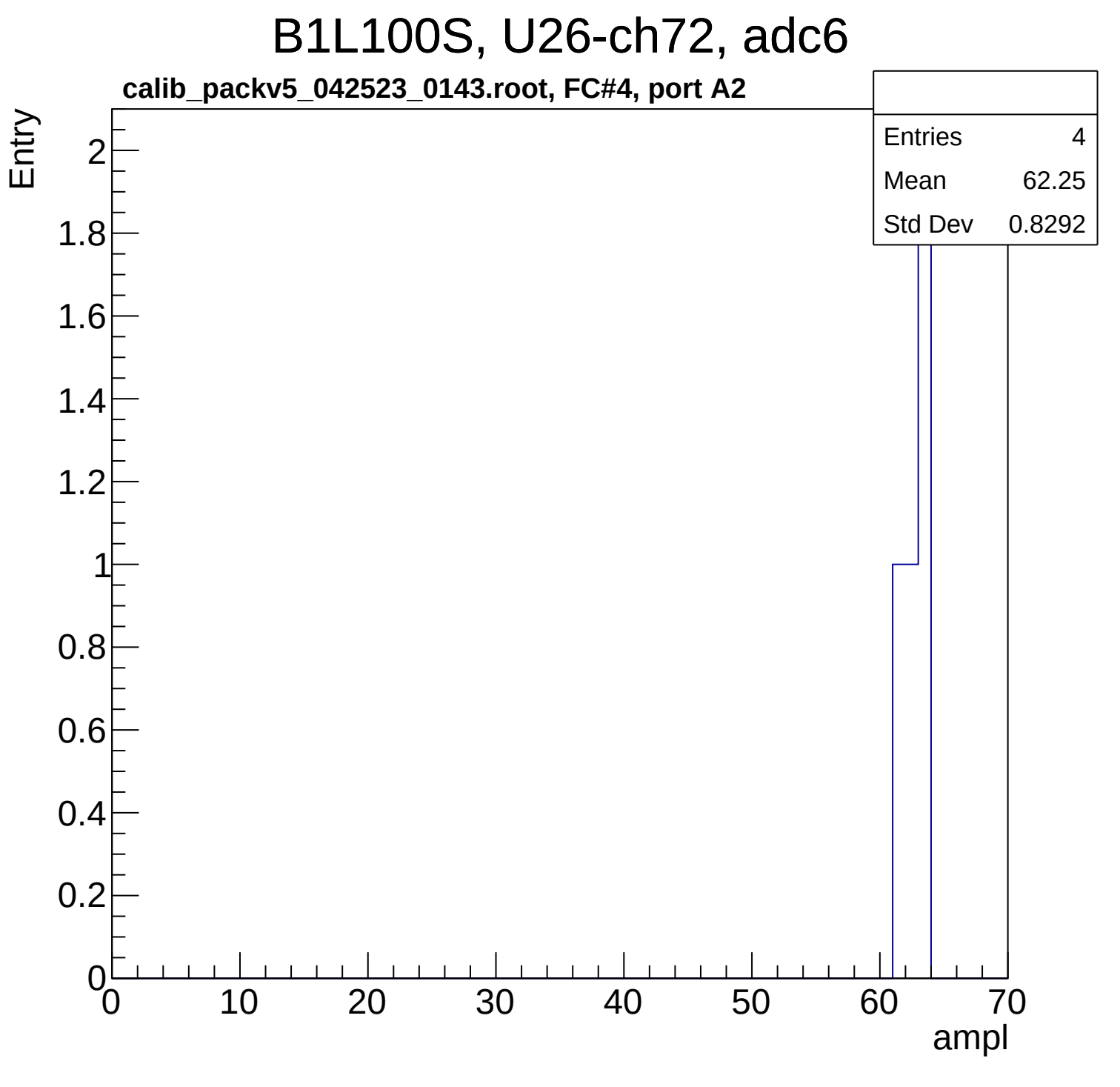
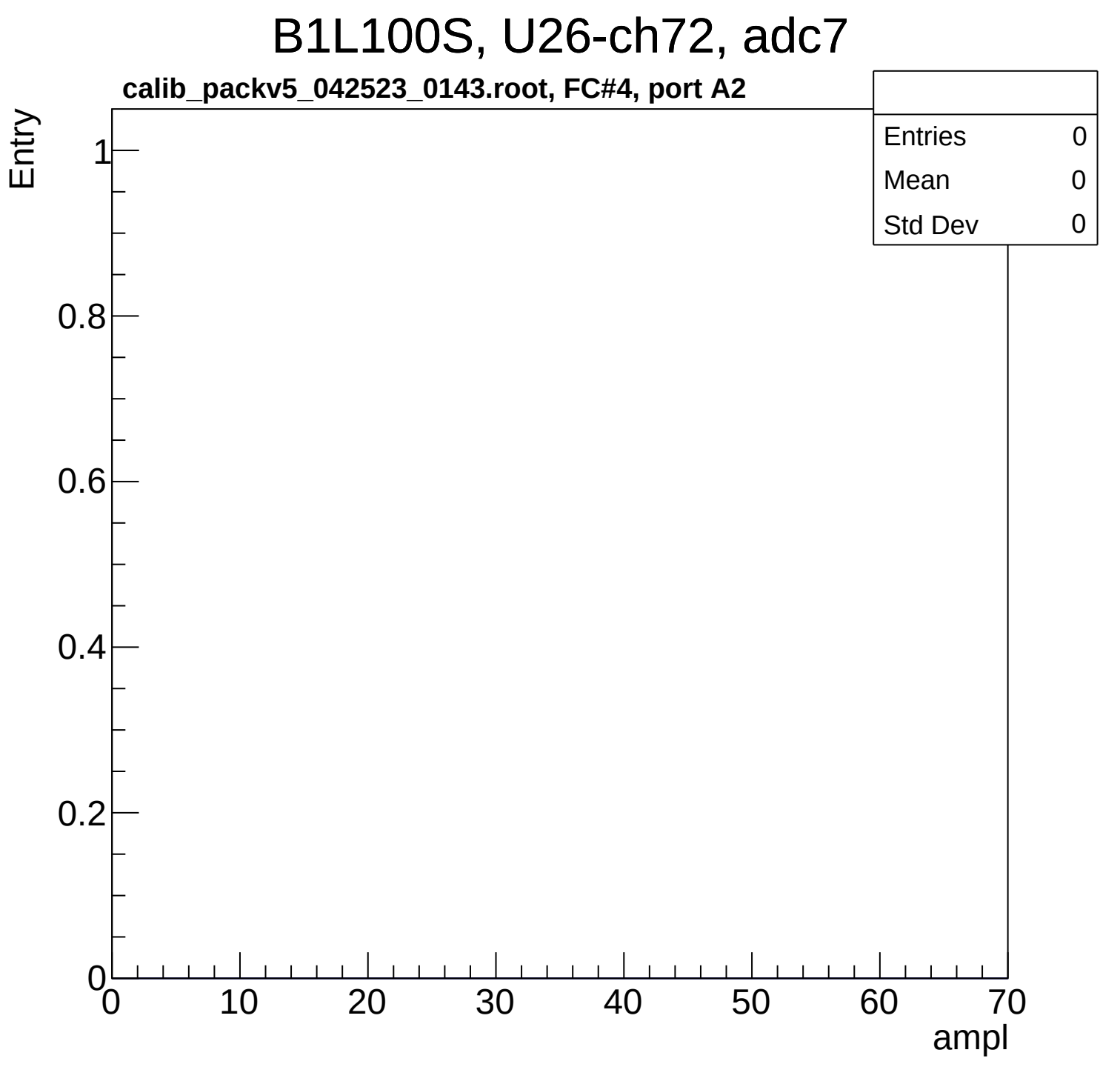

B1L100S, U26-ch72, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch72, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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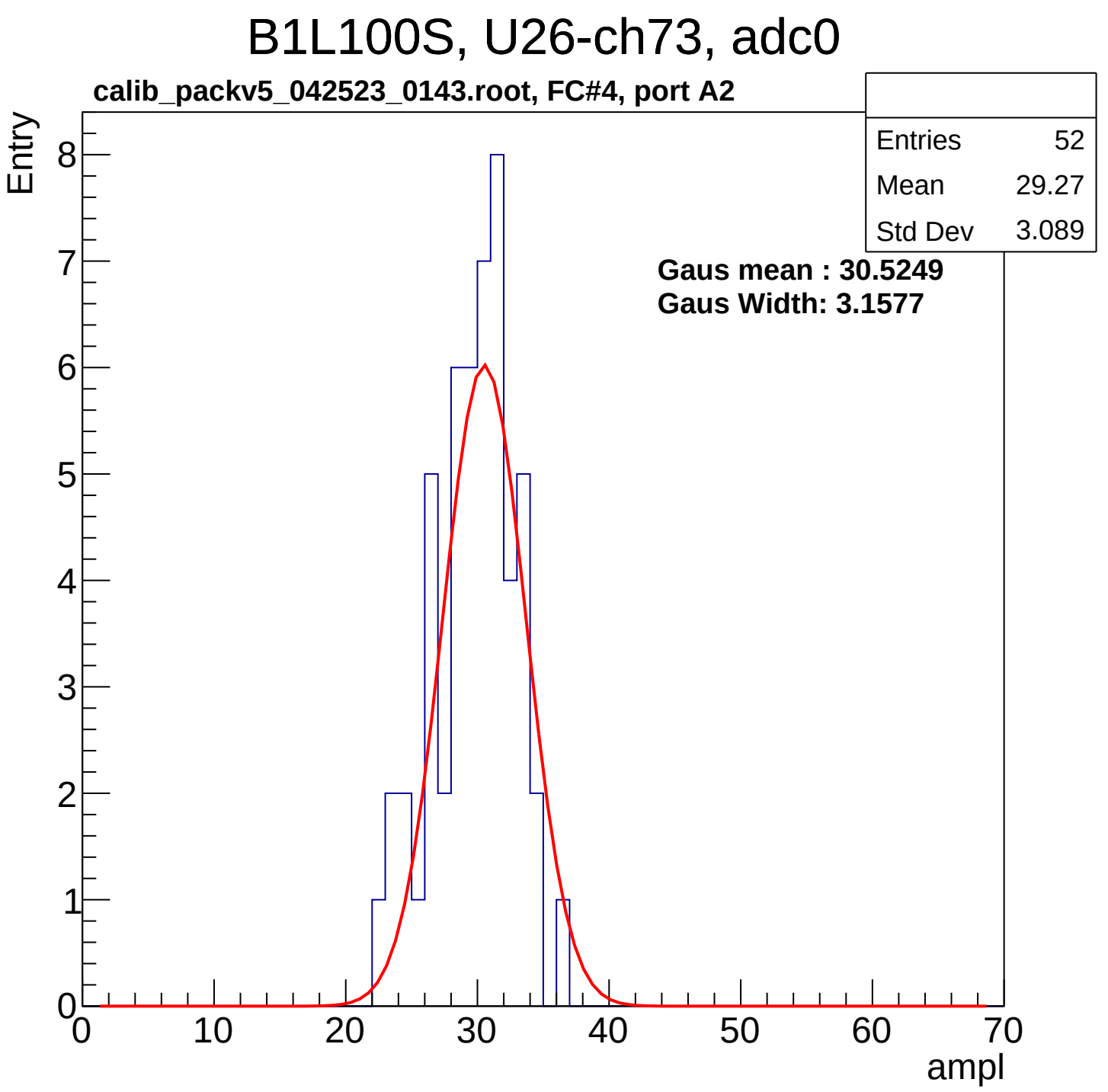
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 29.27
Std Dev 3.089
Gaus mean : 30.5249
Gaus Width: 3.1577
Entry
ampl
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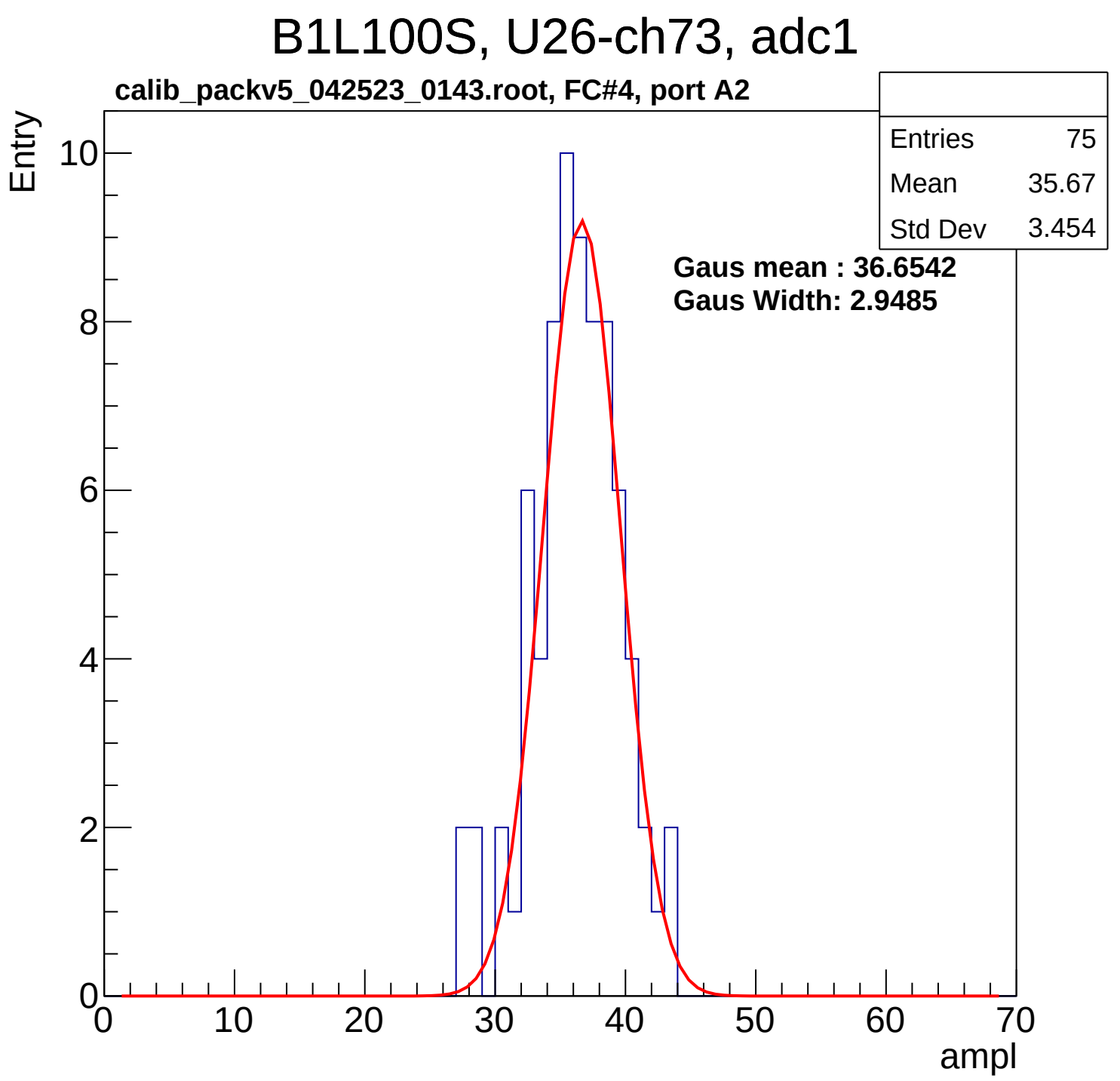
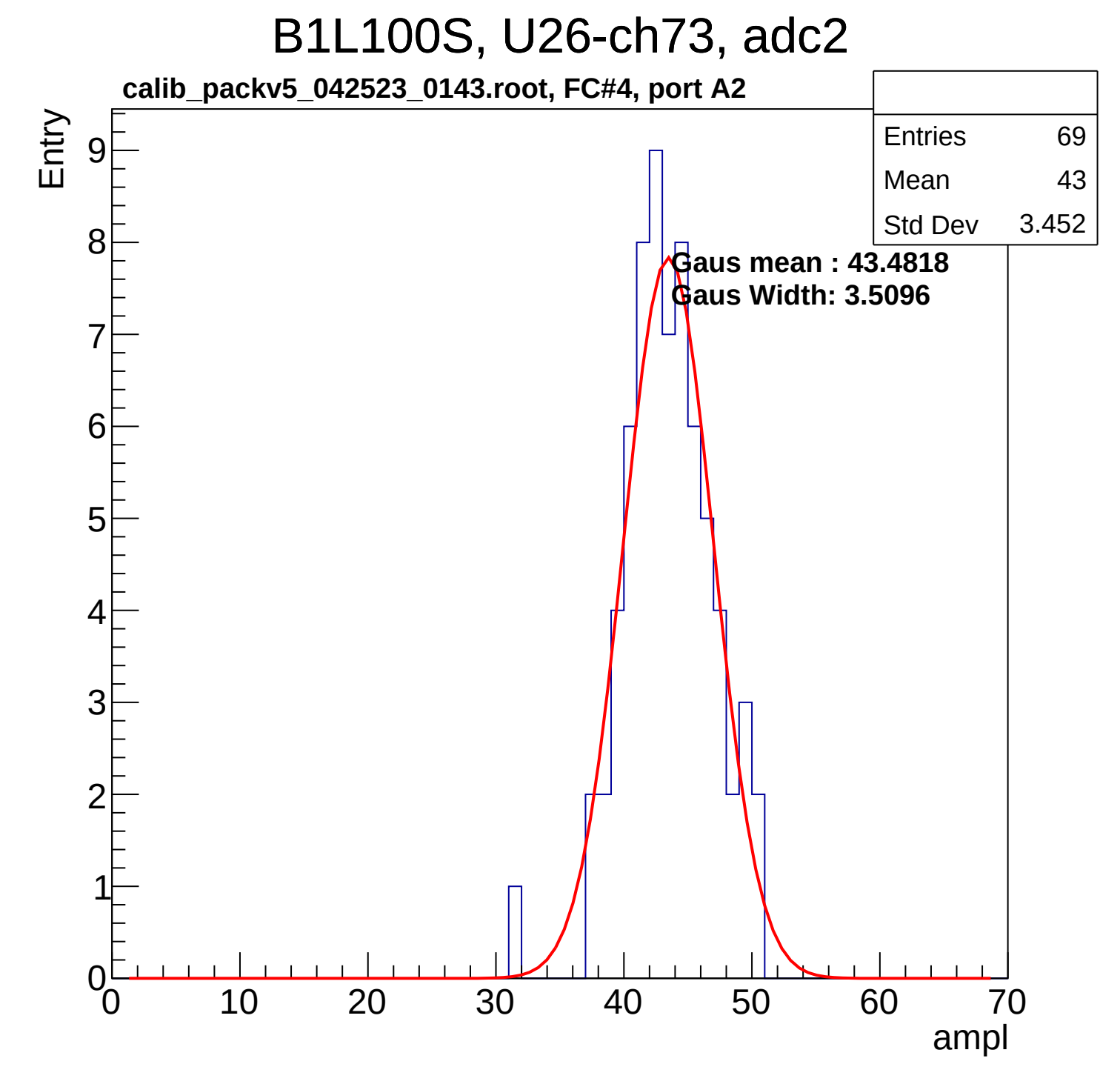
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.67
Std Dev 3.454
Gaus mean : 36.6542
Gaus Width: 2.9485
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43
Std Dev 3.452
Gaus mean : 43.4818
Gaus Width: 3.5096
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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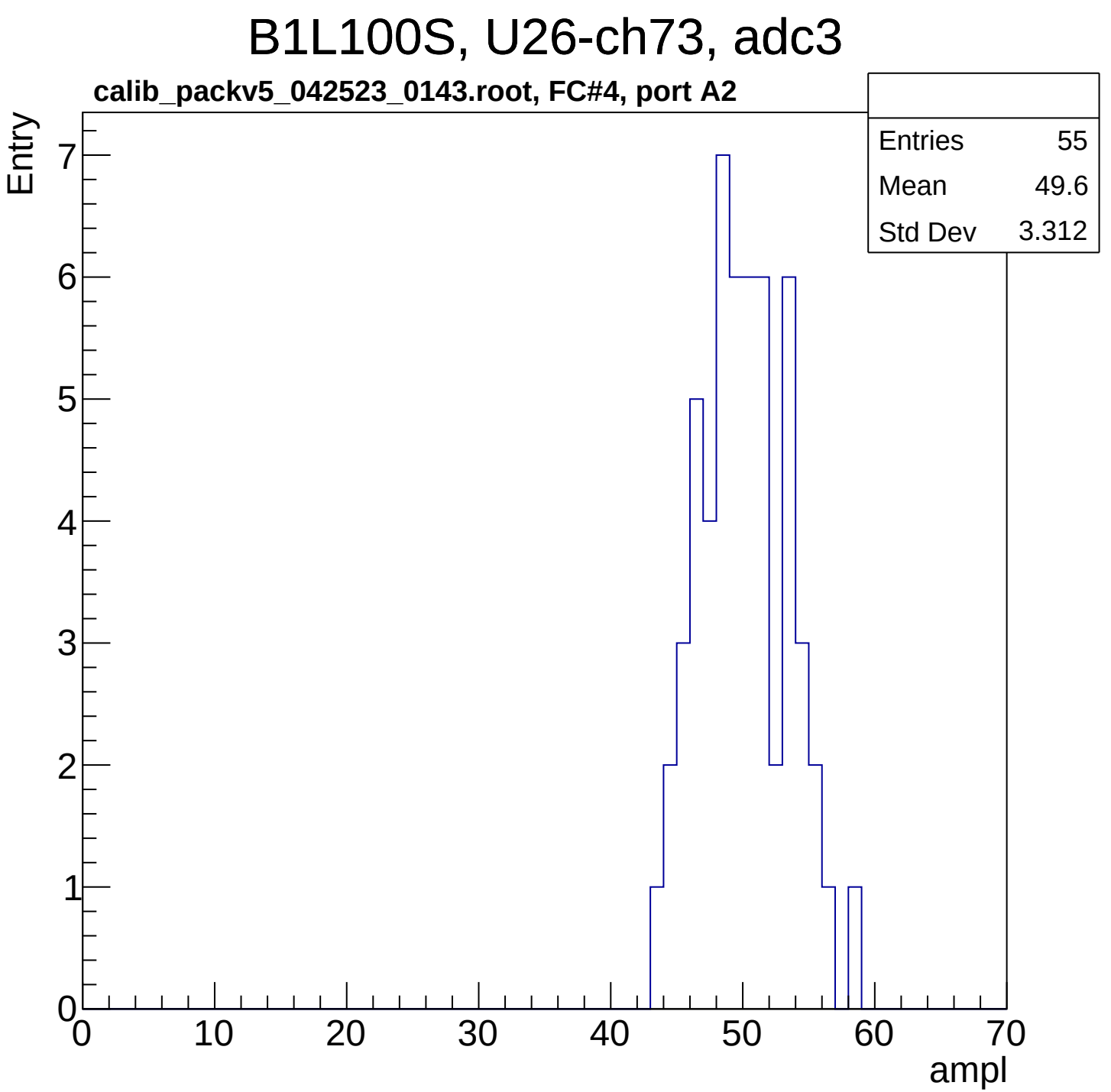
B1L100S, U26-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 49.6
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
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0
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60
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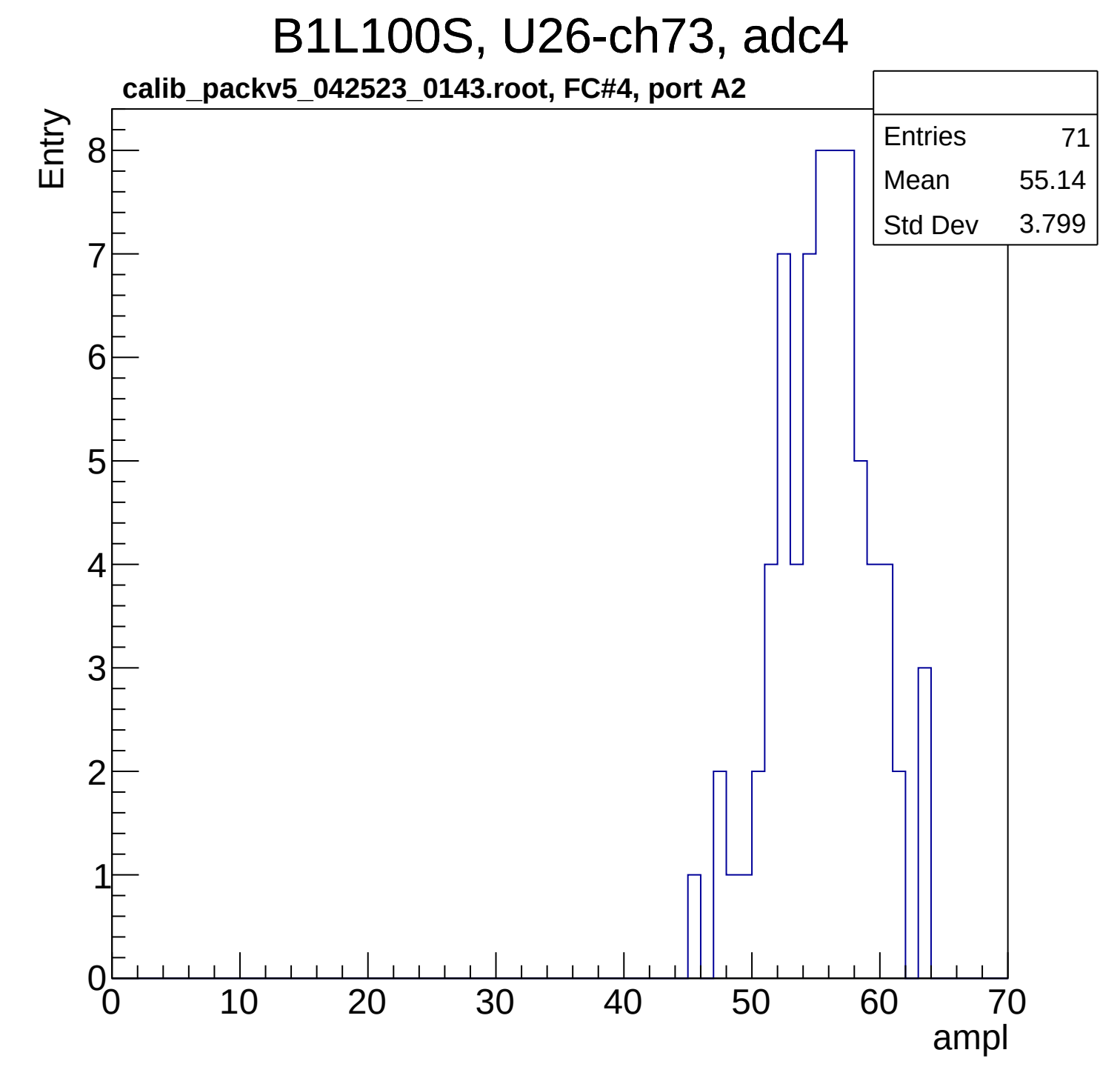
B1L100S, U26-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 55.14
Std Dev 3.799
Entry
ampl
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1
2
3
4
5
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7
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0
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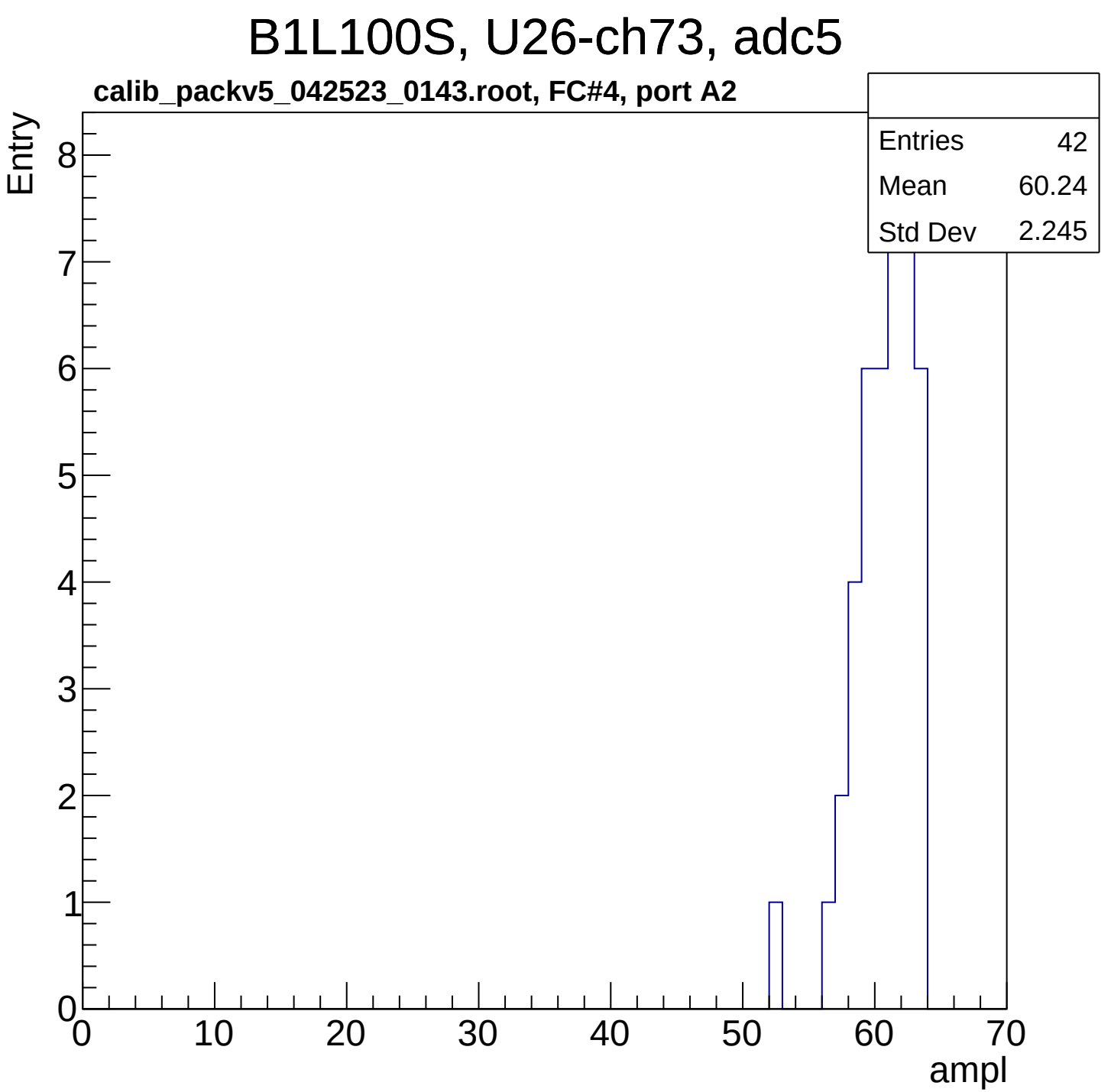
B1L100S, U26-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 60.24
Std Dev 2.245
Entry
ampl
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1
2
3
4
5
6
7
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0
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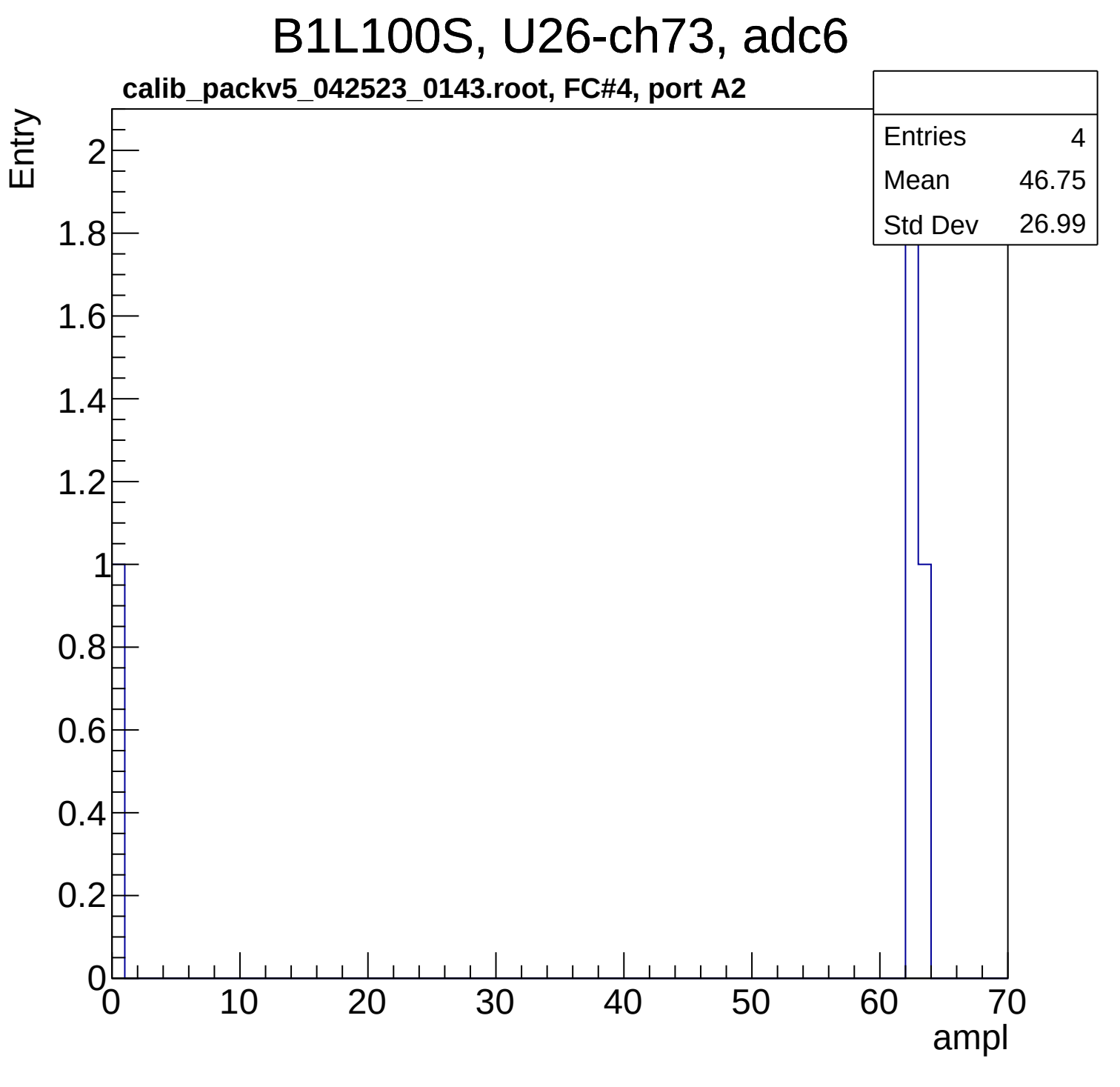
B1L100S, U26-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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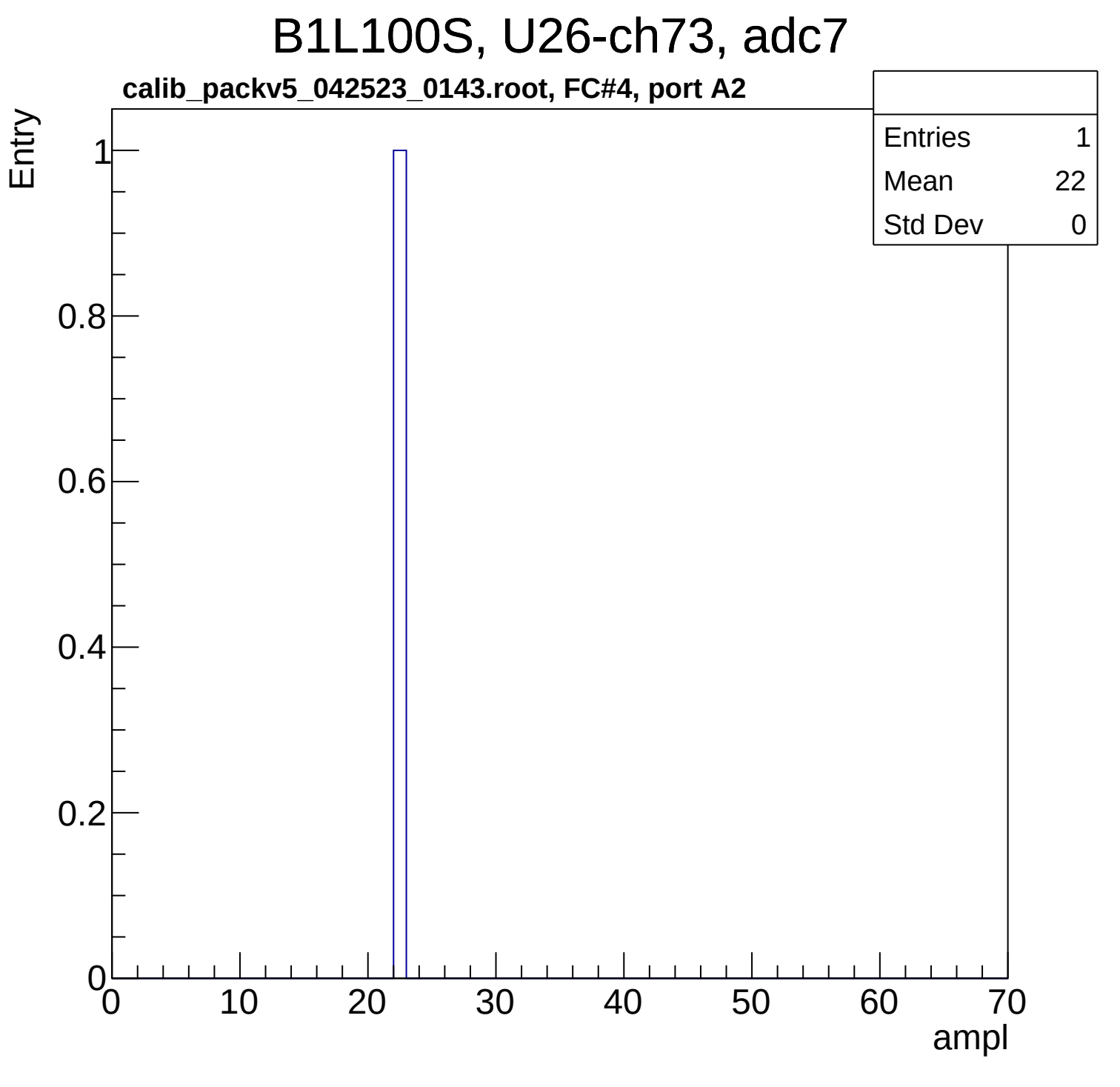
B1L100S, U26-ch73, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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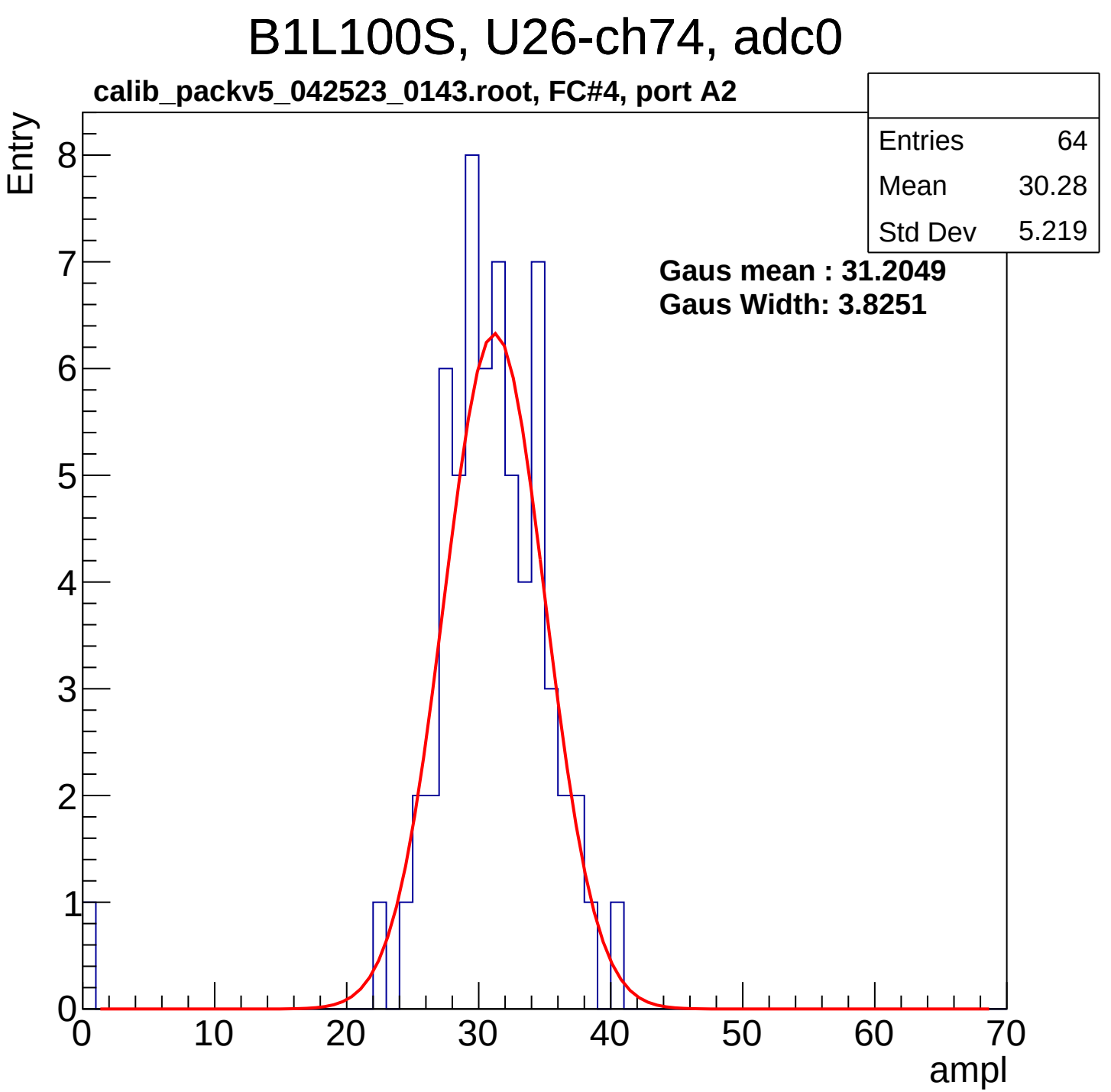
50
60
70
ampl


B1L100S, U26-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.28
Std Dev 5.219
Gaus mean : 31.2049
Gaus Width: 3.8251
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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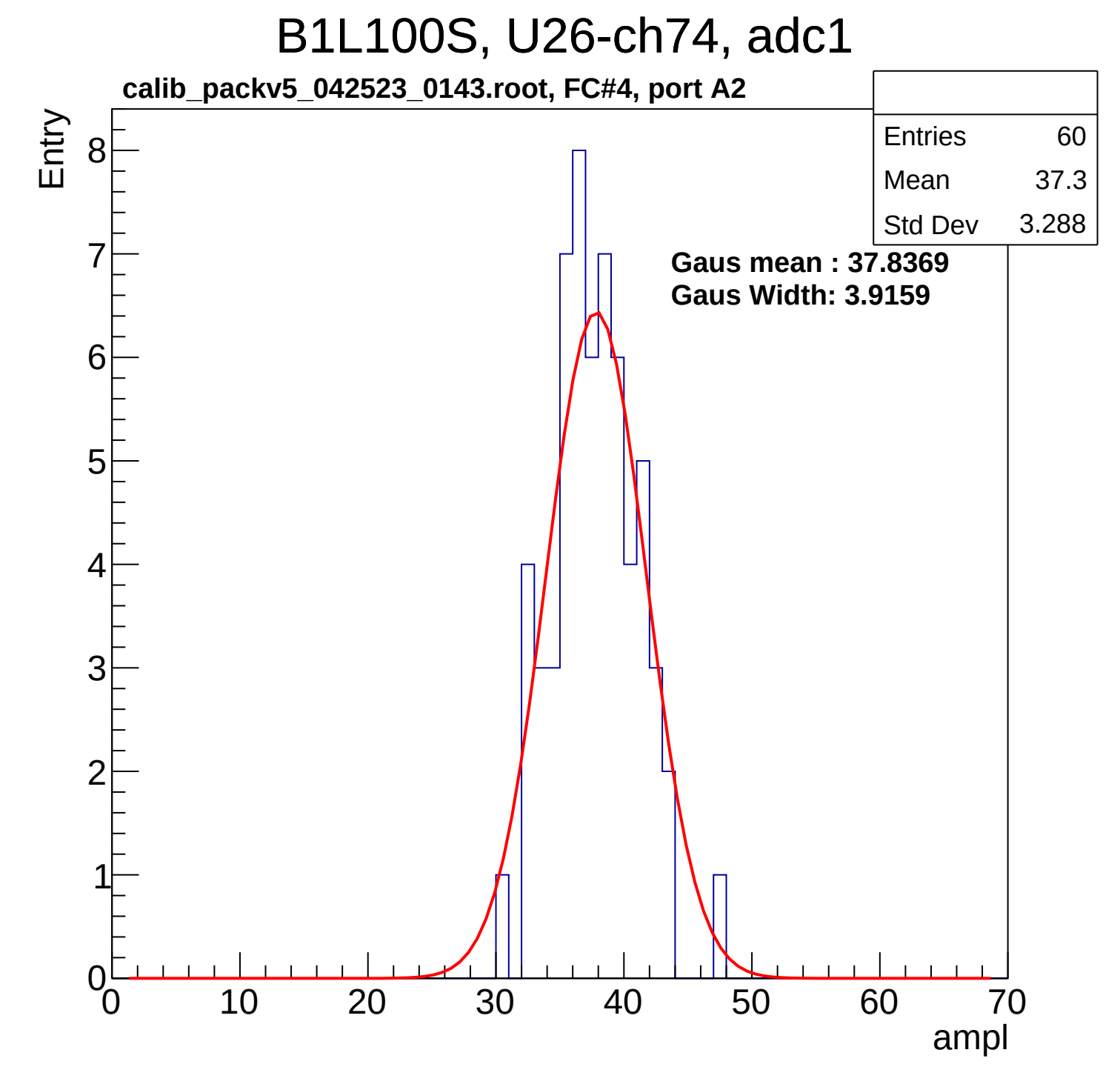
B1L100S, U26-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 37.3
Std Dev 3.288
Gaus mean : 37.8369
Gaus Width: 3.9159
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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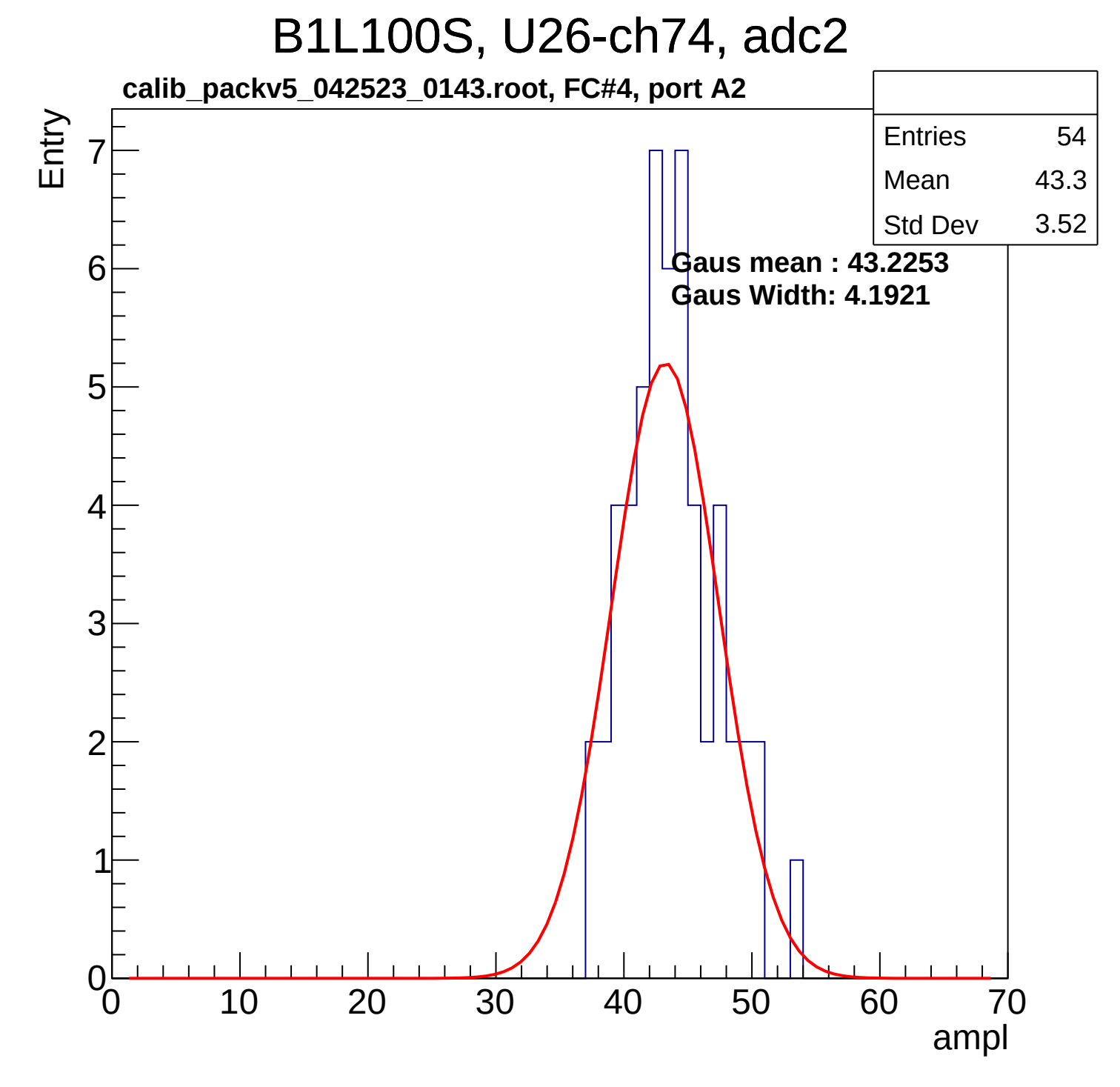
B1L100S, U26-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 43.3
Std Dev 3.52
Gaus mean : 43.2253
Gaus Width: 4.1921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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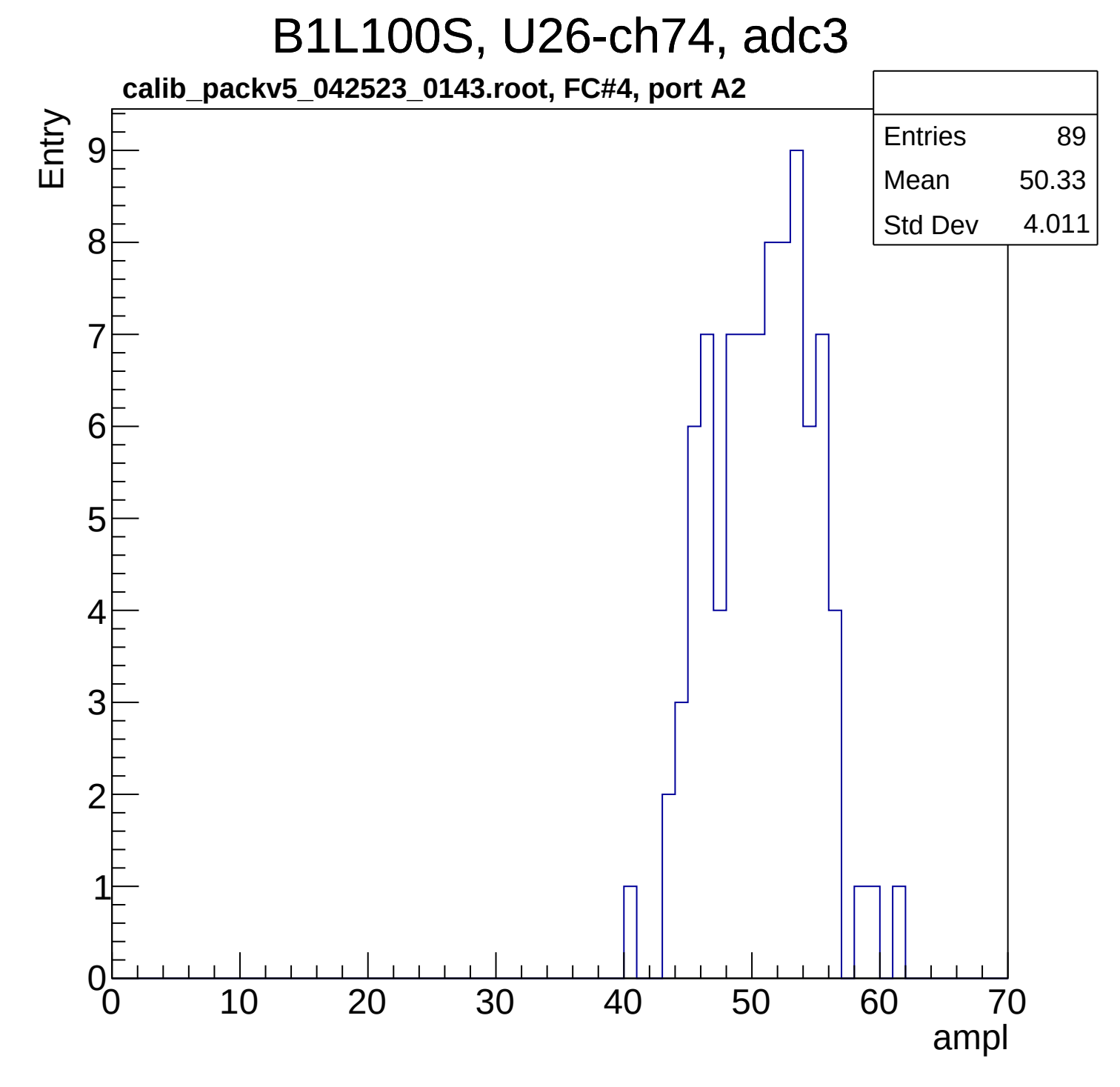
B1L100S, U26-ch74, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 50.33
Std Dev 4.011
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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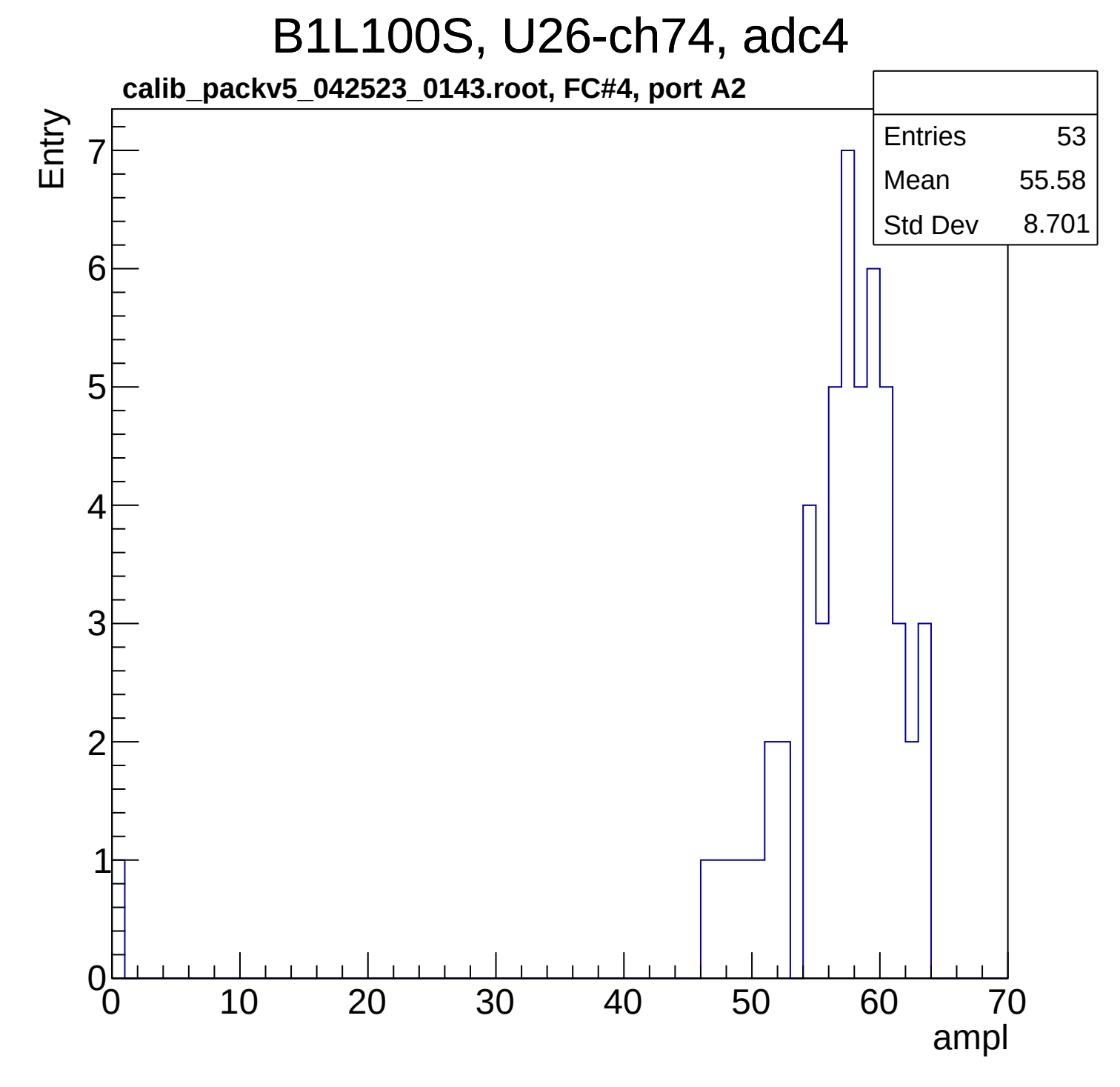
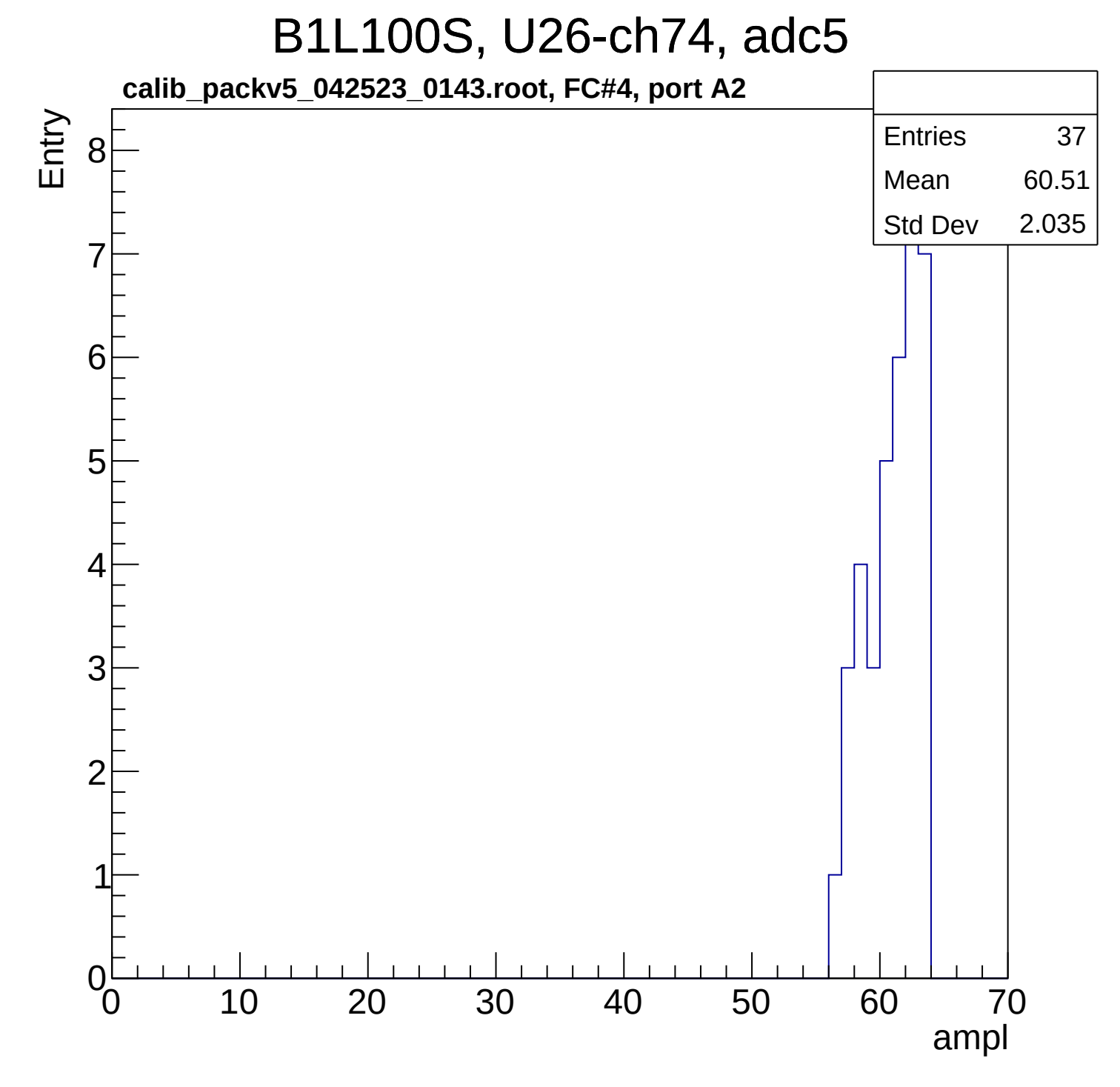
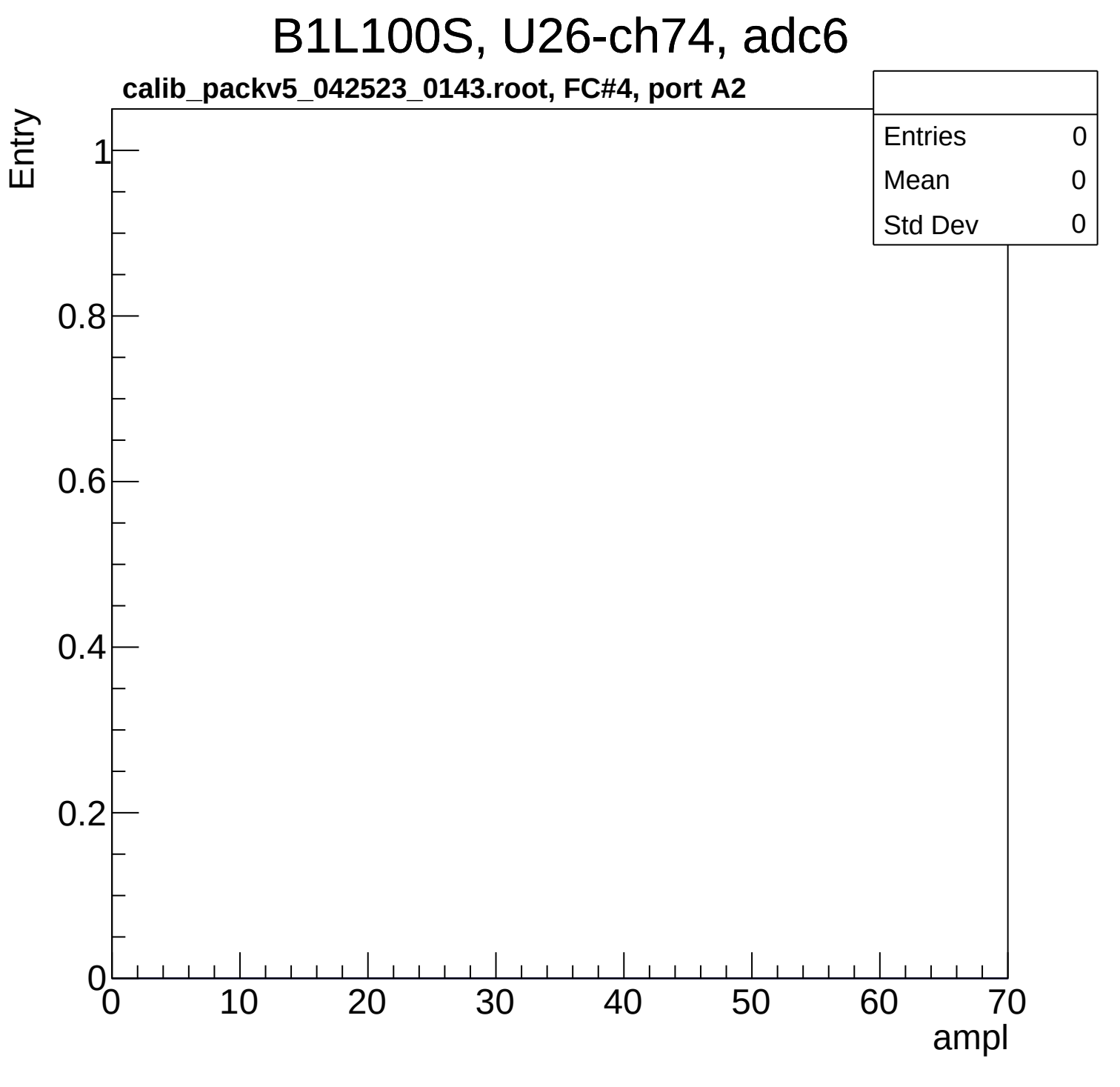
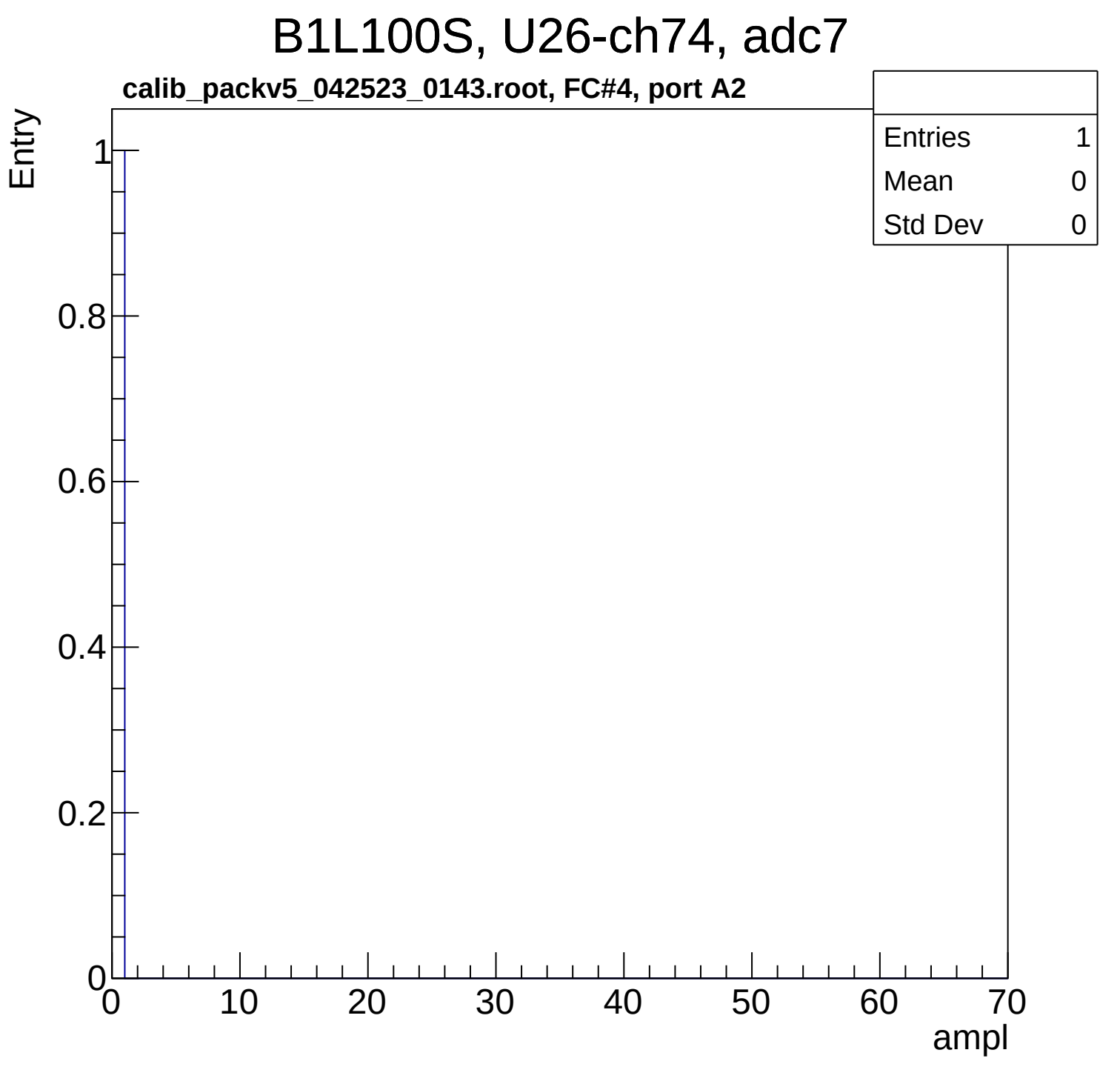
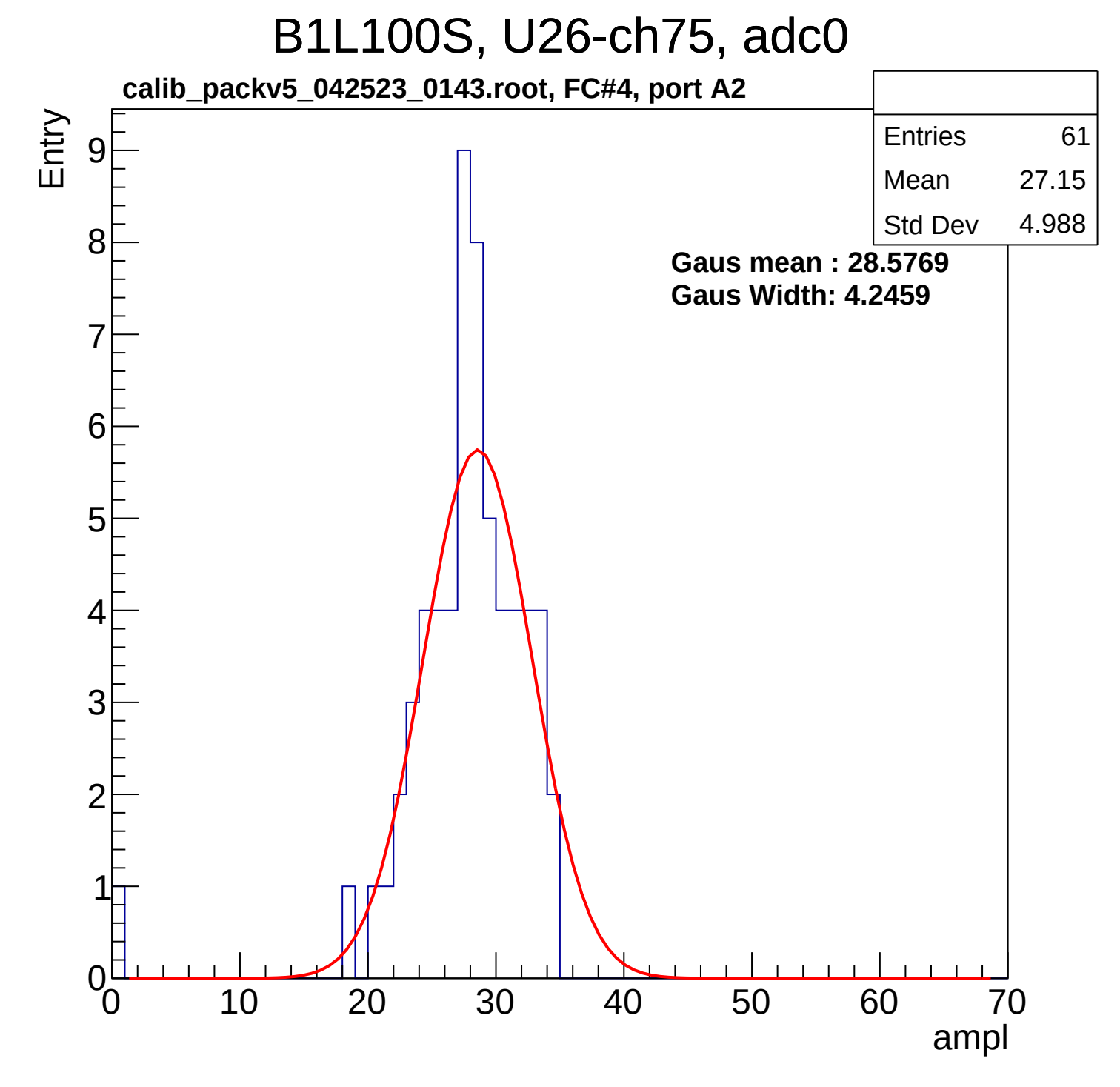
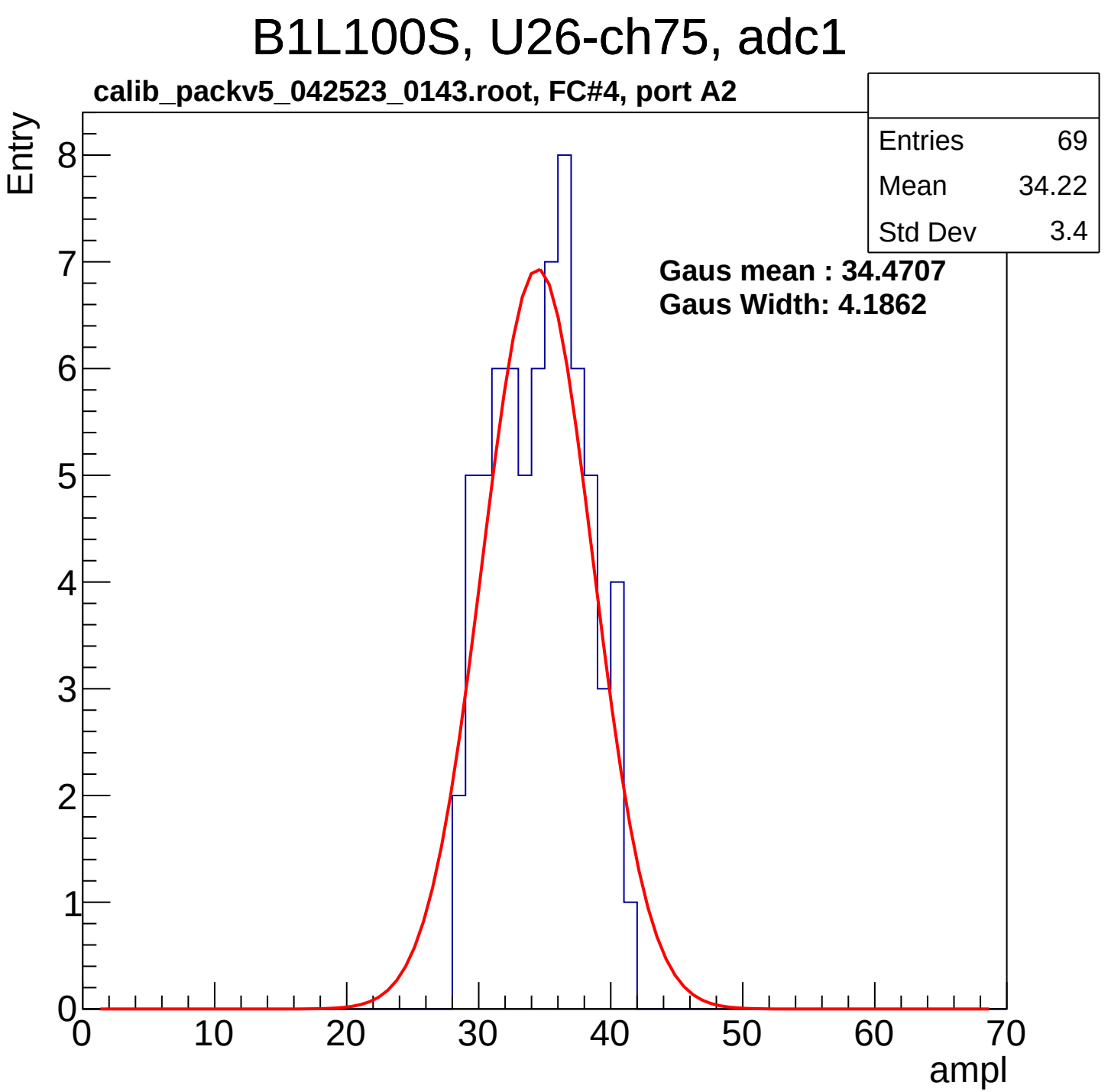

B1L100S, U26-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.58
Std Dev 8.701
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.51
Std Dev 2.035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 27.15
Std Dev 4.988
Gaus mean : 28.5769
Gaus Width: 4.2459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 34.22
Std Dev 3.4
Gaus mean : 34.4707
Gaus Width: 4.1862
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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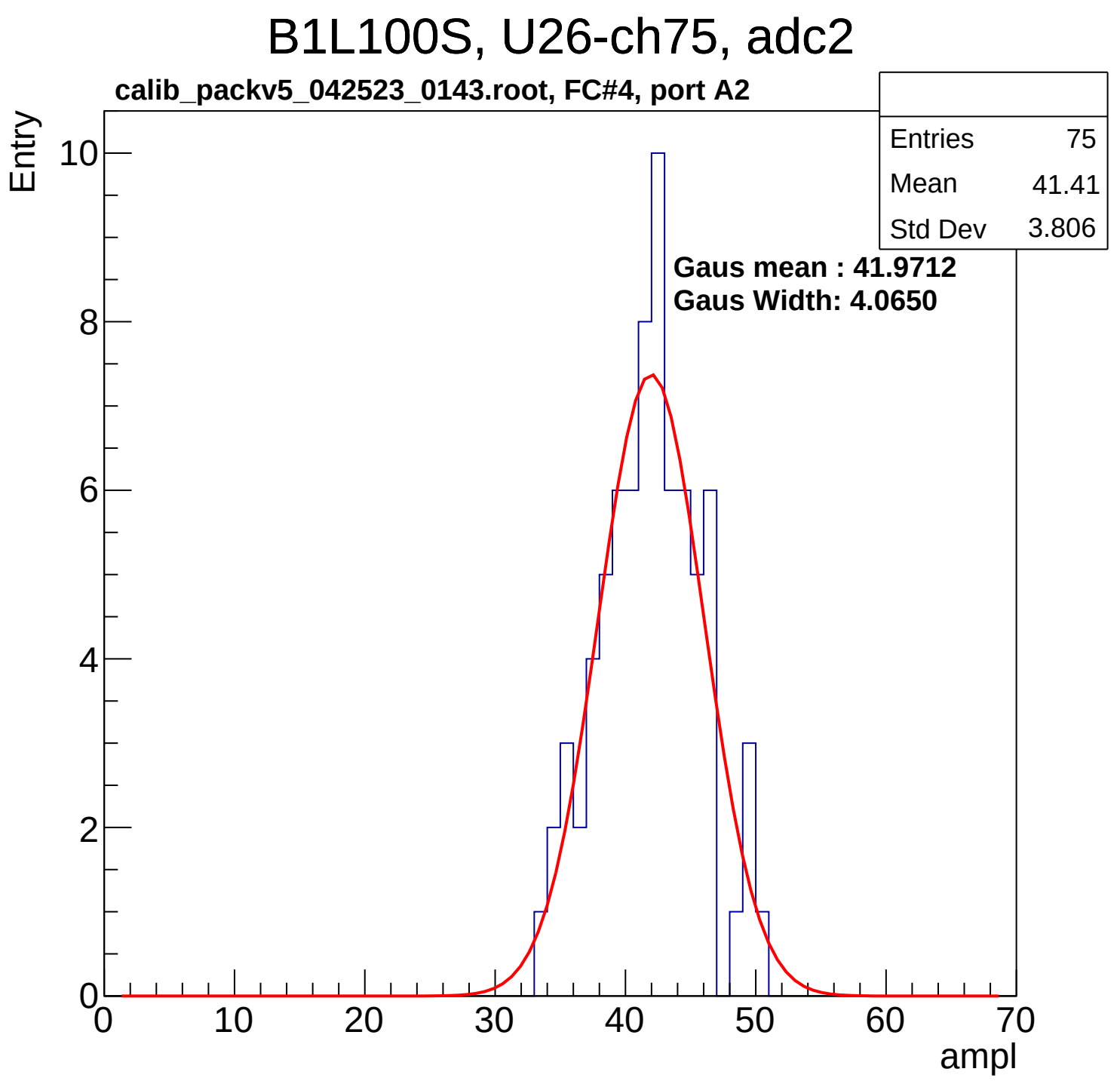
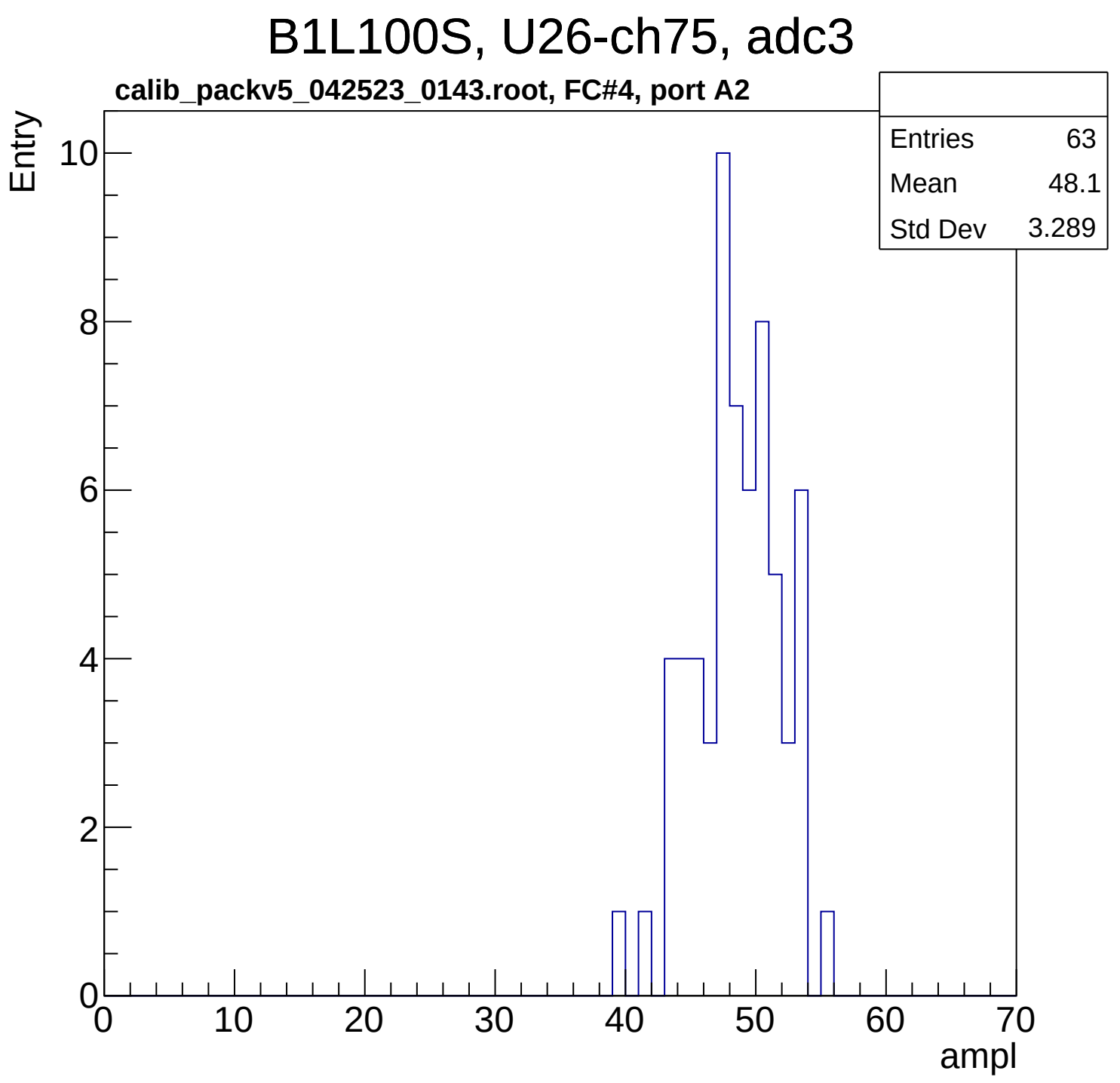
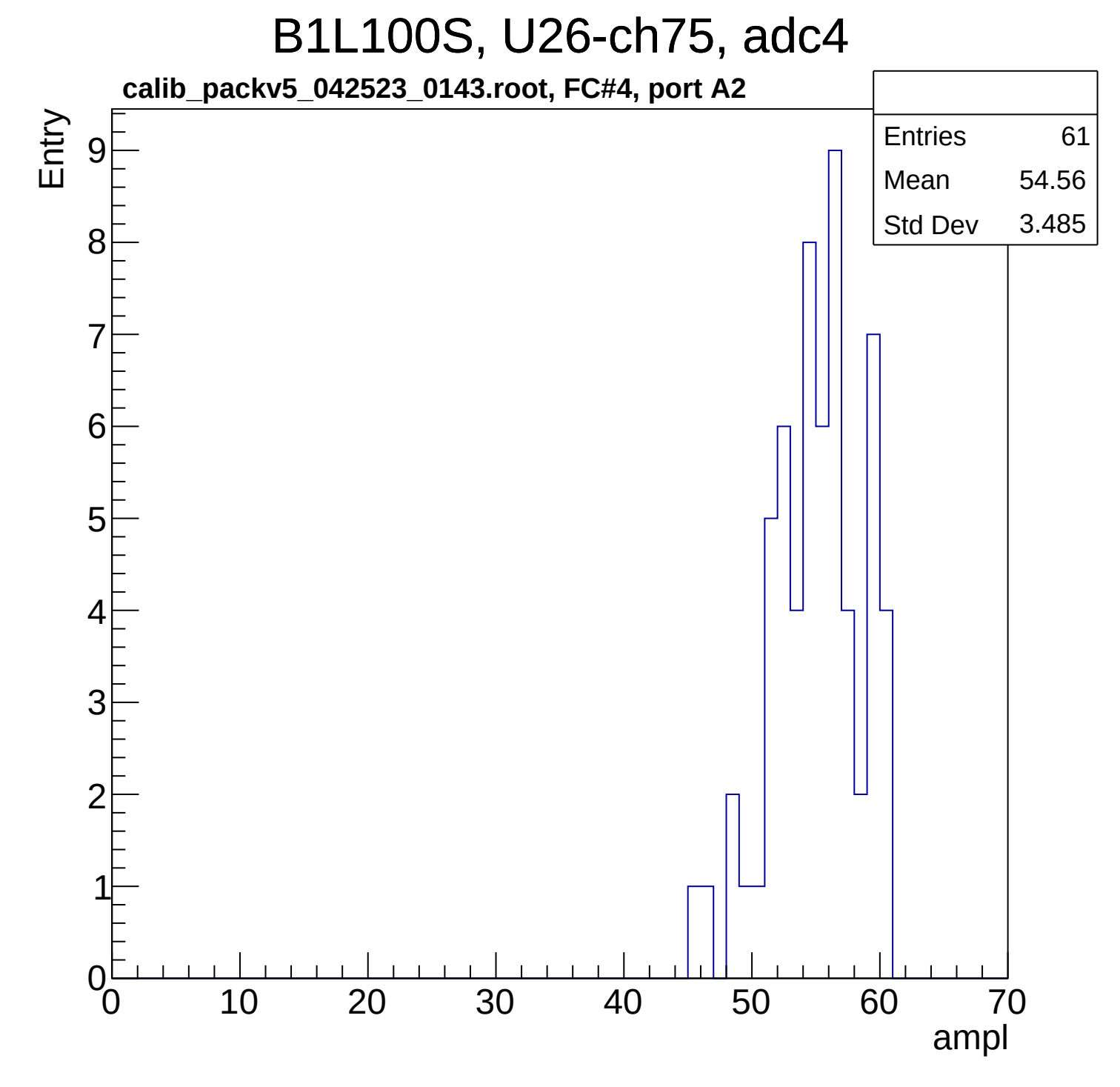
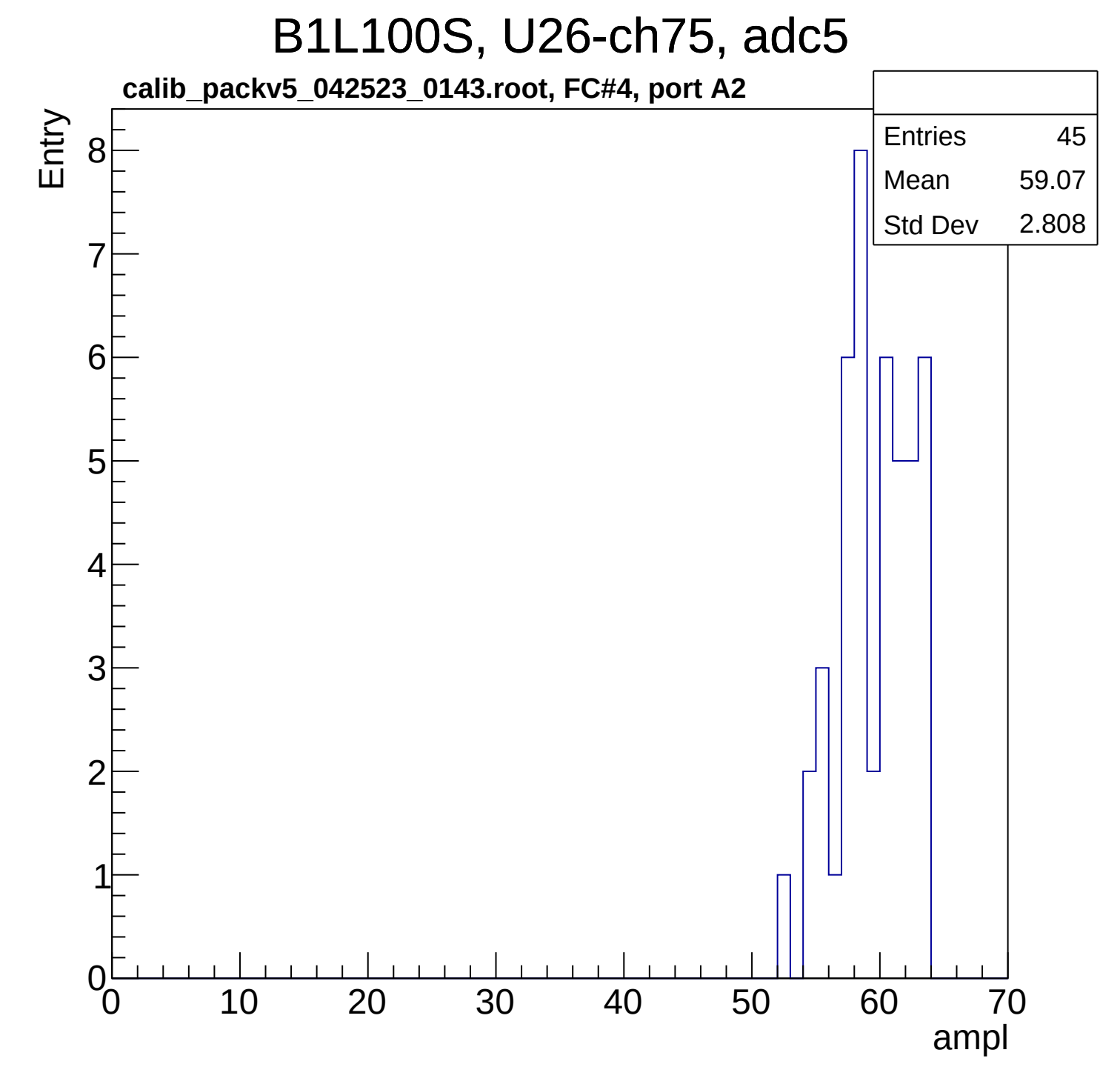
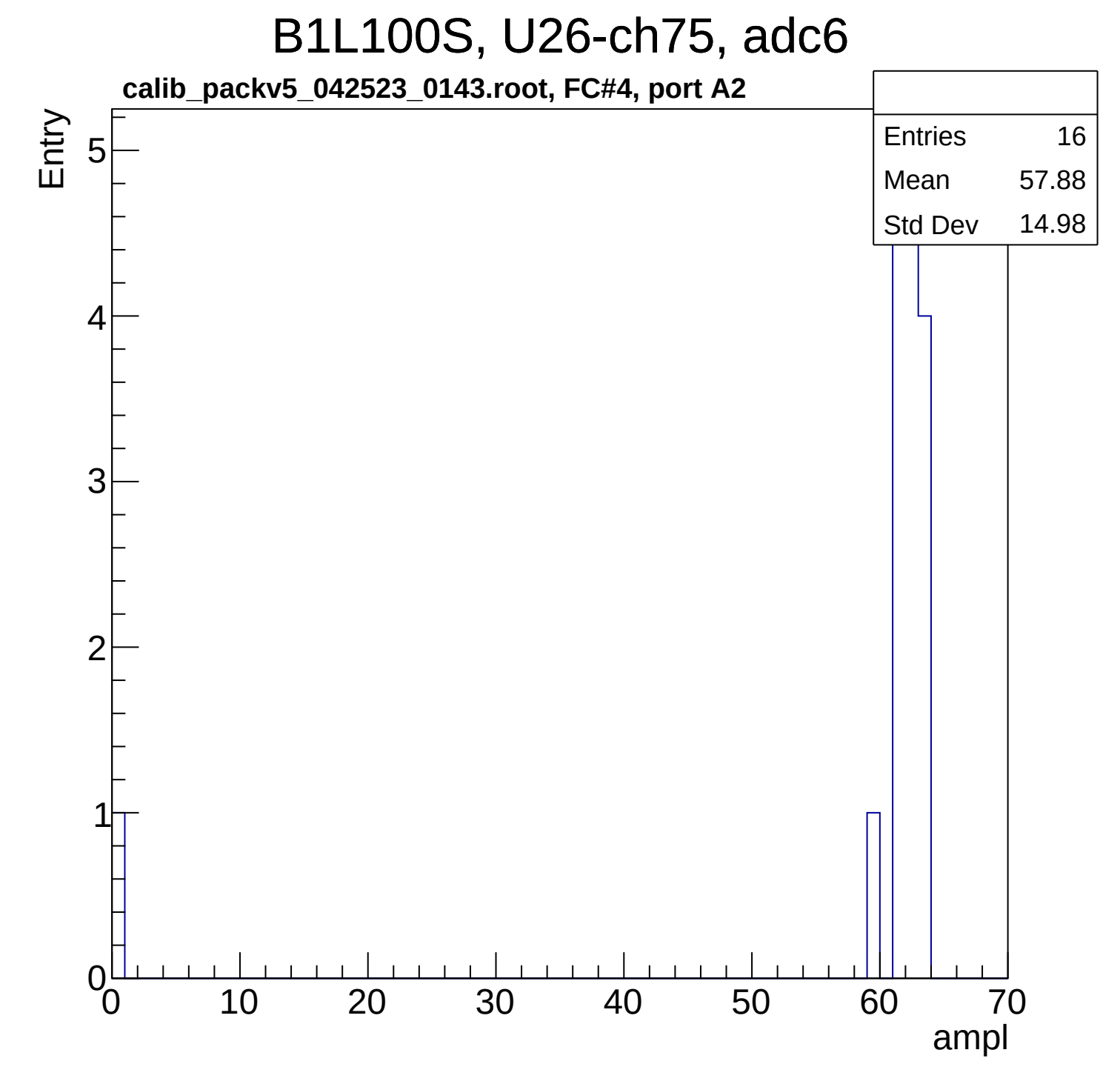
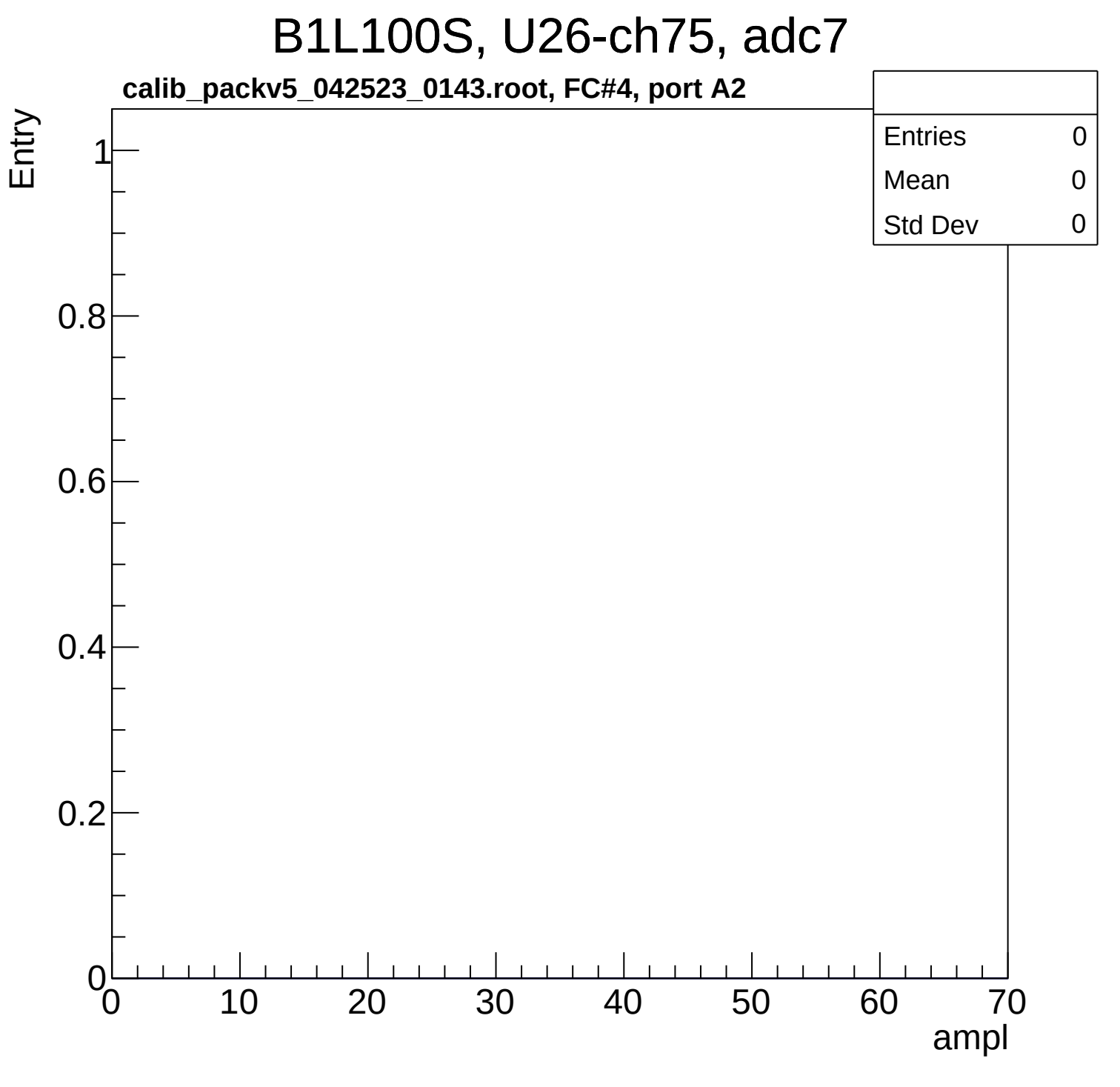

B1L100S, U26-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 41.41
Std Dev 3.806
Gaus mean : 41.9712
Gaus Width: 4.0650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

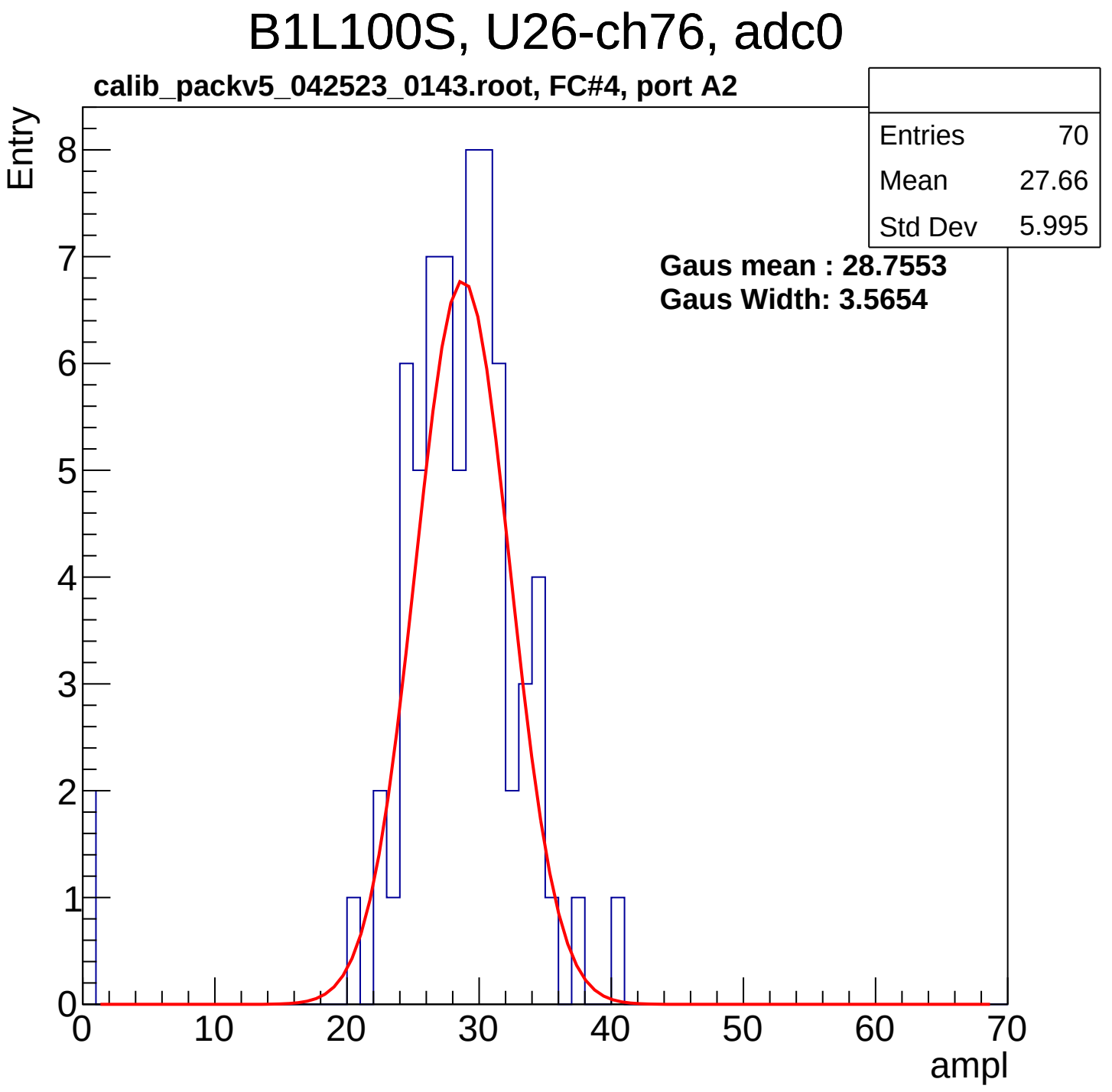

B1L100S, U26-ch75, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.1
Std Dev 3.289
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 54.56
Std Dev 3.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.07
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch75, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 57.88
Std Dev 14.98
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 27.66
Std Dev 5.995
Gaus mean : 28.7553
Gaus Width: 3.5654
Entry
ampl
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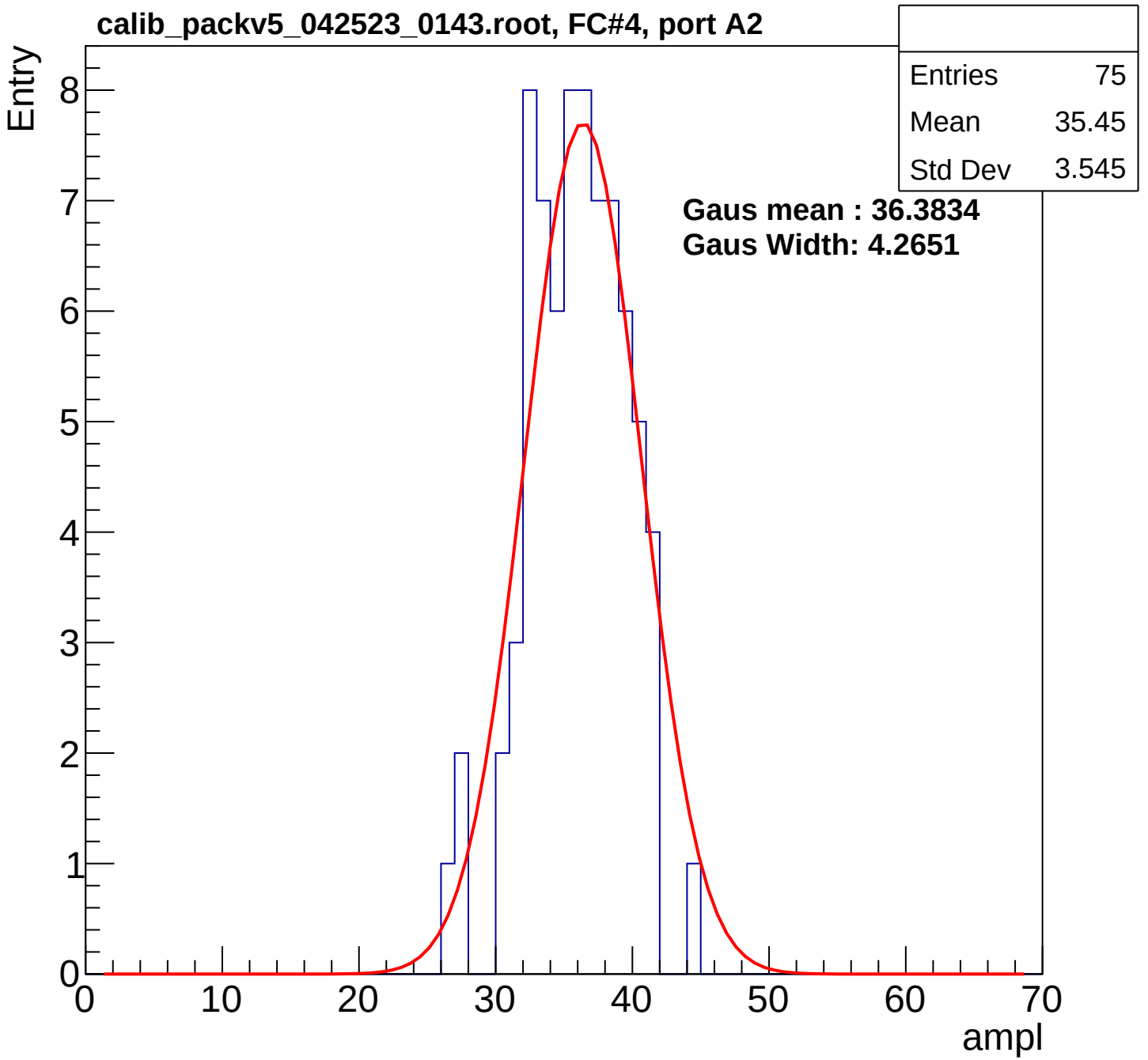
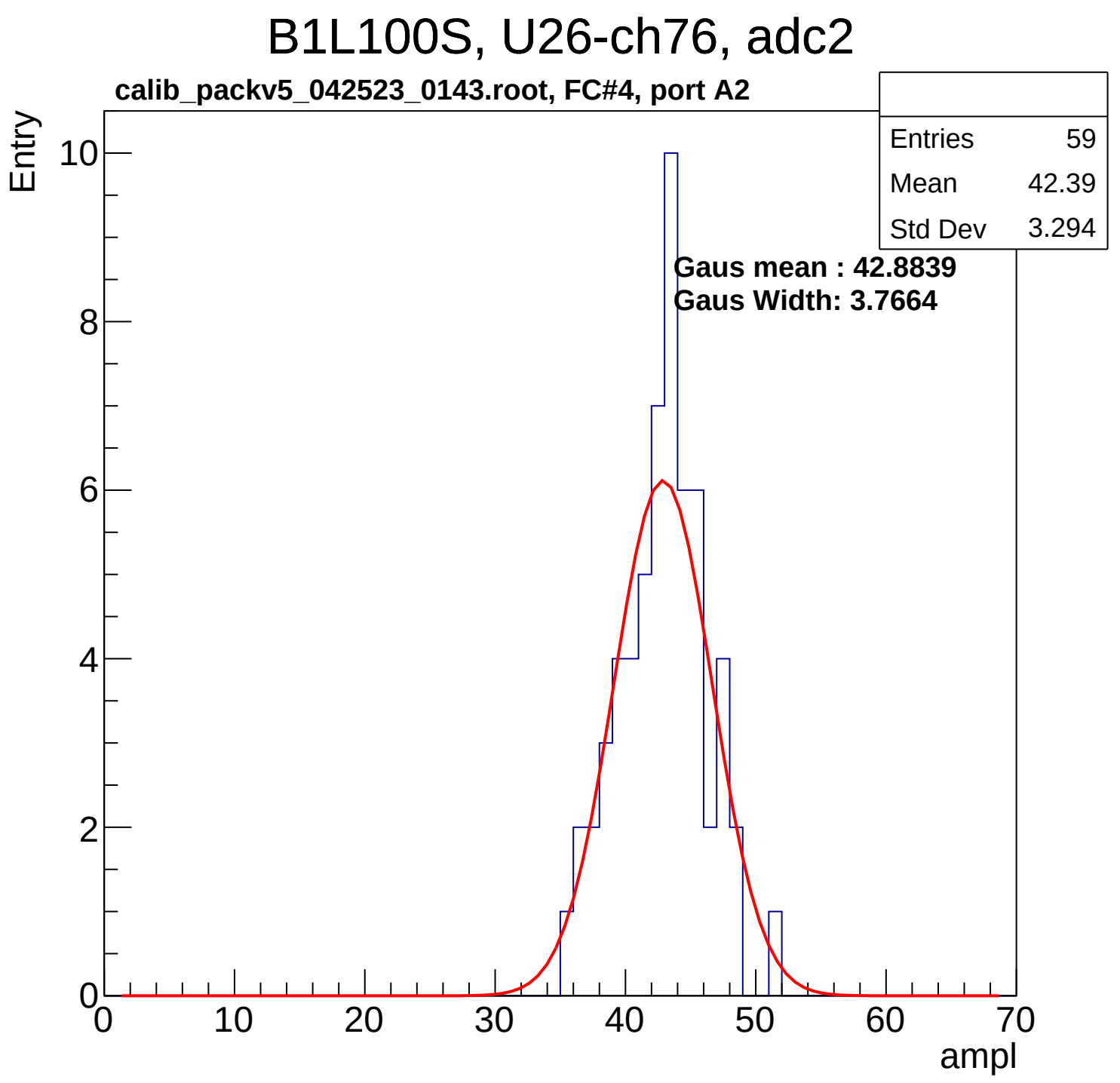
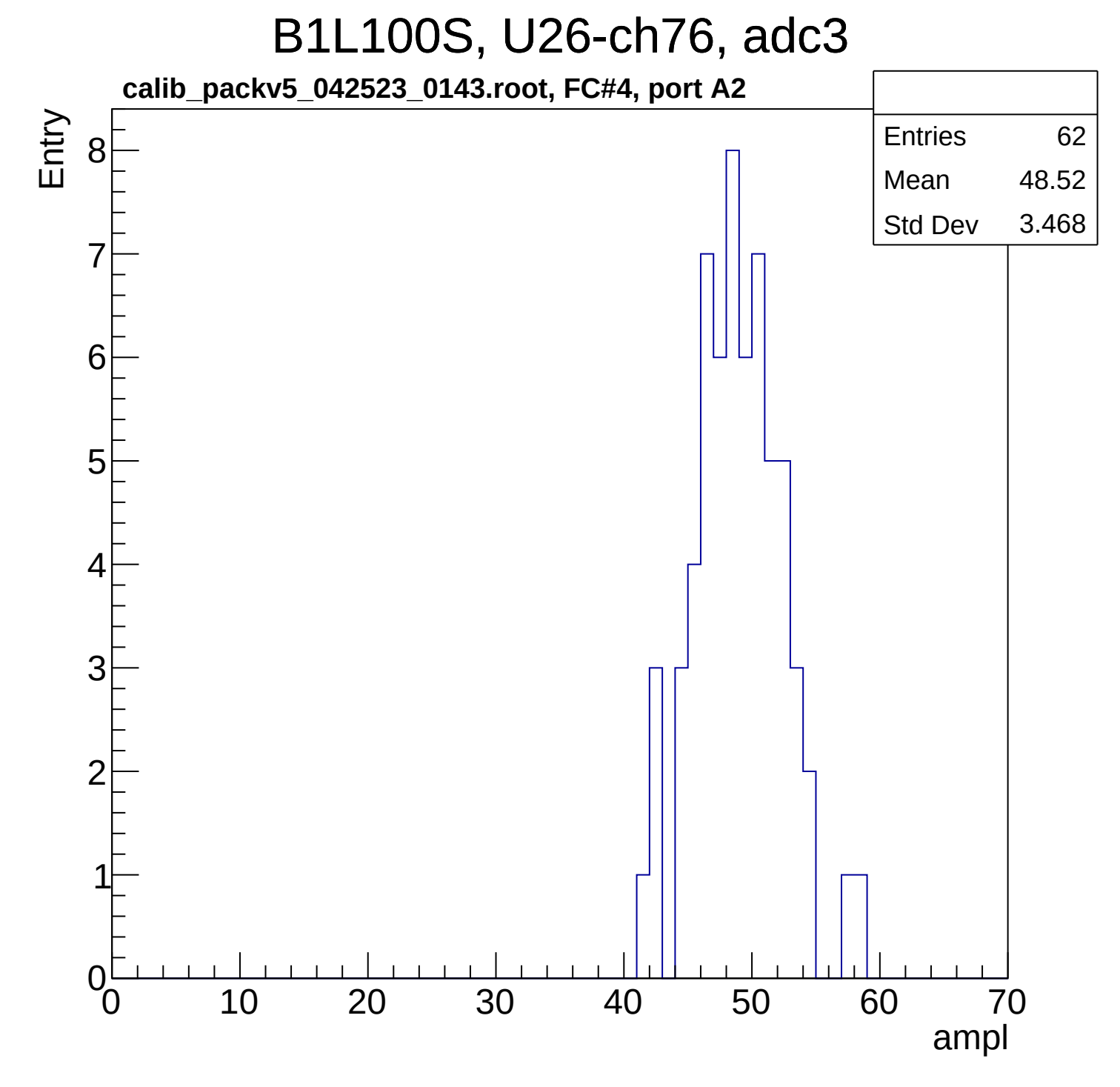
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.45
Std Dev 3.545
Gaus mean : 36.3834
Gaus Width: 4.2651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.39
Std Dev 3.294
Gaus mean : 42.8839
Gaus Width: 3.7664
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 48.52
Std Dev 3.468
Entry
ampl
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2
3
4
5
6
7
8
0
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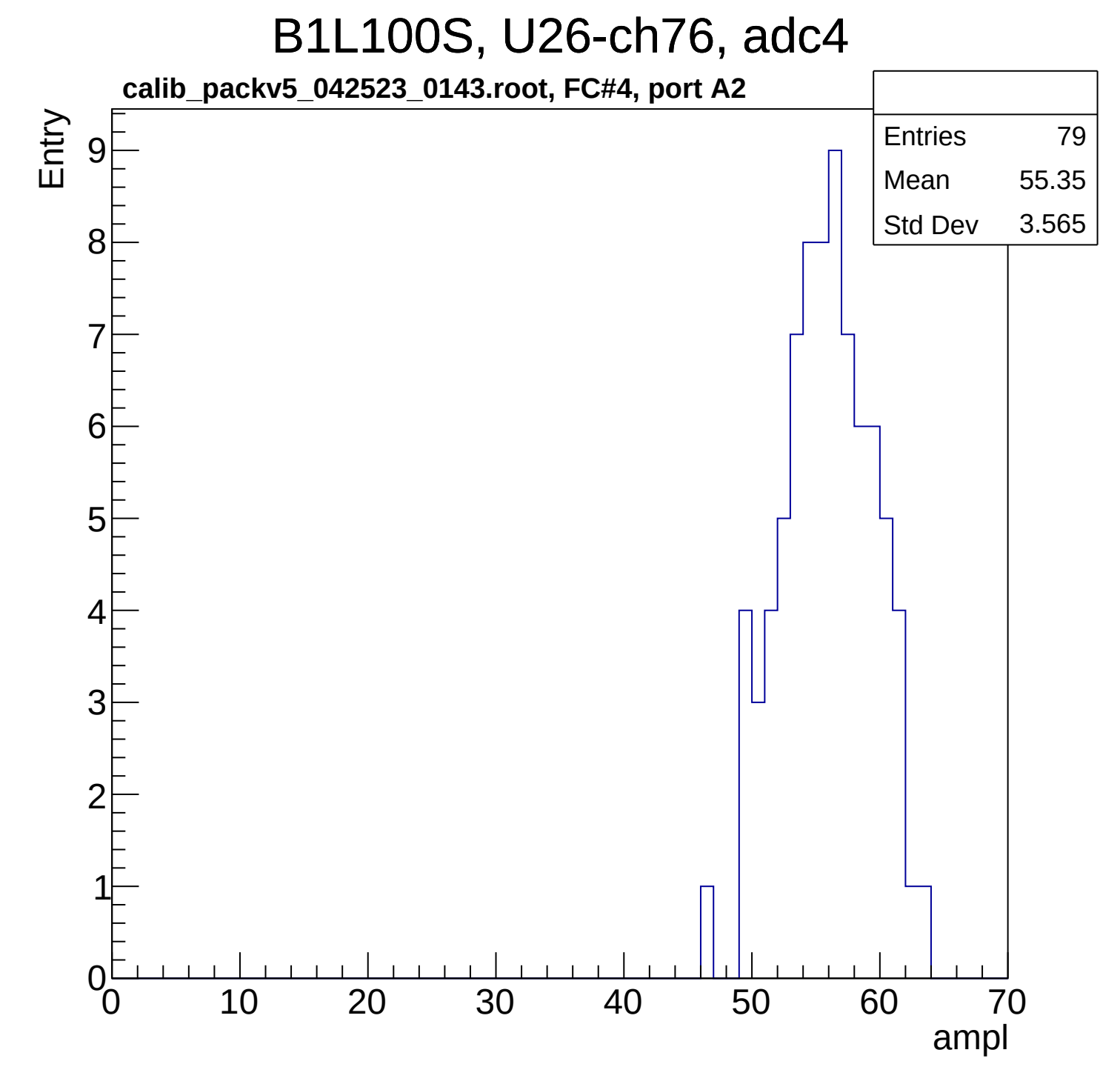
40
50
60
70


B1L100S, U26-ch76, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 55.35
Std Dev 3.565
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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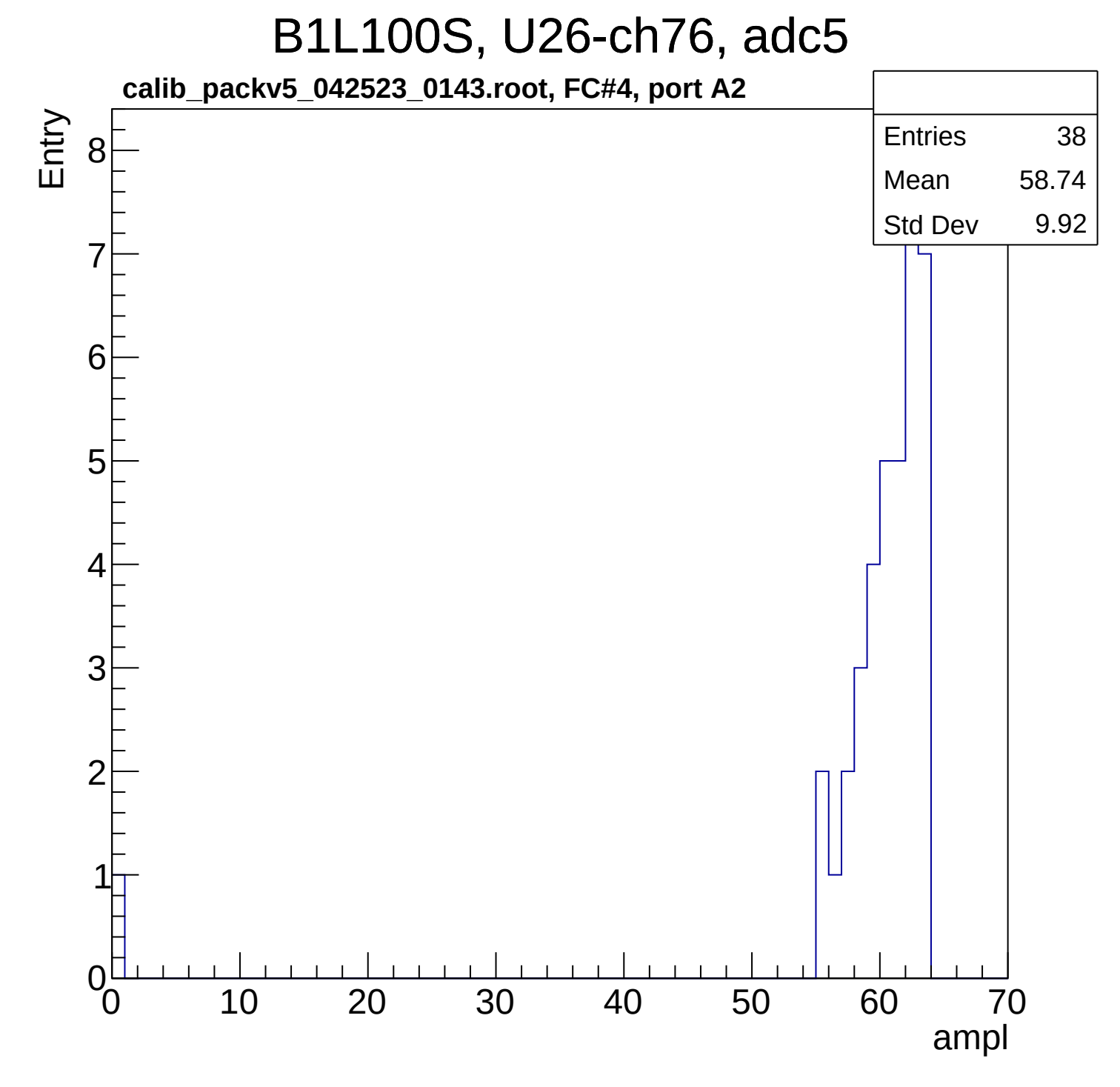
B1L100S, U26-ch76, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.74
Std Dev 9.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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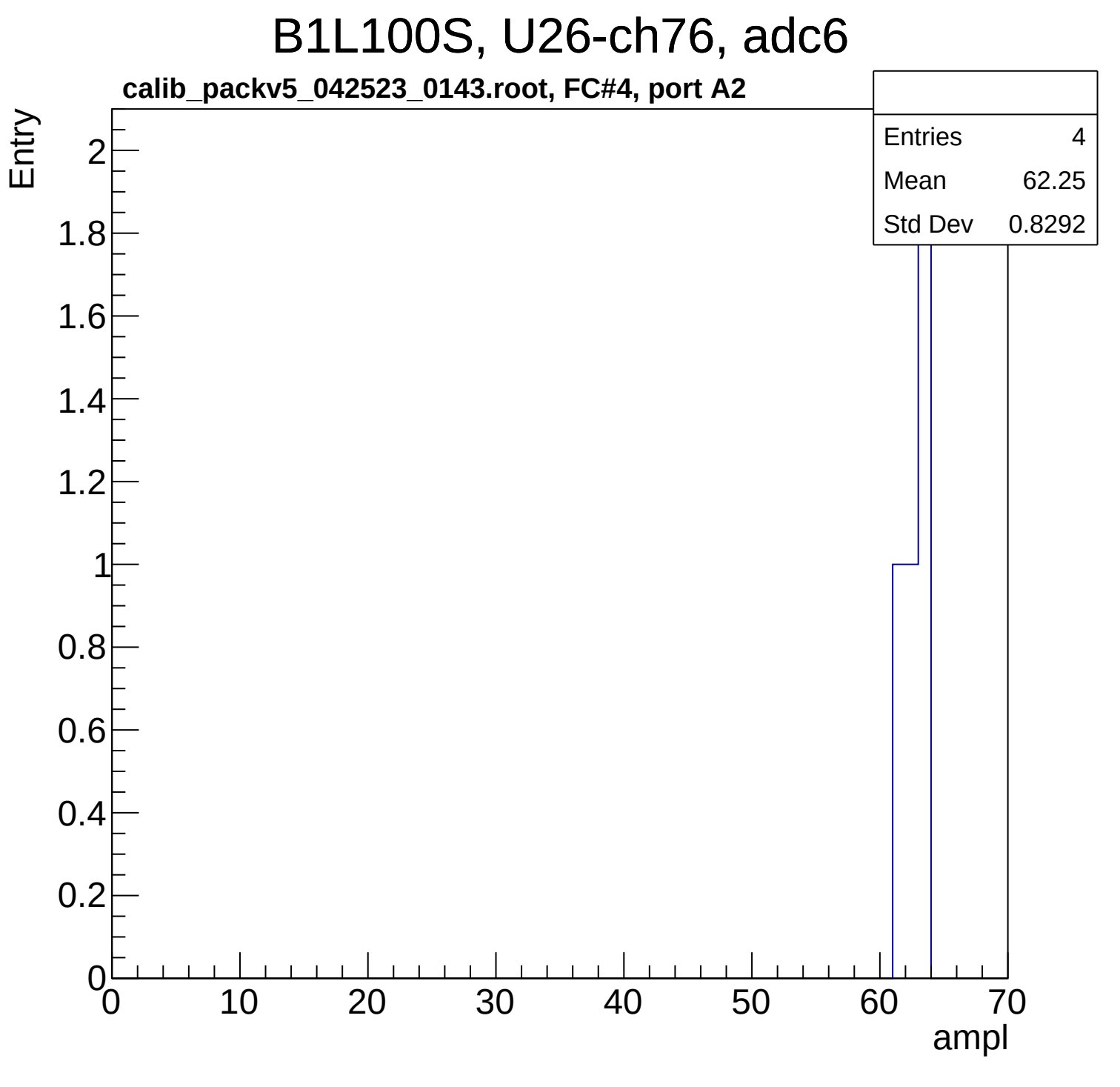
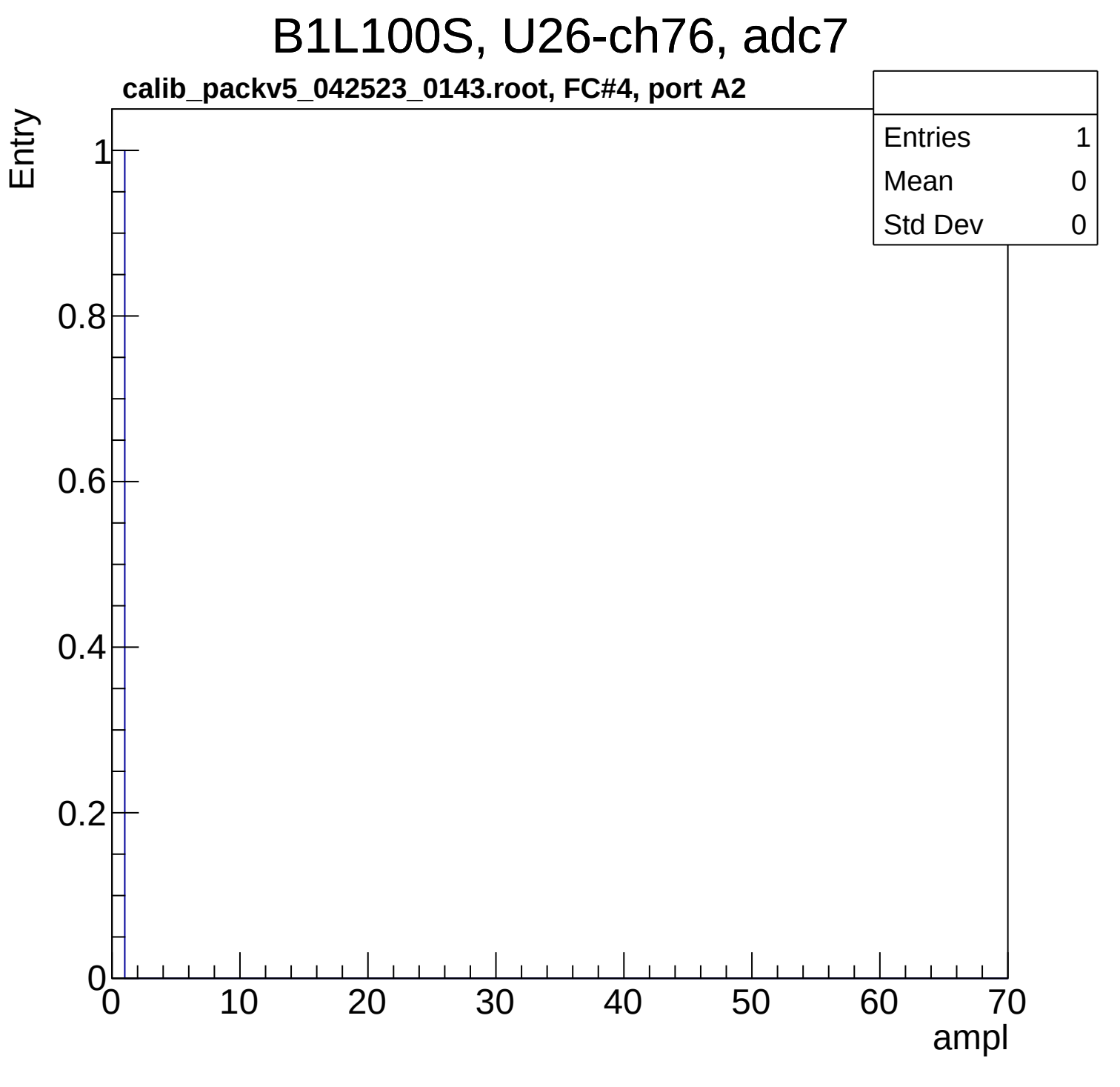
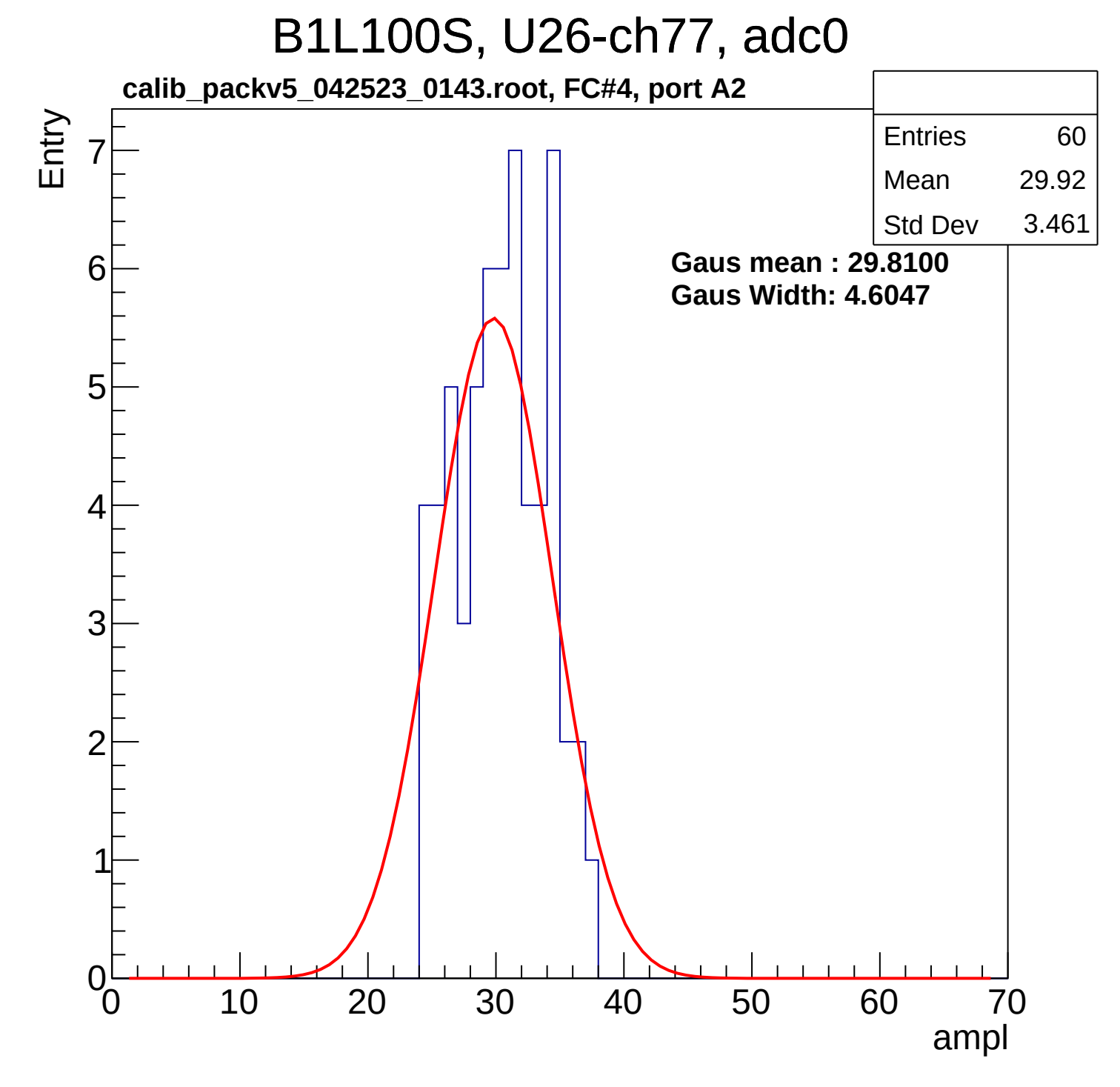
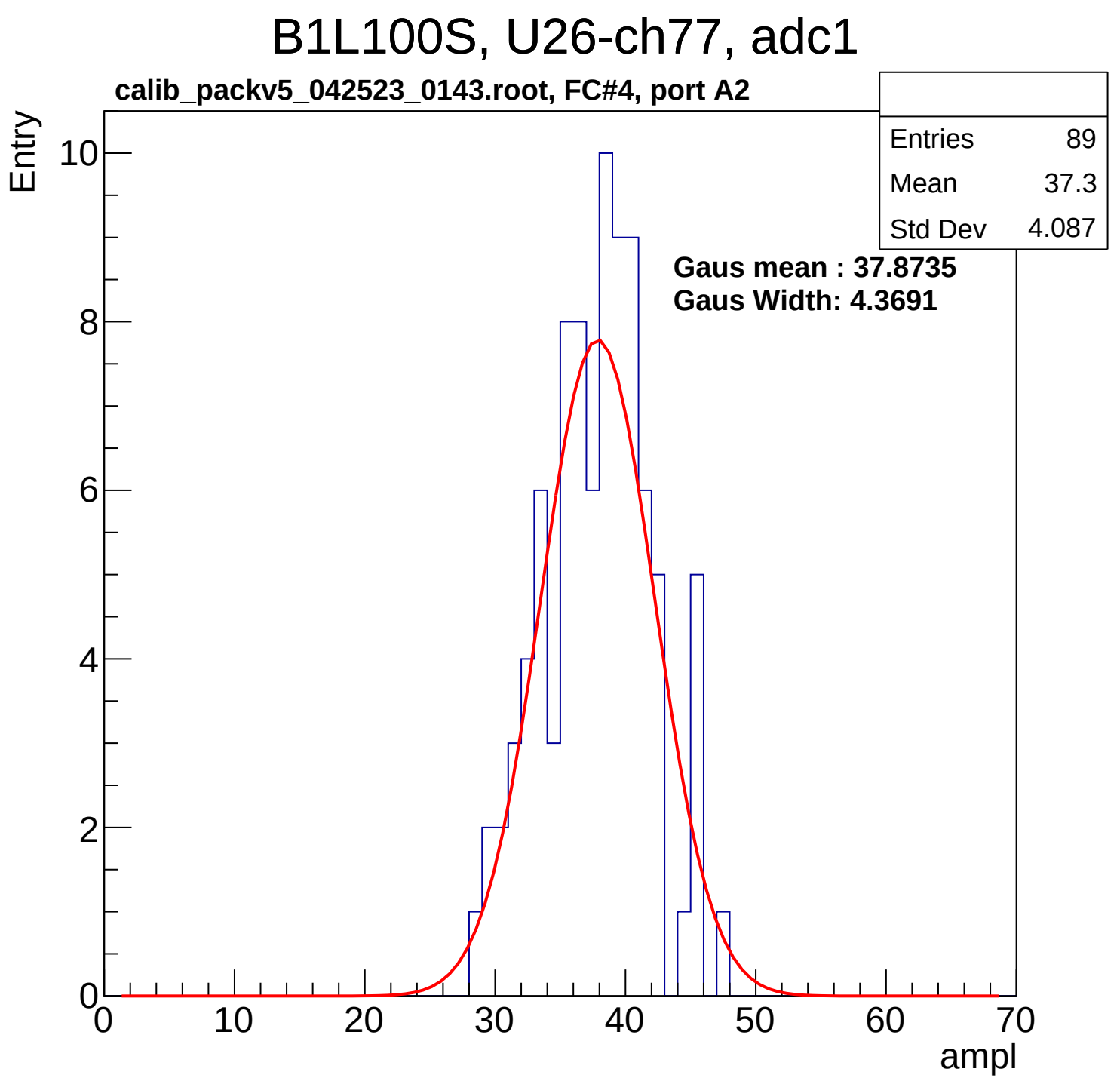
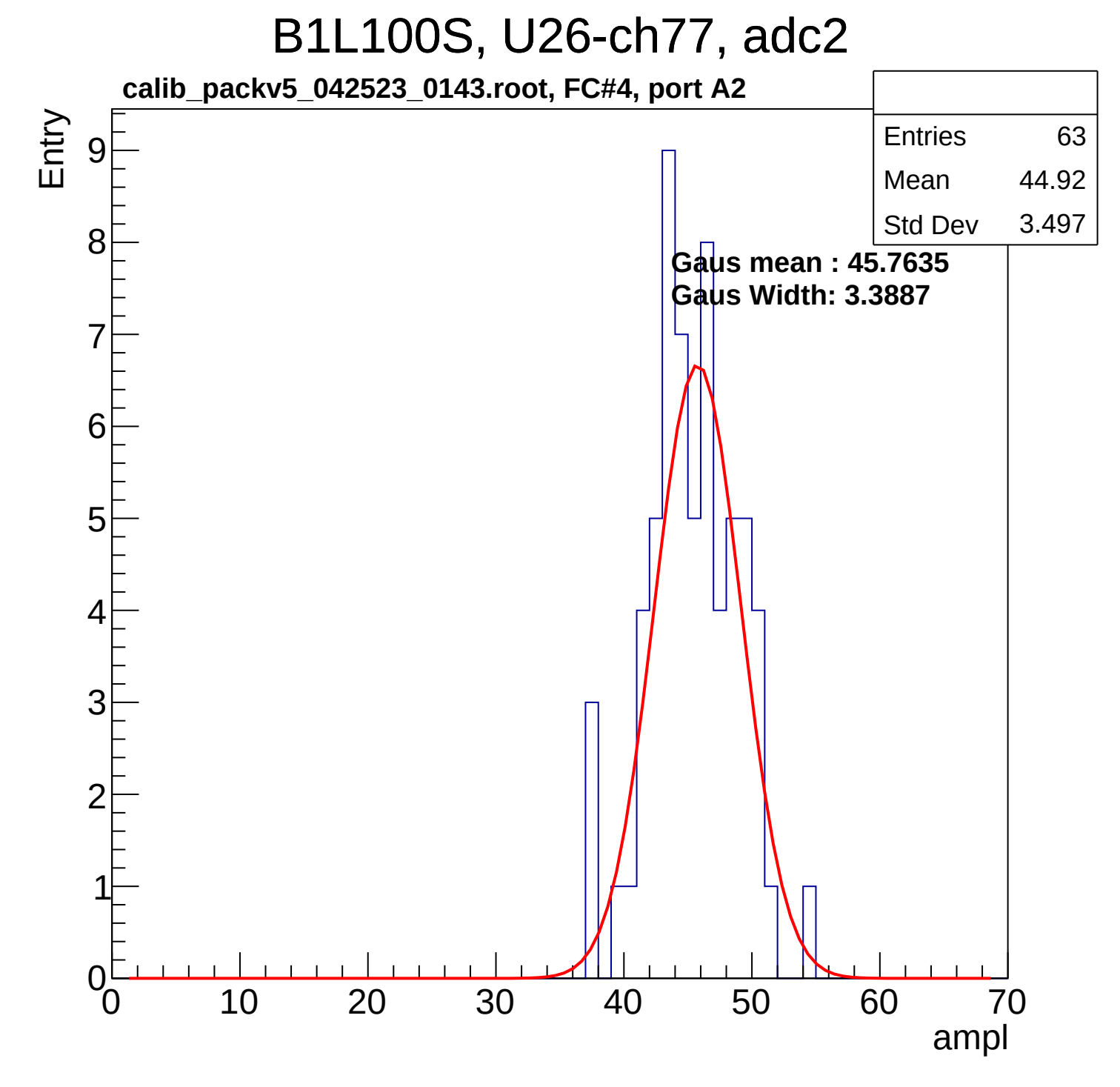

B1L100S, U26-ch76, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch76, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.92
Std Dev 3.461
Gaus mean : 29.8100
Gaus Width: 4.6047
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch77, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 37.3
Std Dev 4.087
Gaus mean : 37.8735
Gaus Width: 4.3691
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.92
Std Dev 3.497
Gaus mean : 45.7635
Gaus Width: 3.3887
Entry
ampl
0
1
2
3
4
5
6
7
8
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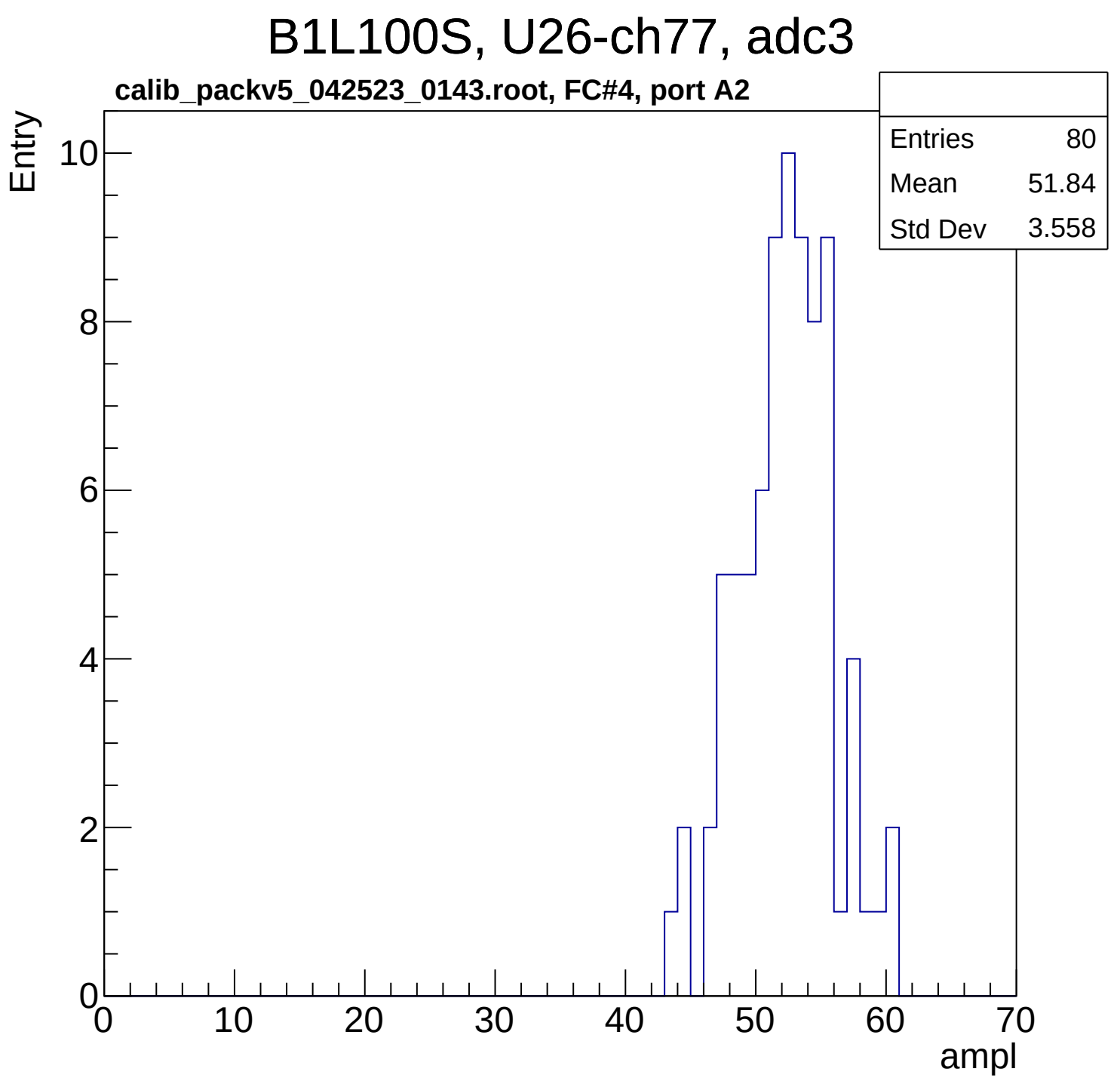
B1L100S, U26-ch77, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 51.84
Std Dev 3.558
Entry
0
2
4
6
8
10
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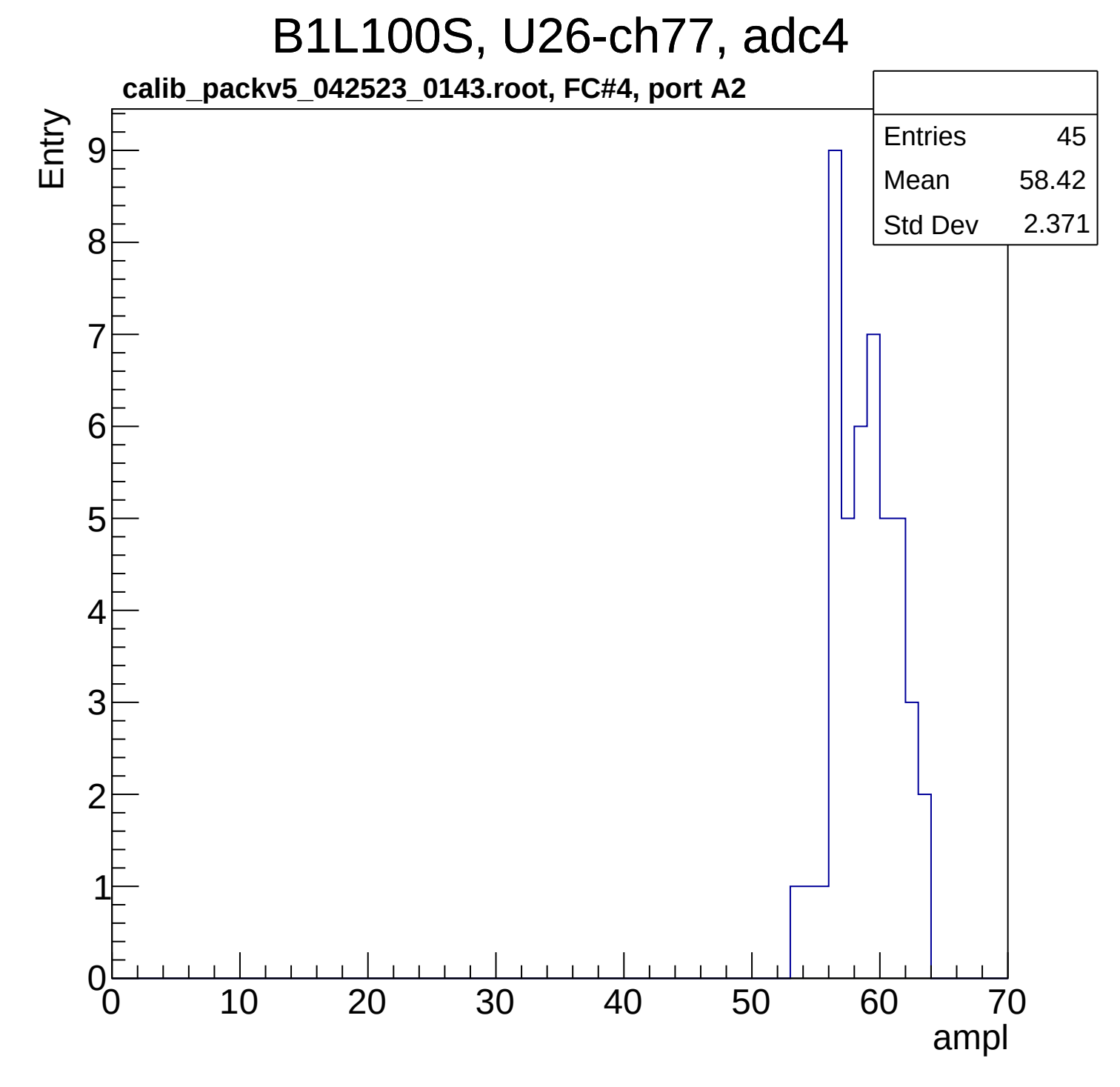
70
ampl


B1L100S, U26-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.42
Std Dev 2.371
Entry
ampl
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1
2
3
4
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6
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0
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60
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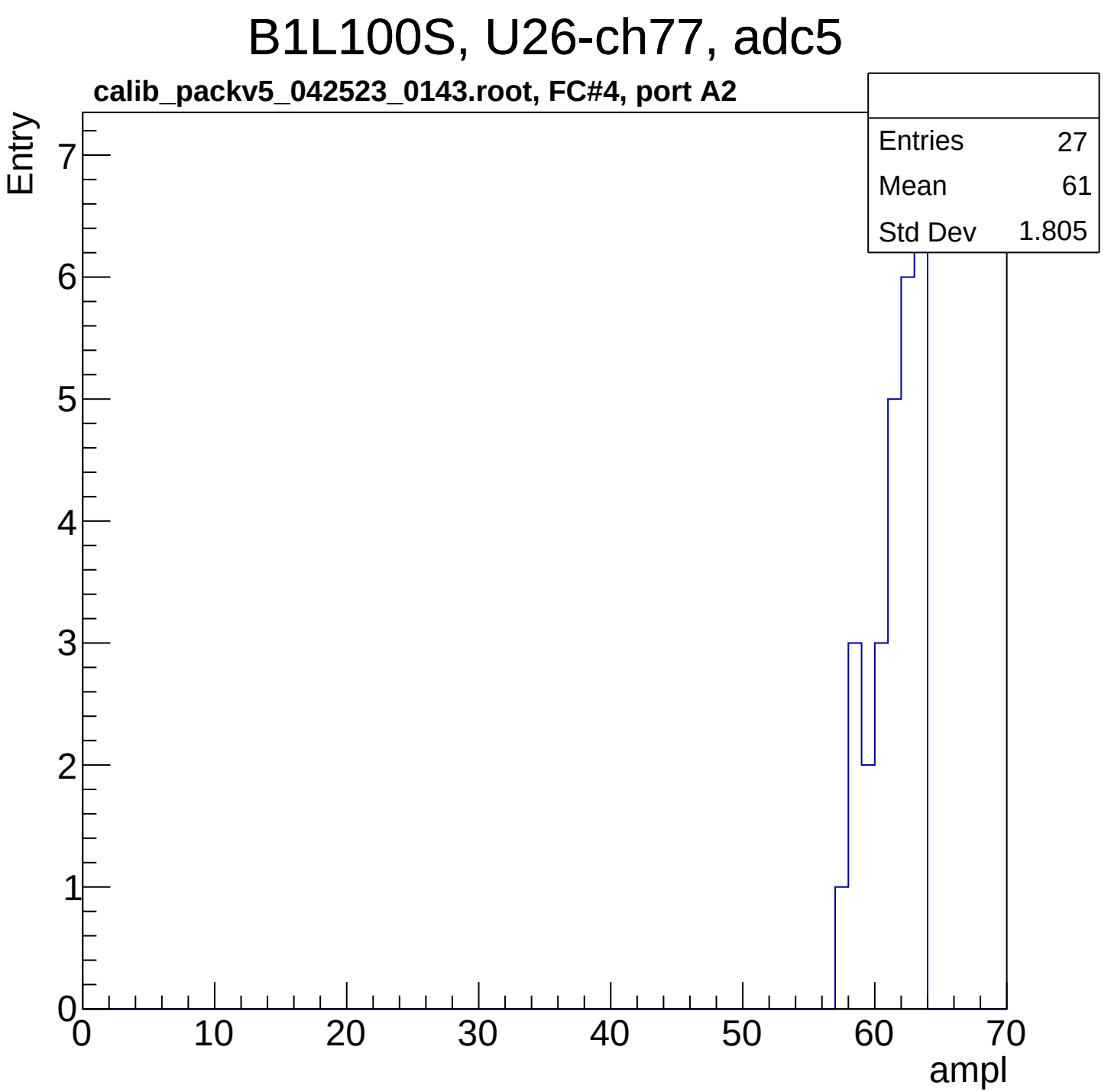
B1L100S, U26-ch77, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 61
Std Dev 1.805
Entry
ampl
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1
2
3
4
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6
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0
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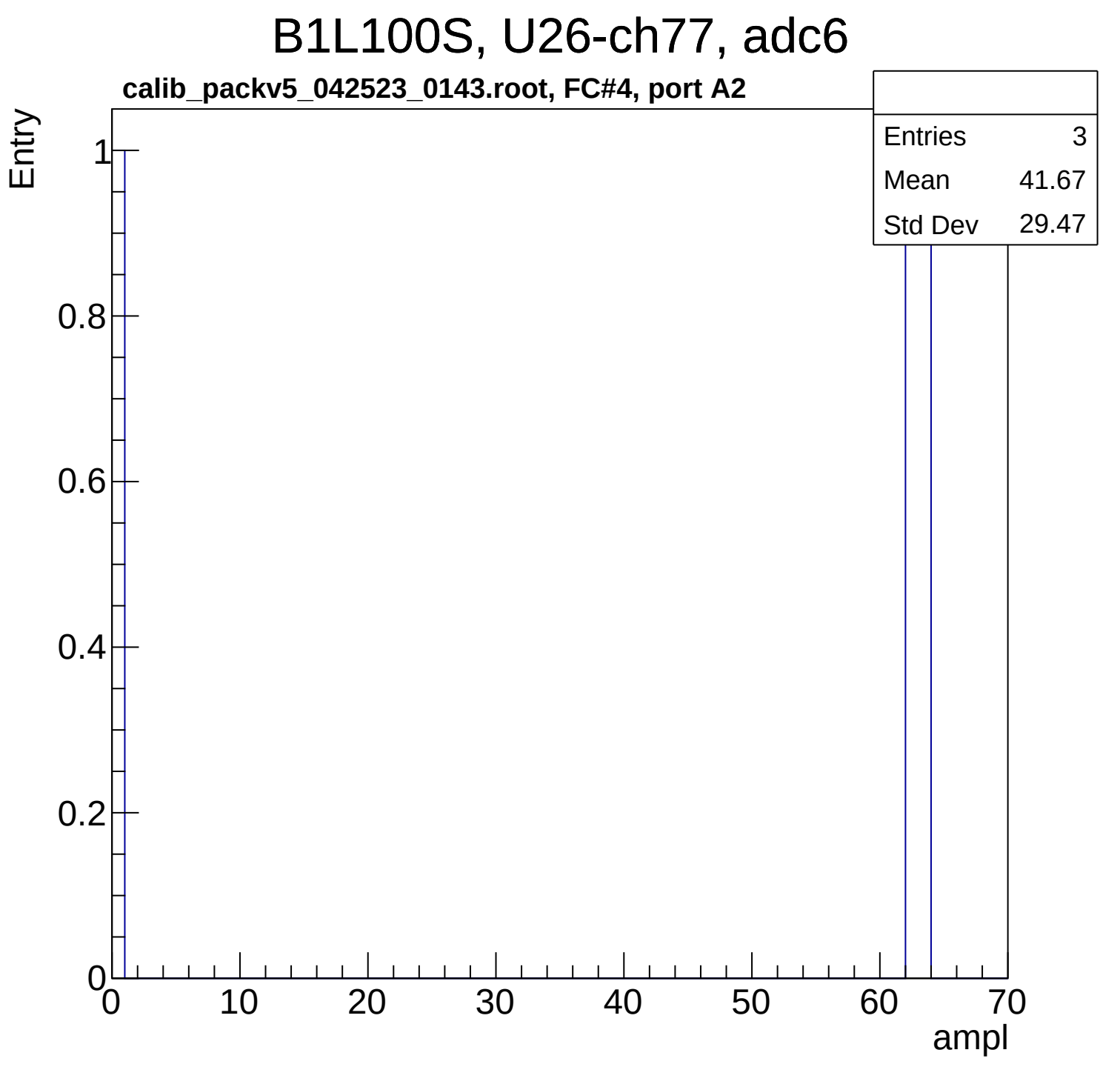
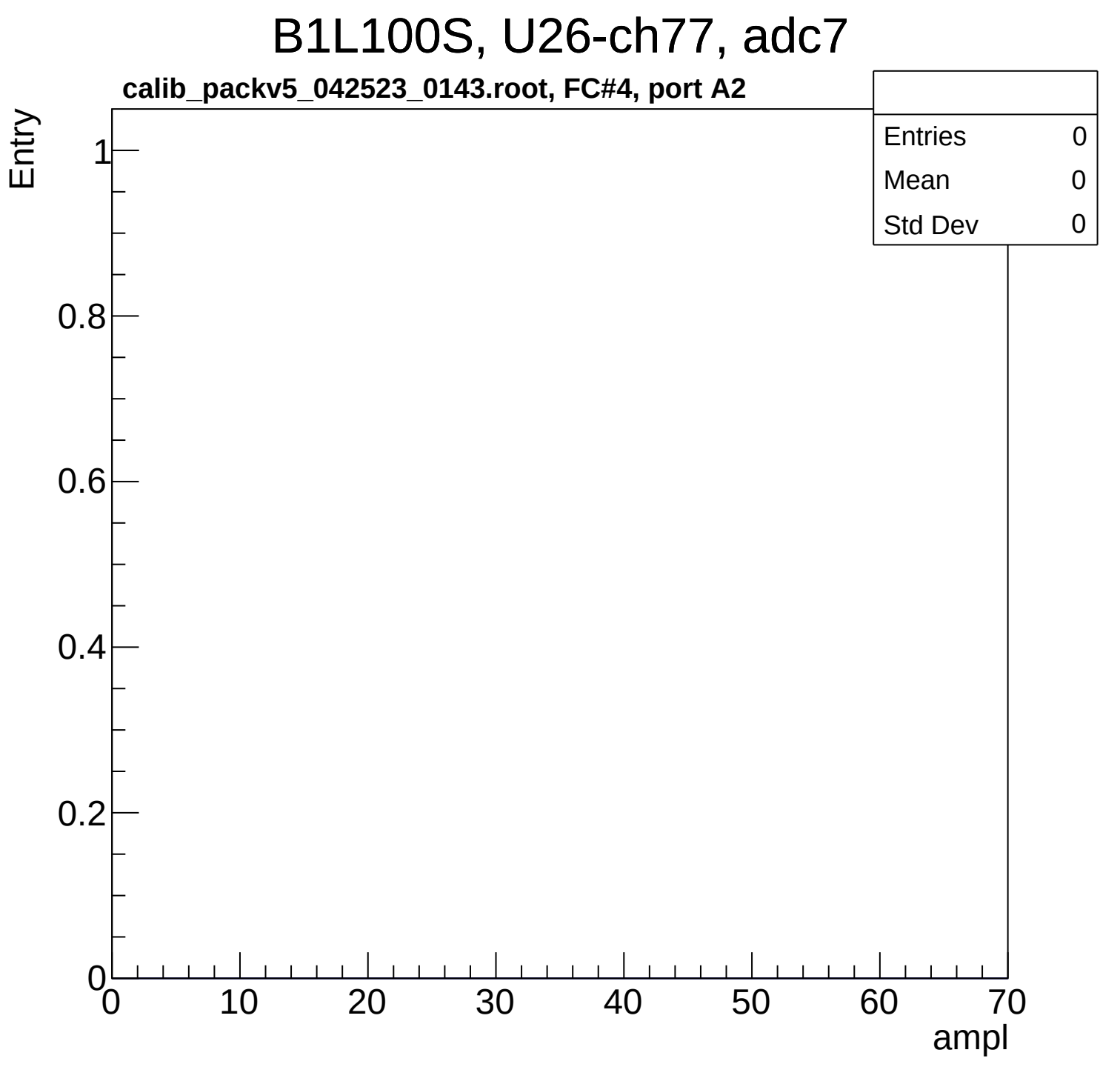
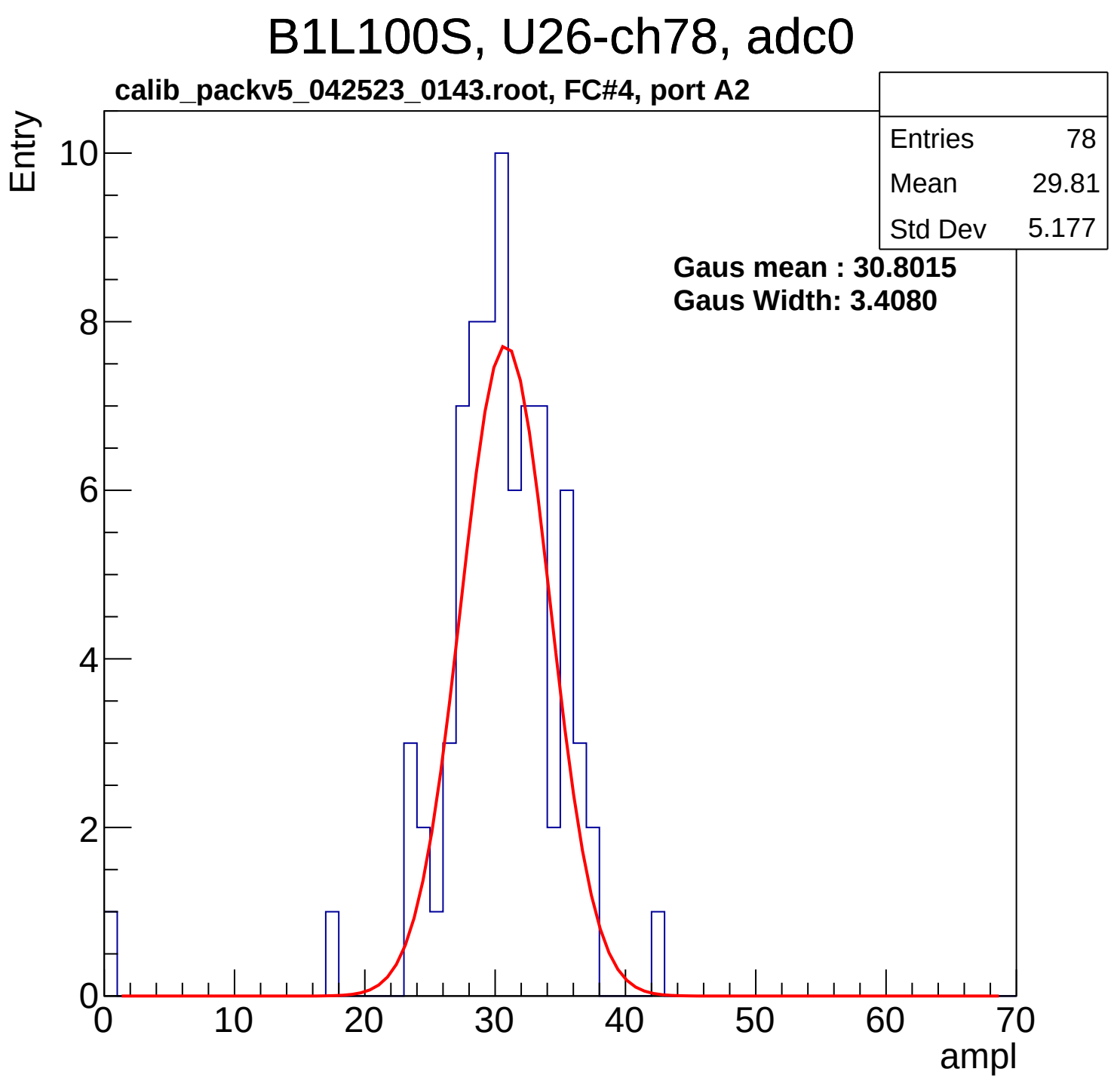

B1L100S, U26-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.81
Std Dev 5.177
Gaus mean : 30.8015
Gaus Width: 3.4080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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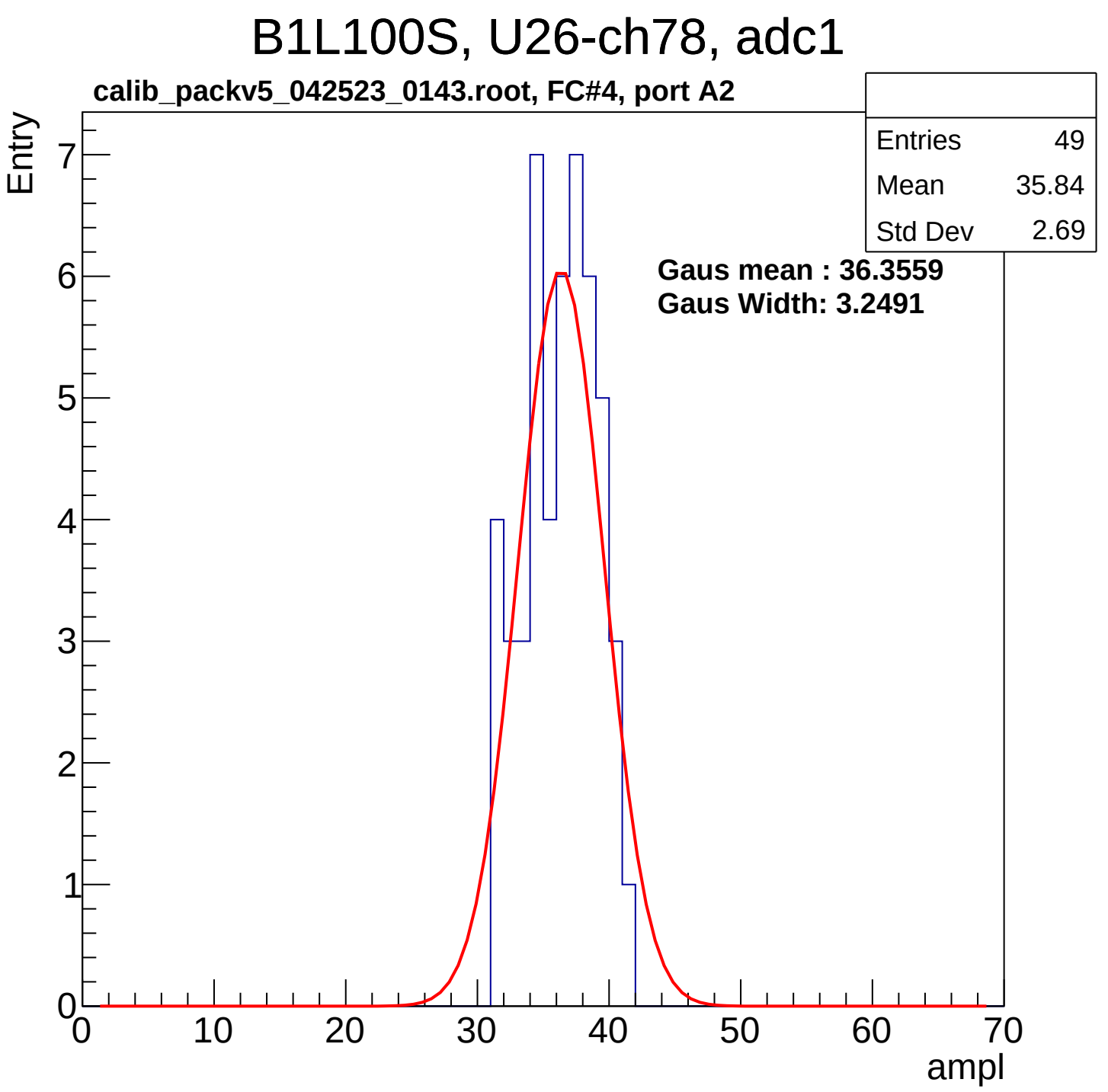
B1L100S, U26-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 35.84
Std Dev 2.69
Gaus mean : 36.3559
Gaus Width: 3.2491
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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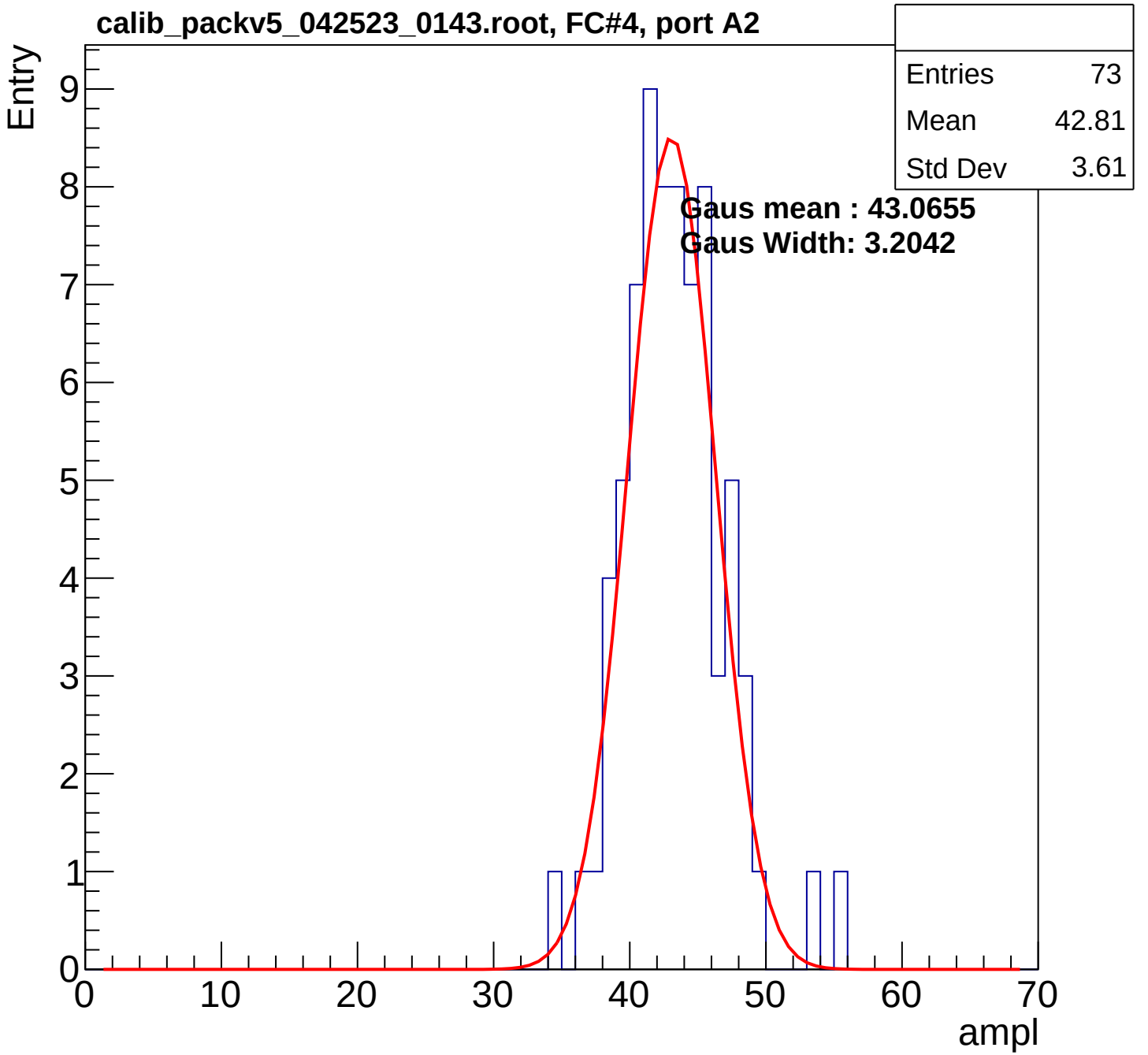
B1L100S, U26-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 42.81
Std Dev 3.61
Gaus mean : 43.0655
Gaus Width: 3.2042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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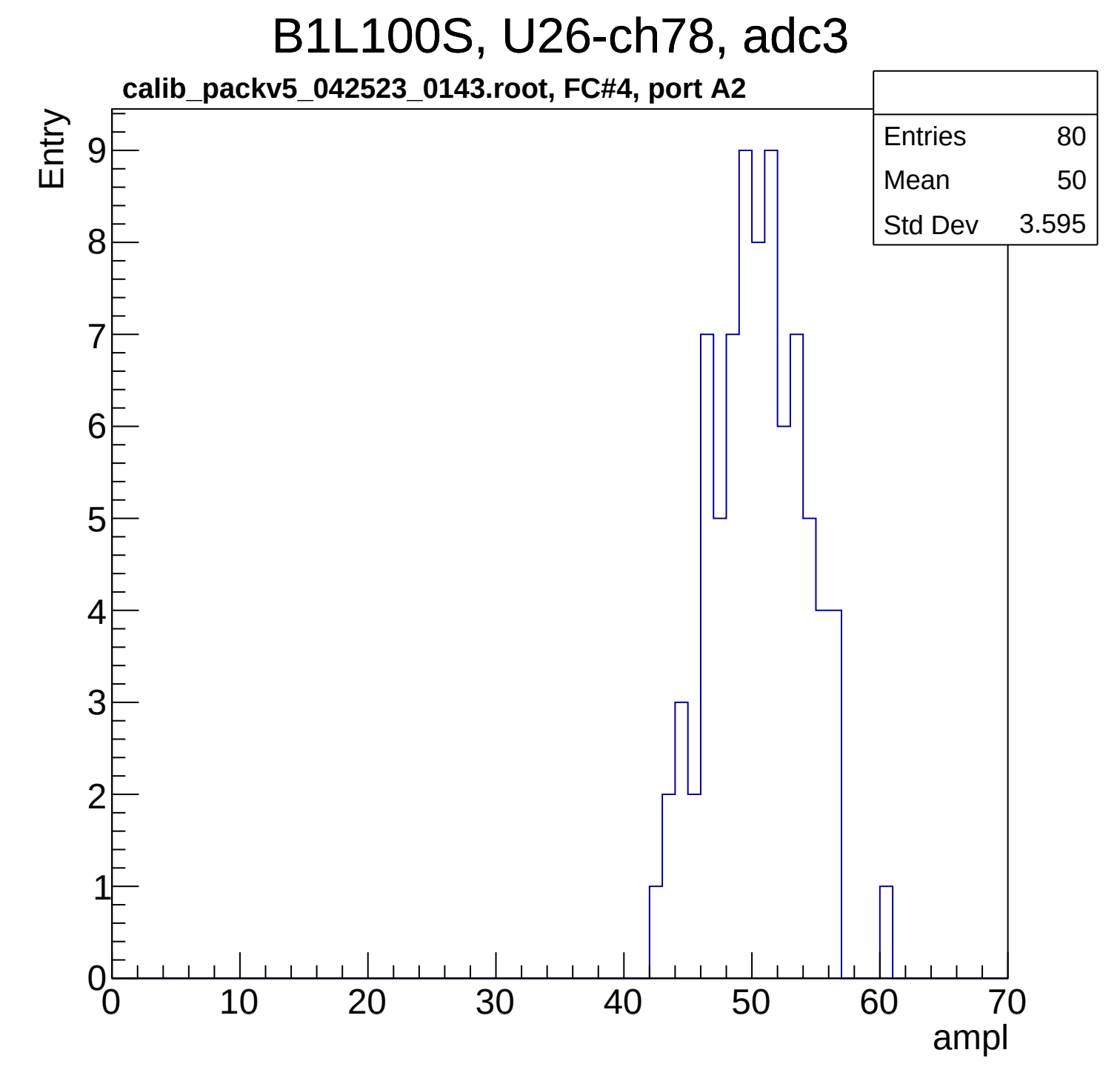
B1L100S, U26-ch78, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 50
Std Dev 3.595
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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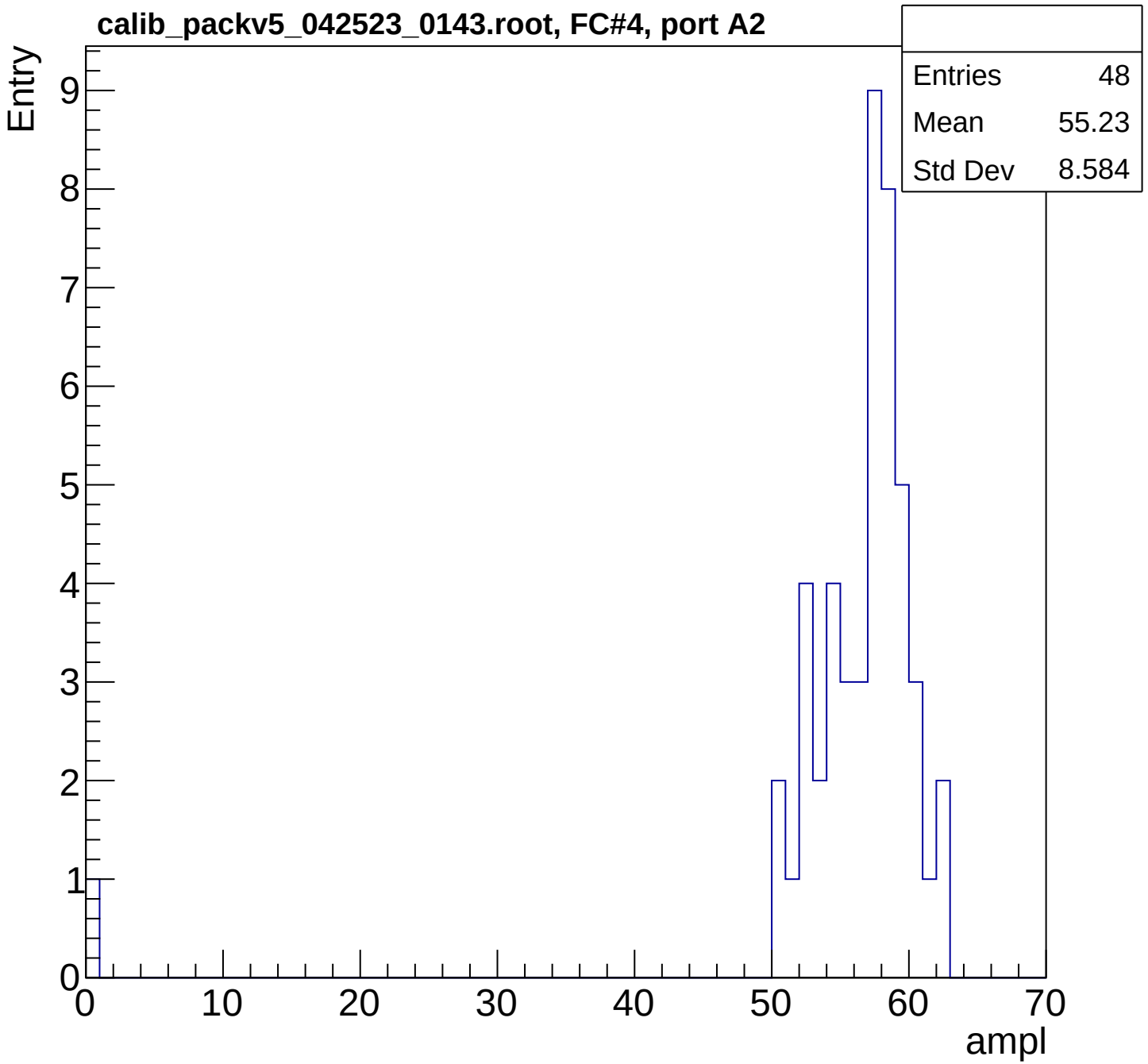
B1L100S, U26-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 55.23
Std Dev 8.584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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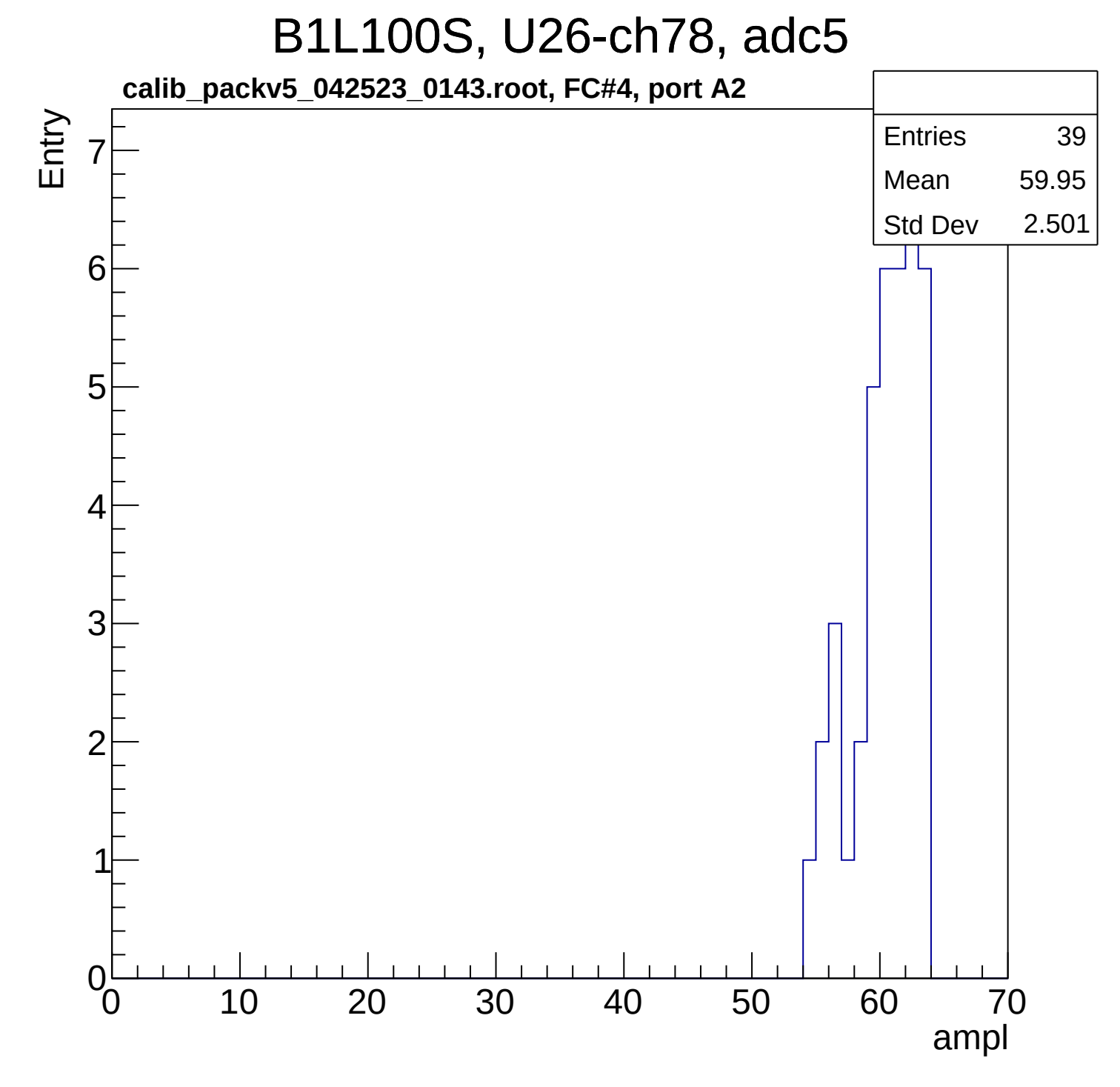
B1L100S, U26-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.95
Std Dev 2.501
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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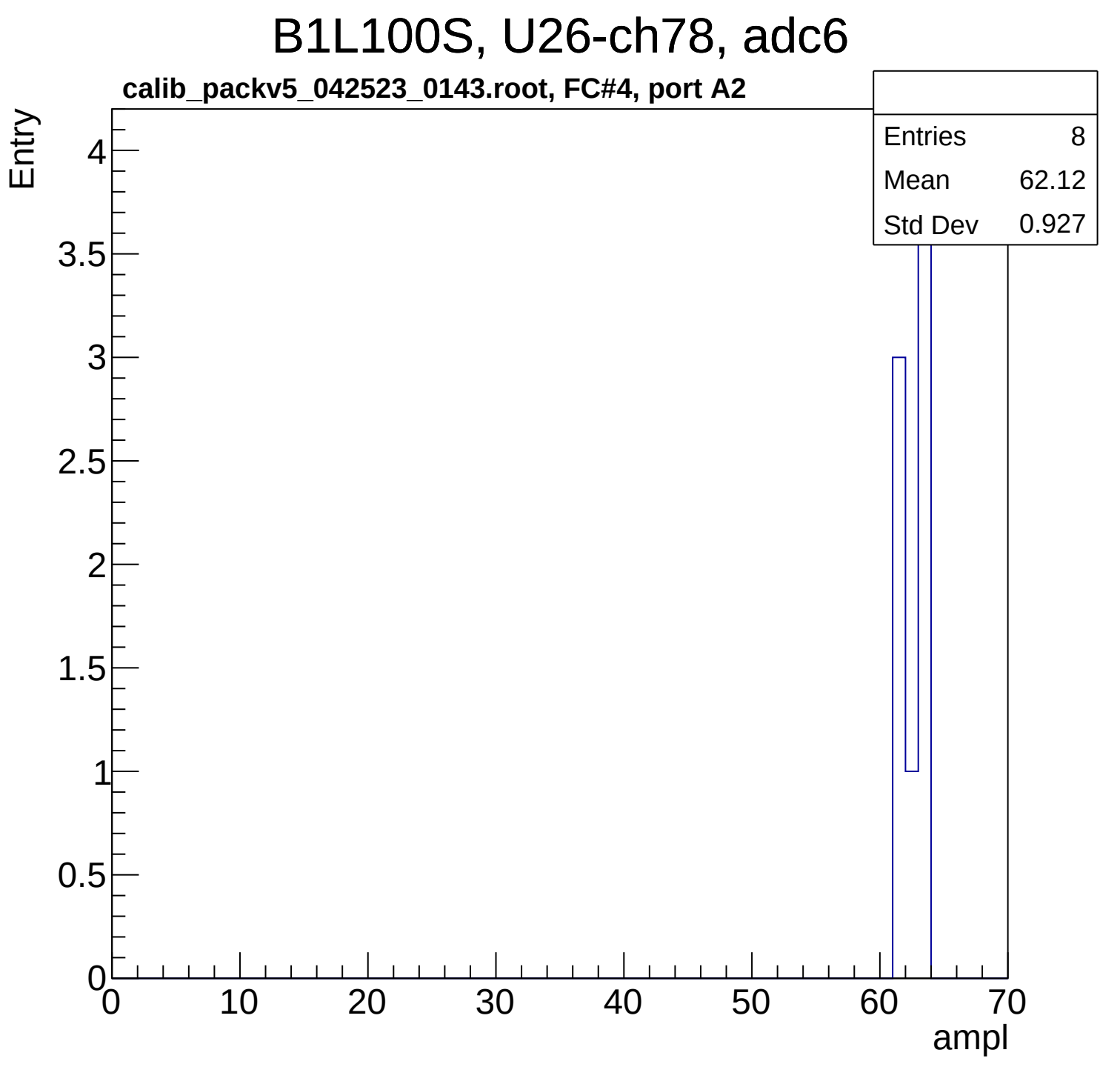
B1L100S, U26-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
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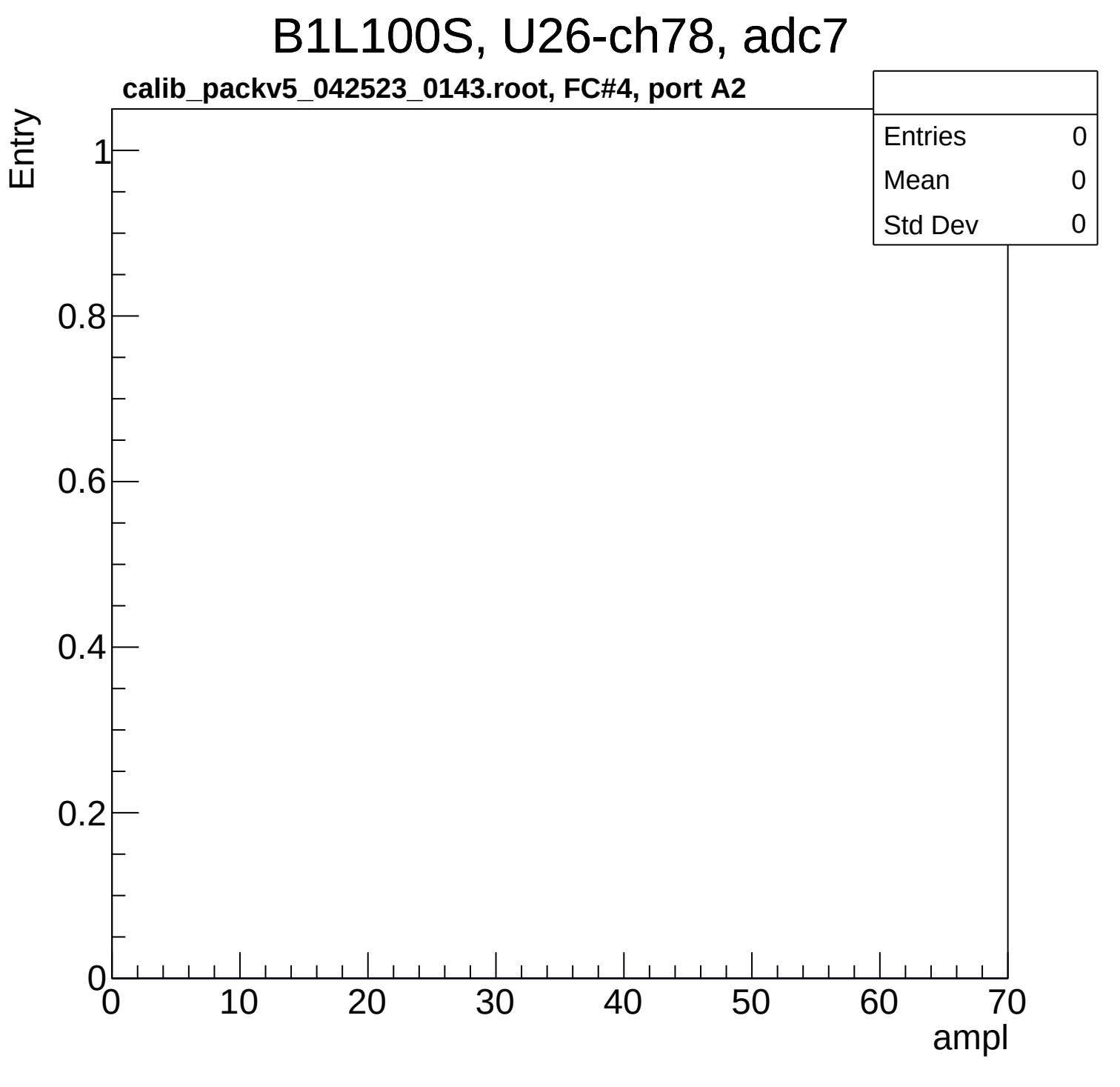
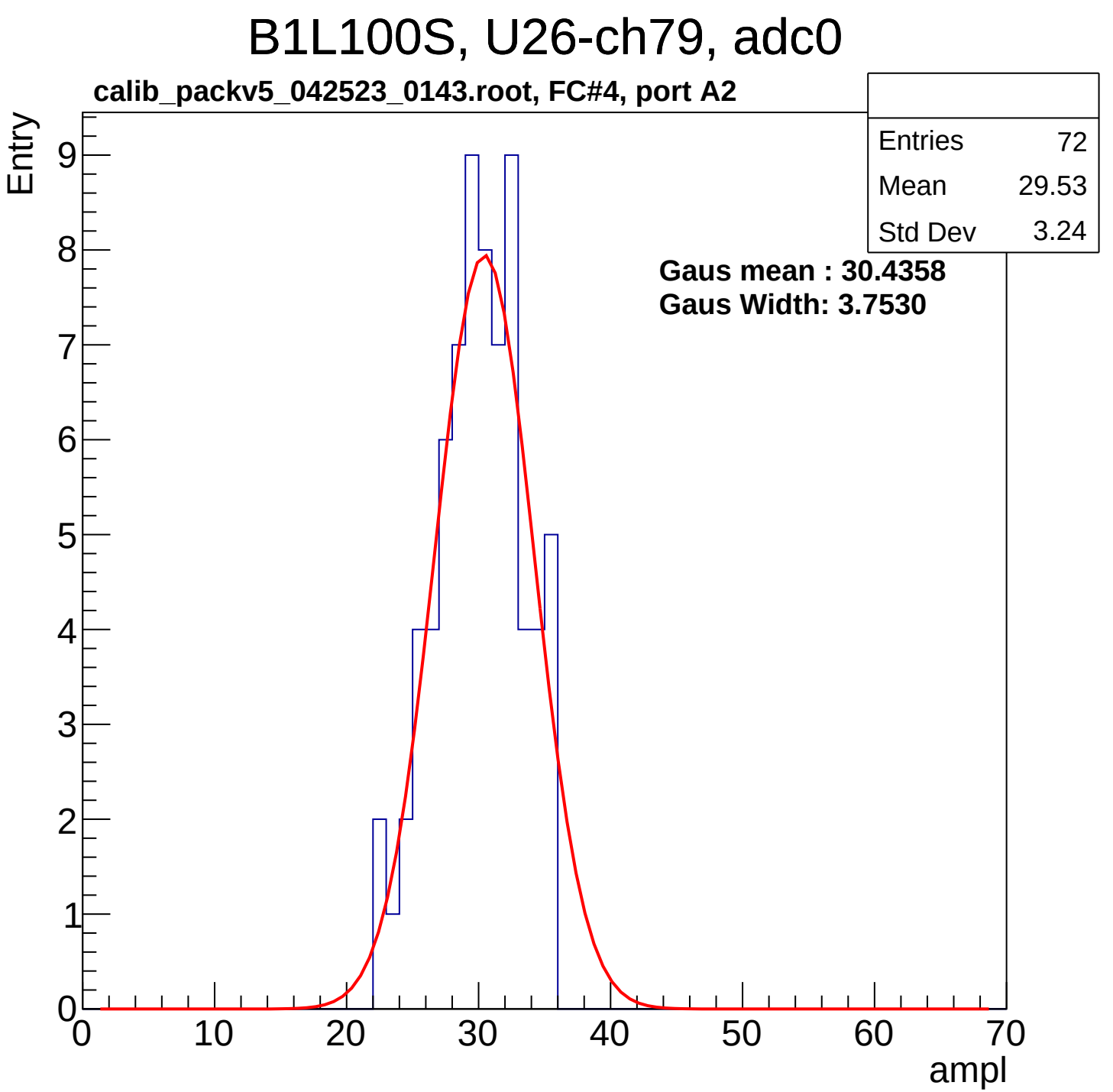
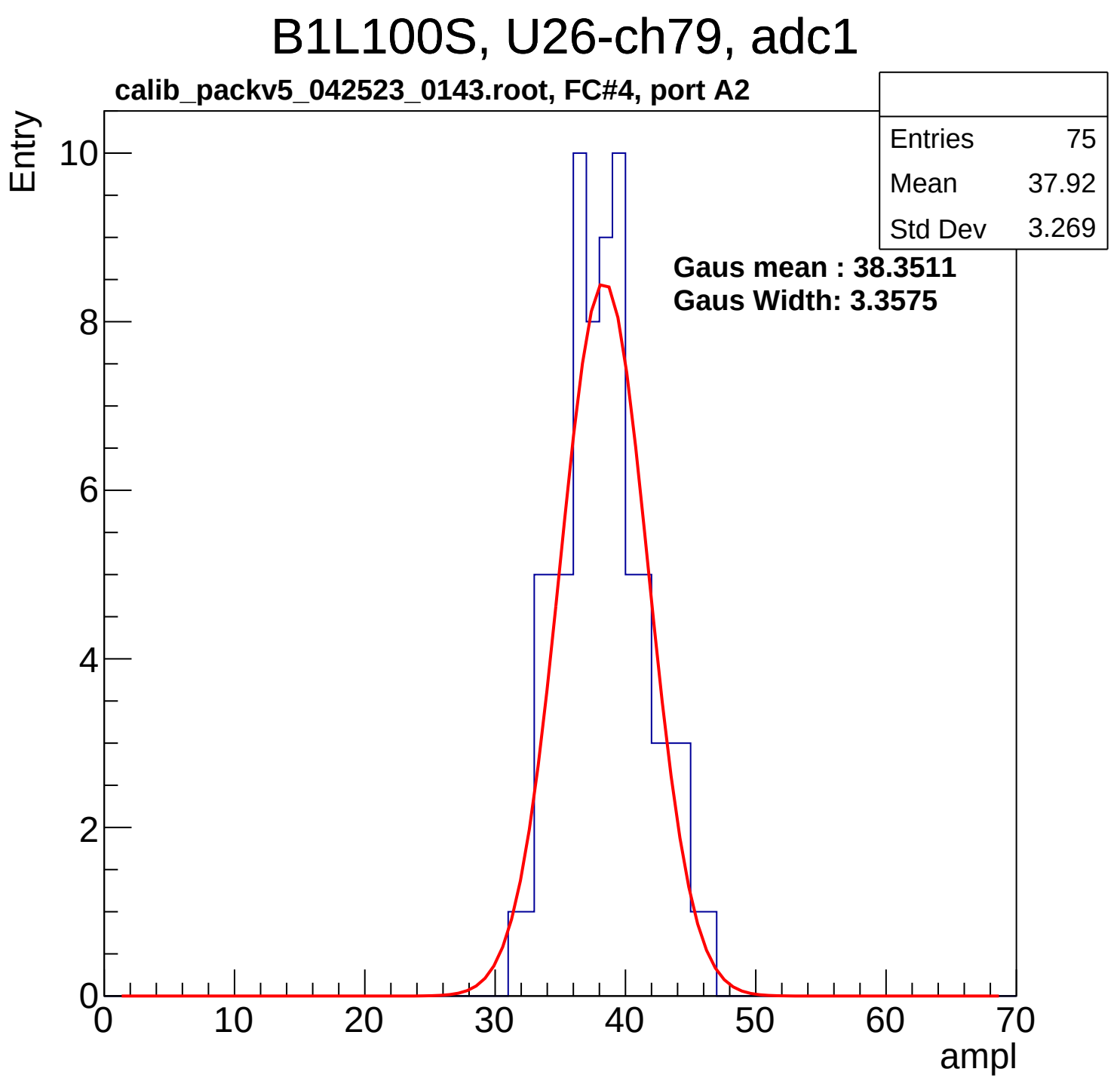
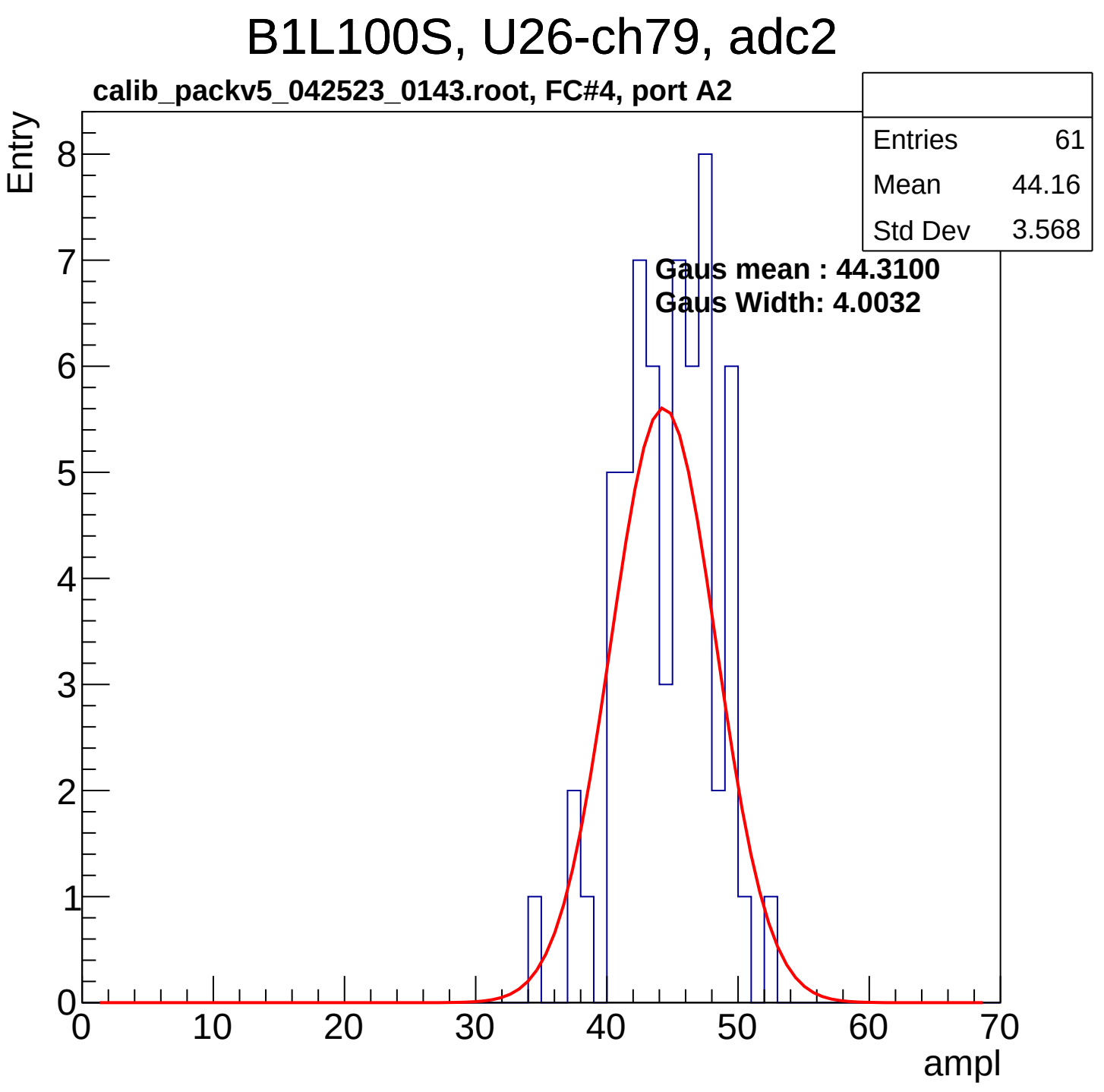
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U26-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.53
Std Dev 3.24
Gaus mean : 30.4358
Gaus Width: 3.7530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.92
Std Dev 3.269
Gaus mean : 38.3511
Gaus Width: 3.3575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 44.16
Std Dev 3.568
Gaus mean : 44.3100
Gaus Width: 4.0032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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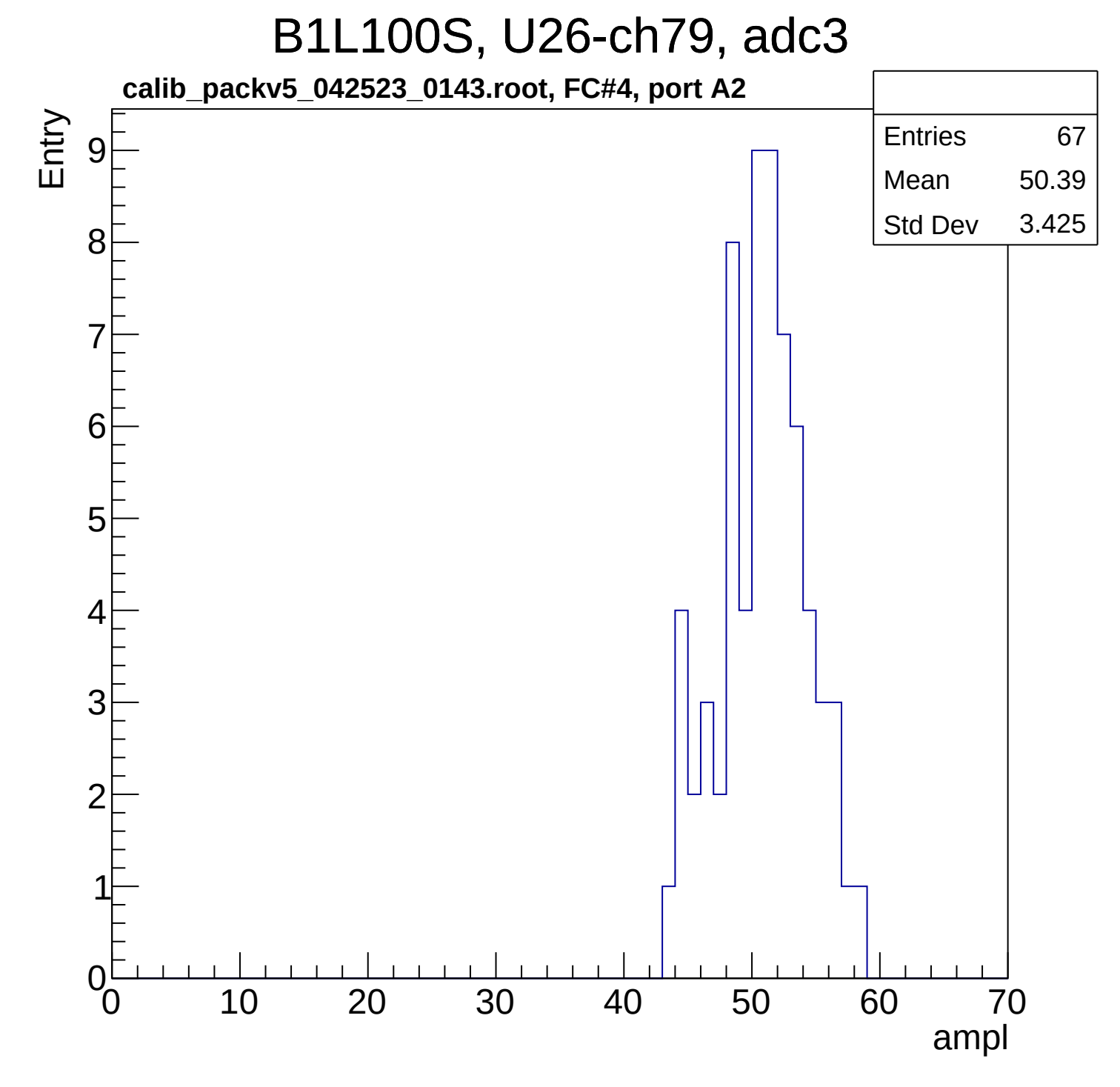
B1L100S, U26-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.39
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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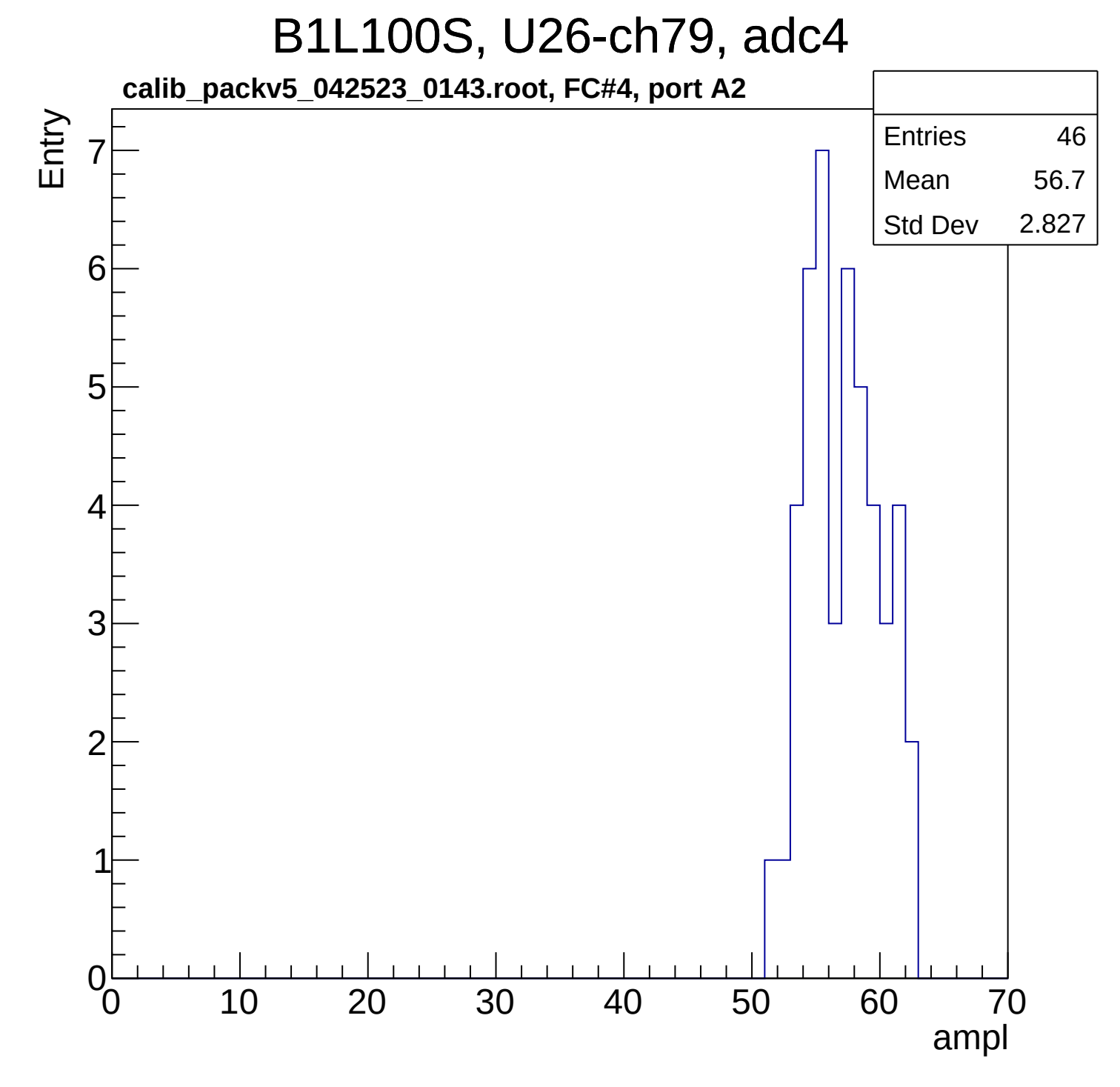
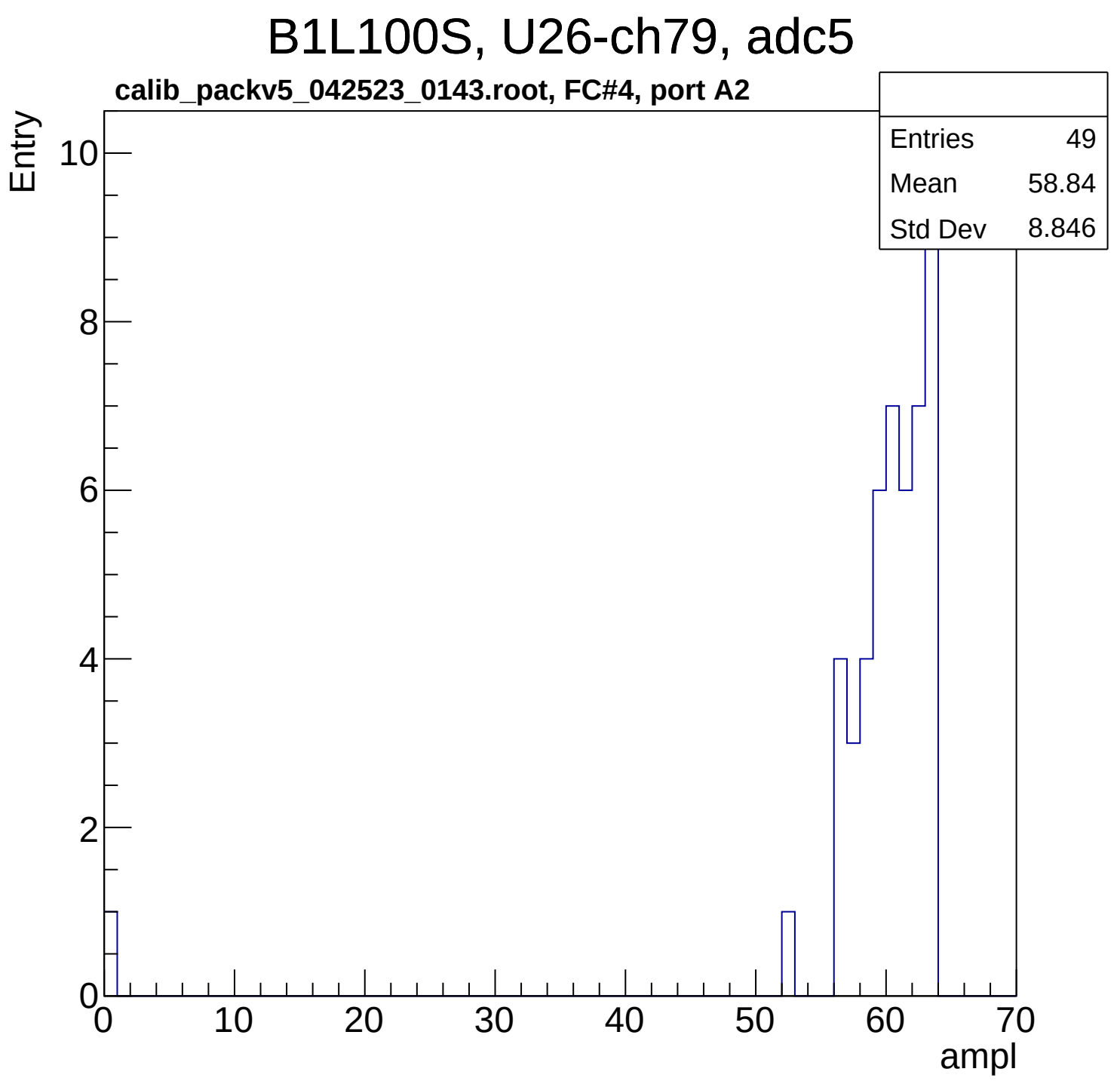
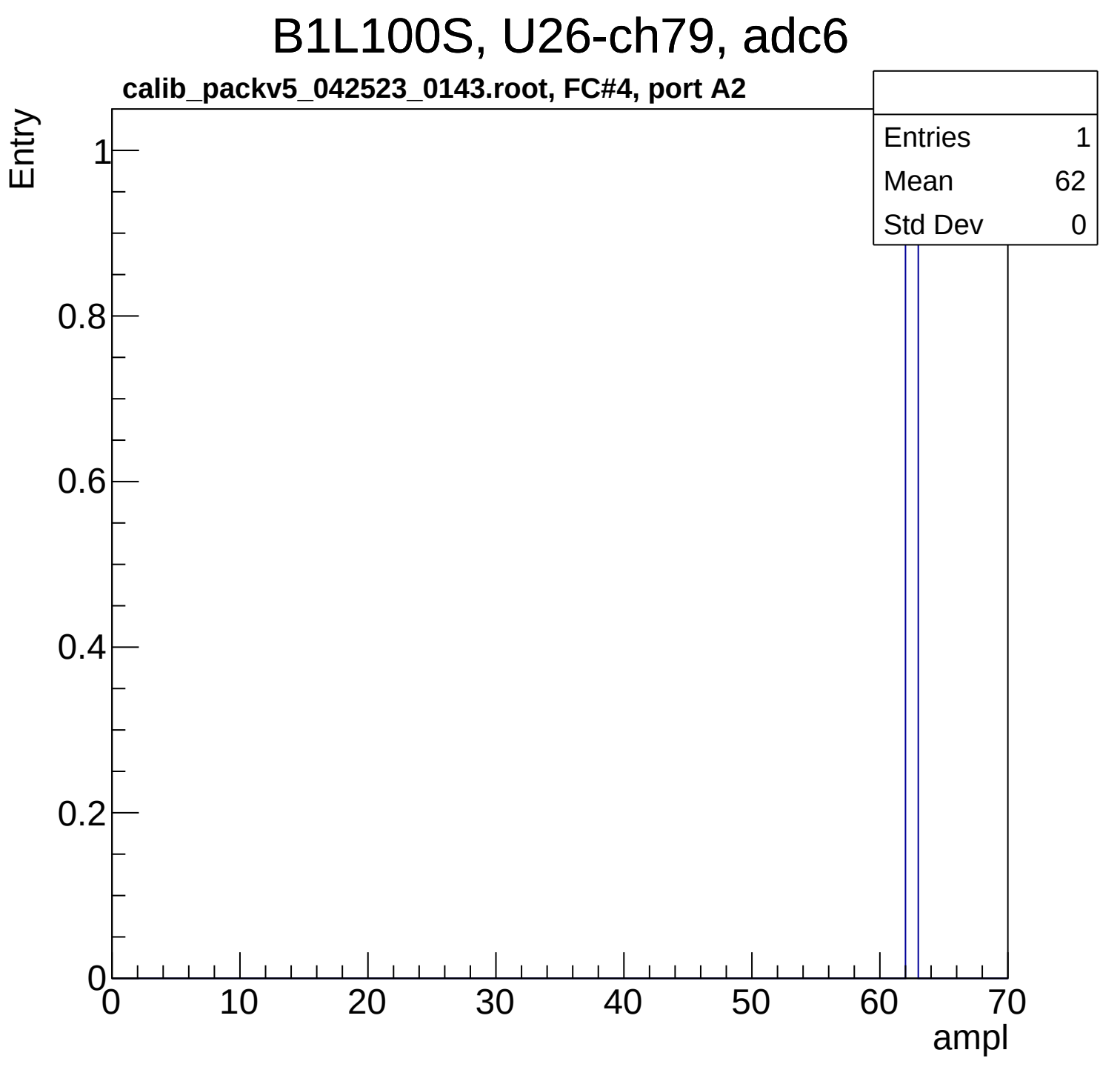
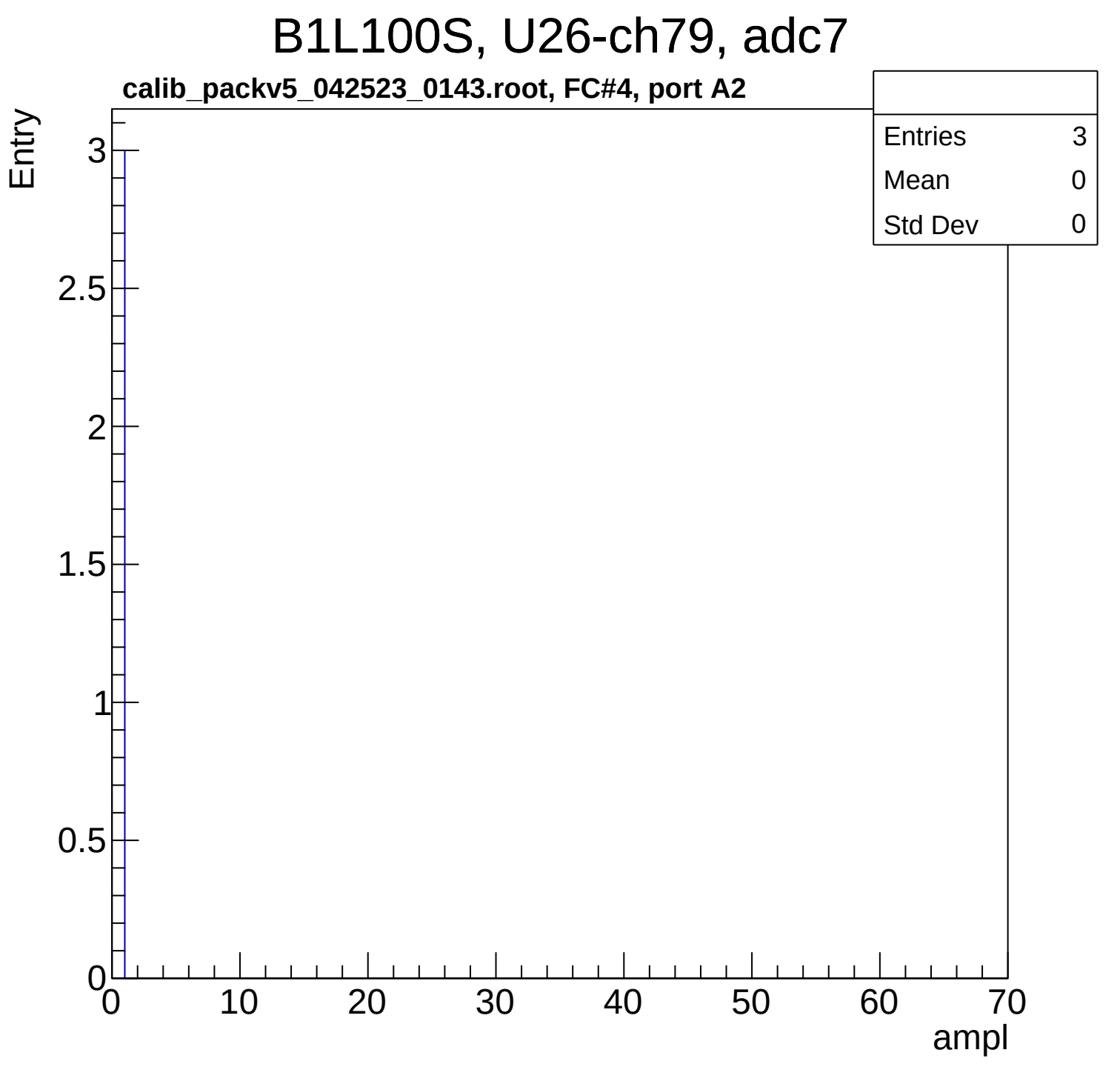
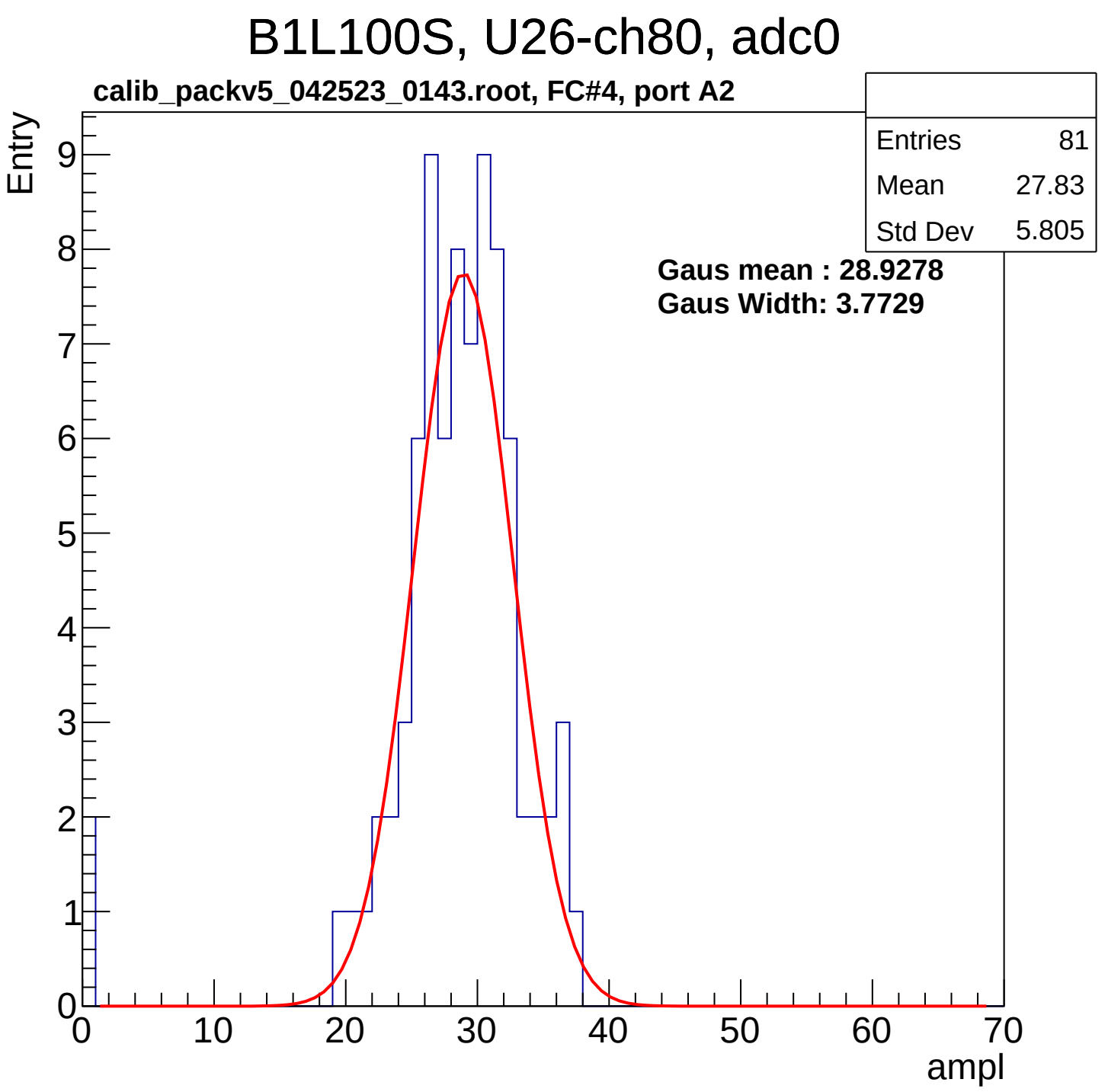
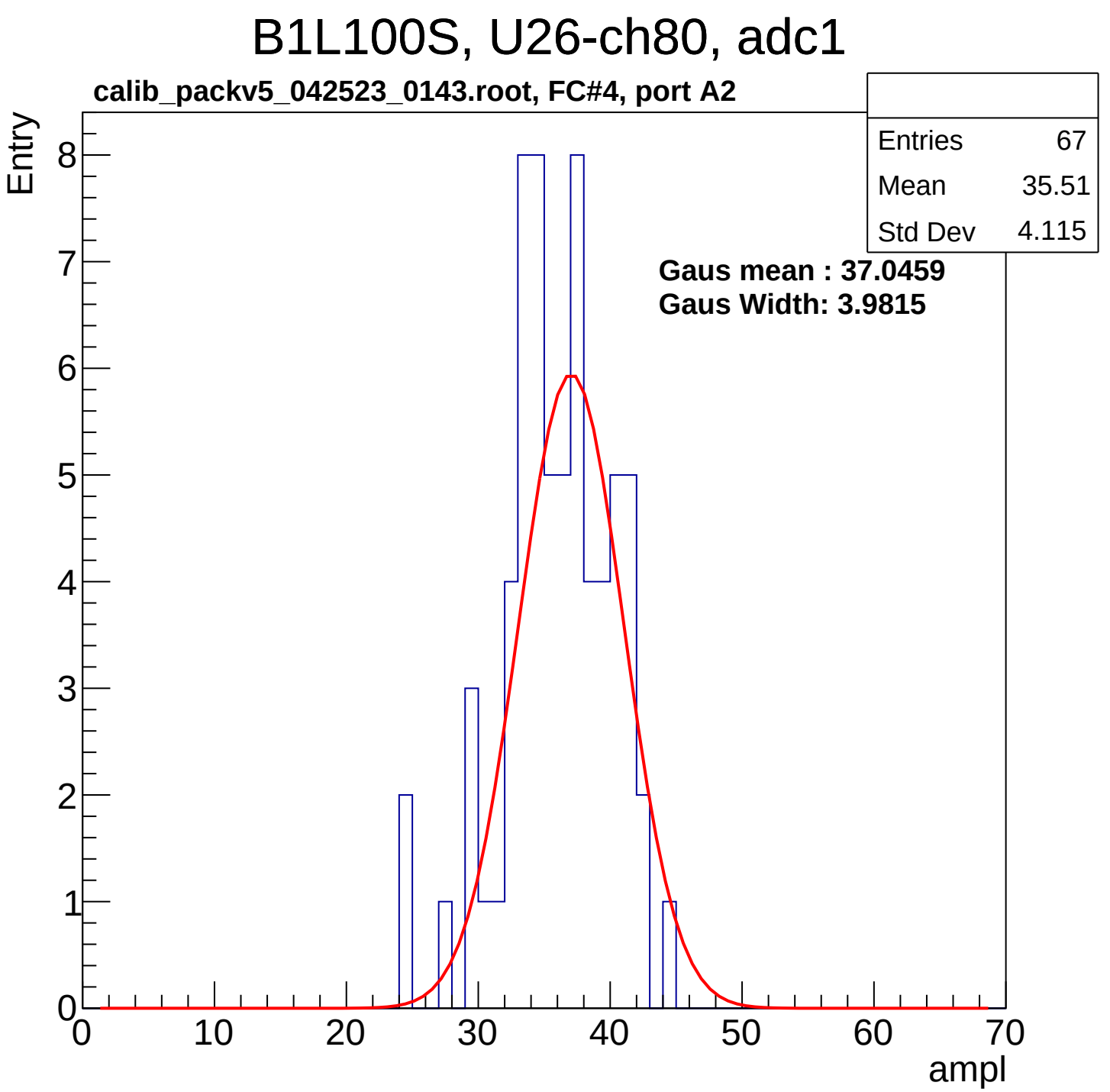

B1L100S, U26-ch79, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 56.7
Std Dev 2.827
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

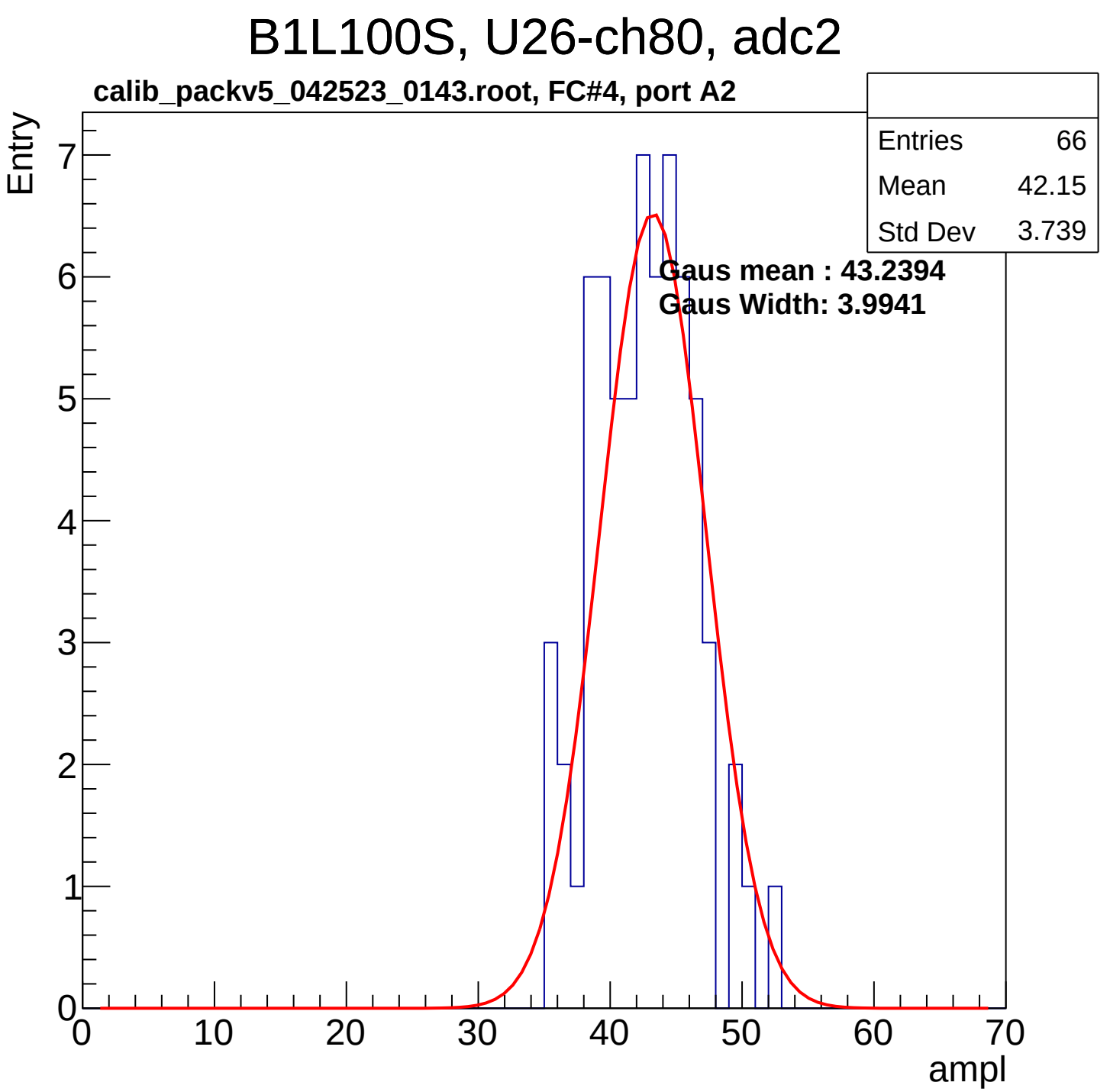

B1L100S, U26-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.84
Std Dev 8.846
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

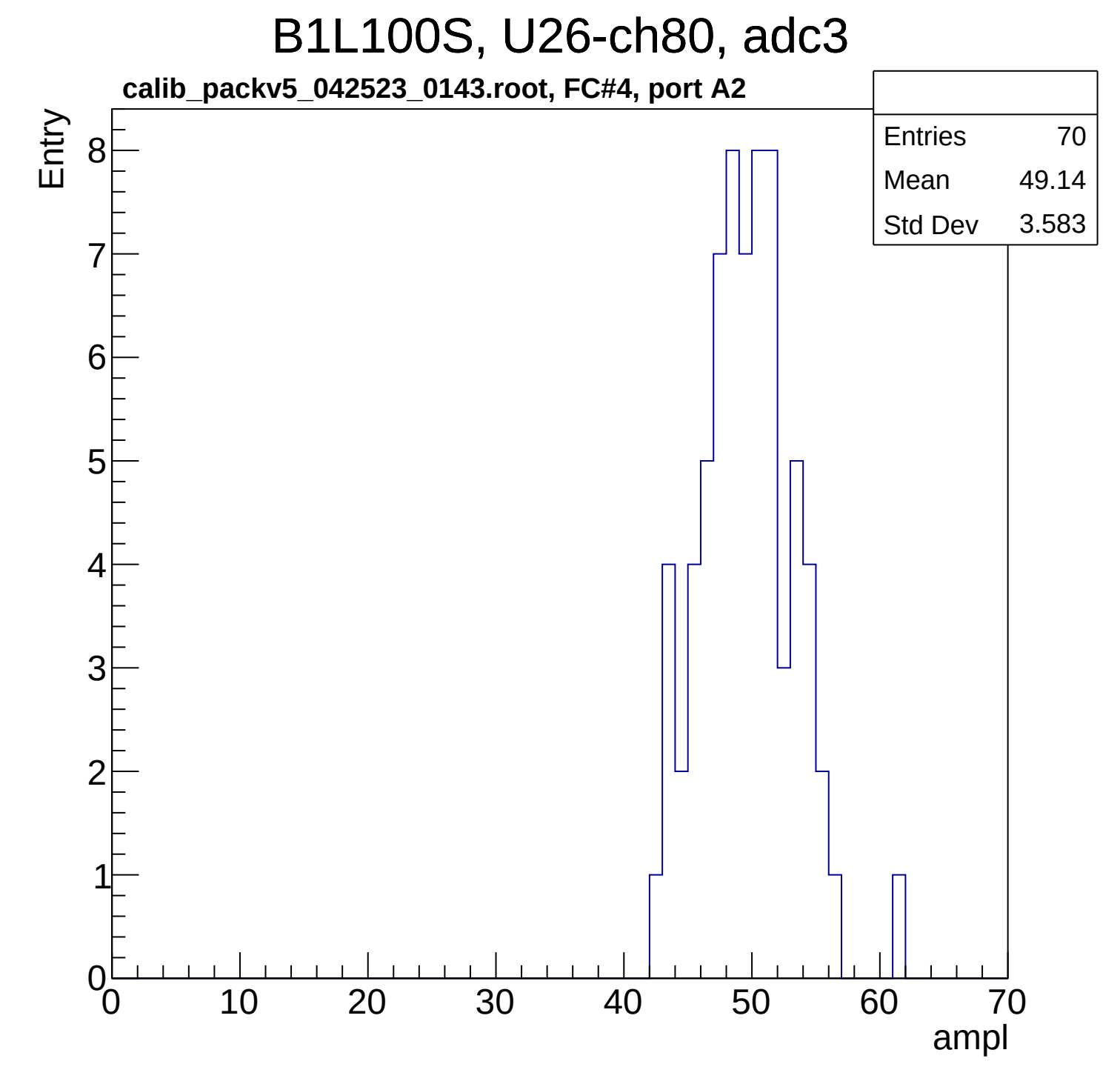

B1L100S, U26-ch79, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 27.83
Std Dev 5.805
Gaus mean : 28.9278
Gaus Width: 3.7729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 35.51
Std Dev 4.115
Gaus mean : 37.0459
Gaus Width: 3.9815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.15
Std Dev 3.739
Gaus mean : 43.2394
Gaus Width: 3.9941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.14
Std Dev 3.583
Entry
ampl
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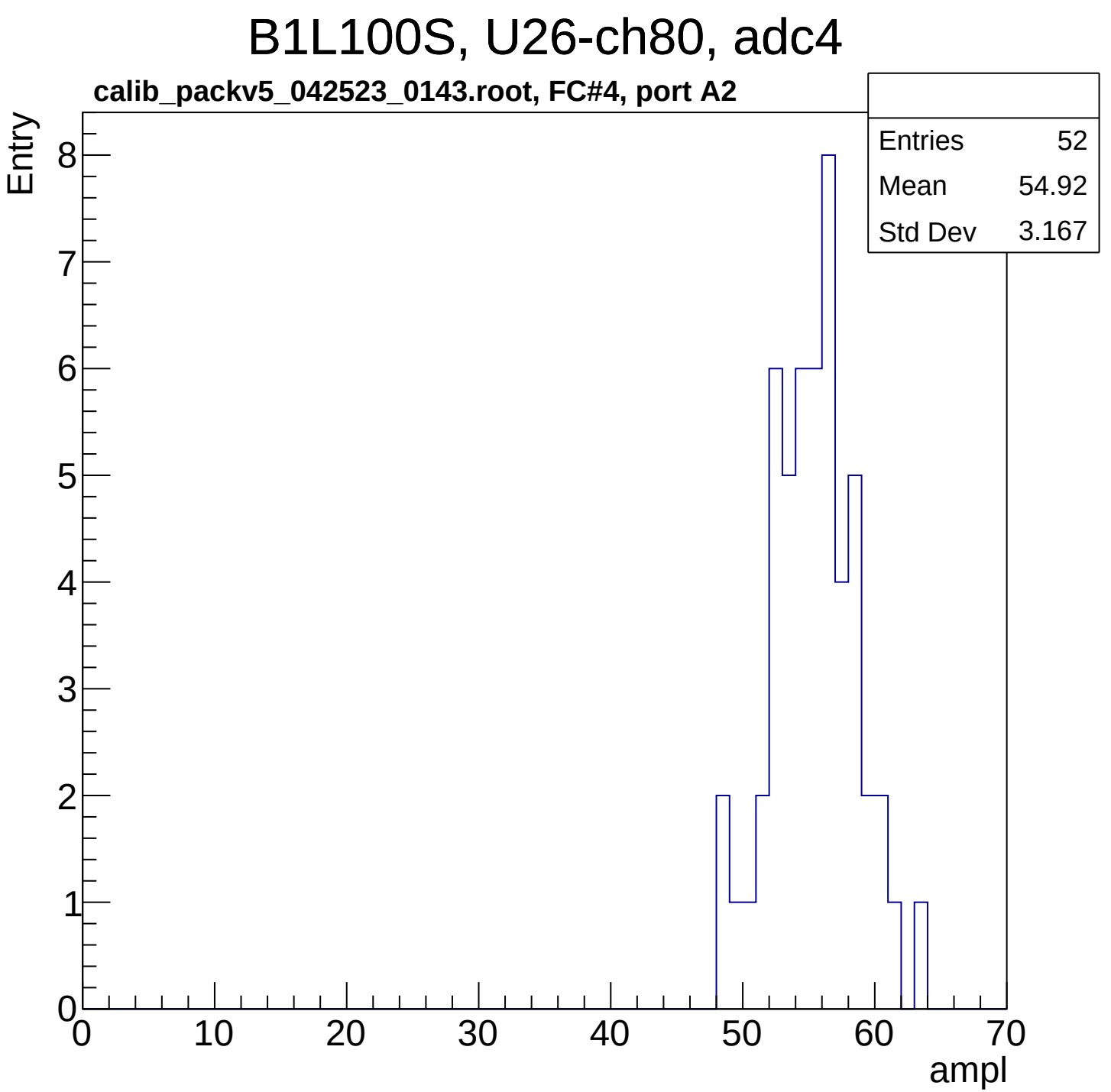
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.92
Std Dev 3.167
Entry
ampl
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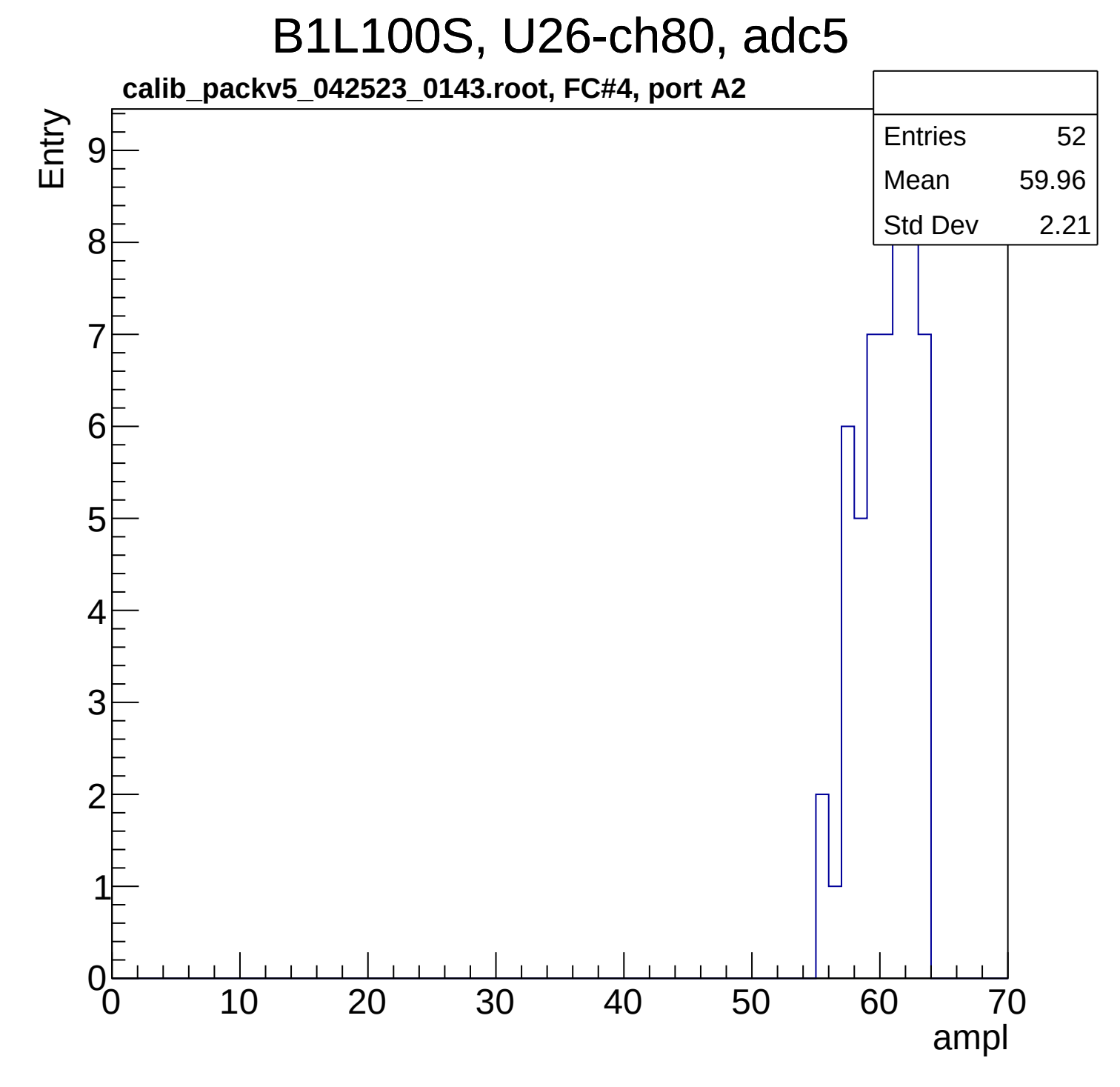
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 59.96
Std Dev 2.21
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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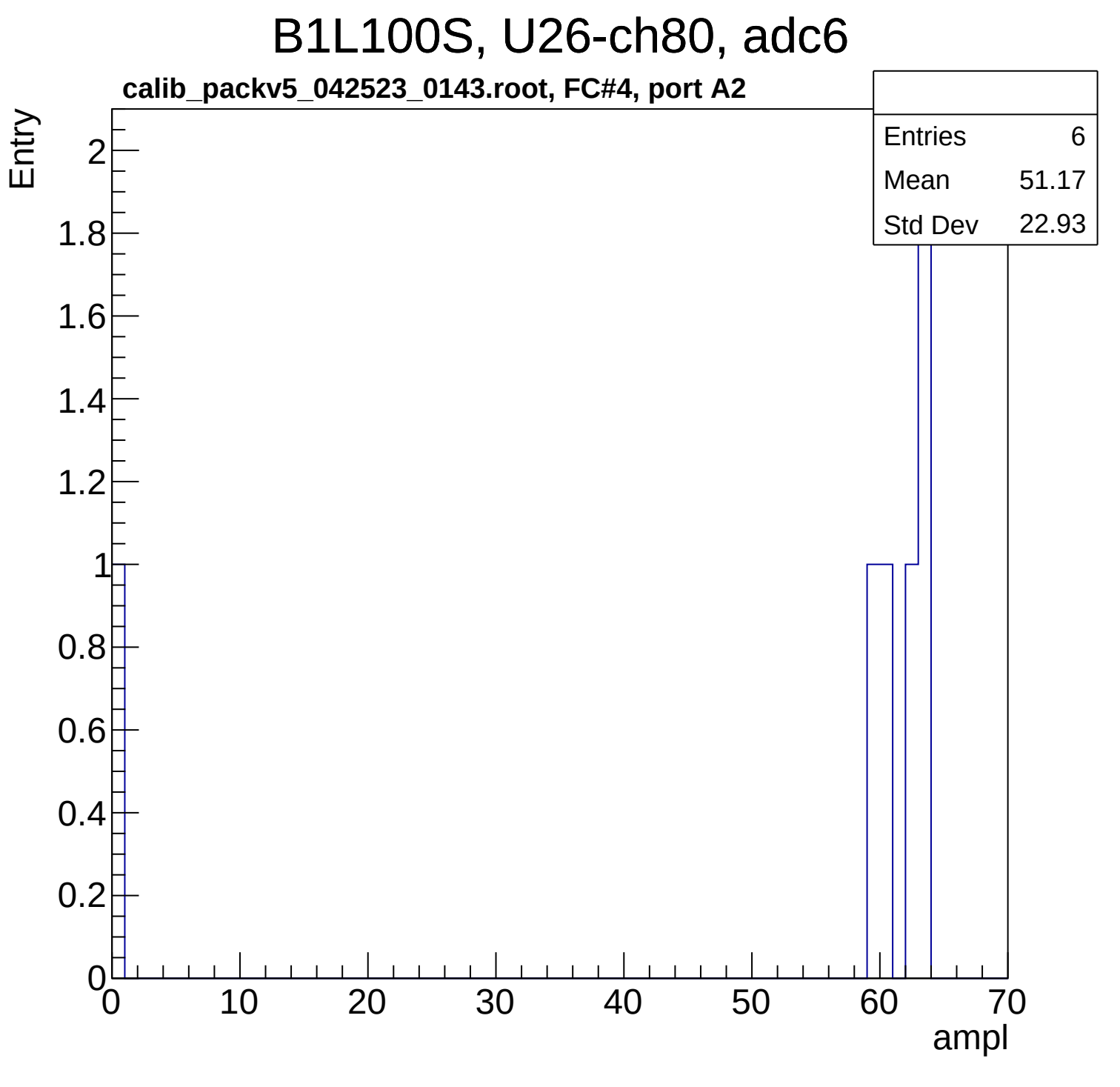
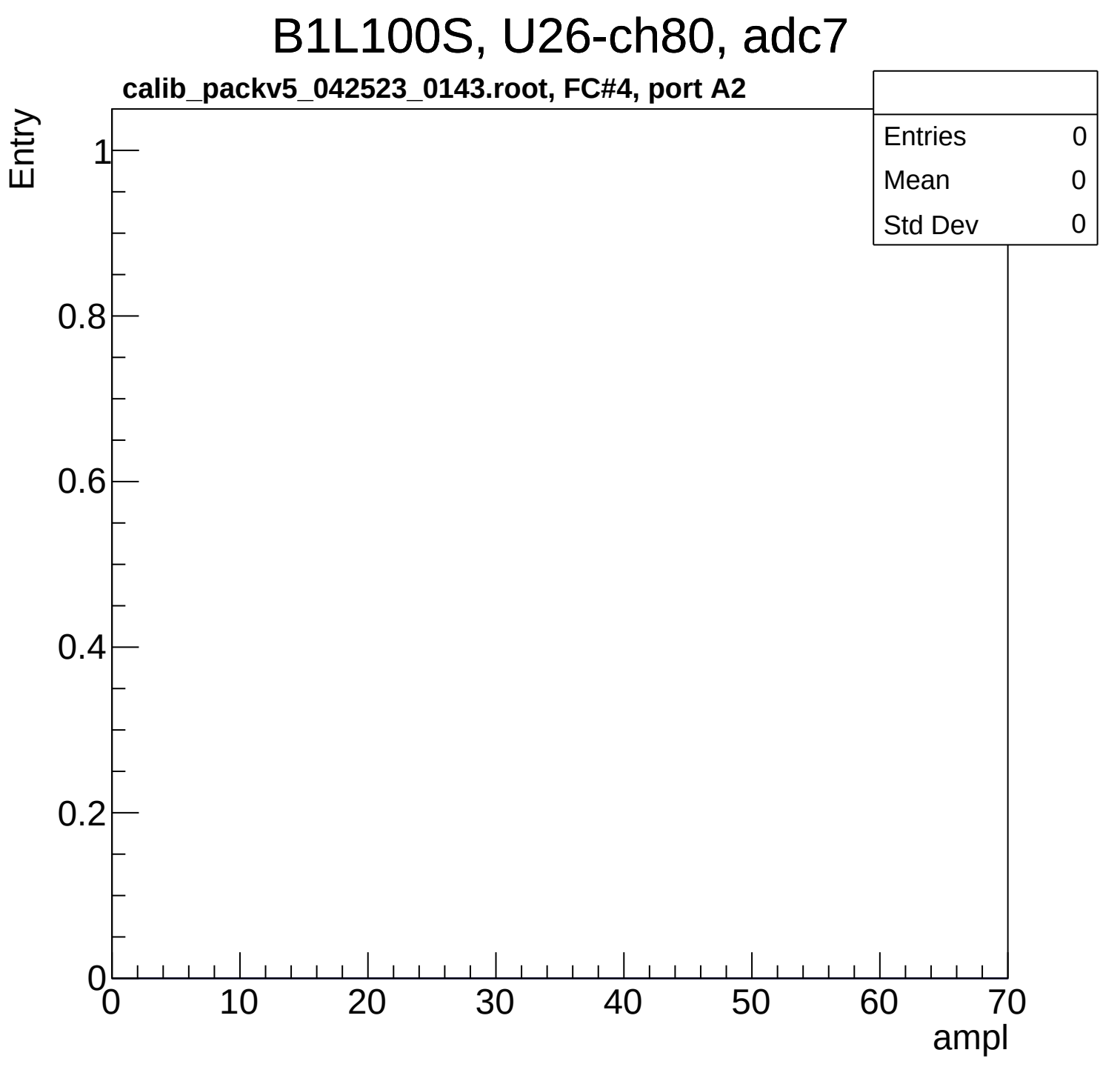
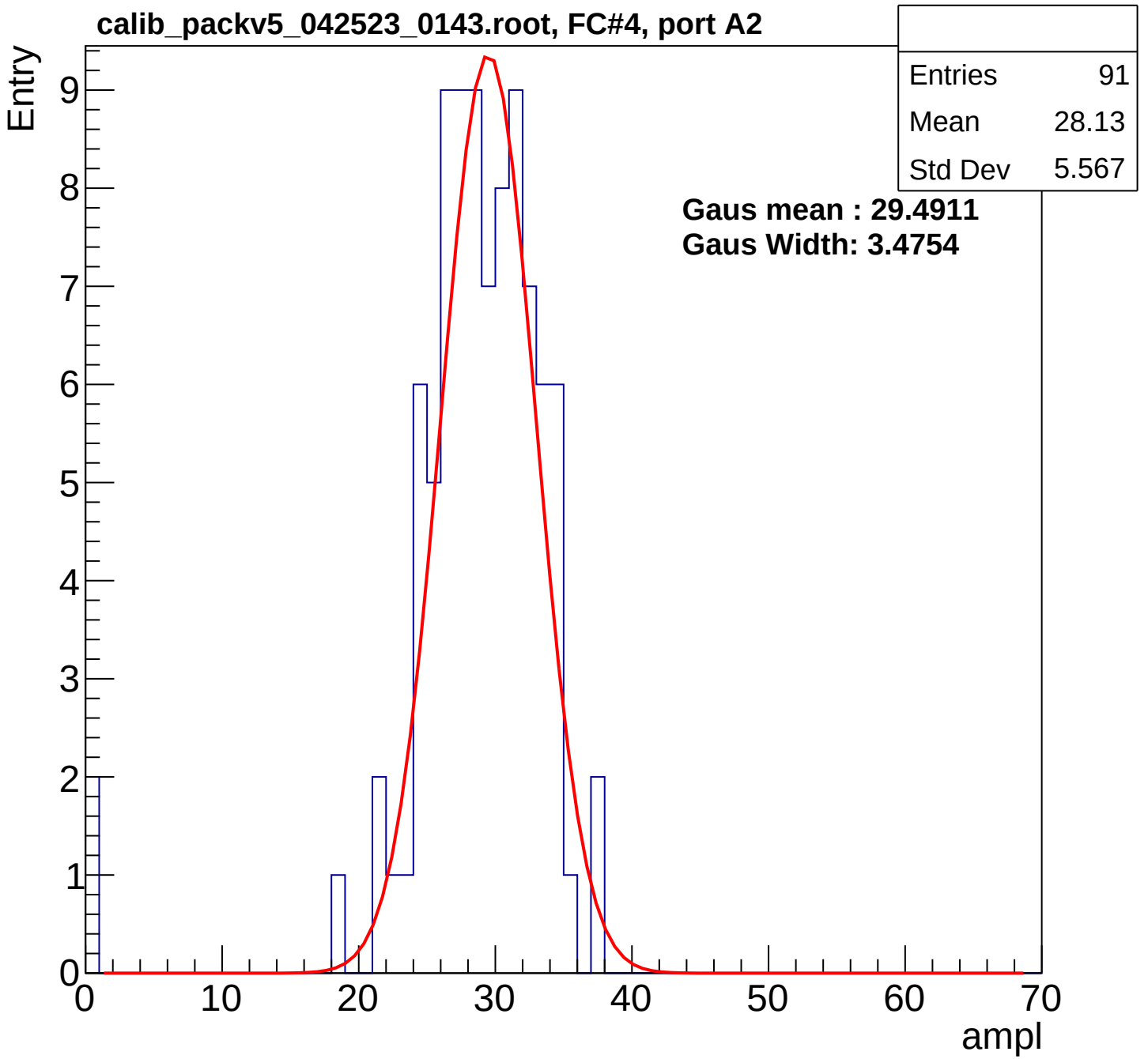

B1L100S, U26-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.17
Std Dev 22.93
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch80, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 28.13
Std Dev 5.567
Gaus mean : 29.4911
Gaus Width: 3.4754
Entry
ampl
0
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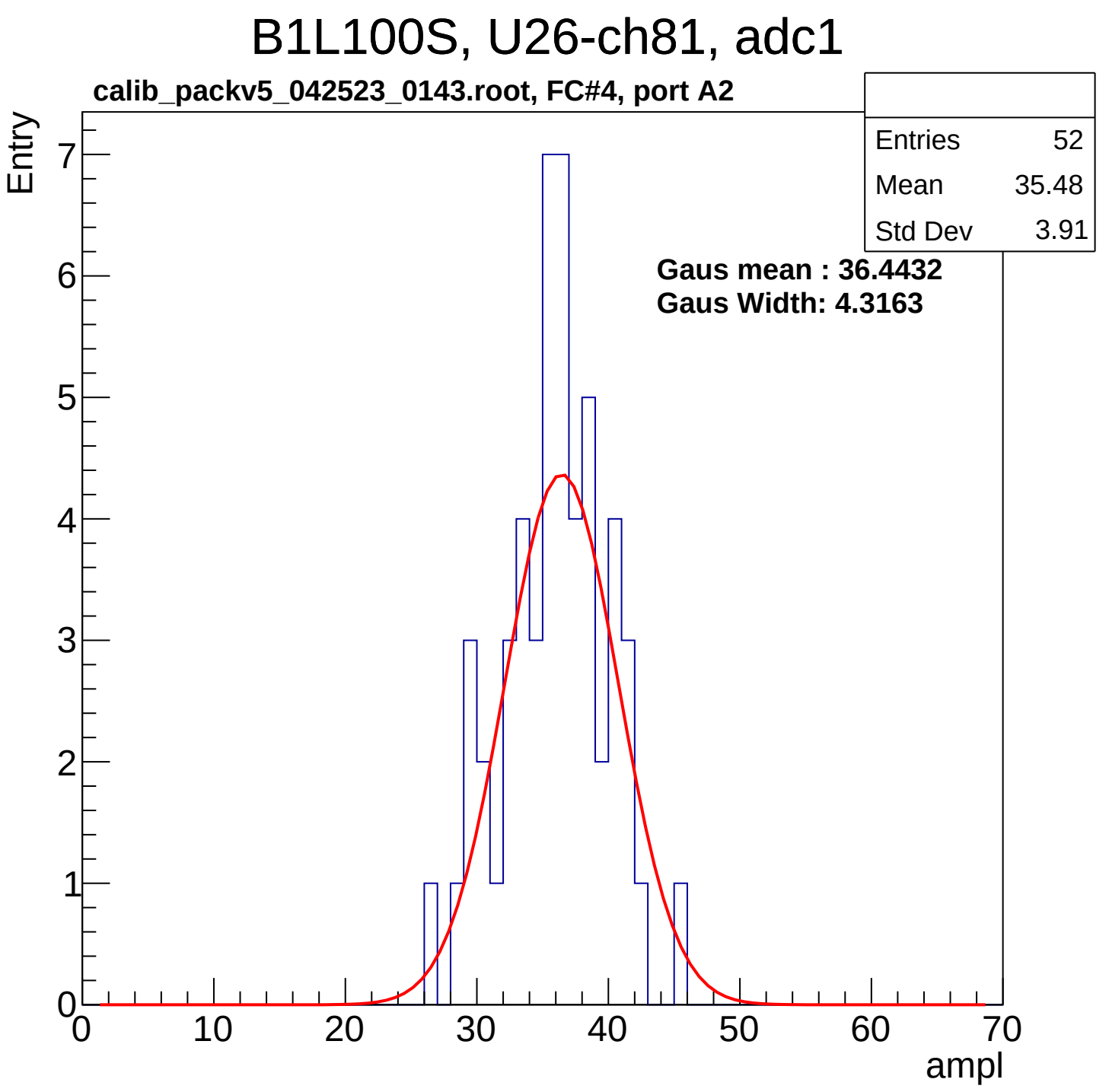
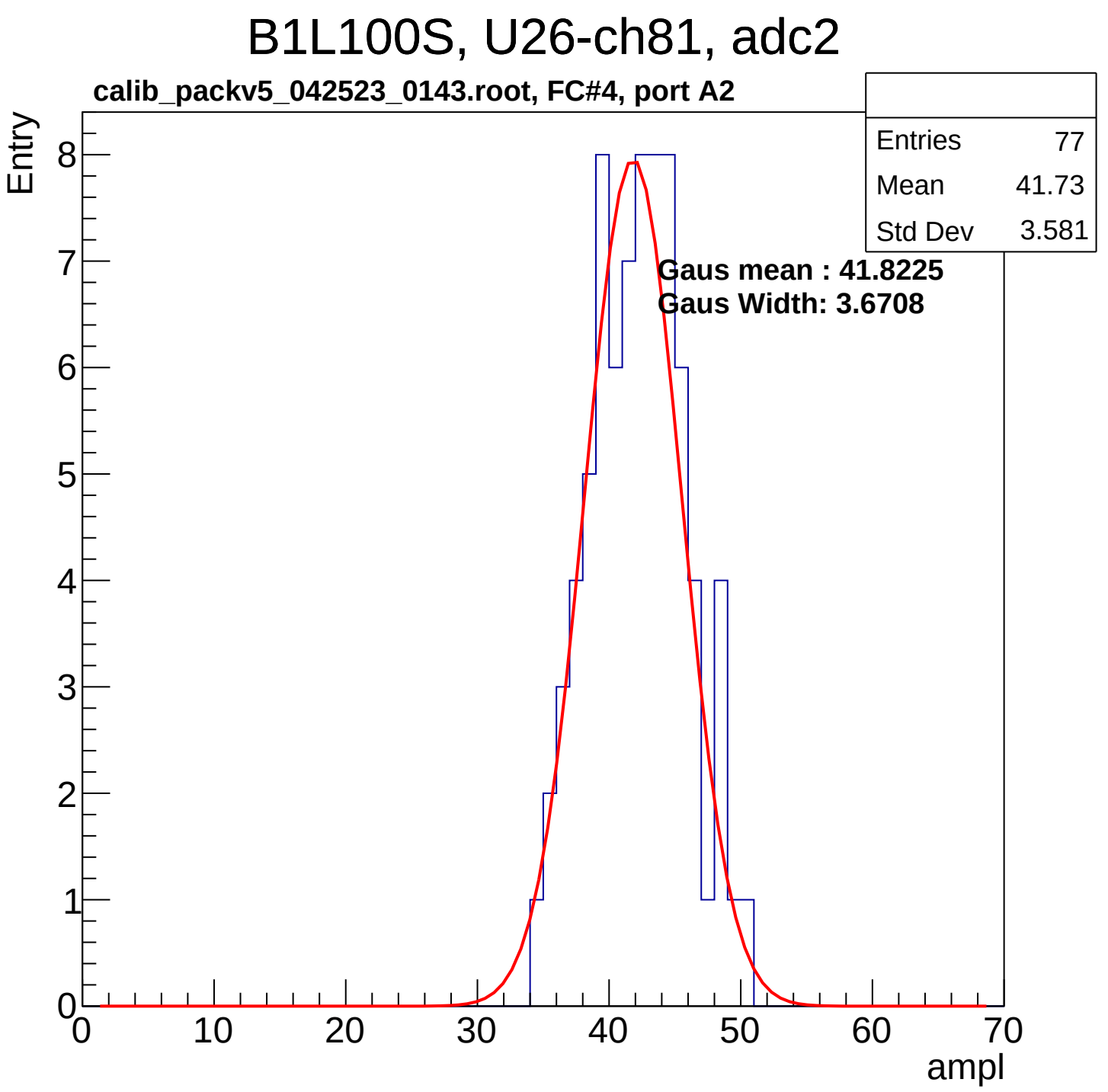
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 35.48
Std Dev 3.91
Gaus mean : 36.4432
Gaus Width: 4.3163
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 41.73
Std Dev 3.581
Gaus mean : 41.8225
Gaus Width: 3.6708
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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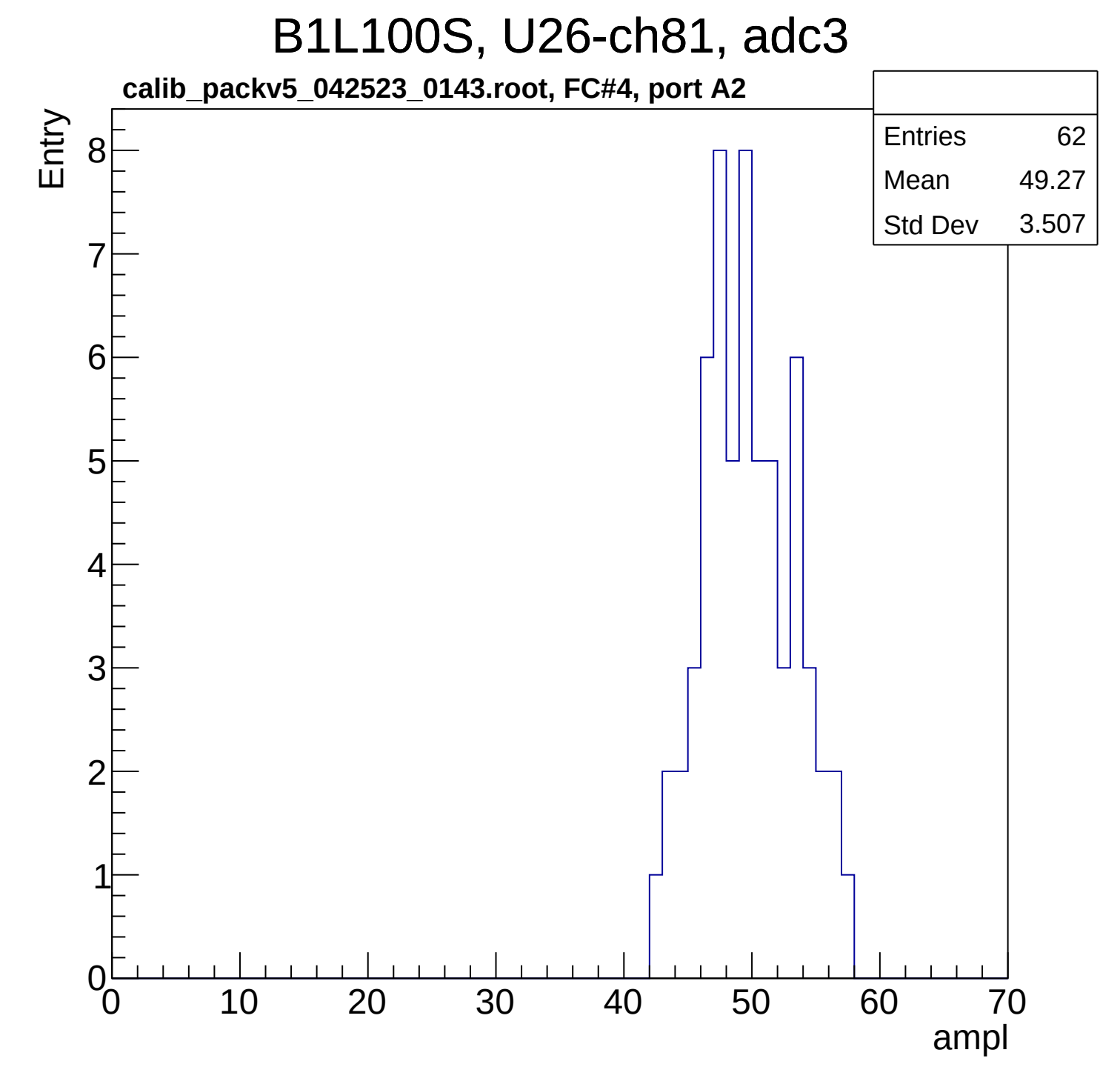
B1L100S, U26-ch81, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 49.27
Std Dev 3.507
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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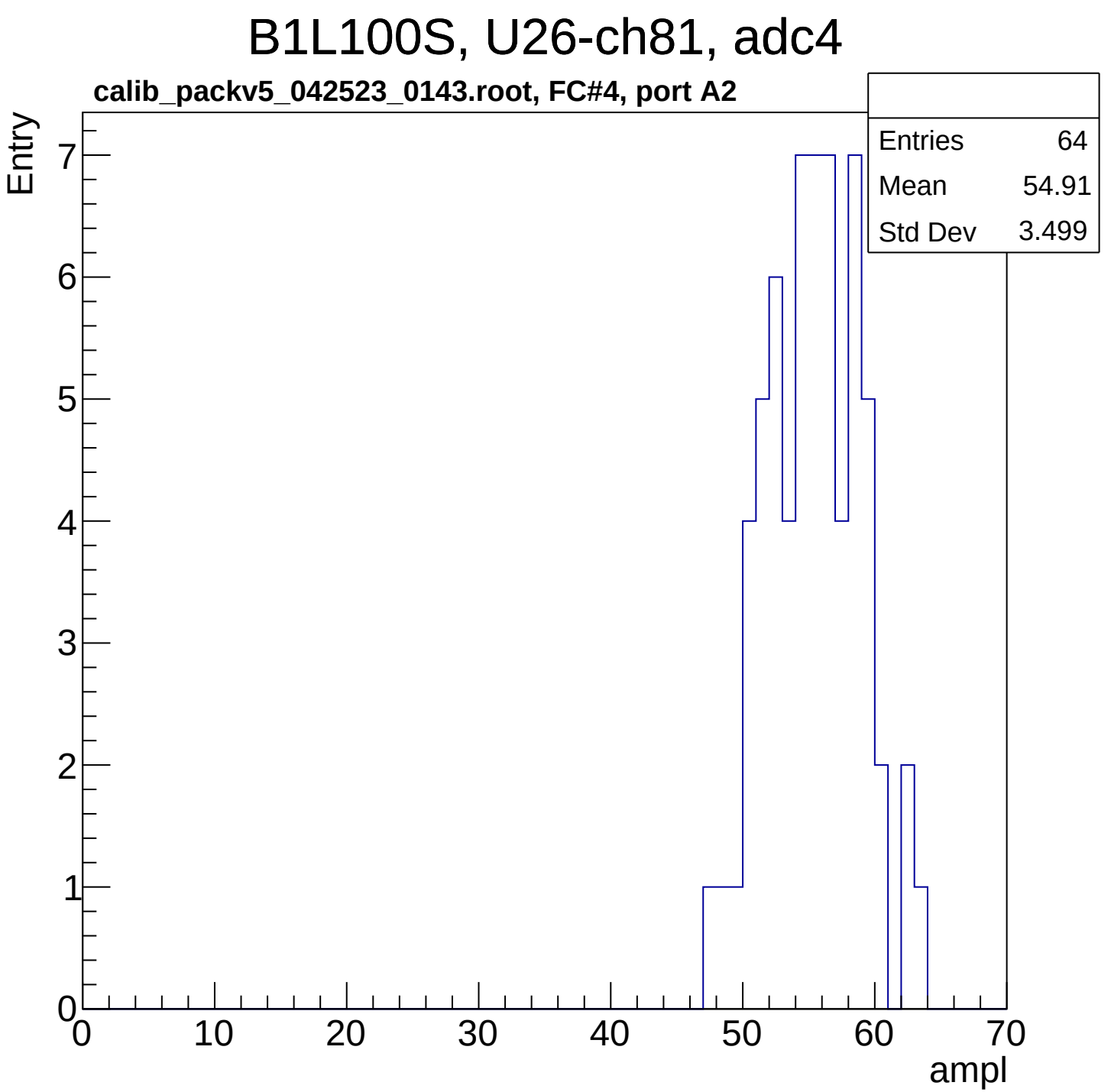
B1L100S, U26-ch81, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.91
Std Dev 3.499
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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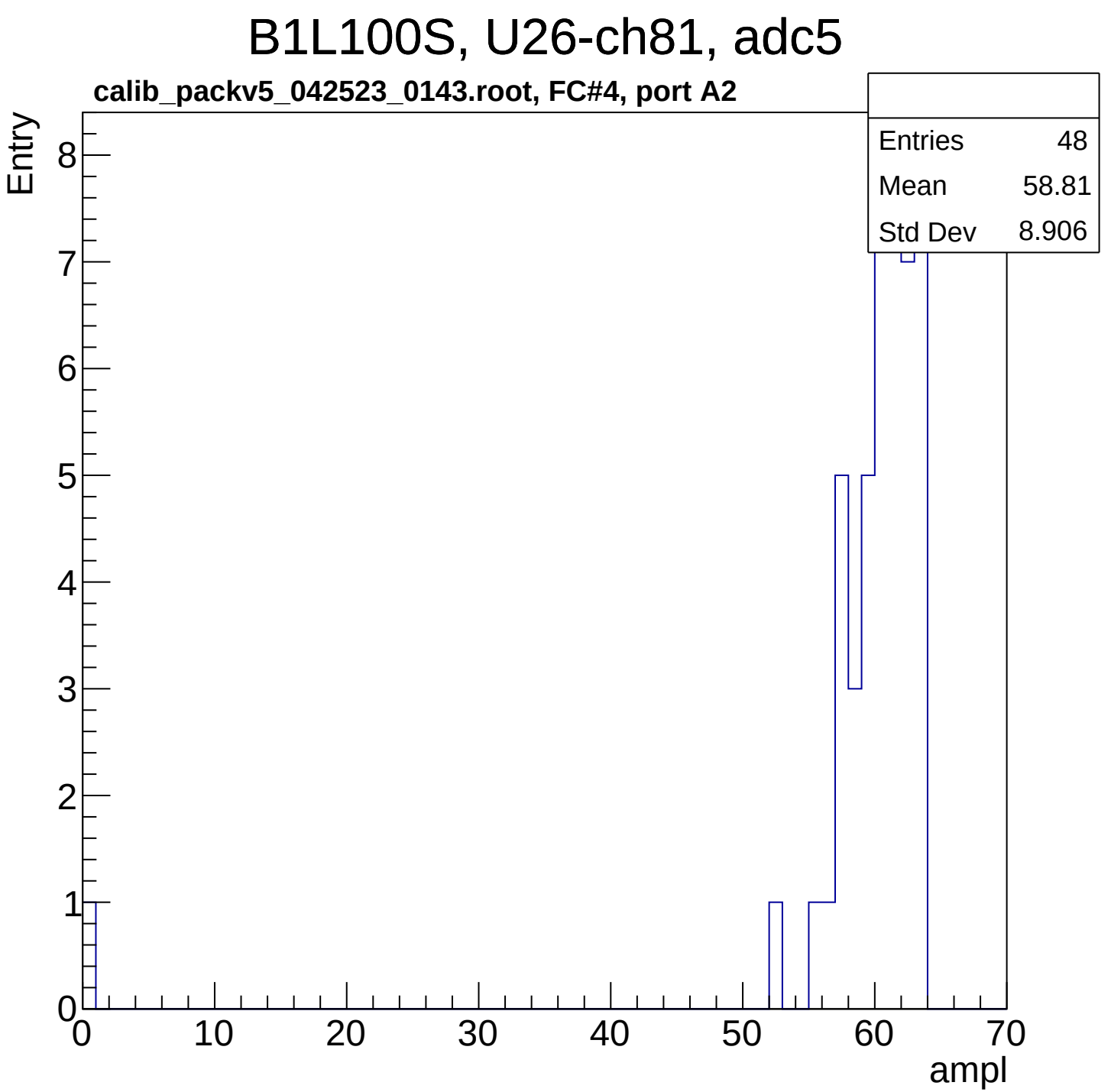
B1L100S, U26-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.81
Std Dev 8.906
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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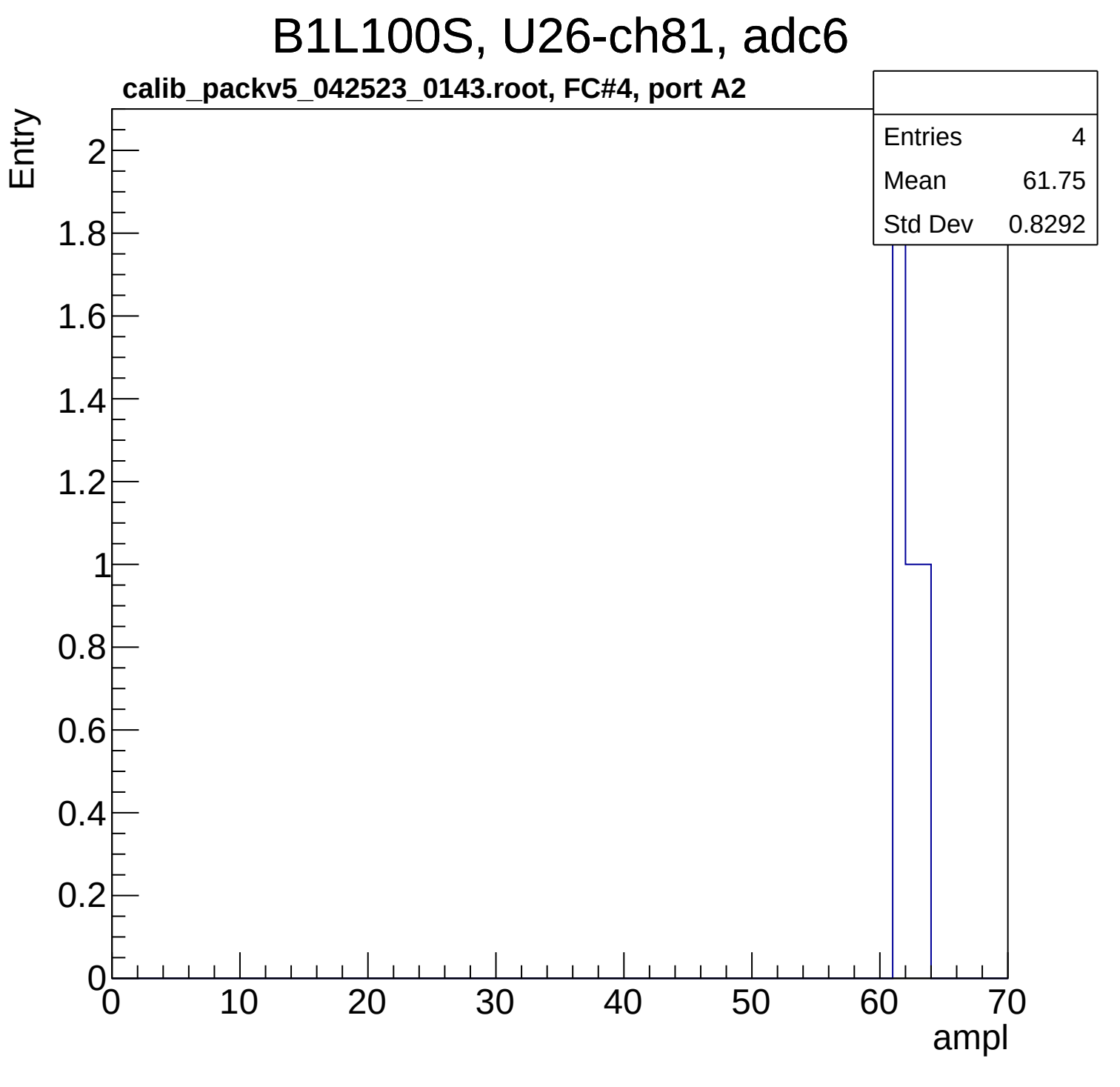
B1L100S, U26-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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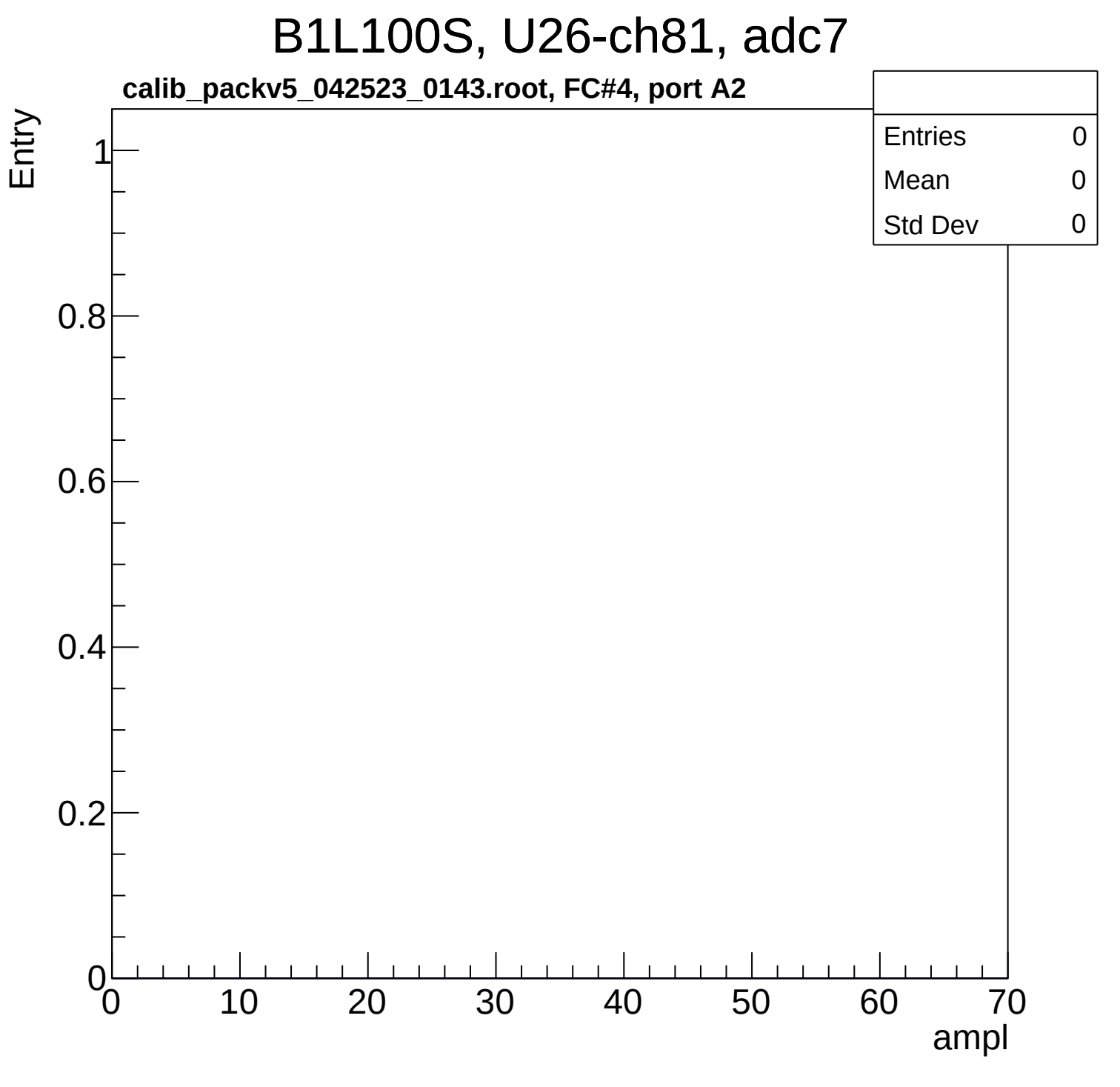
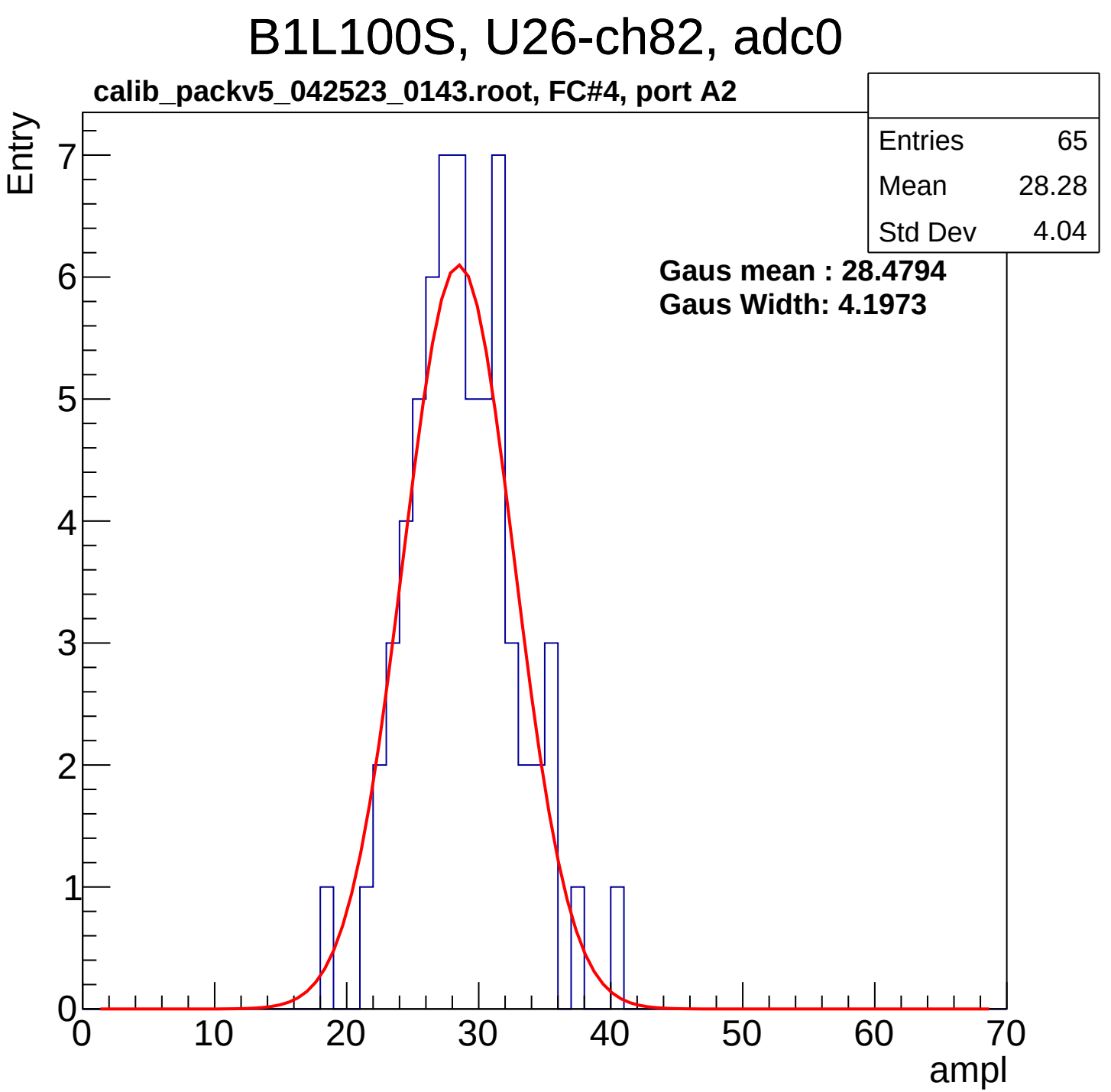
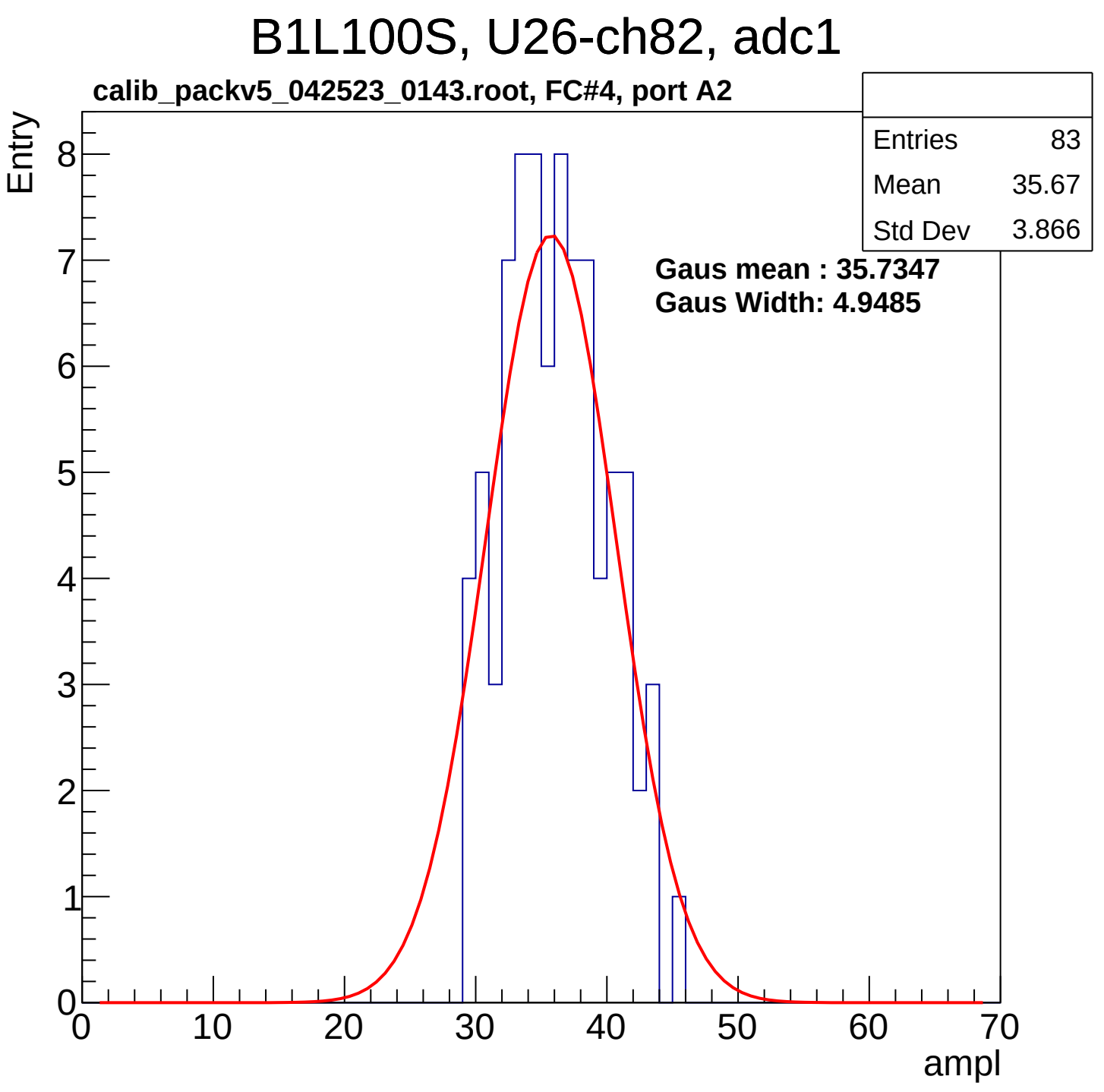

B1L100S, U26-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.28
Std Dev 4.04
Gaus mean : 28.4794
Gaus Width: 4.1973
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 35.67
Std Dev 3.866
Gaus mean : 35.7347
Gaus Width: 4.9485
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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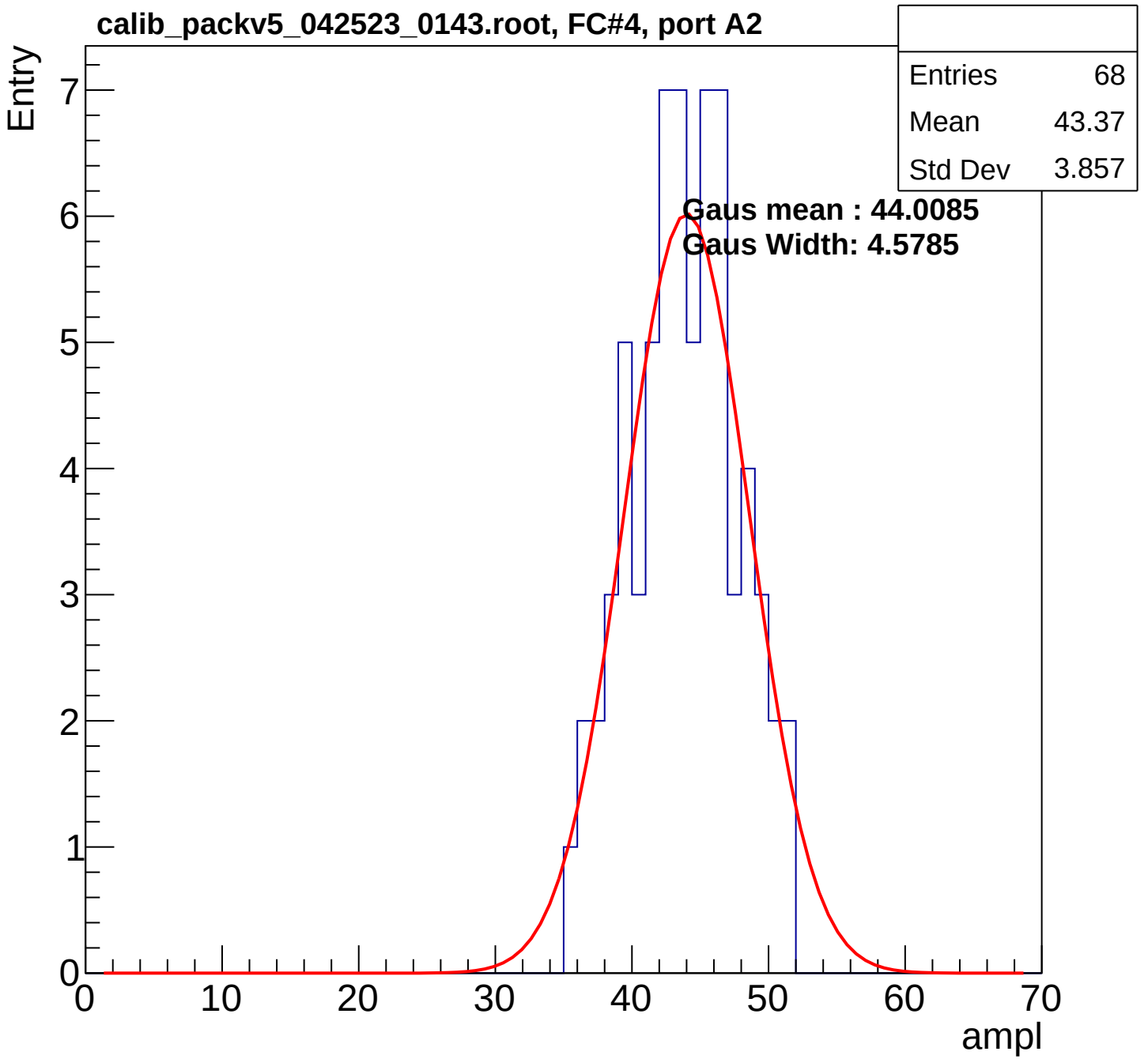
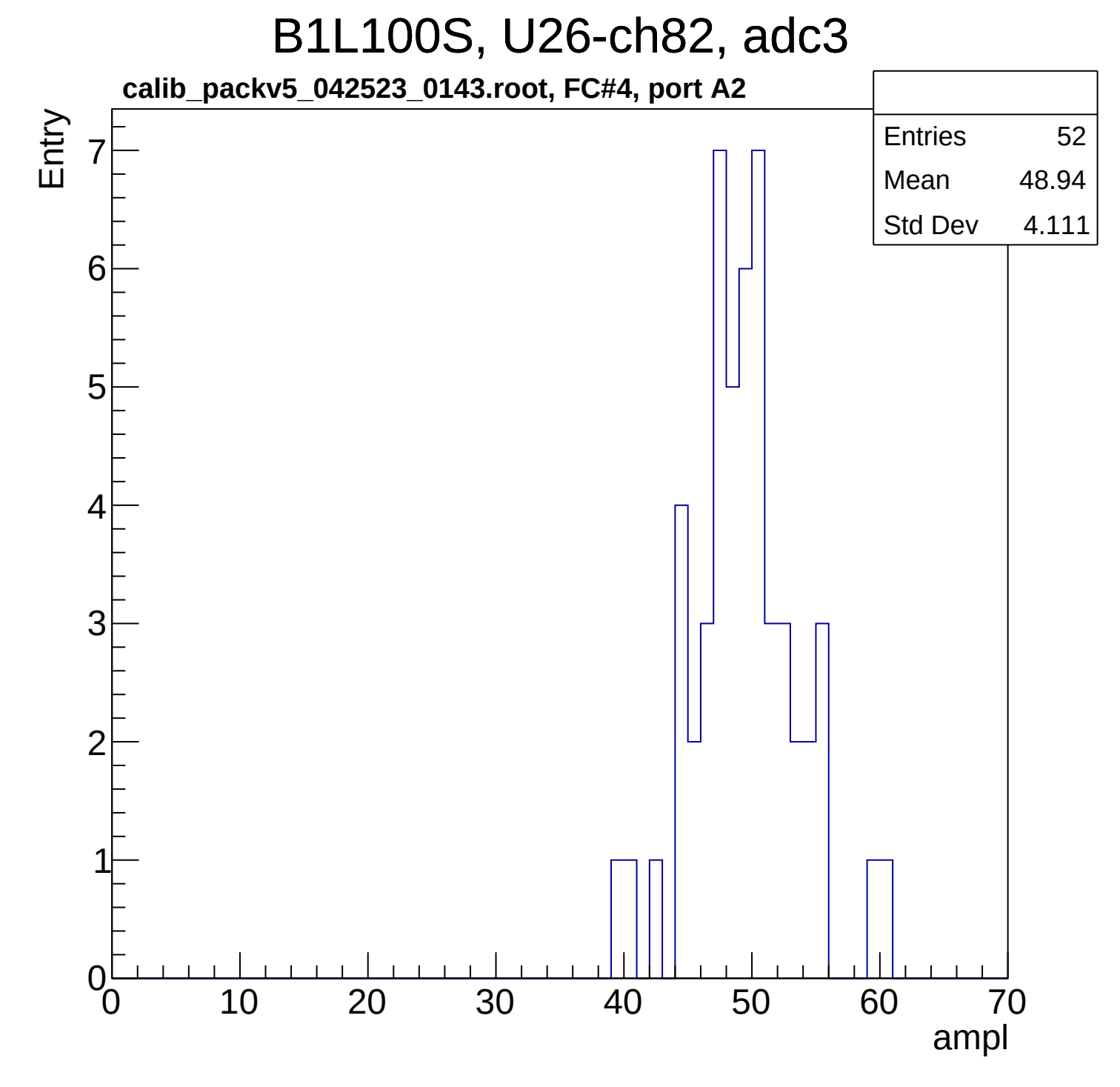
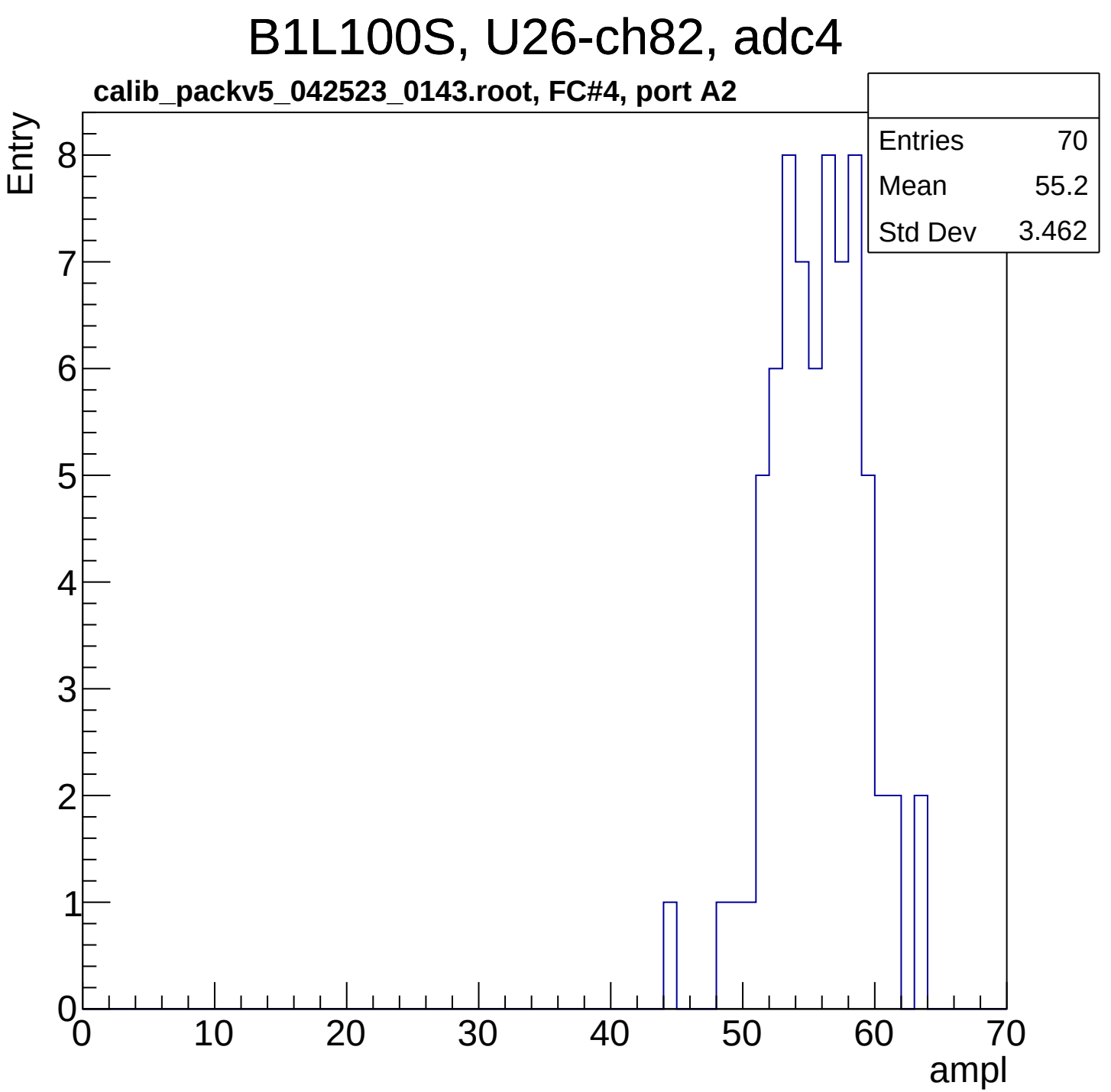
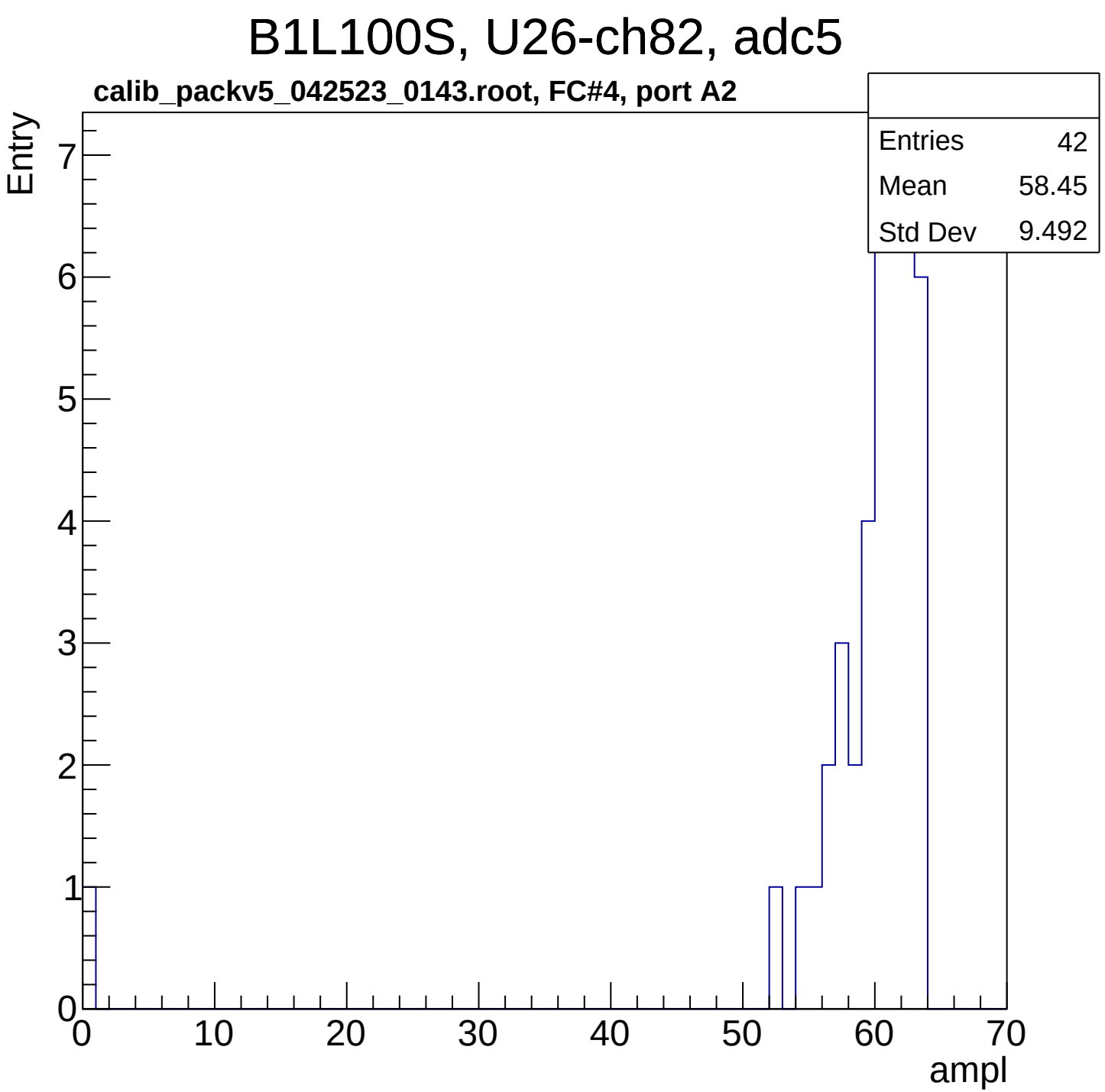
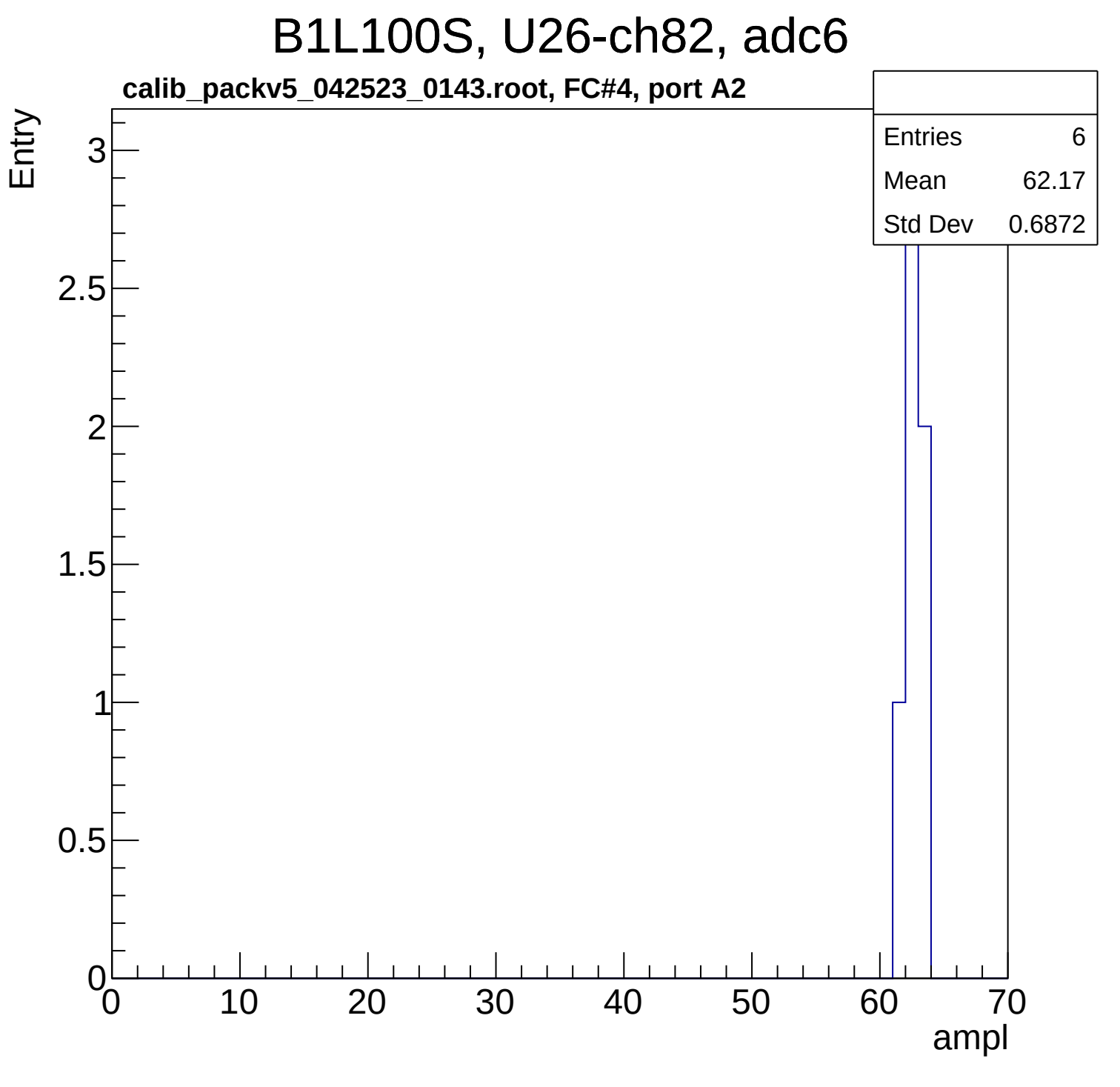
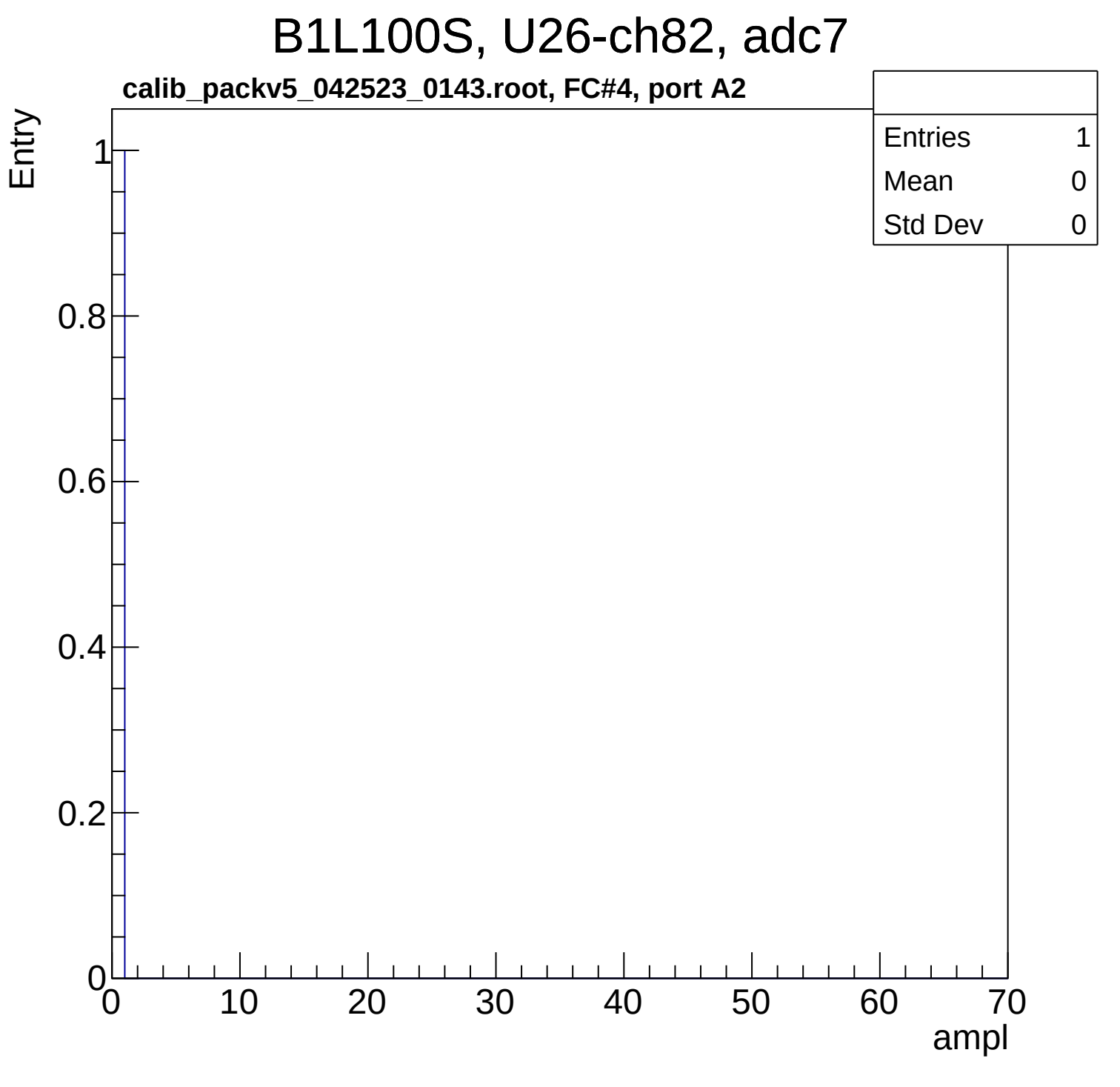

B1L100S, U26-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.37
Std Dev 3.857
Gaus mean : 44.0085
Gaus Width: 4.5785
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

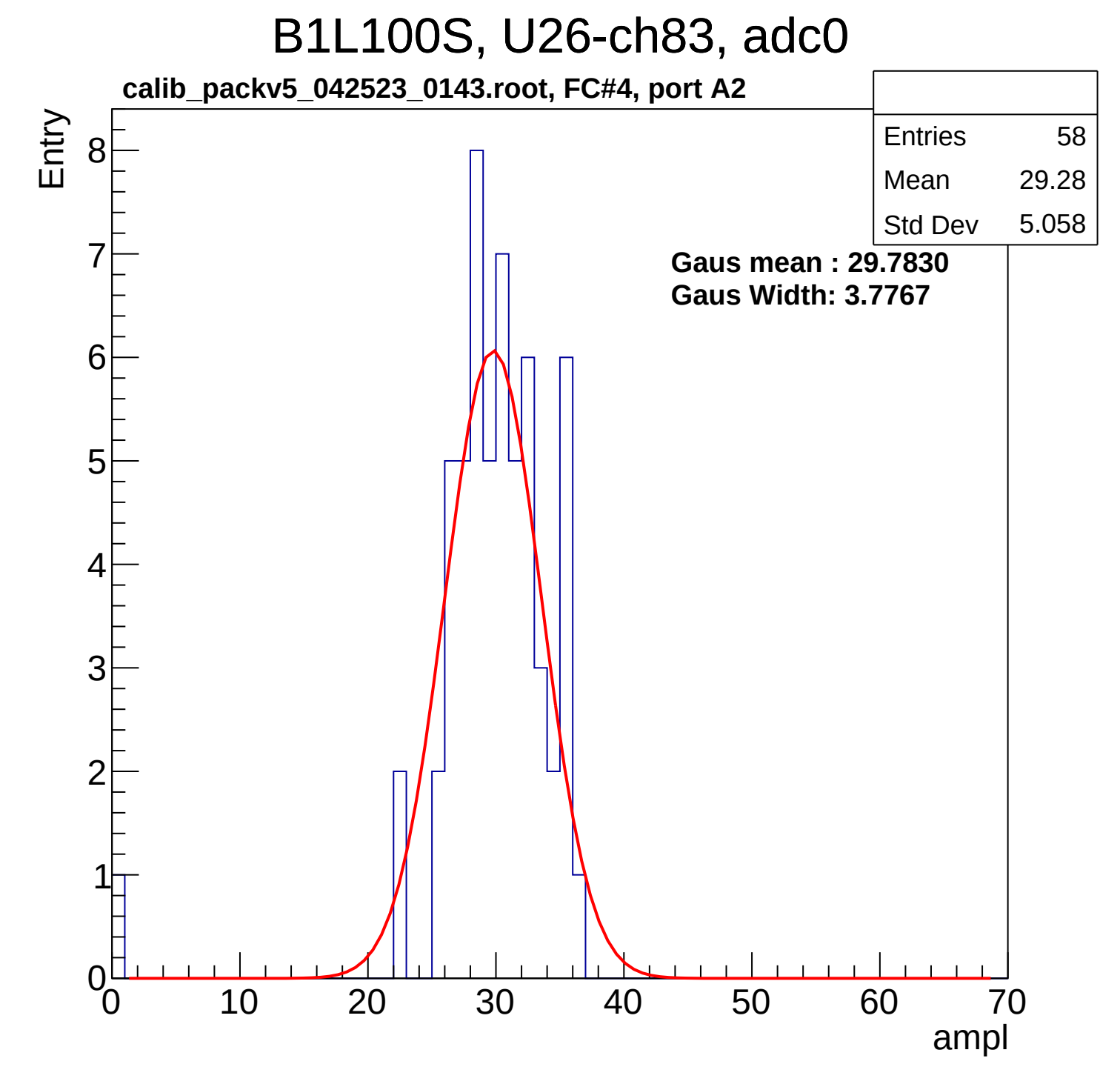

B1L100S, U26-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 48.94
Std Dev 4.111
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch82, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 55.2
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.45
Std Dev 9.492
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch82, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 29.28
Std Dev 5.058
Gaus mean : 29.7830
Gaus Width: 3.7767
Entry
ampl
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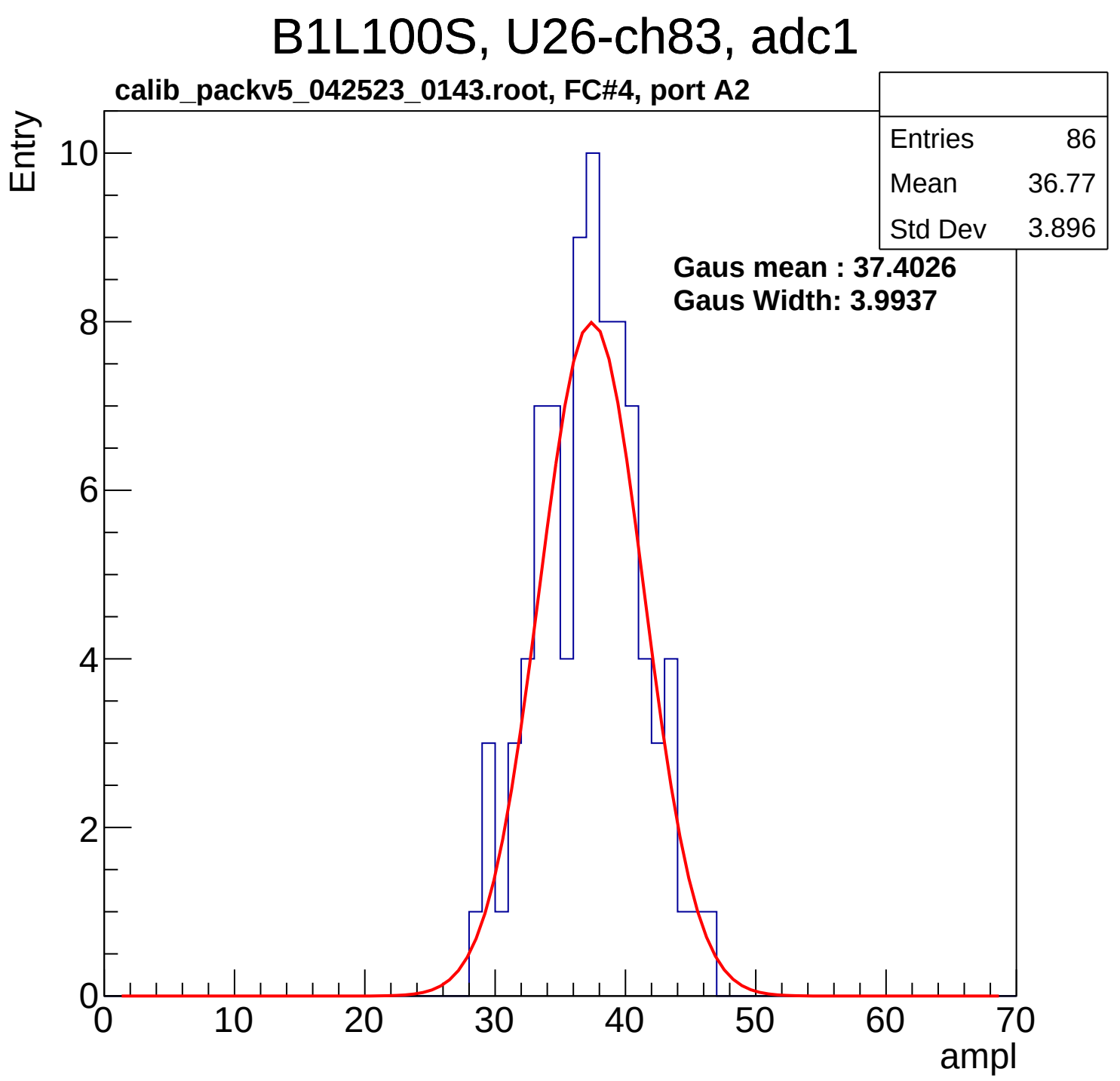
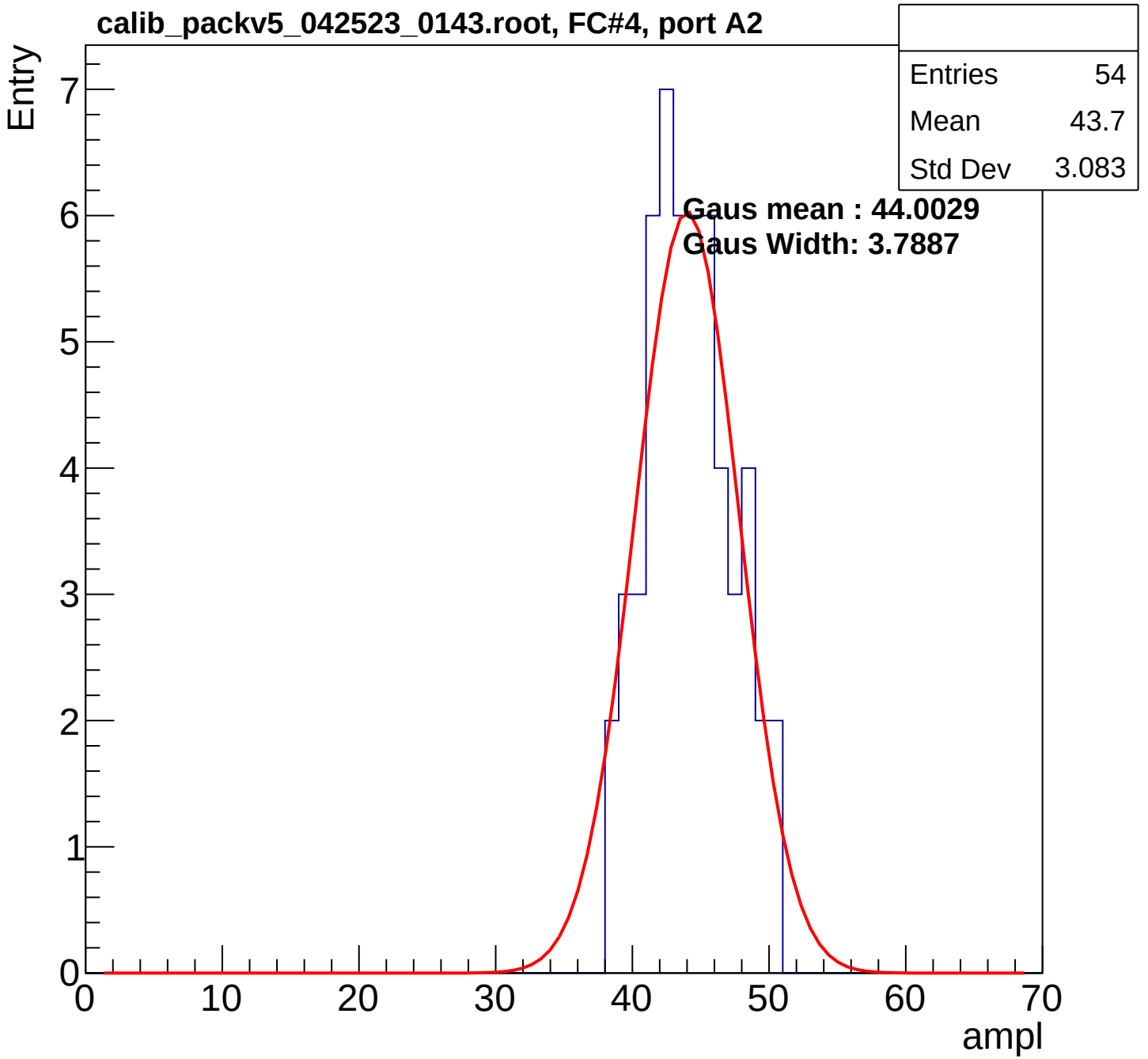
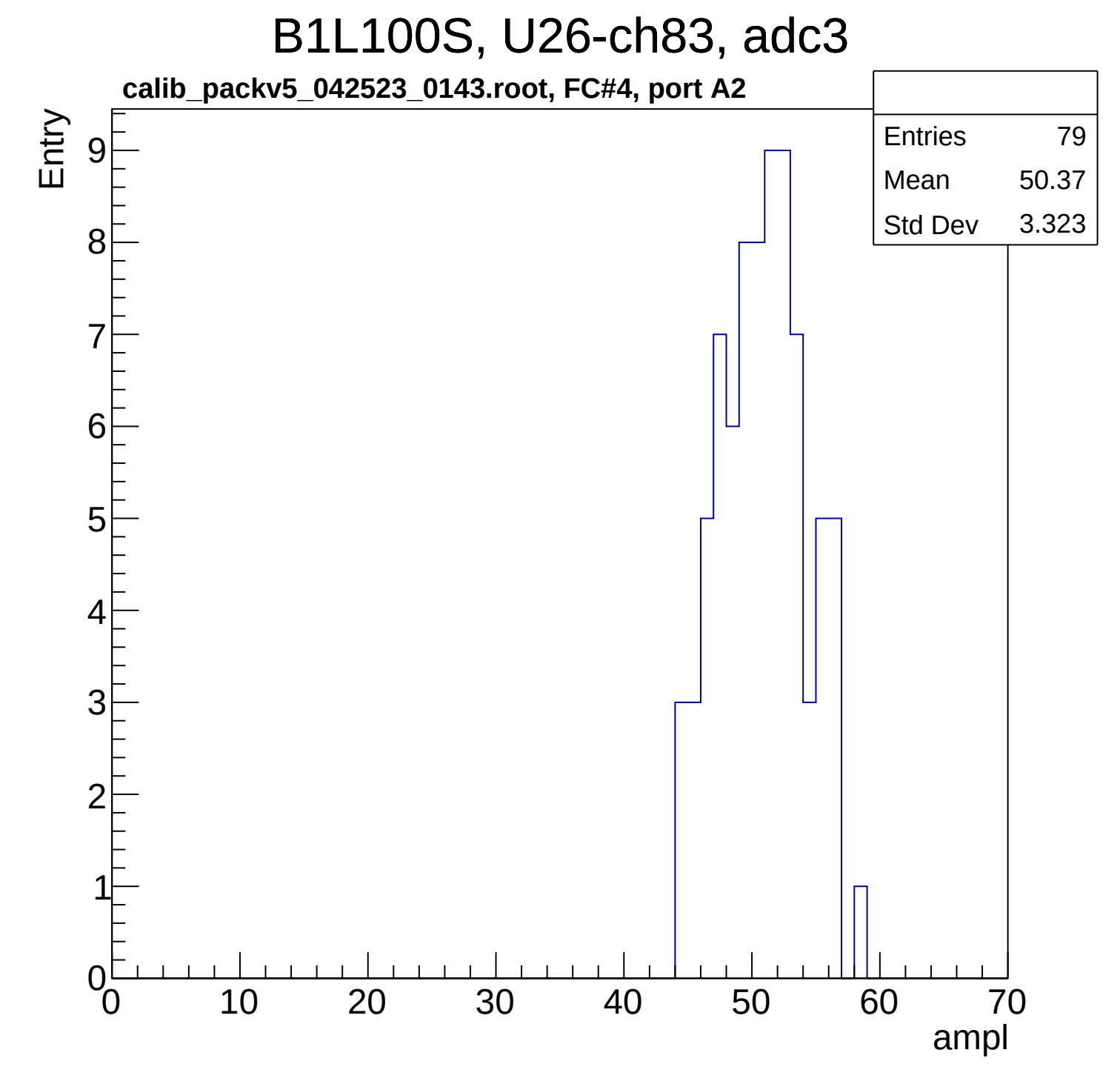
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch83, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 36.77
Std Dev 3.896
Gaus mean : 37.4026
Gaus Width: 3.9937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 43.7
Std Dev 3.083
Gaus mean : 44.0029
Gaus Width: 3.7887
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch83, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 50.37
Std Dev 3.323
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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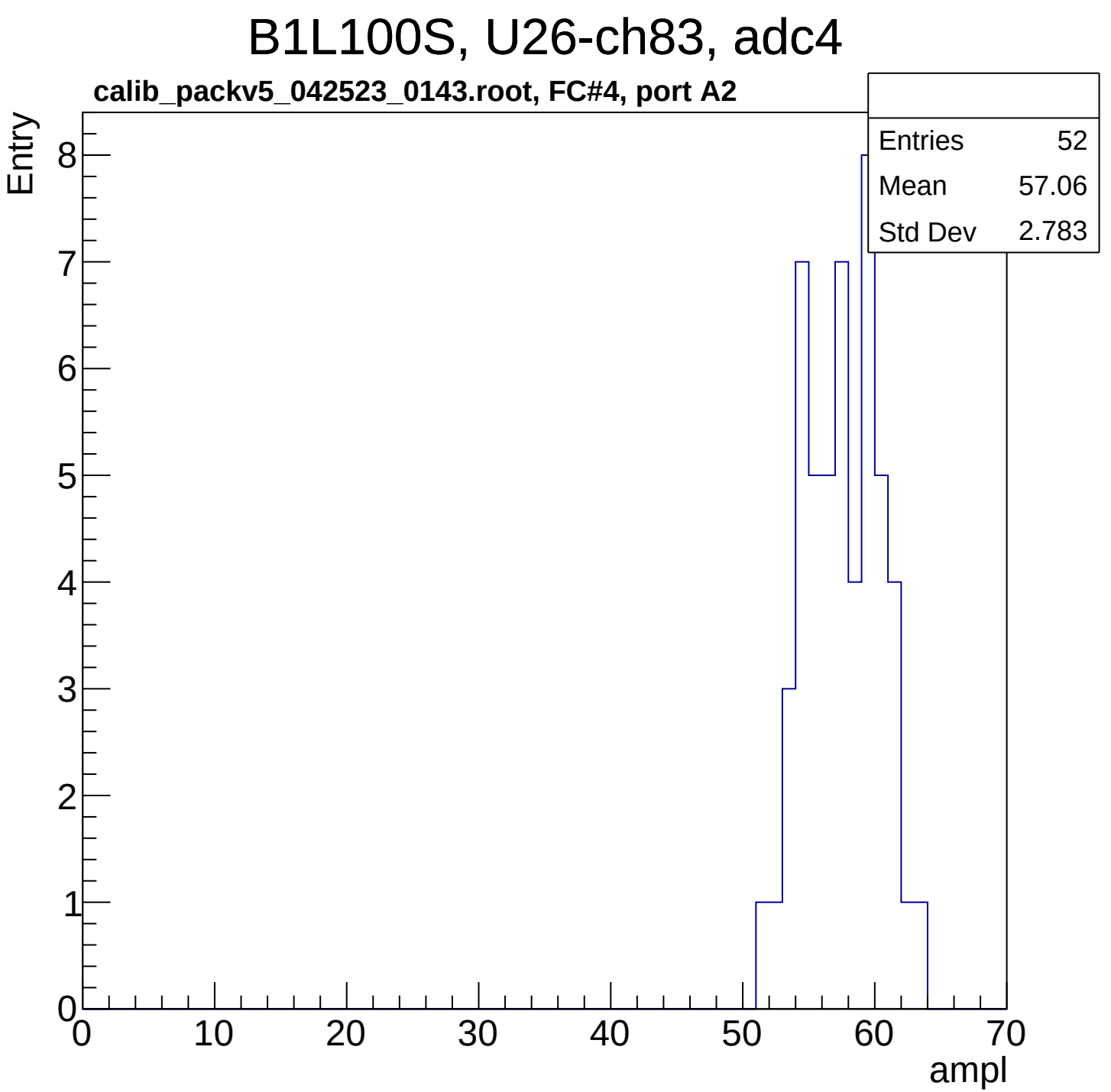
B1L100S, U26-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 57.06
Std Dev 2.783
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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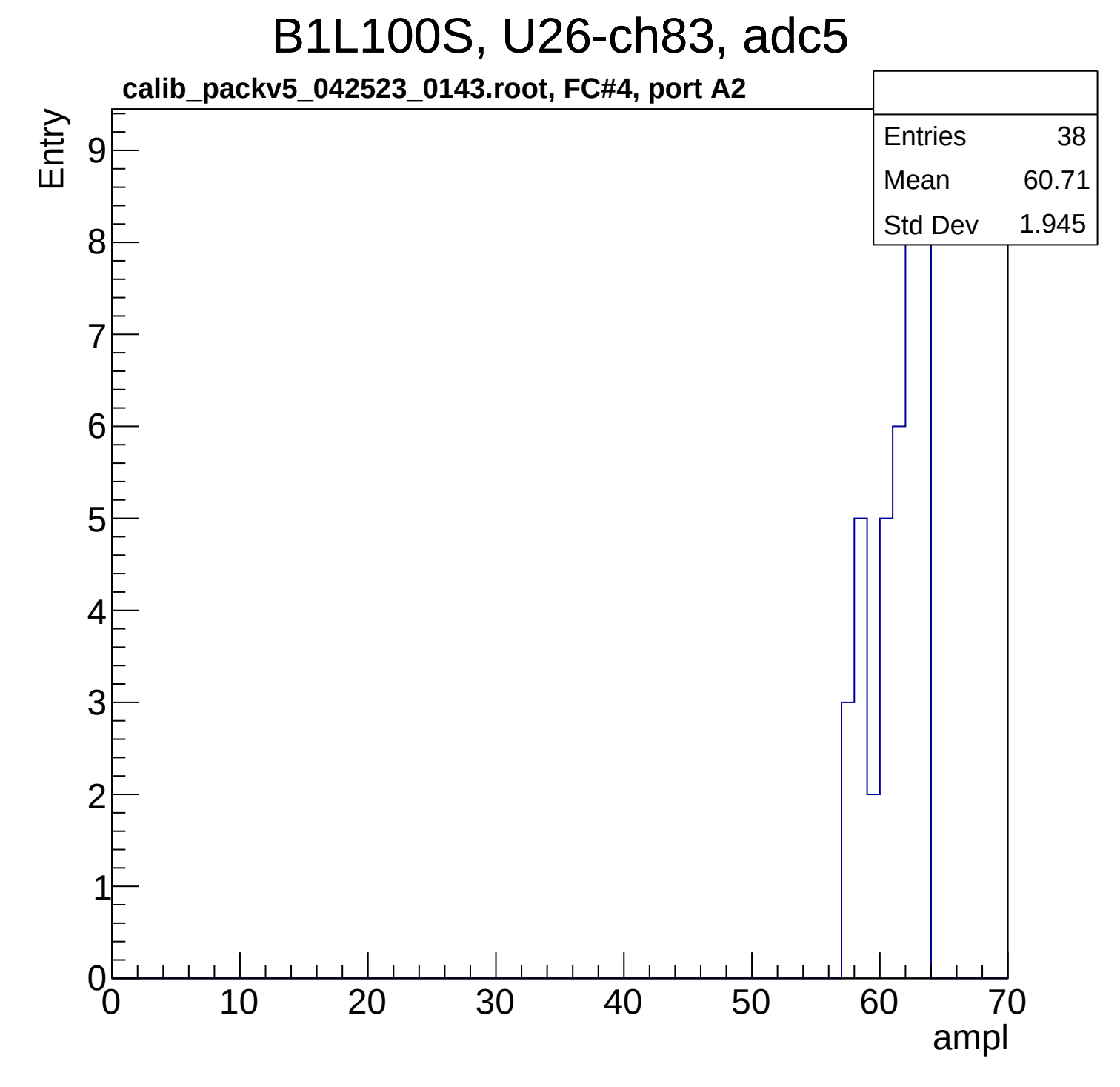
B1L100S, U26-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.71
Std Dev 1.945
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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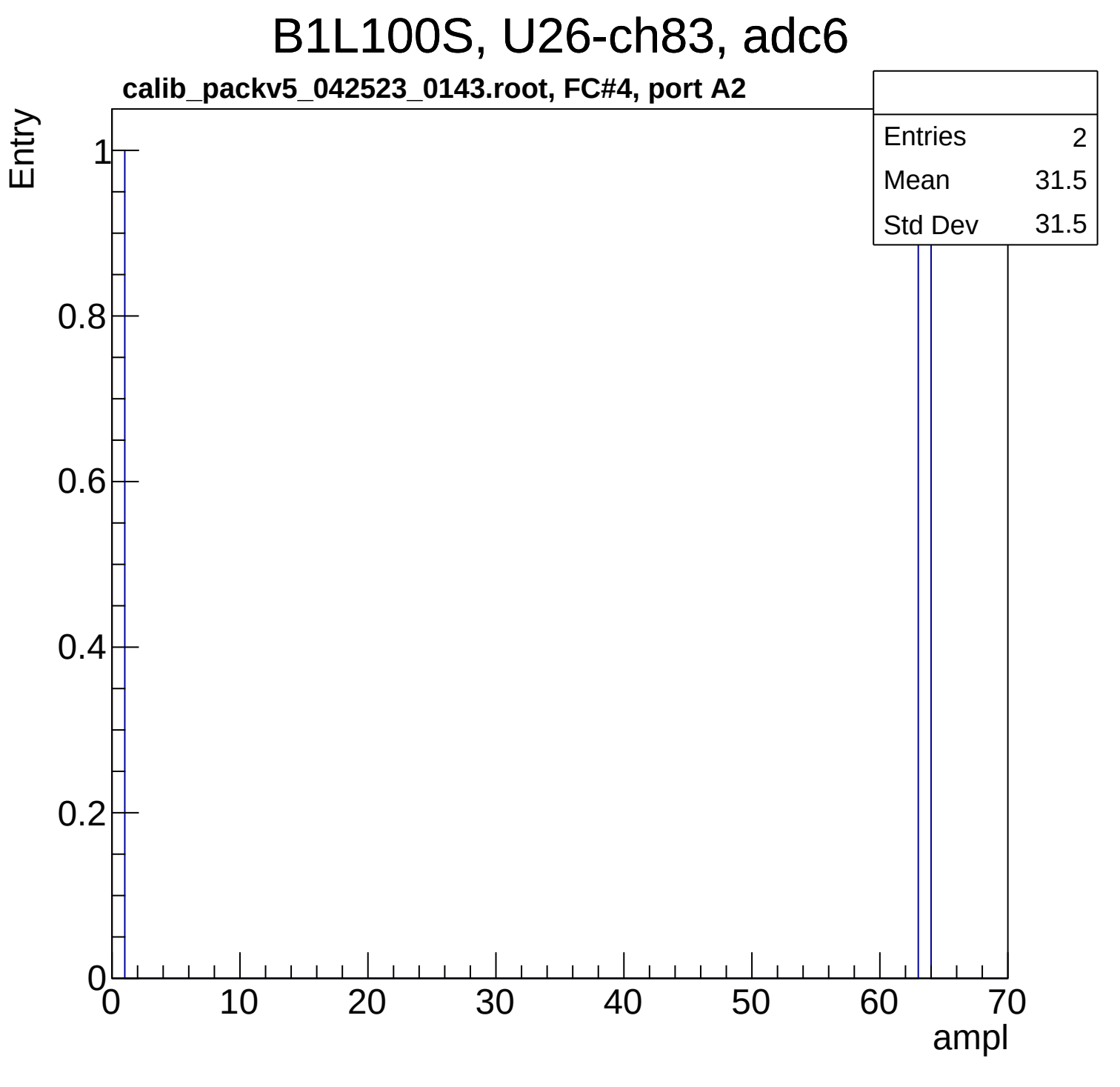
B1L100S, U26-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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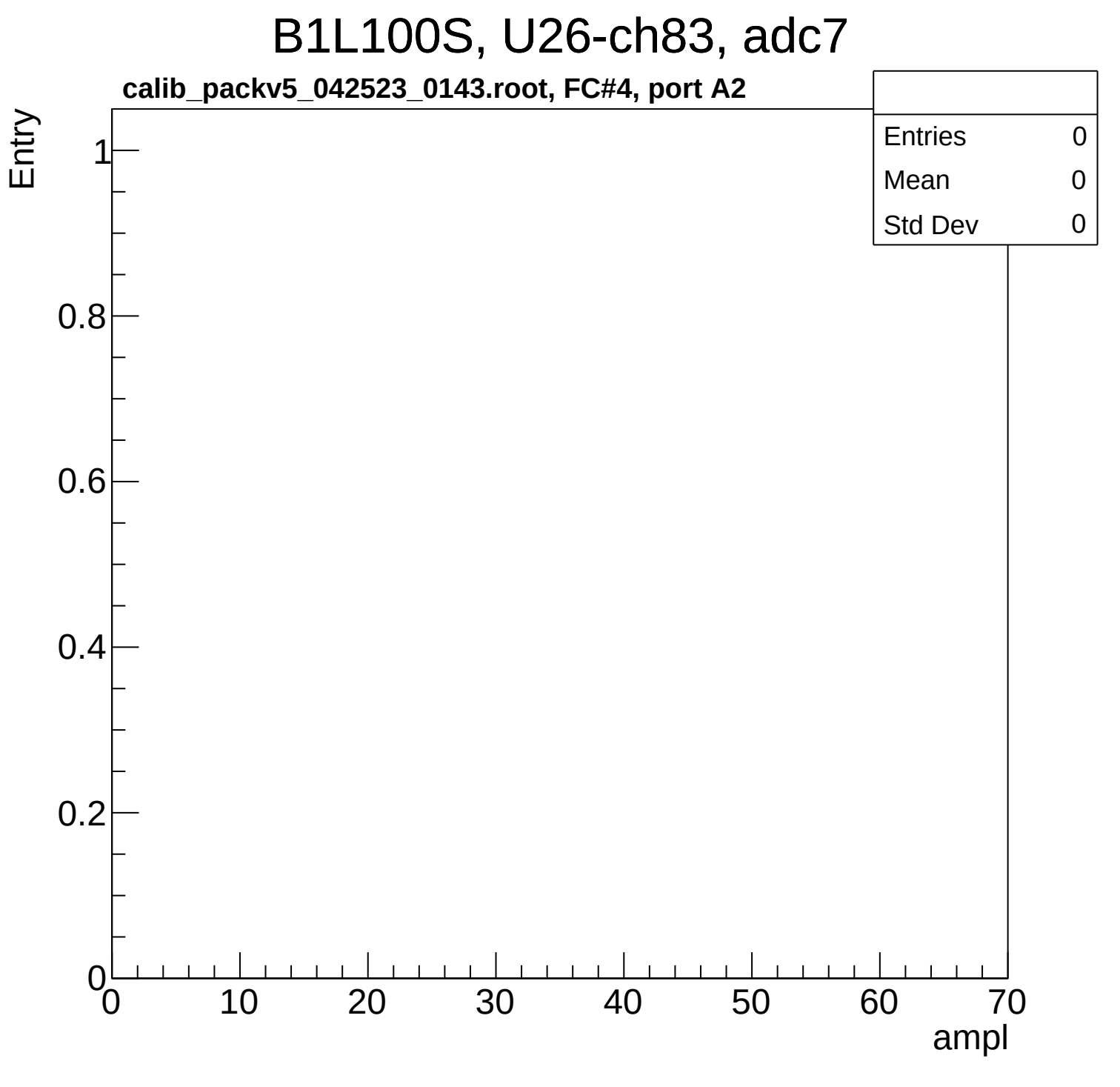
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch83, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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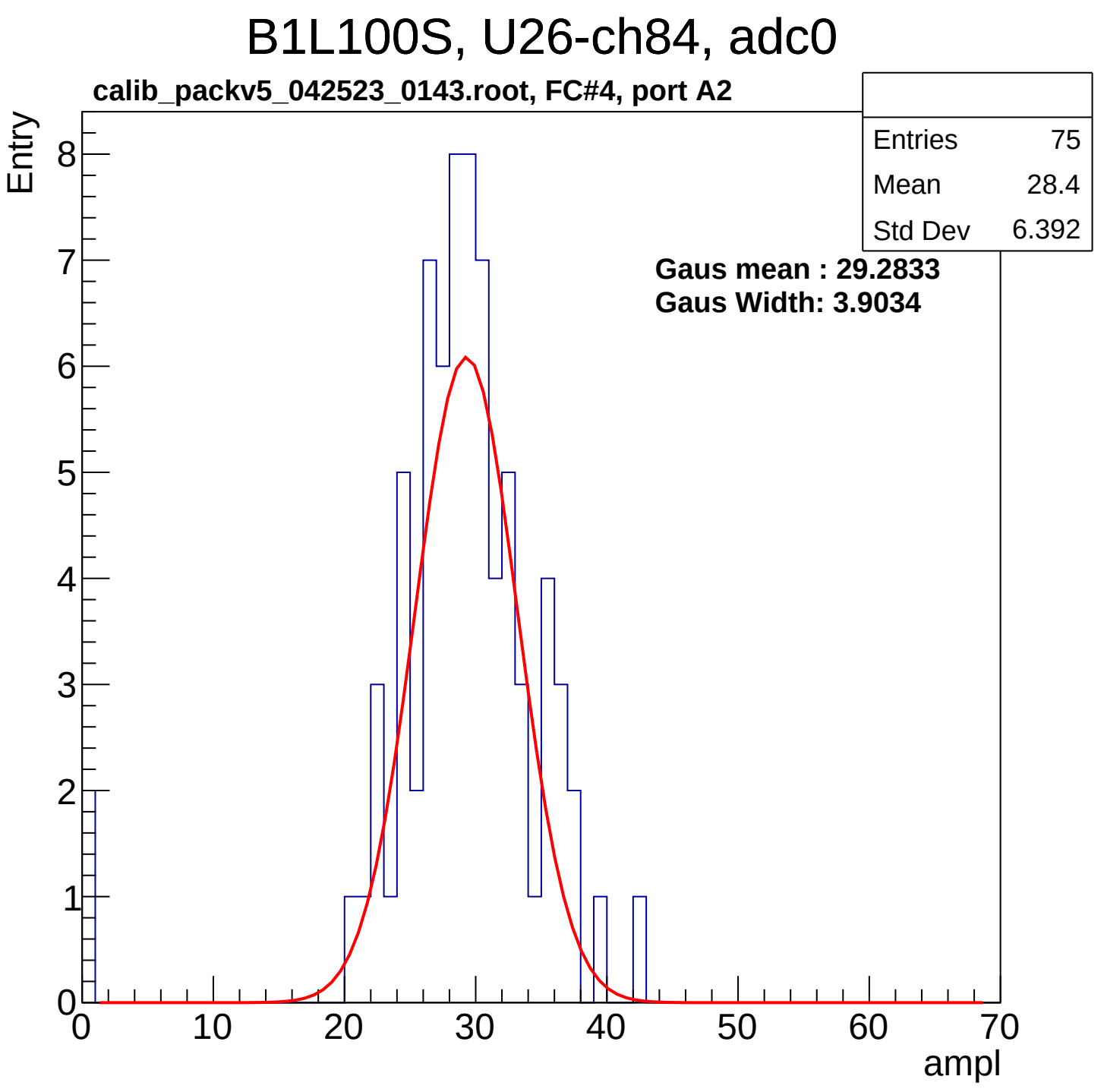
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.4
Std Dev 6.392
Gaus mean : 29.2833
Gaus Width: 3.9034
Entry
ampl
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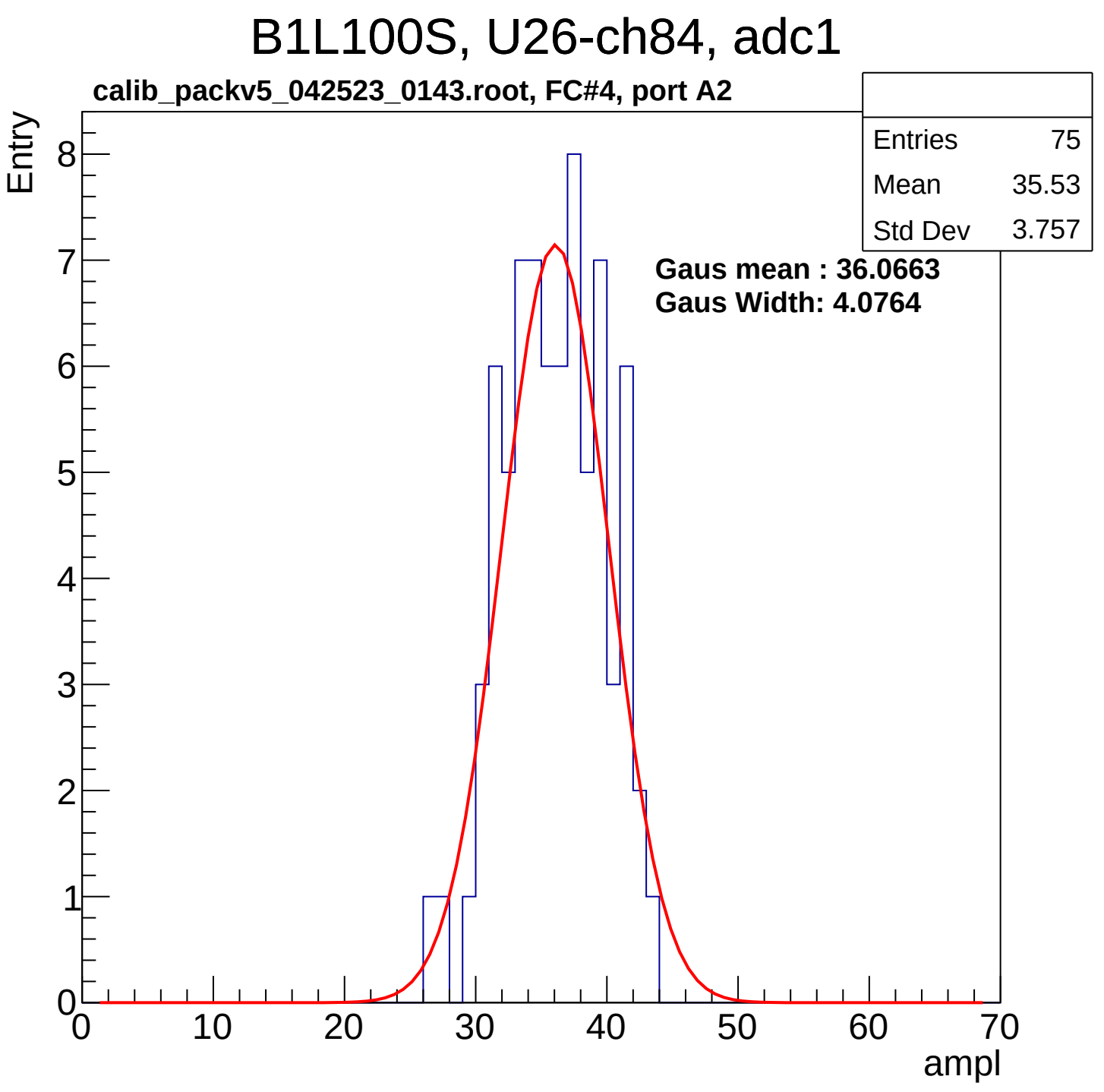
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.53
Std Dev 3.757
Gaus mean : 36.0663
Gaus Width: 4.0764
Entry
ampl
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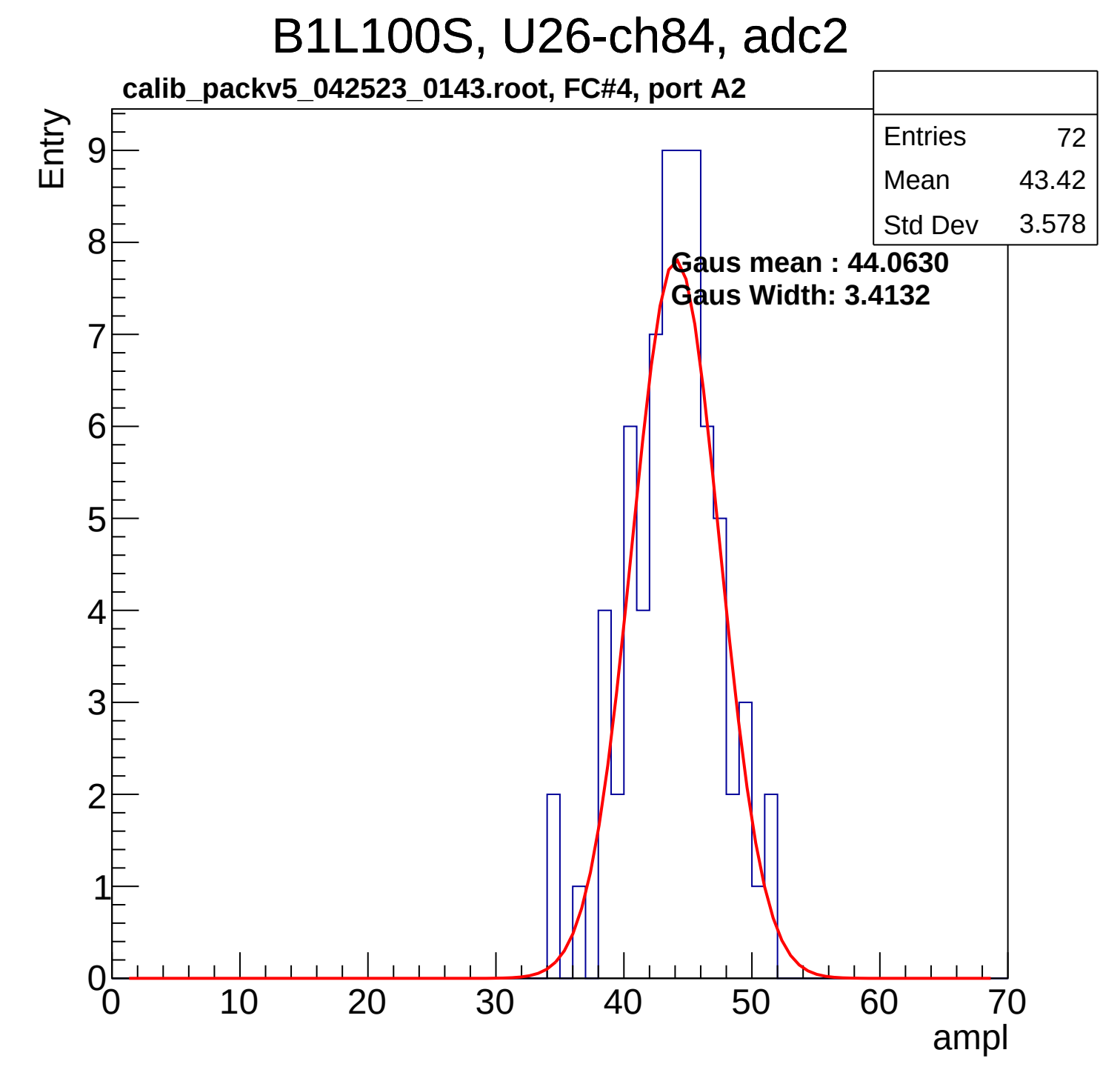
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch84, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 43.42
Std Dev 3.578
Gaus mean : 44.0630
Gaus Width: 3.4132
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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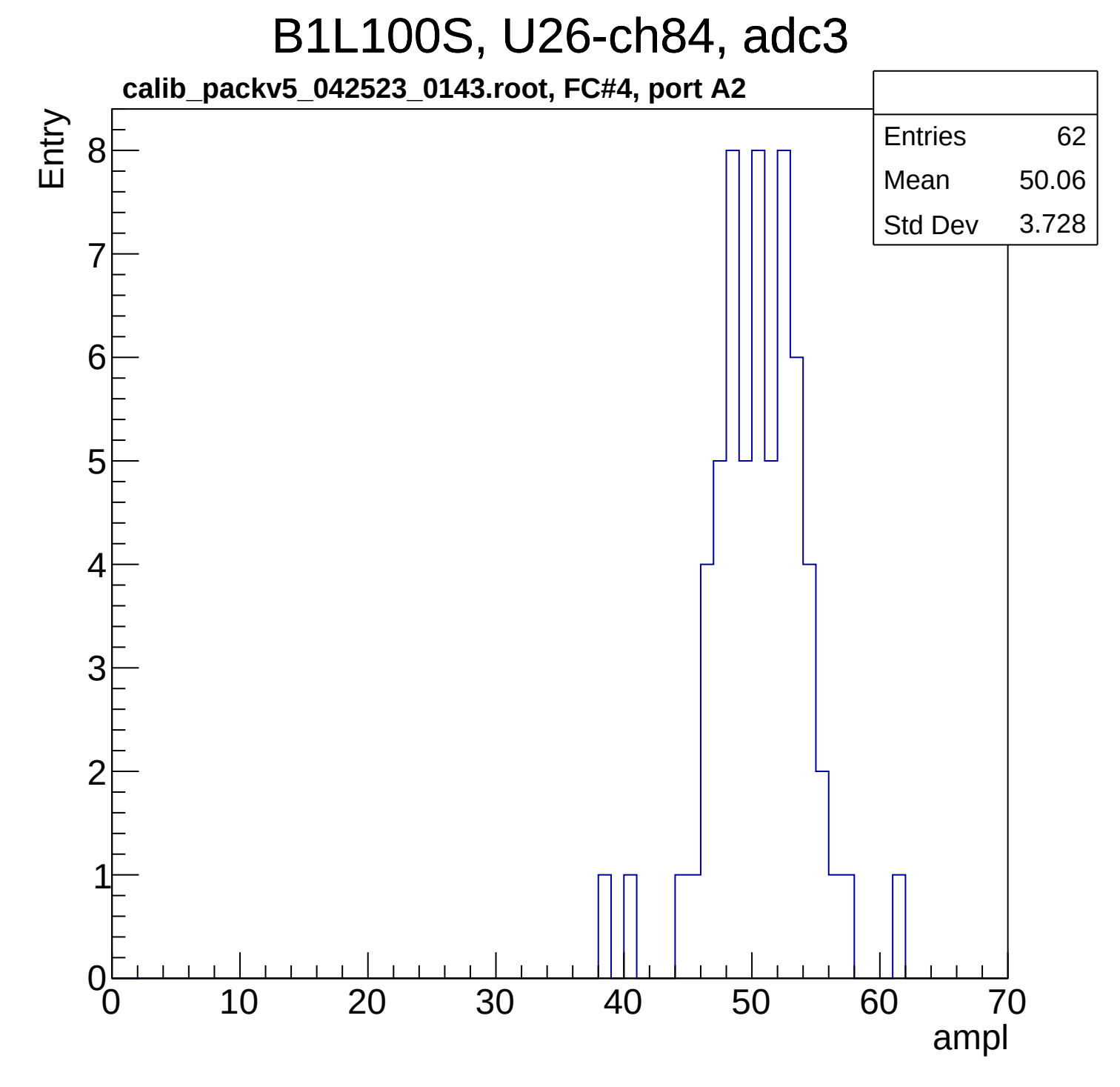
B1L100S, U26-ch84, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.06
Std Dev 3.728
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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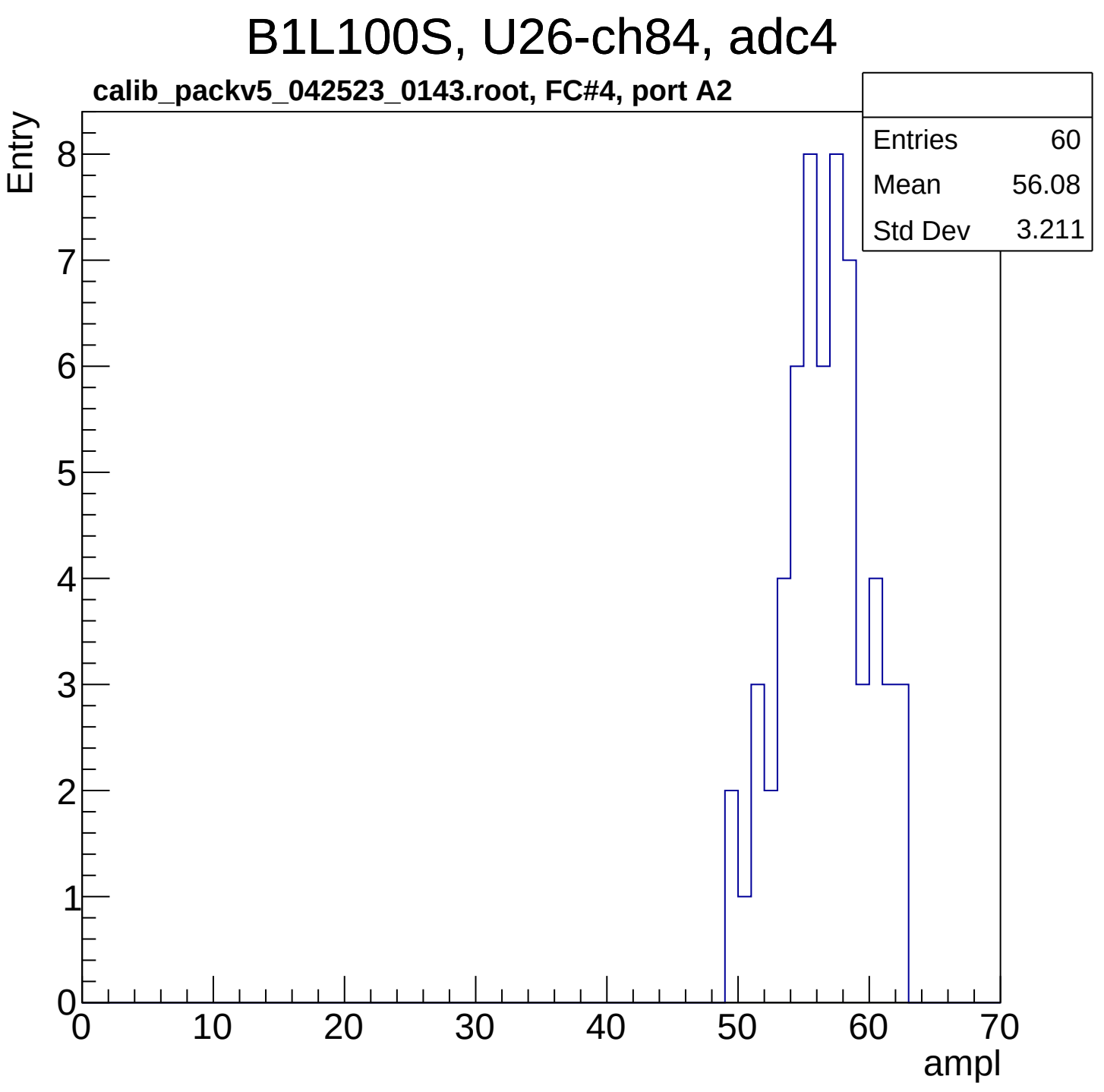
B1L100S, U26-ch84, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.08
Std Dev 3.211
Entry
ampl
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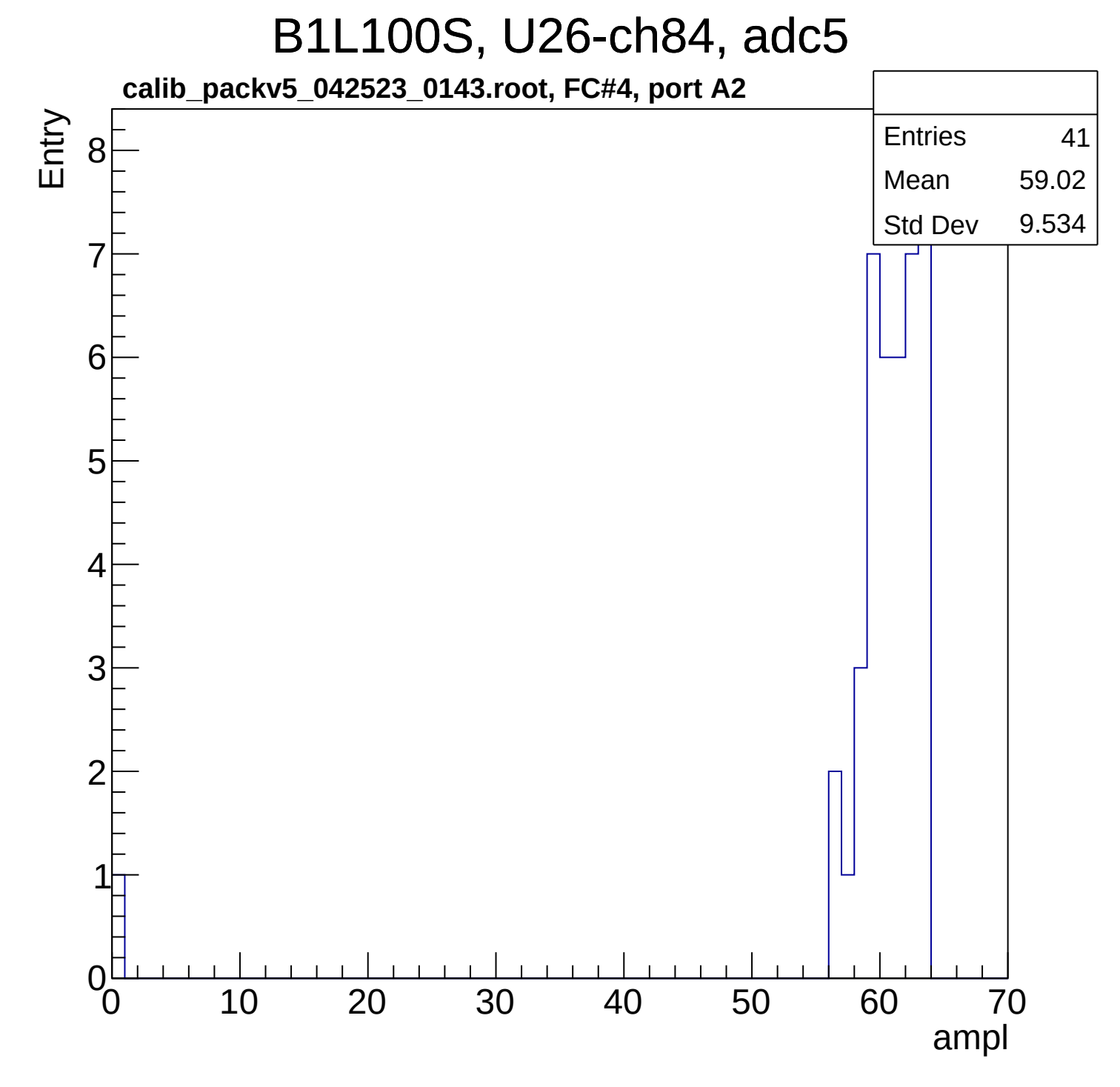
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.02
Std Dev 9.534
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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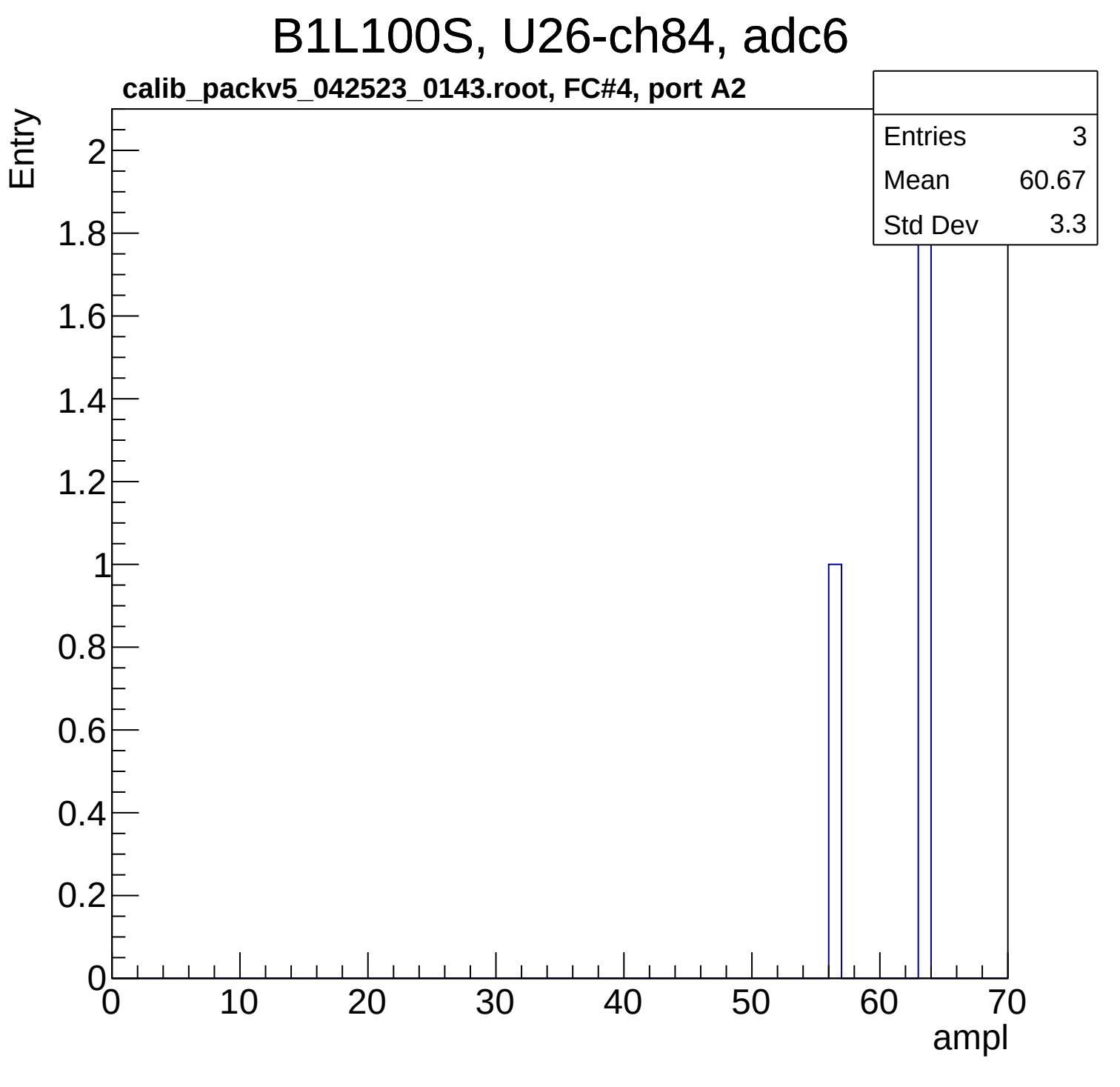
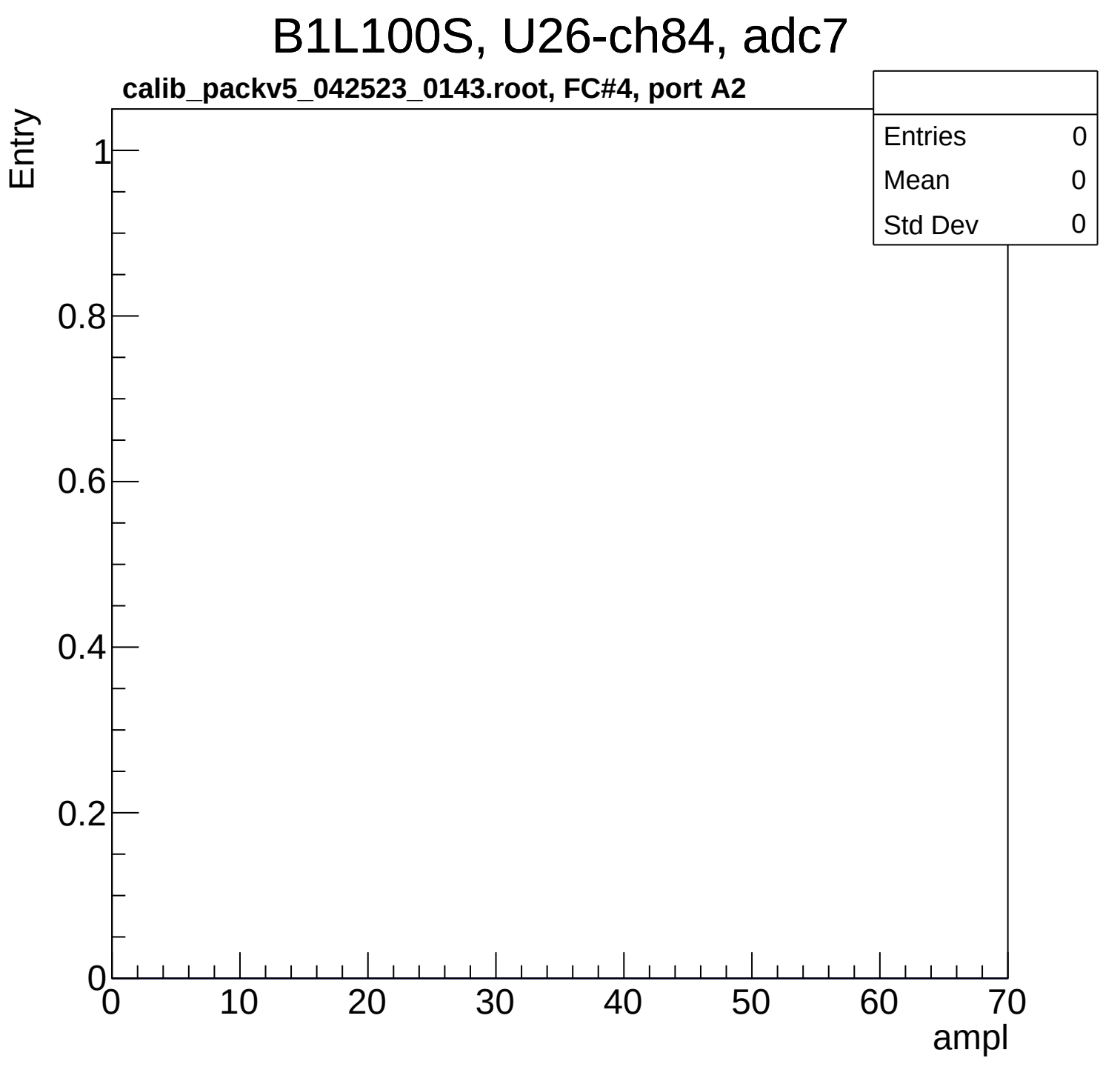

B1L100S, U26-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 3.3
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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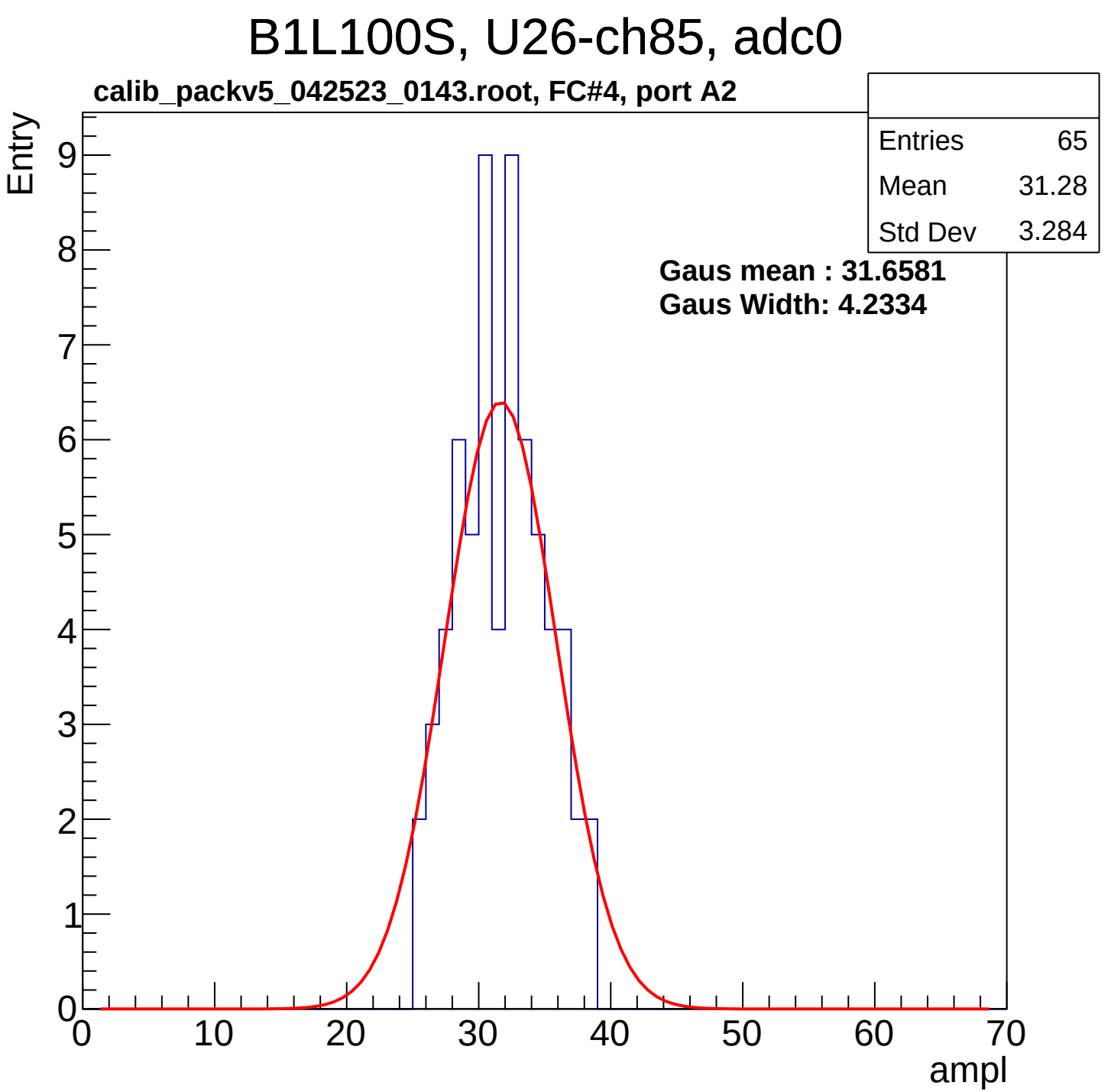
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 31.28
Std Dev 3.284
Gaus mean : 31.6581
Gaus Width: 4.2334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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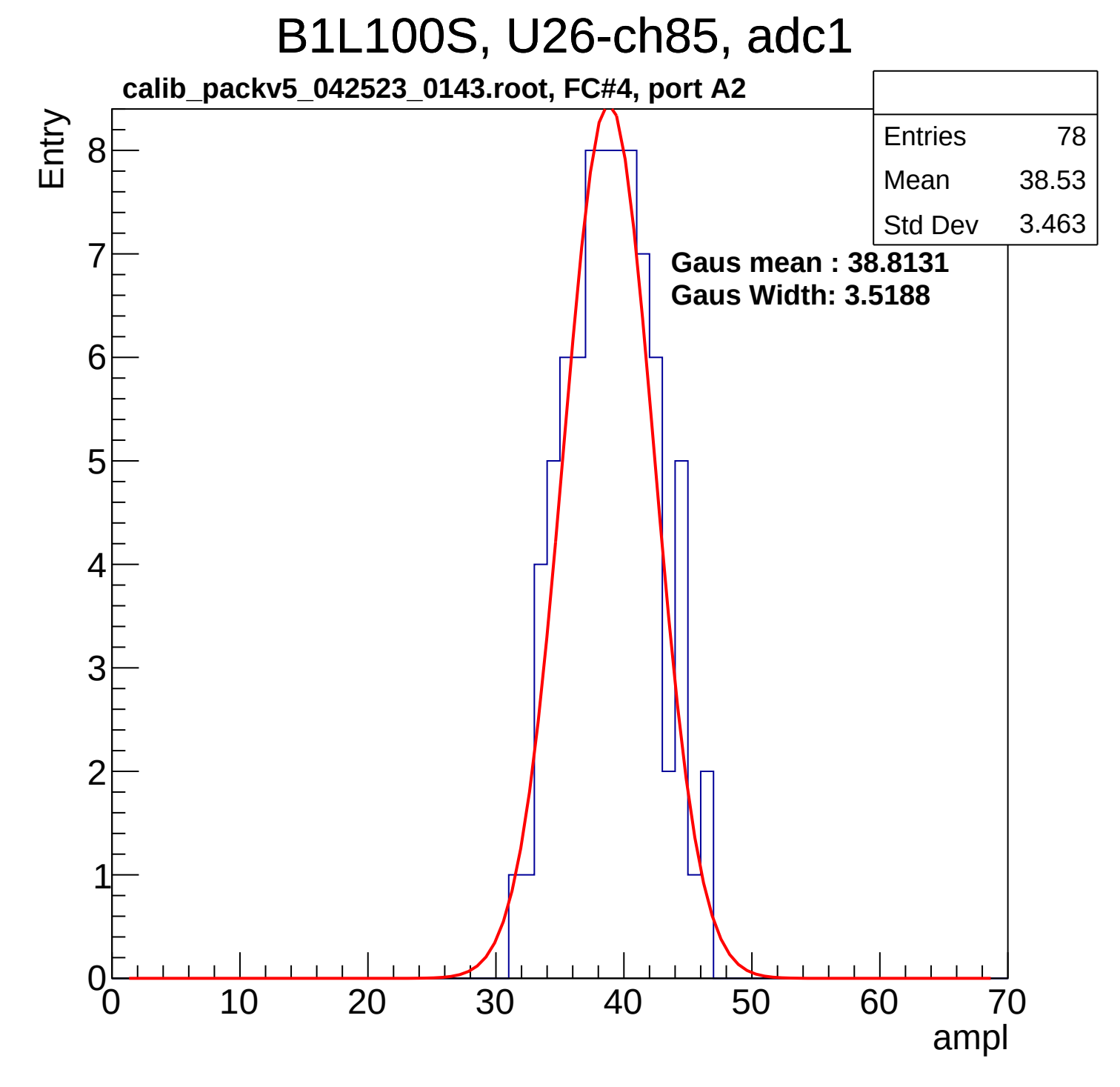
B1L100S, U26-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 38.53
Std Dev 3.463
Gaus mean : 38.8131
Gaus Width: 3.5188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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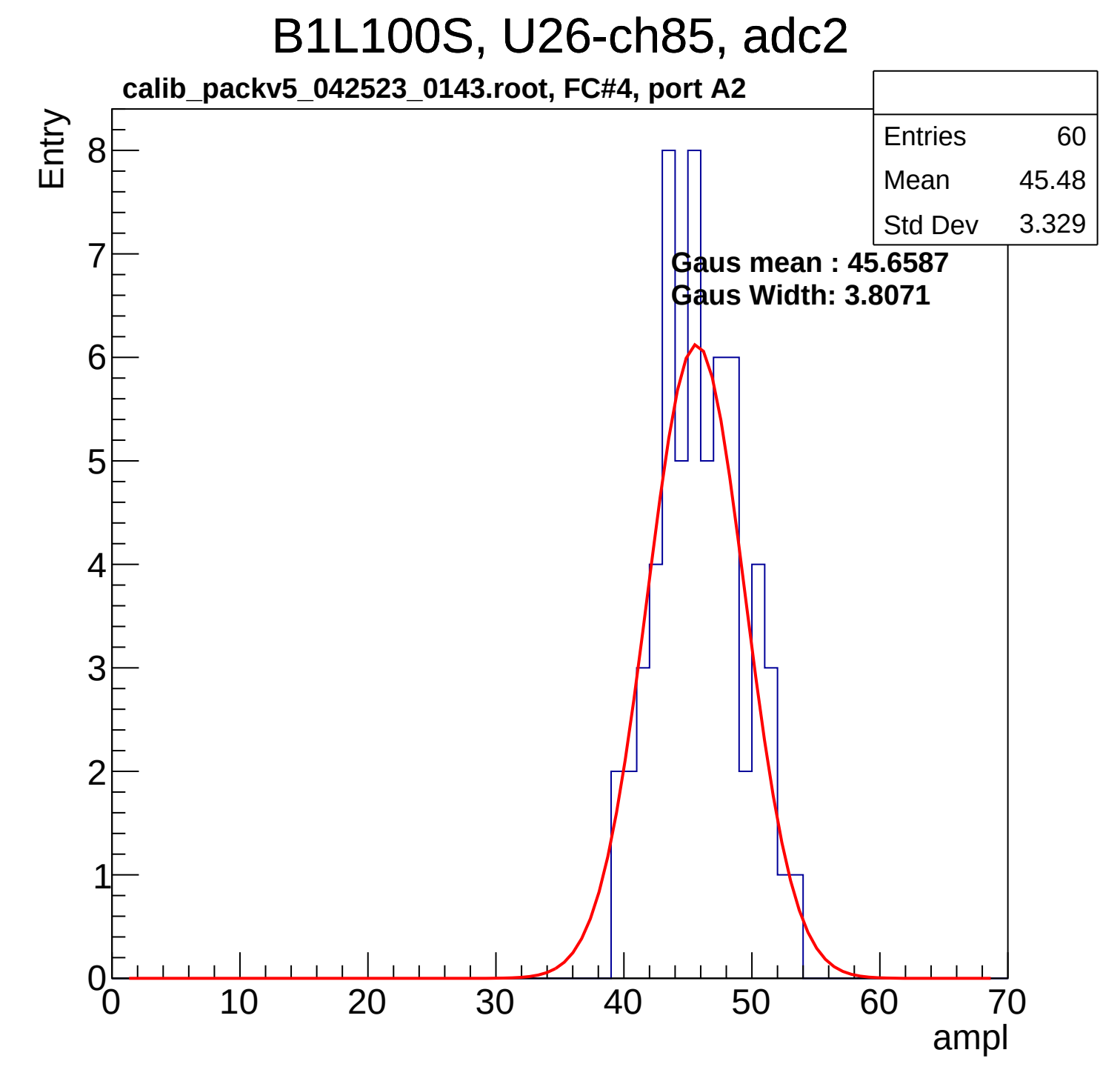
B1L100S, U26-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 45.48
Std Dev 3.329
Gaus mean : 45.6587
Gaus Width: 3.8071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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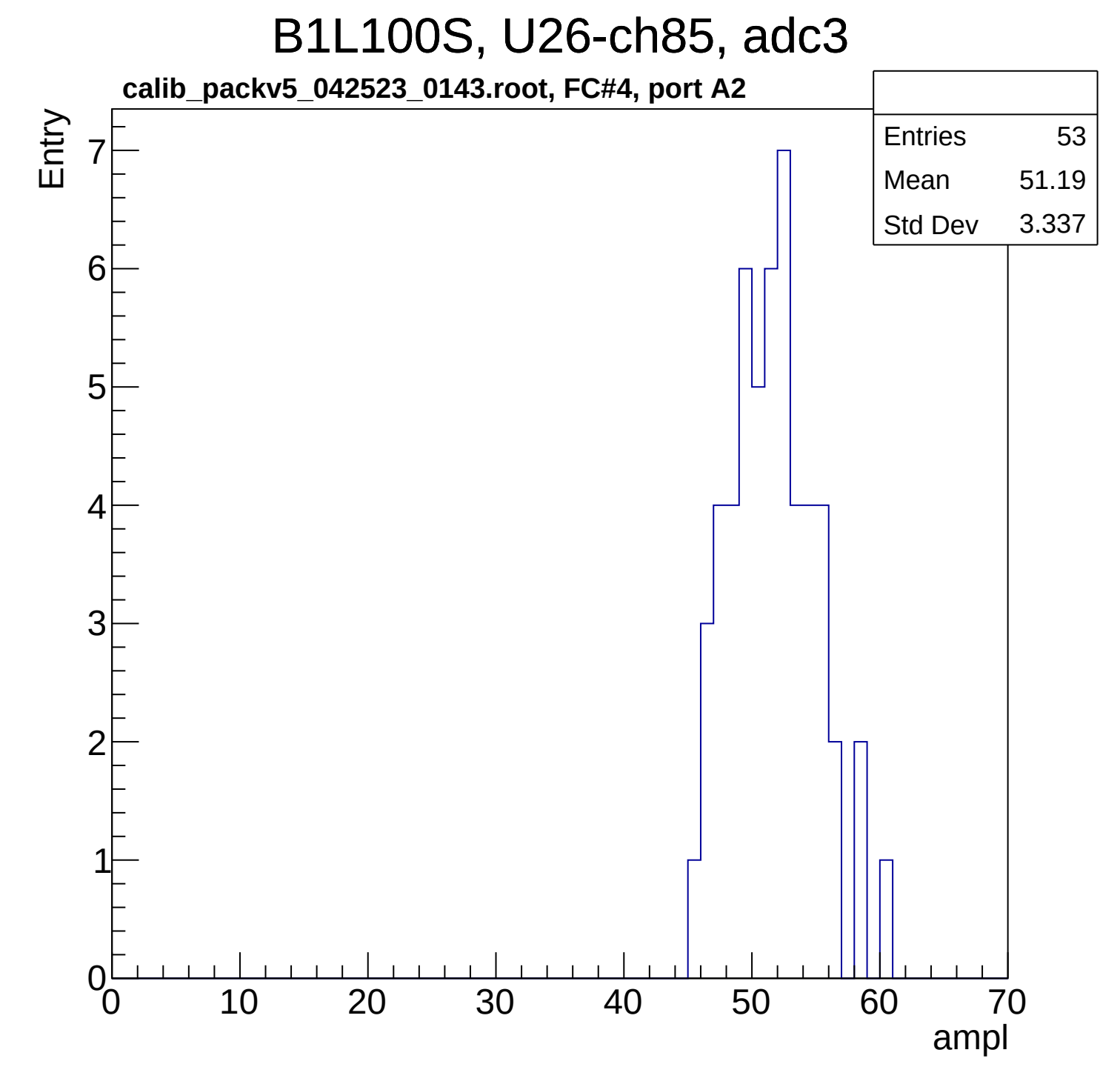
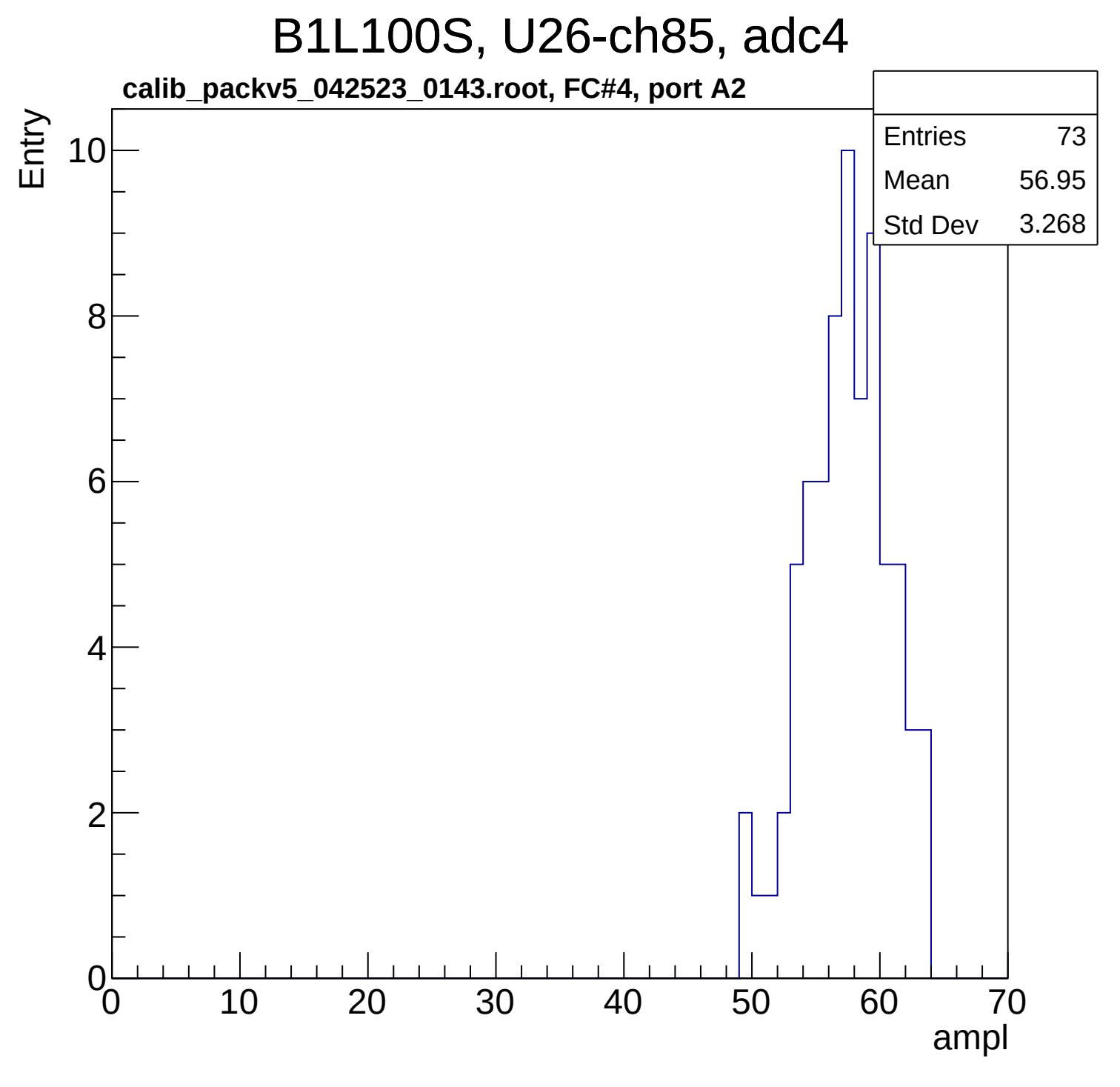

B1L100S, U26-ch85, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 51.19
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch85, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 56.95
Std Dev 3.268
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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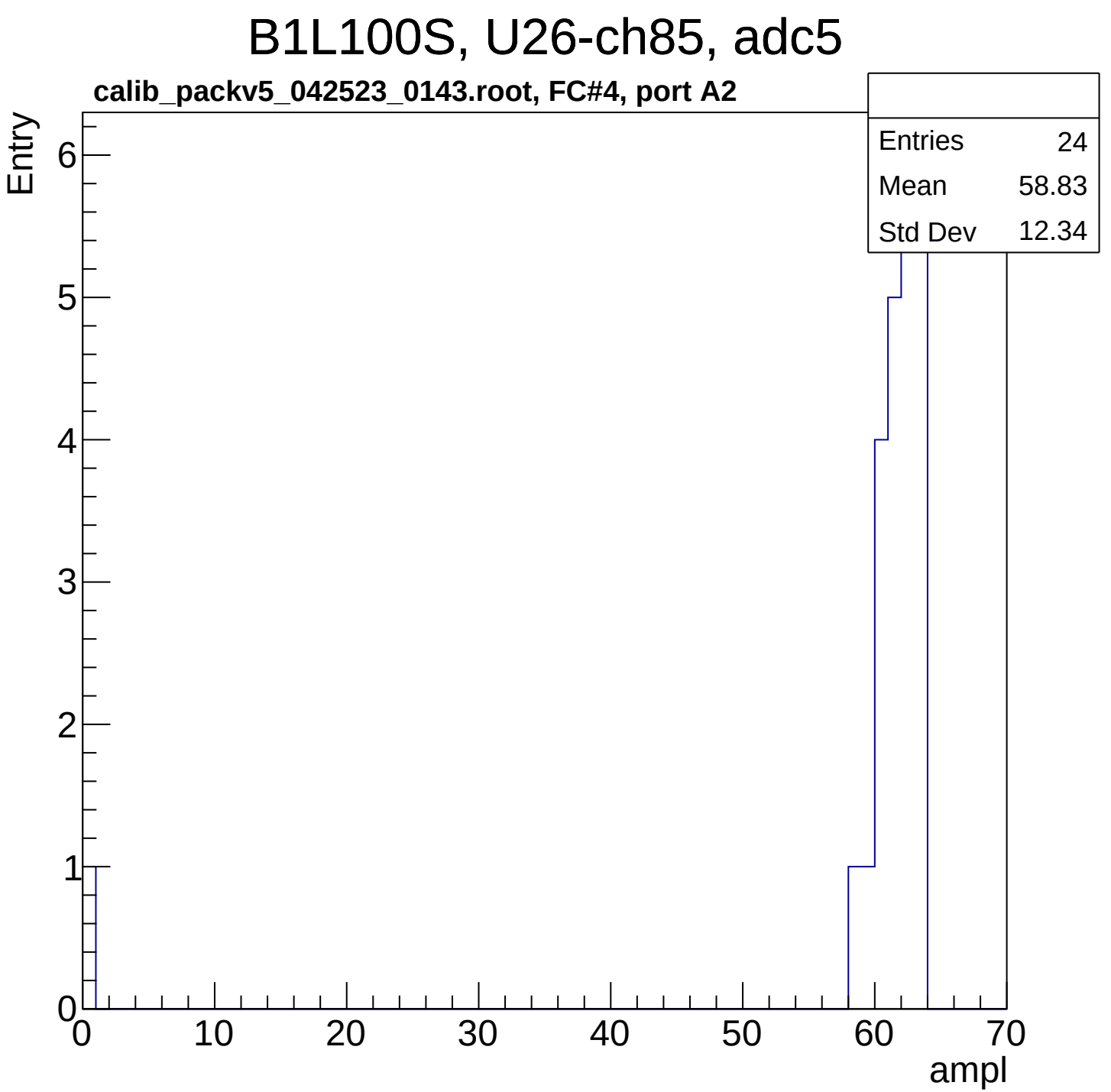
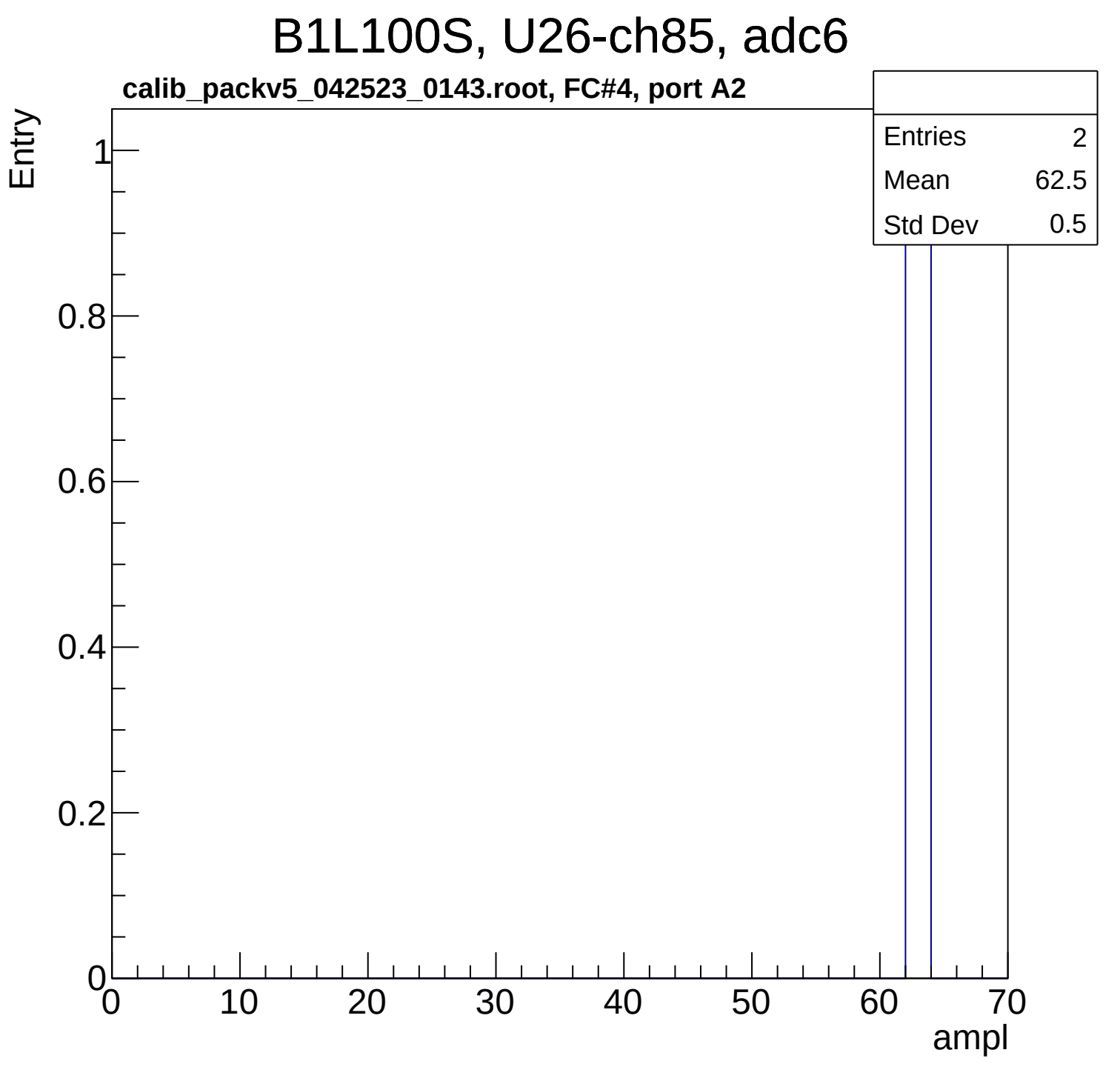
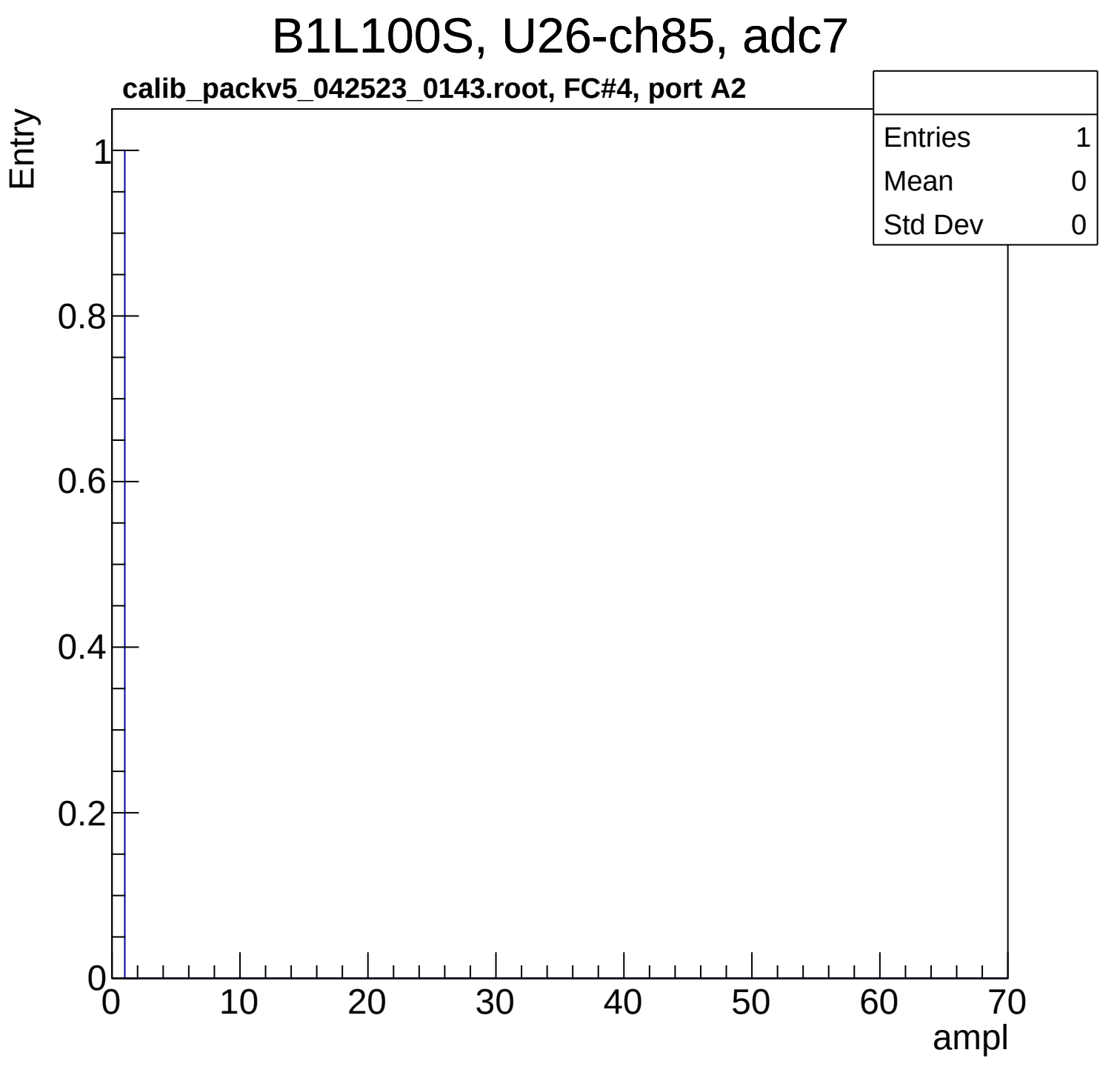
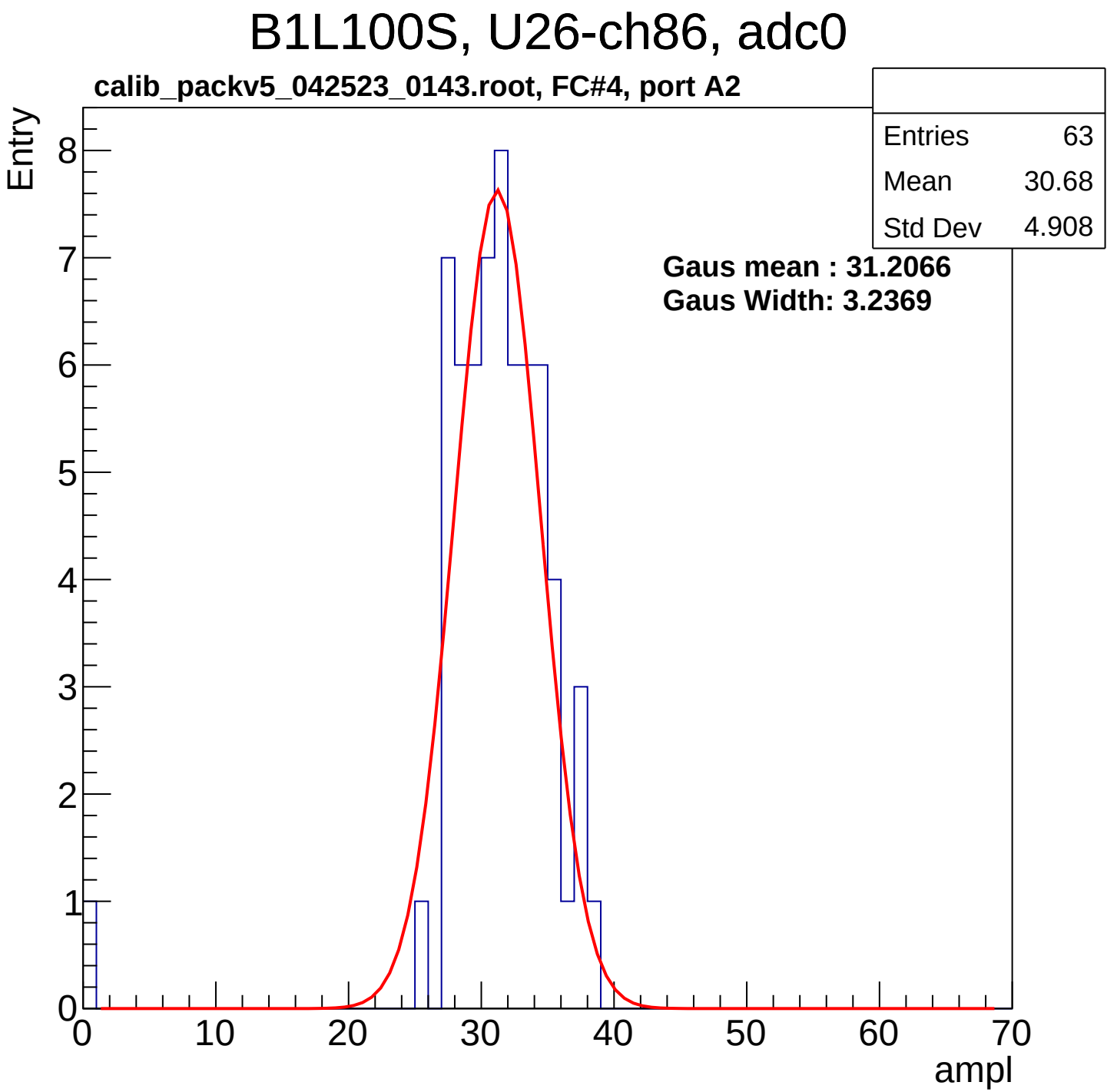
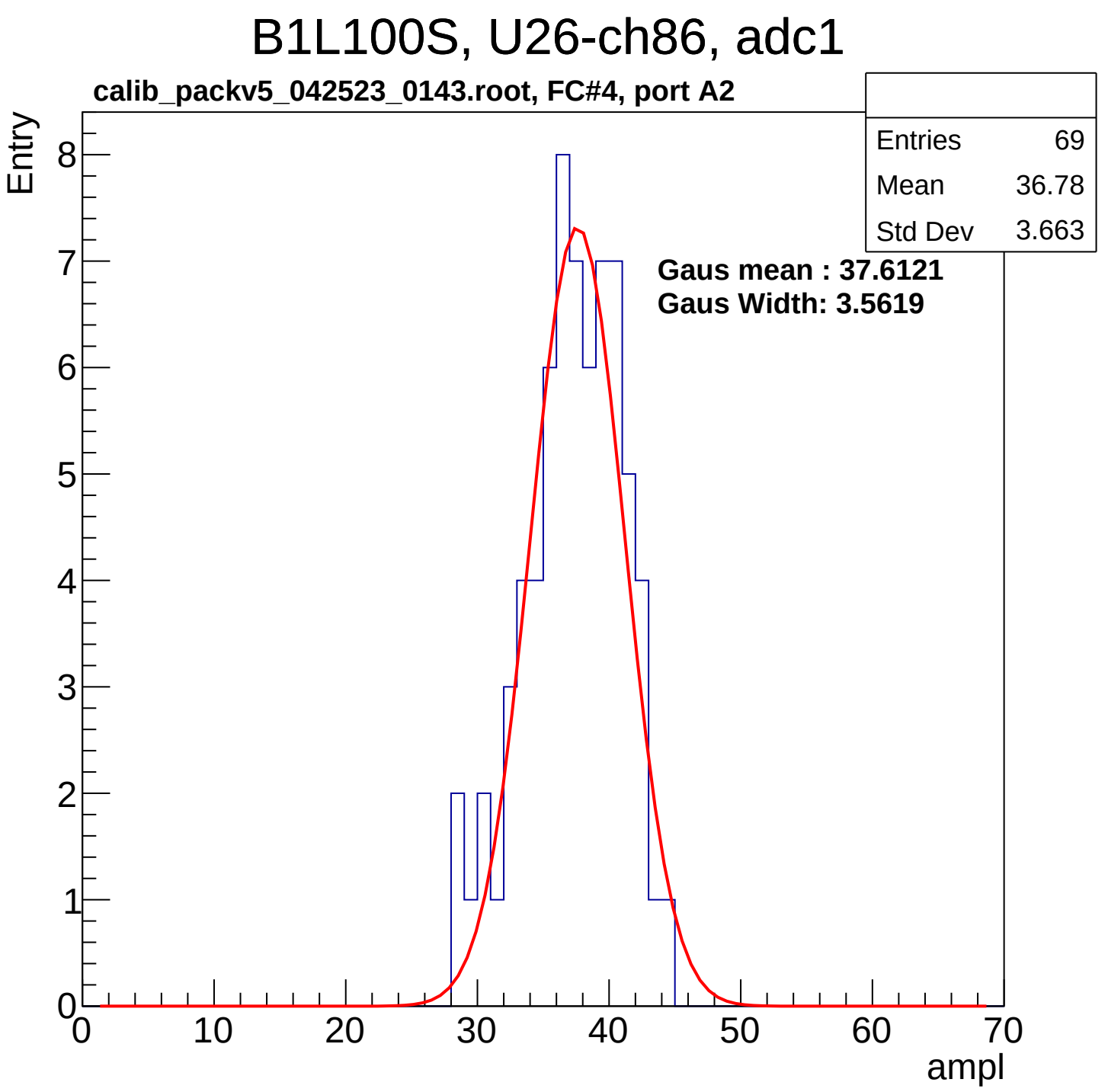

B1L100S, U26-ch85, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.83
Std Dev 12.34
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch85, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 30.68
Std Dev 4.908
Gaus mean : 31.2066
Gaus Width: 3.2369
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.78
Std Dev 3.663
Gaus mean : 37.6121
Gaus Width: 3.5619
Entry
ampl
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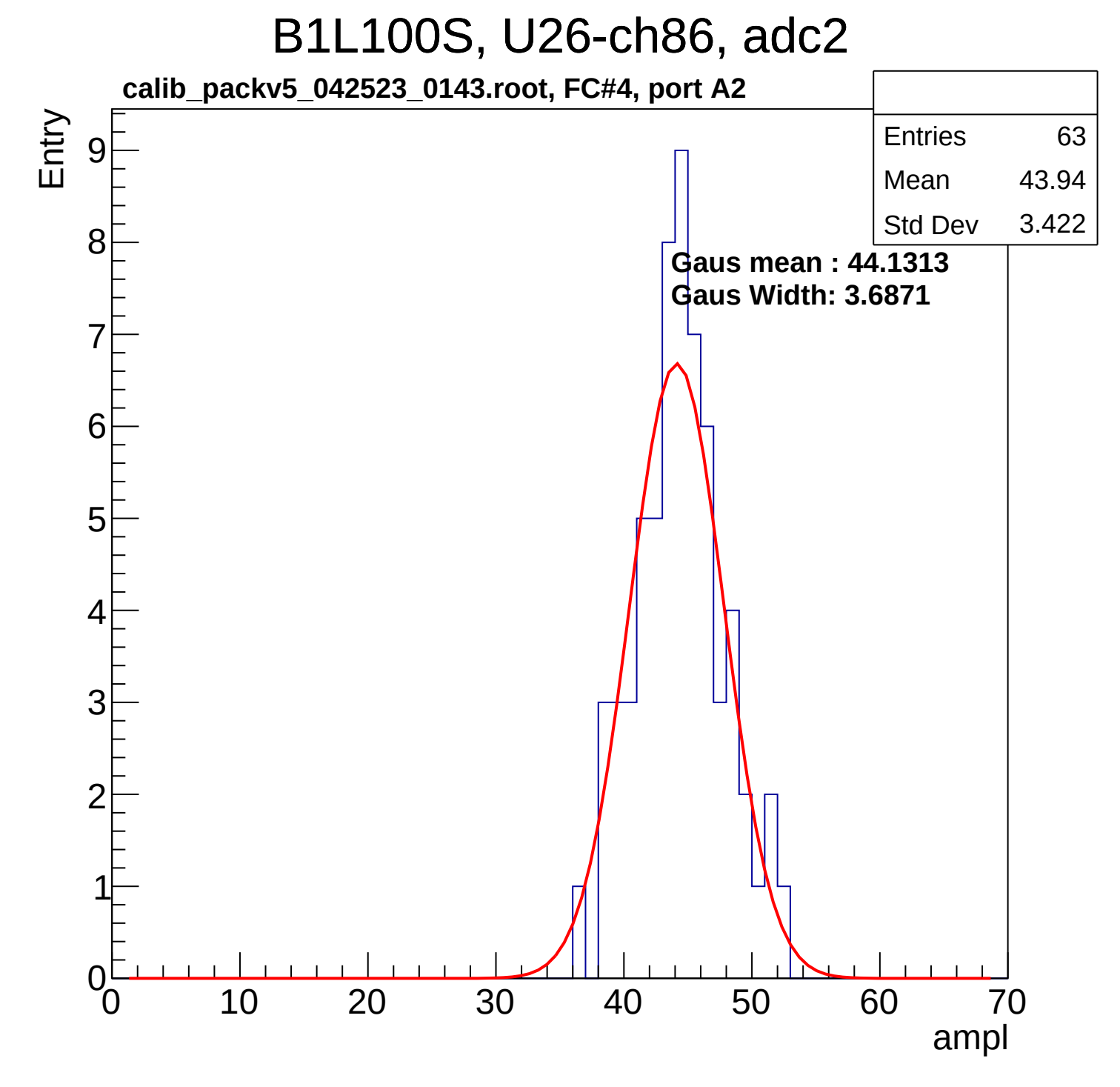
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.94
Std Dev 3.422
Gaus mean : 44.1313
Gaus Width: 3.6871
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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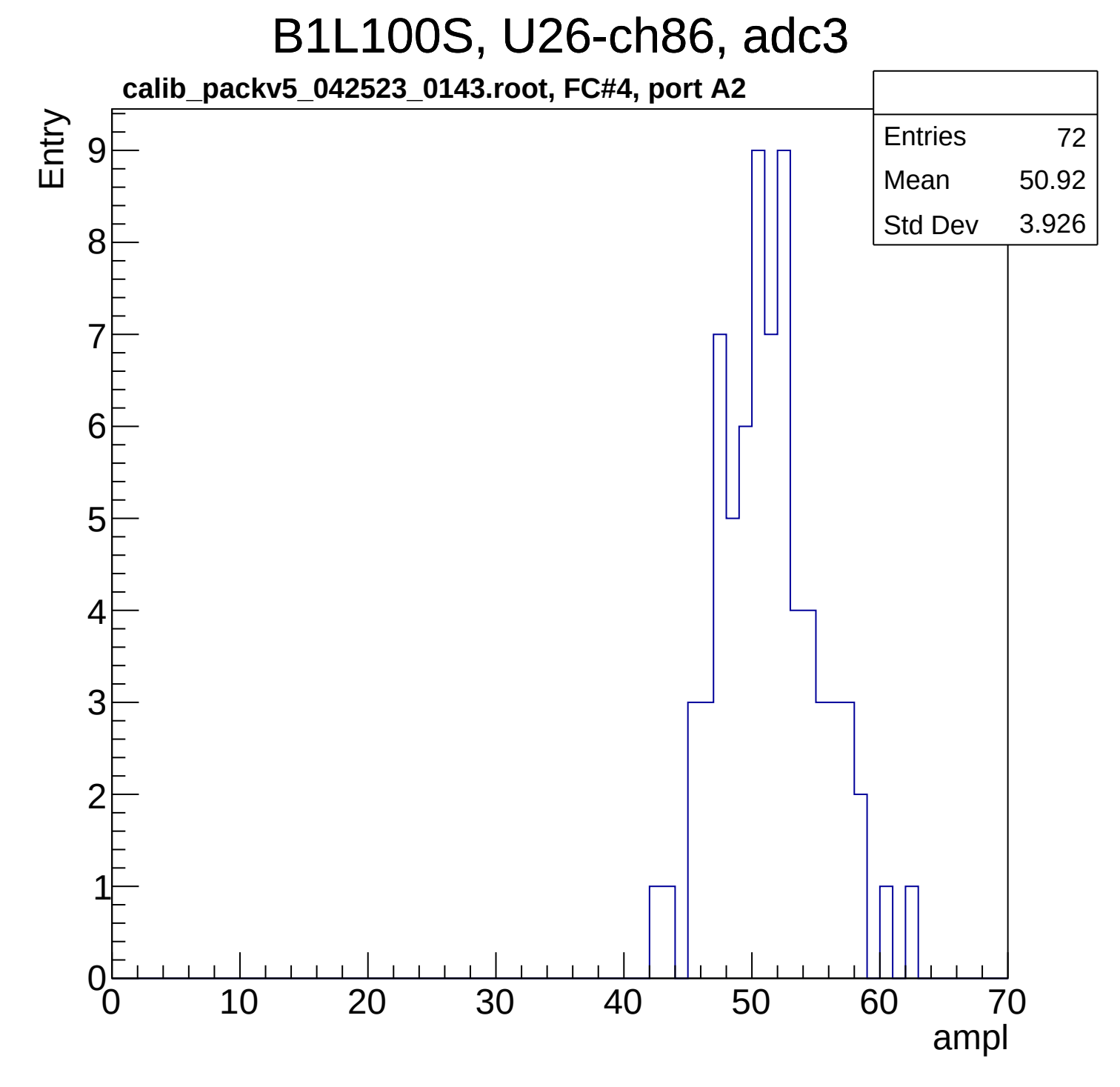
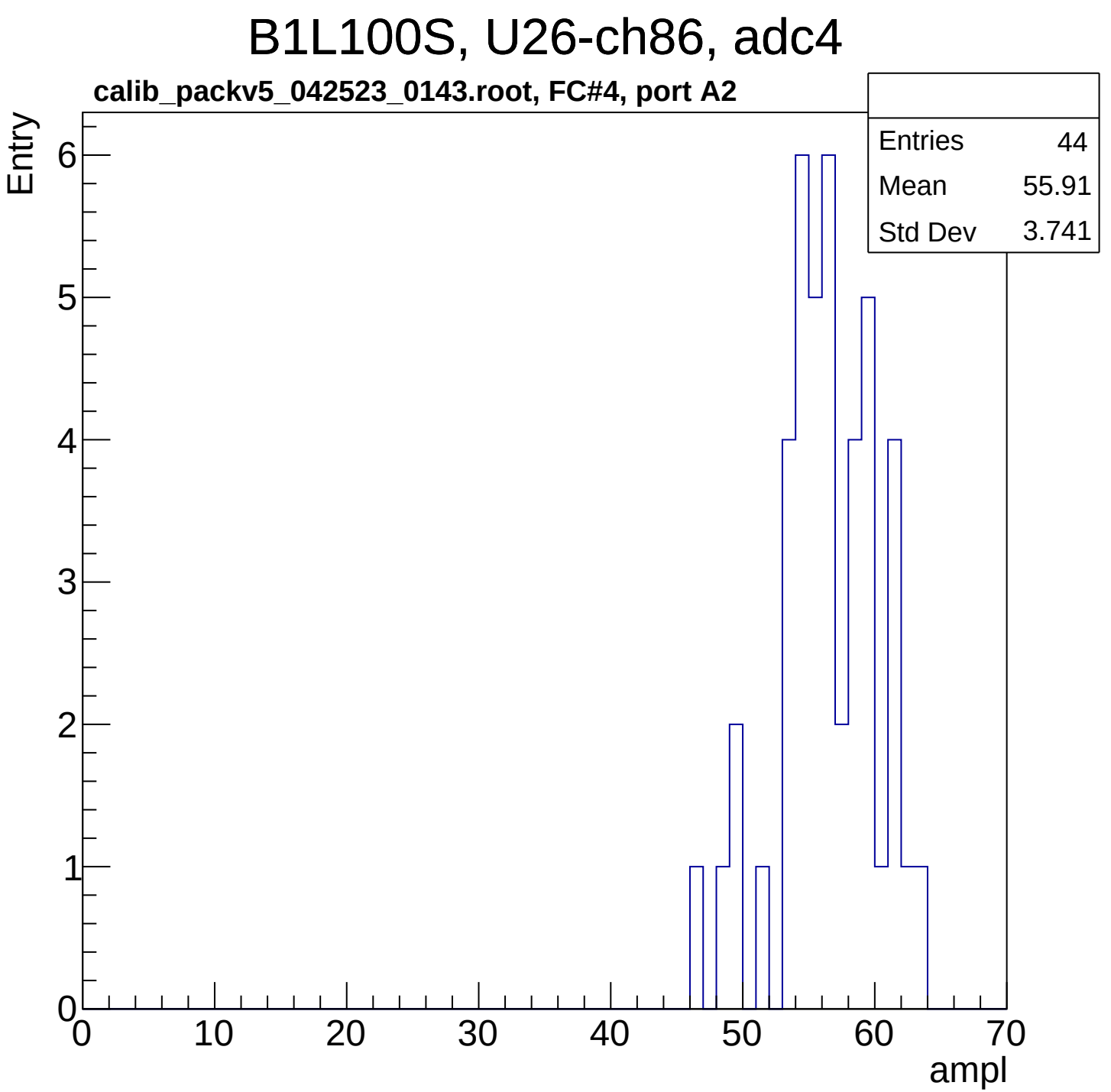
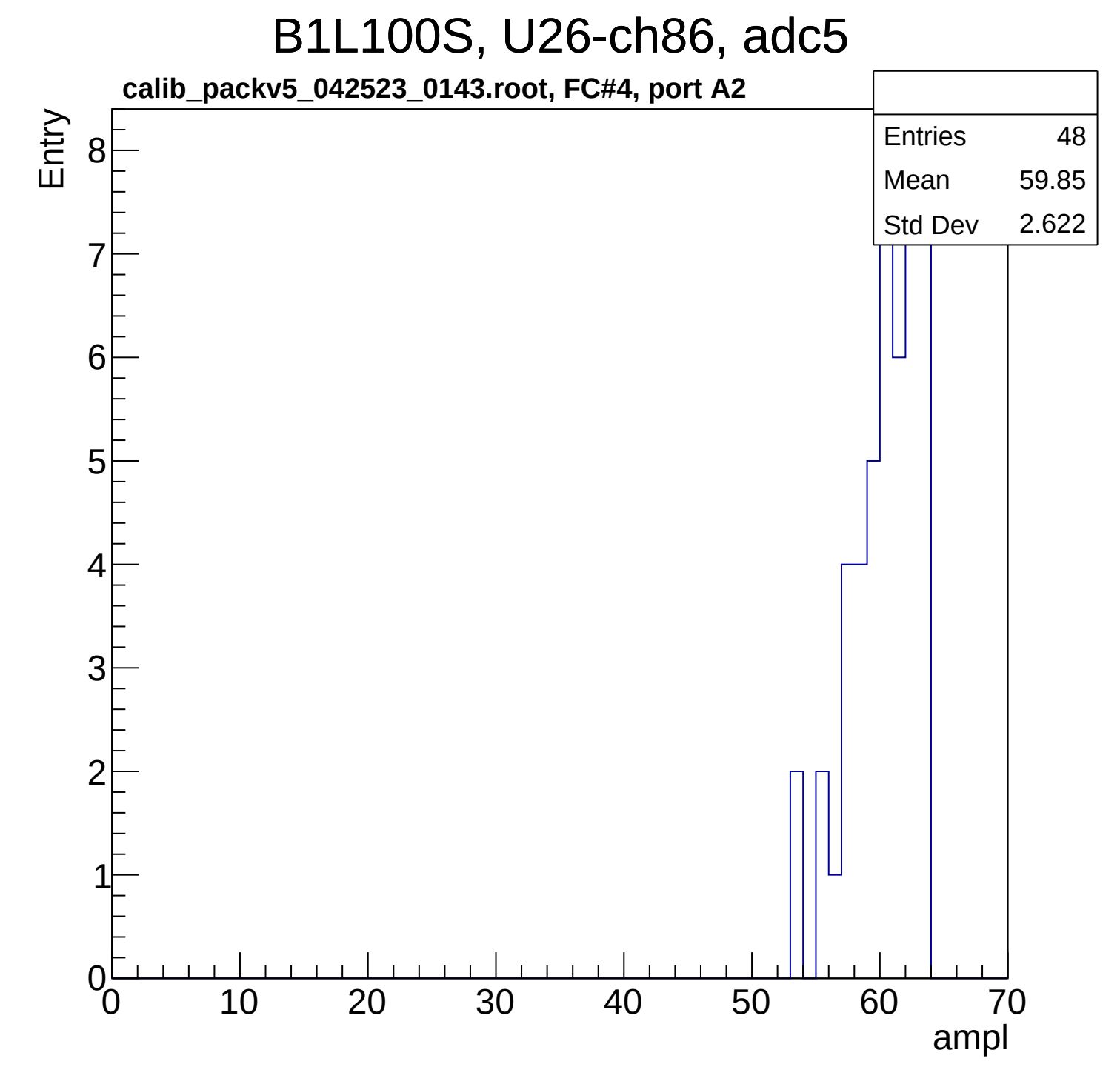

B1L100S, U26-ch86, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.92
Std Dev 3.926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch86, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 55.91
Std Dev 3.741
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch86, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.85
Std Dev 2.622
Entry
ampl
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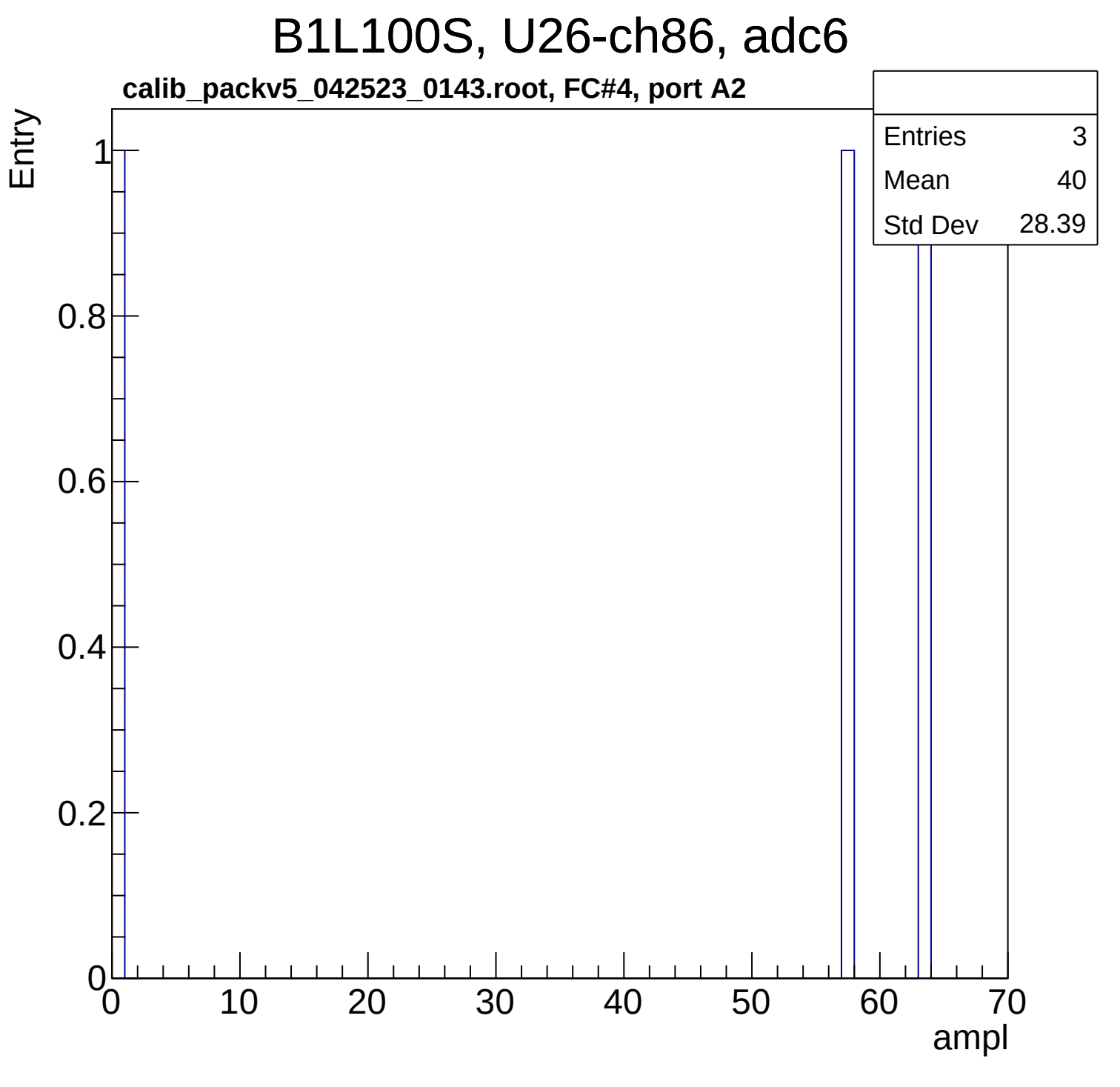
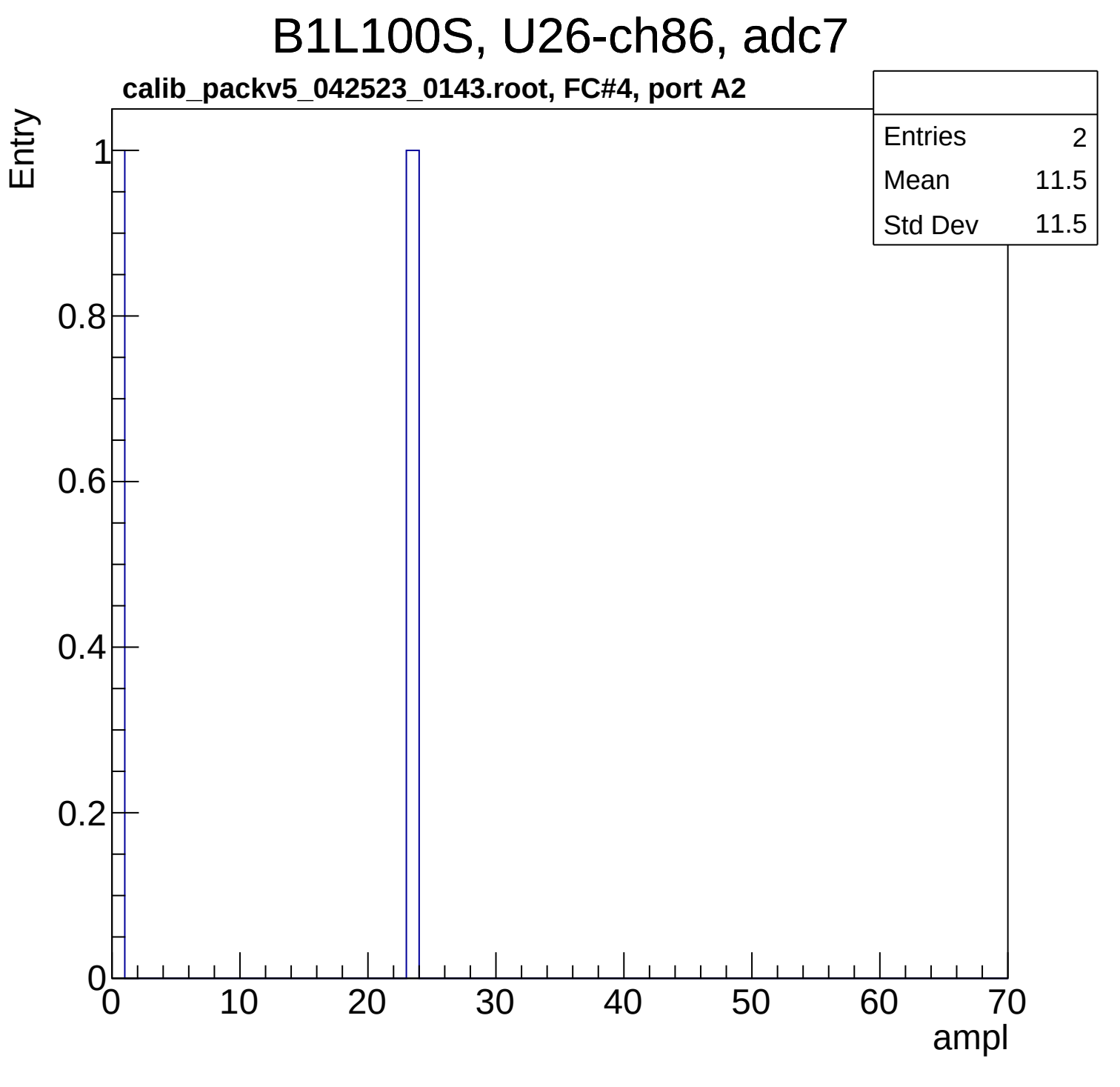
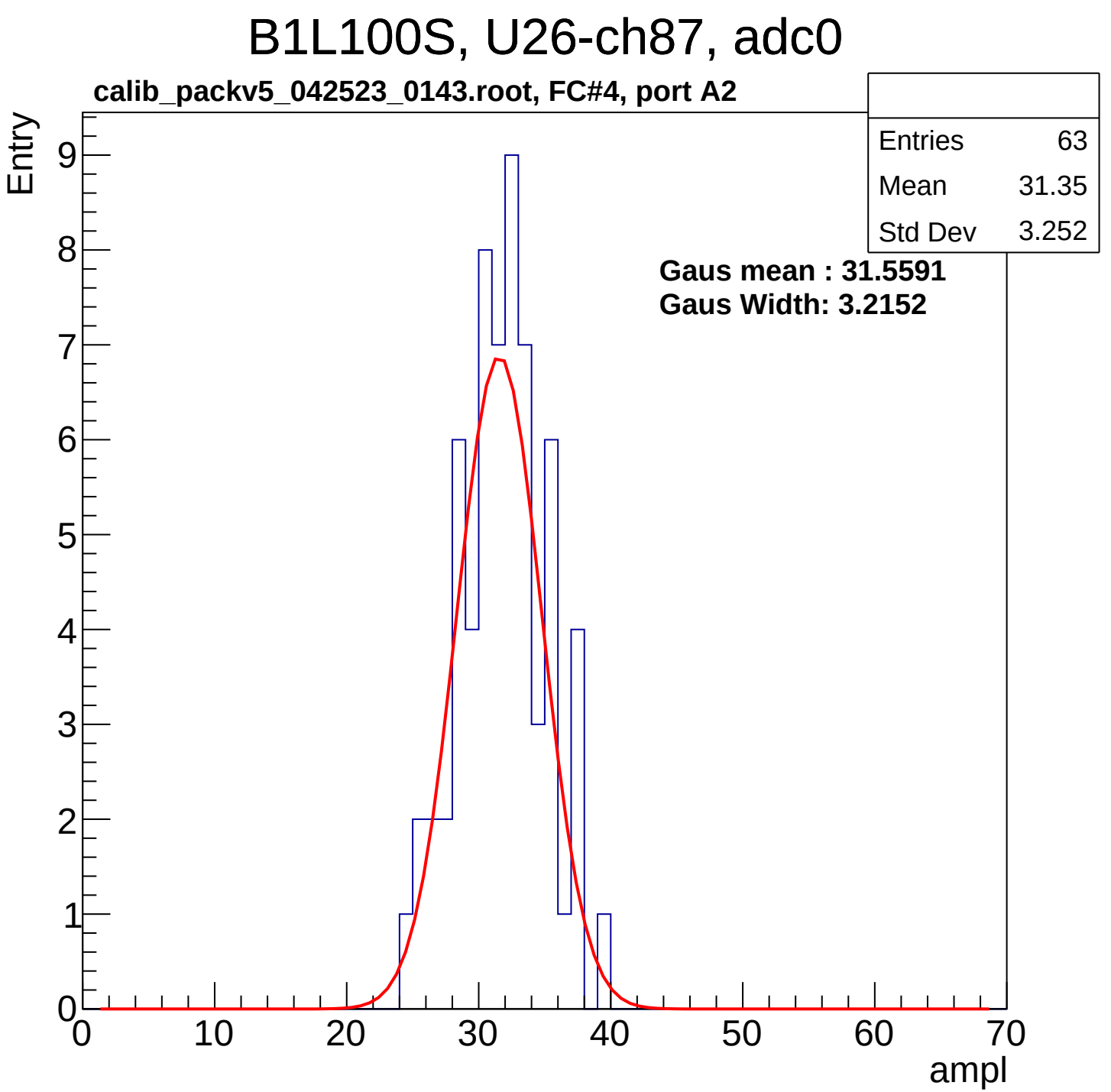
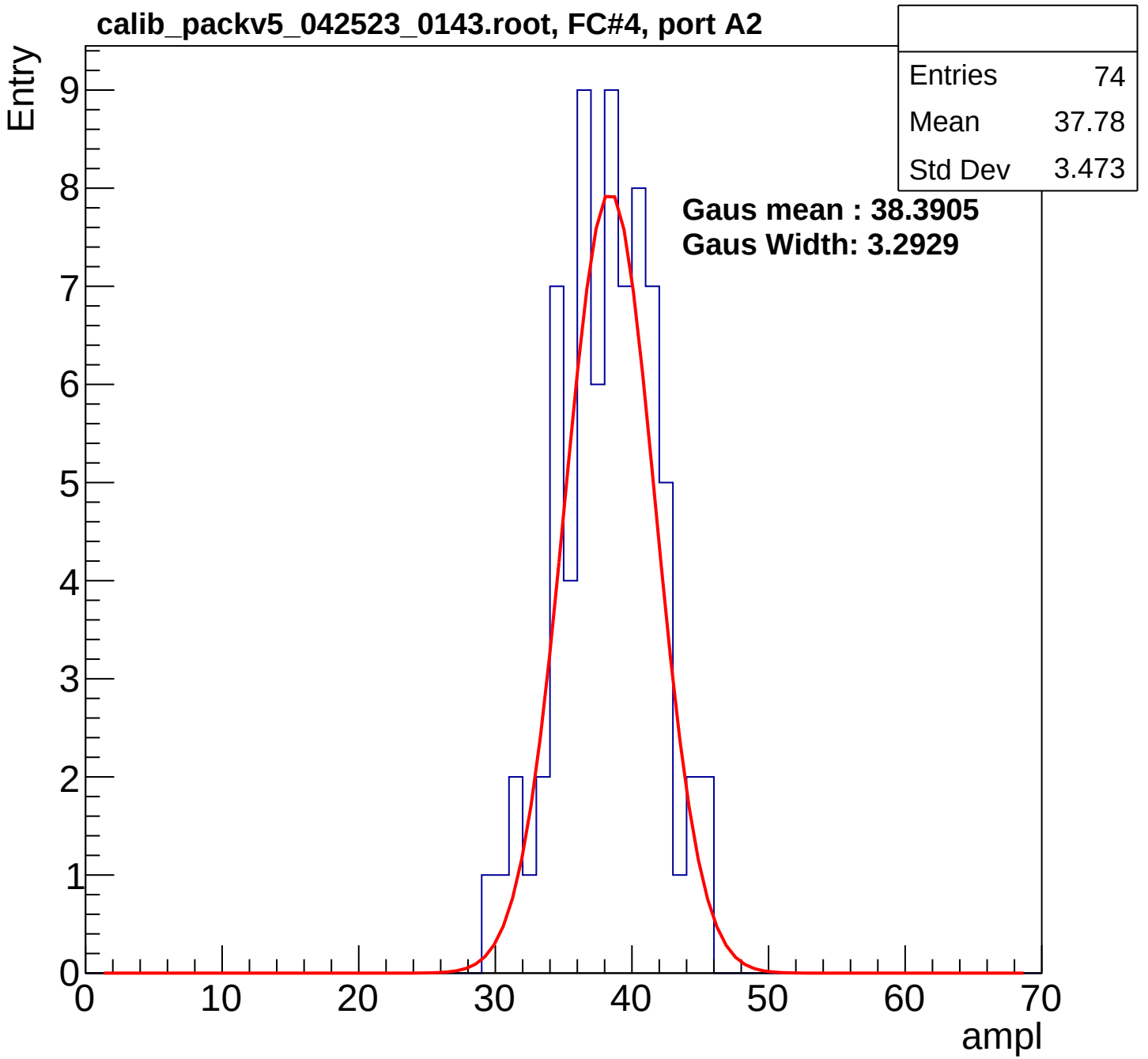
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch86, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 40
Std Dev 28.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U26-ch86, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.35
Std Dev 3.252
Gaus mean : 31.5591
Gaus Width: 3.2152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.78
Std Dev 3.473
Gaus mean : 38.3905
Gaus Width: 3.2929
Entry
ampl
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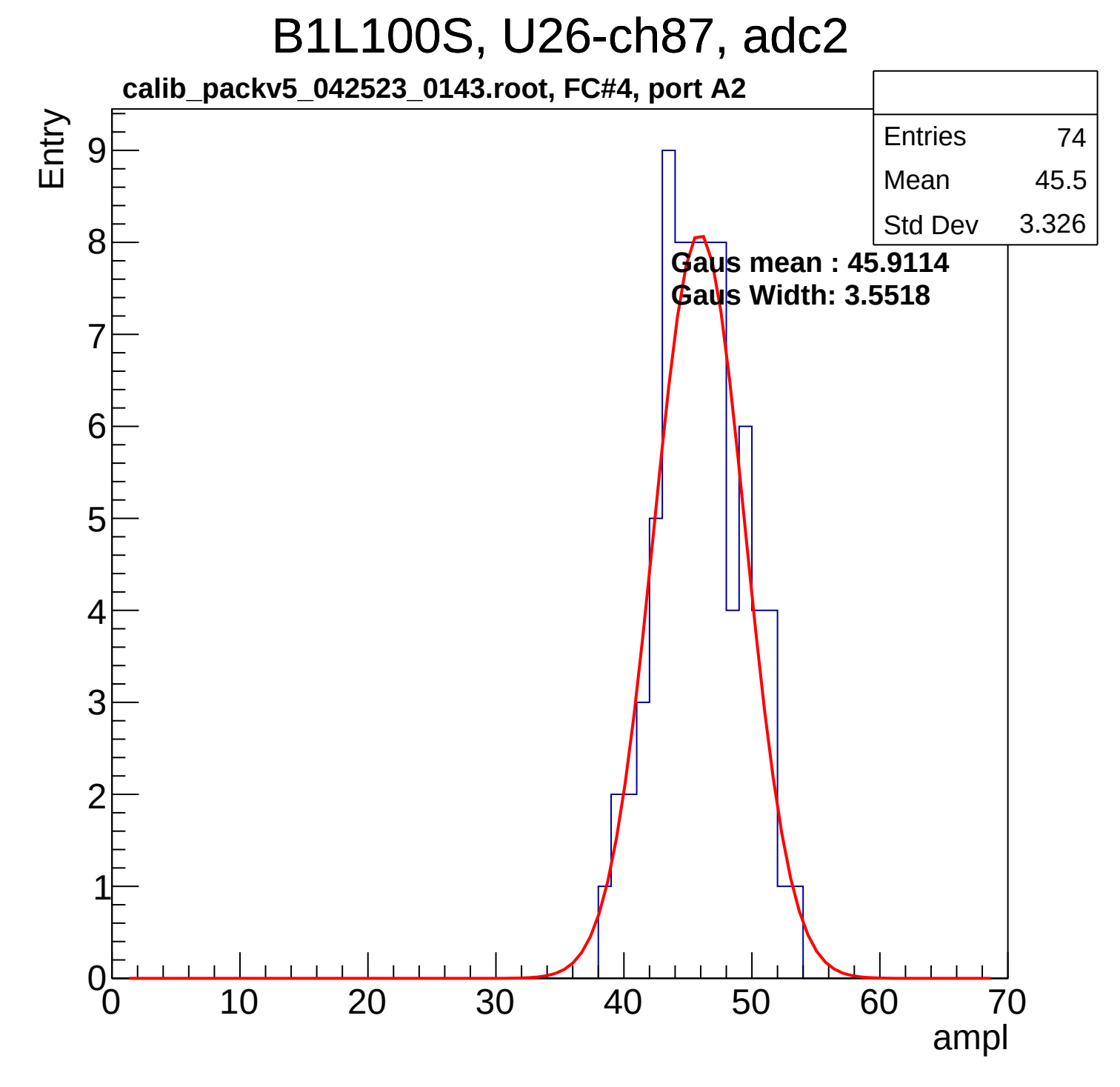
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 45.5
Std Dev 3.326
Gaus mean : 45.9114
Gaus Width: 3.5518
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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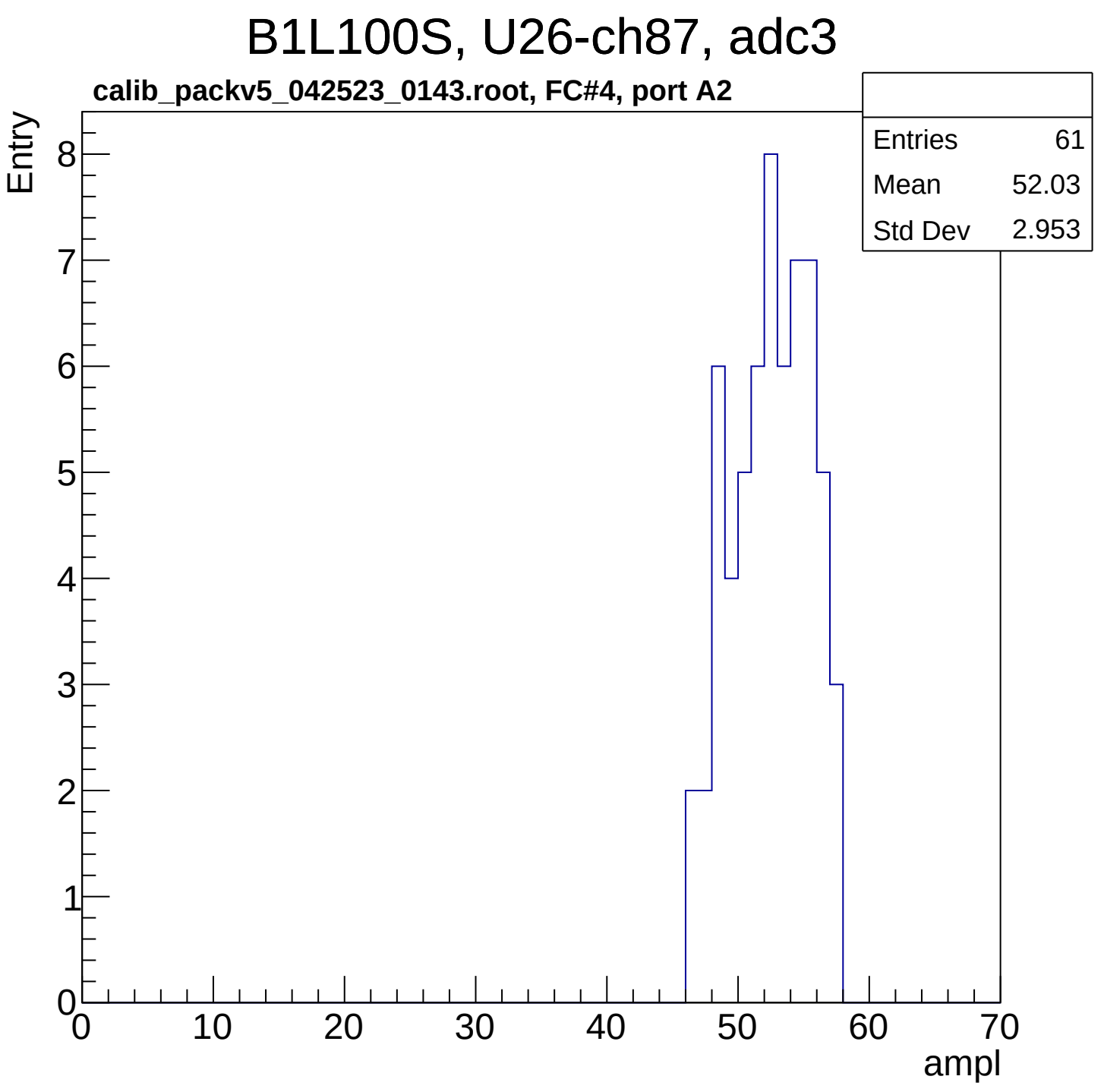
B1L100S, U26-ch87, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 52.03
Std Dev 2.953
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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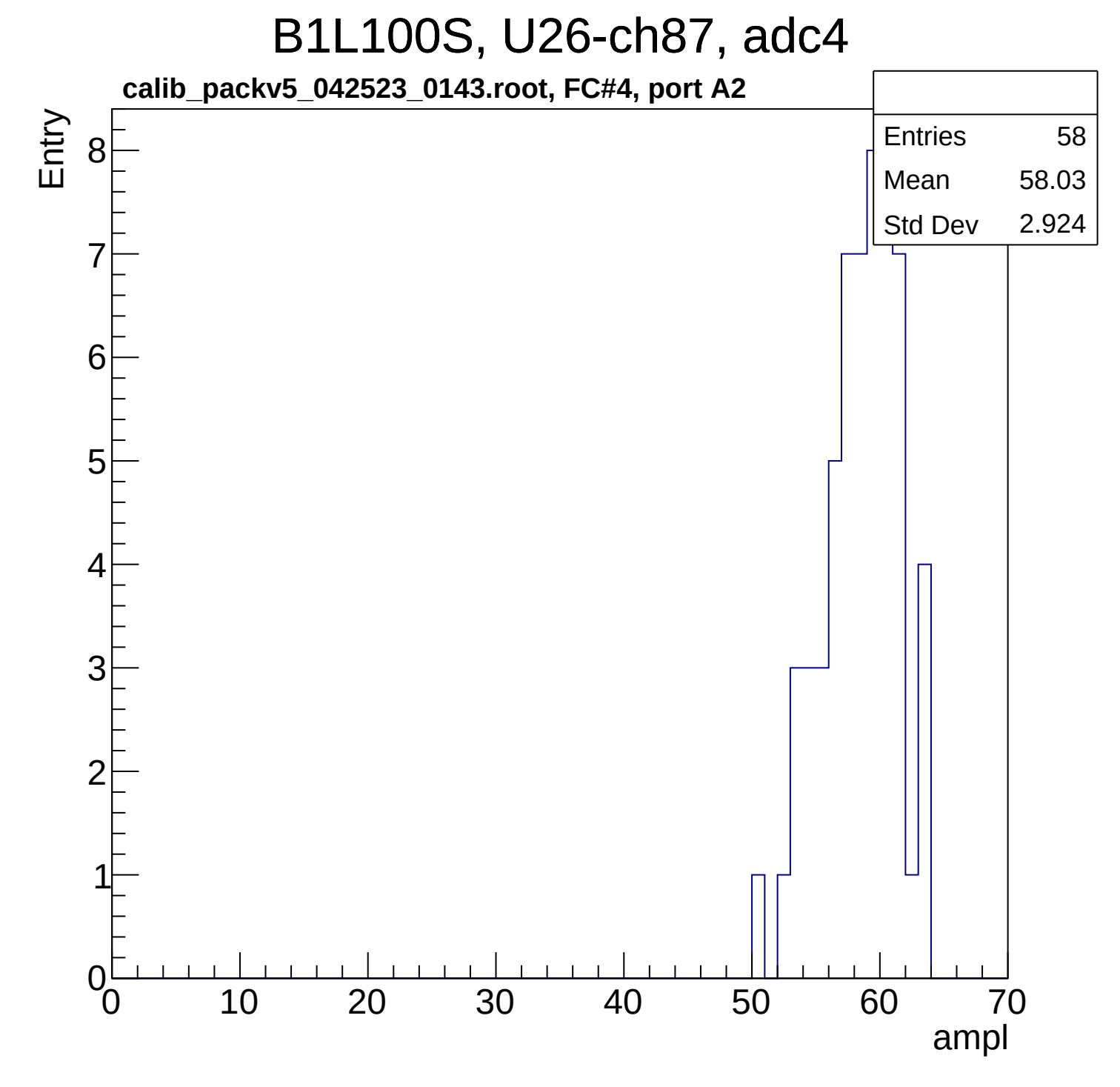
60
70


B1L100S, U26-ch87, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 58.03
Std Dev 2.924
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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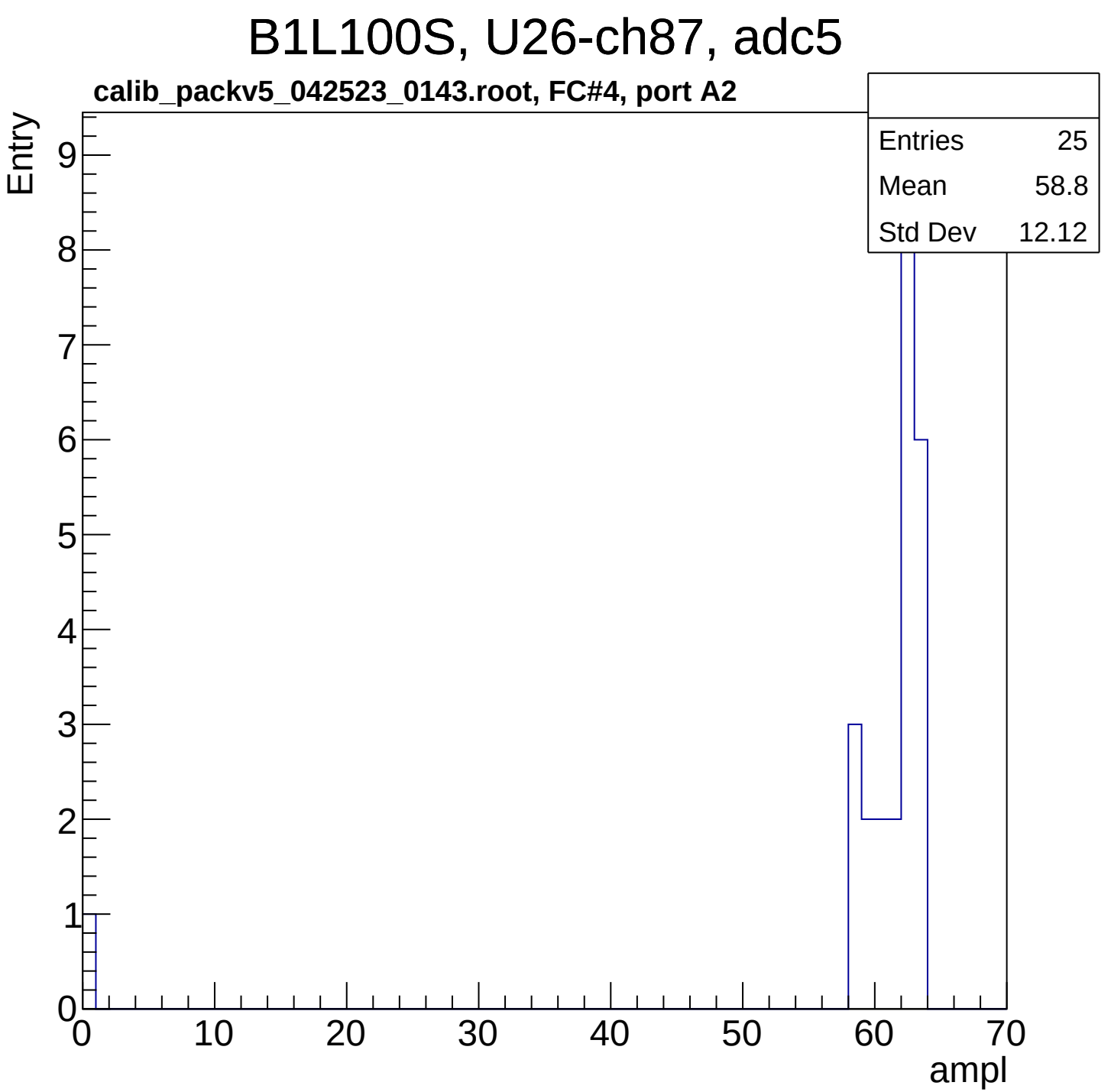
60
70


B1L100S, U26-ch87, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 58.8
Std Dev 12.12
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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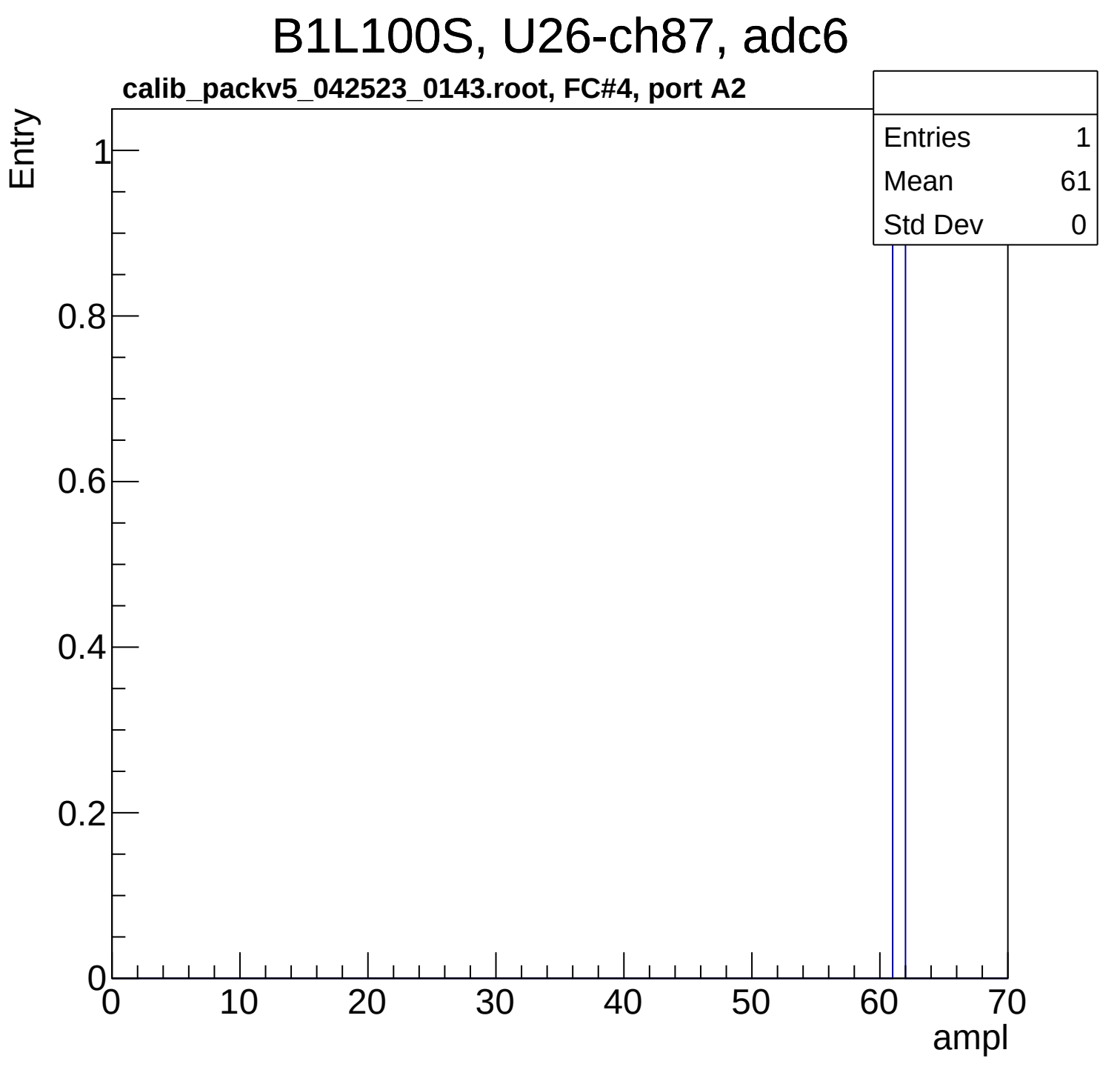
40
50
60
70


B1L100S, U26-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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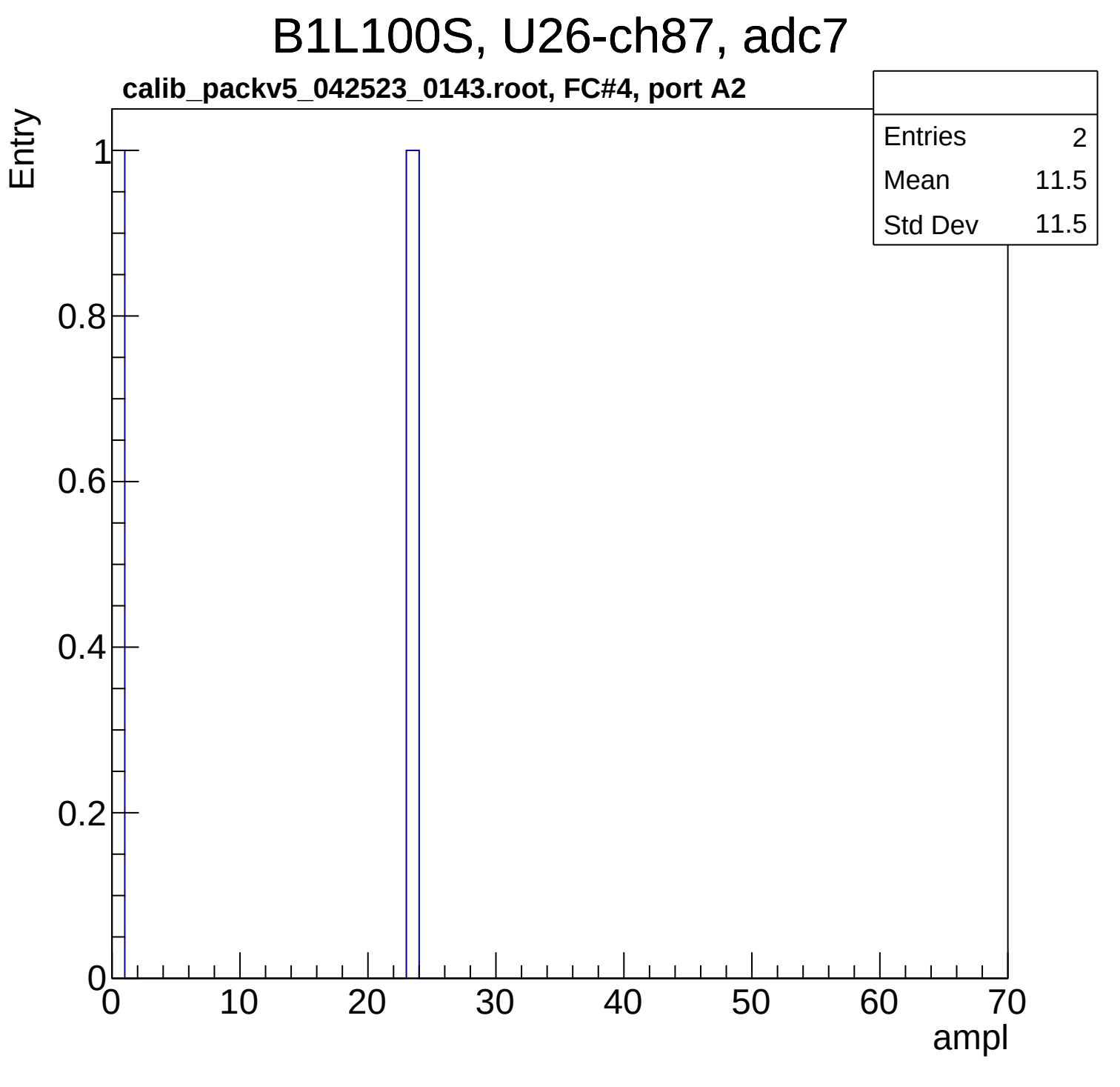
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch87, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
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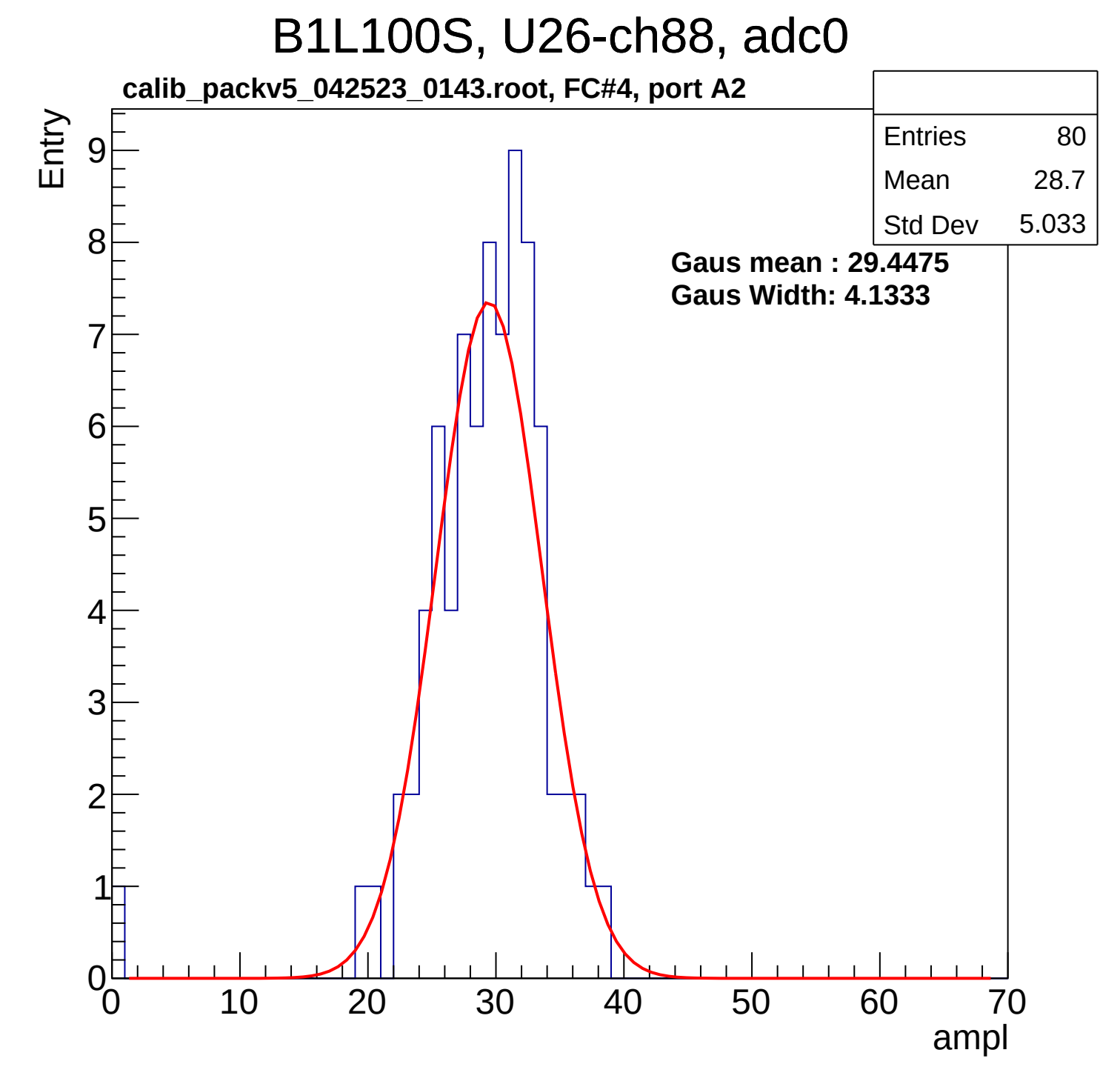
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U26-ch88, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.7
Std Dev 5.033
Gaus mean : 29.4475
Gaus Width: 4.1333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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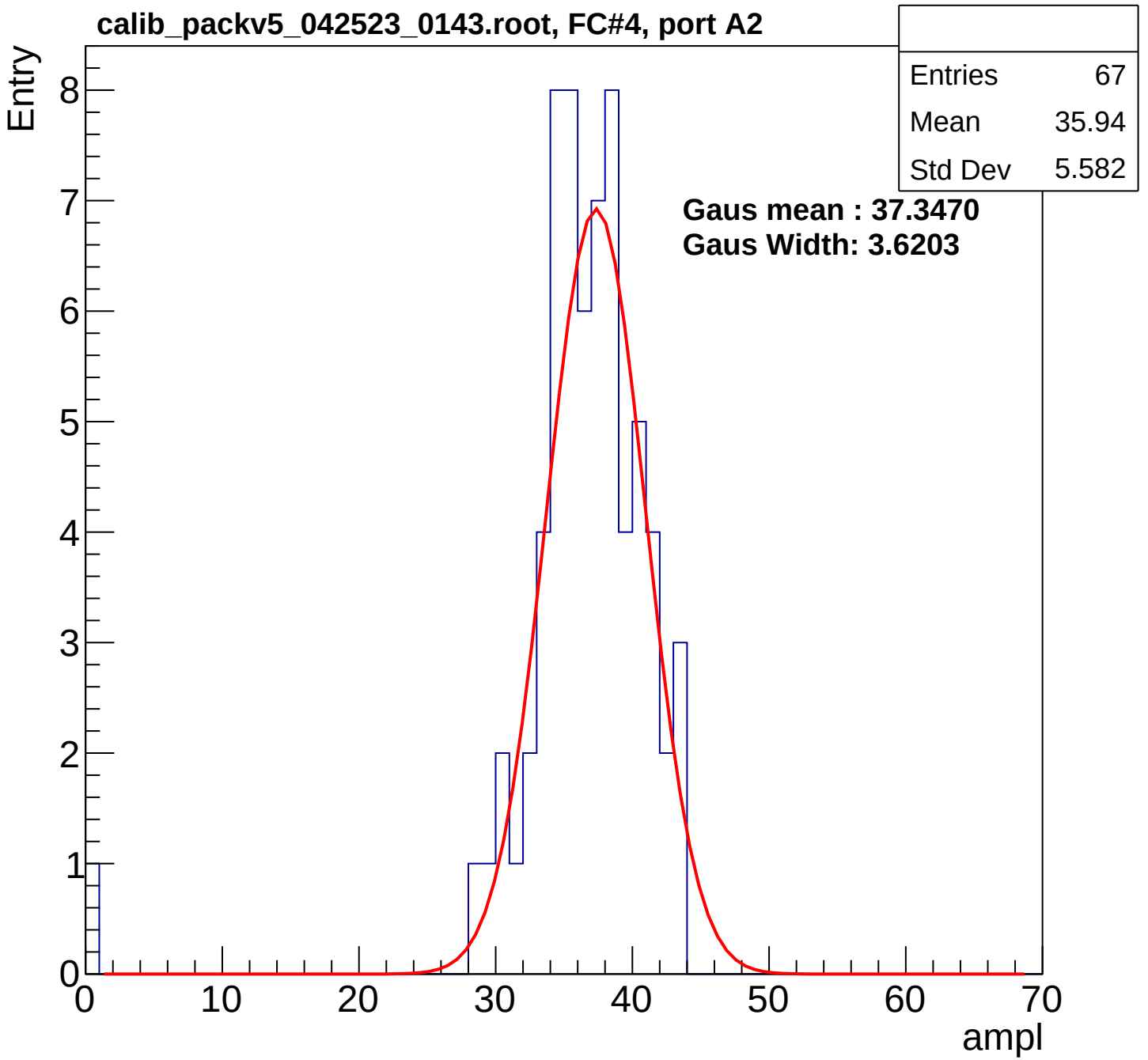
40
50
60
70


B1L100S, U26-ch88, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 35.94
Std Dev 5.582
Gaus mean : 37.3470
Gaus Width: 3.6203
Entry
ampl
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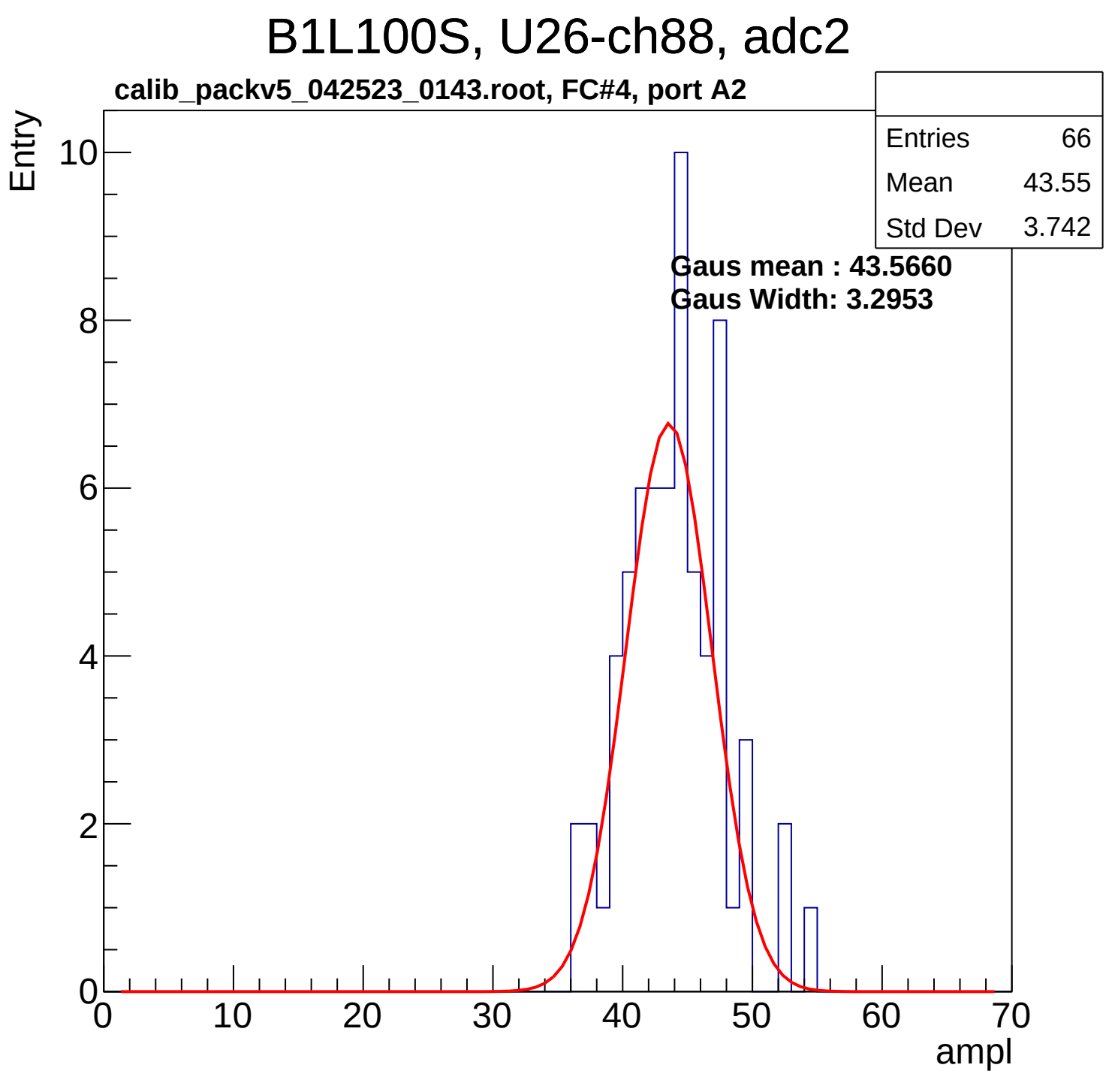
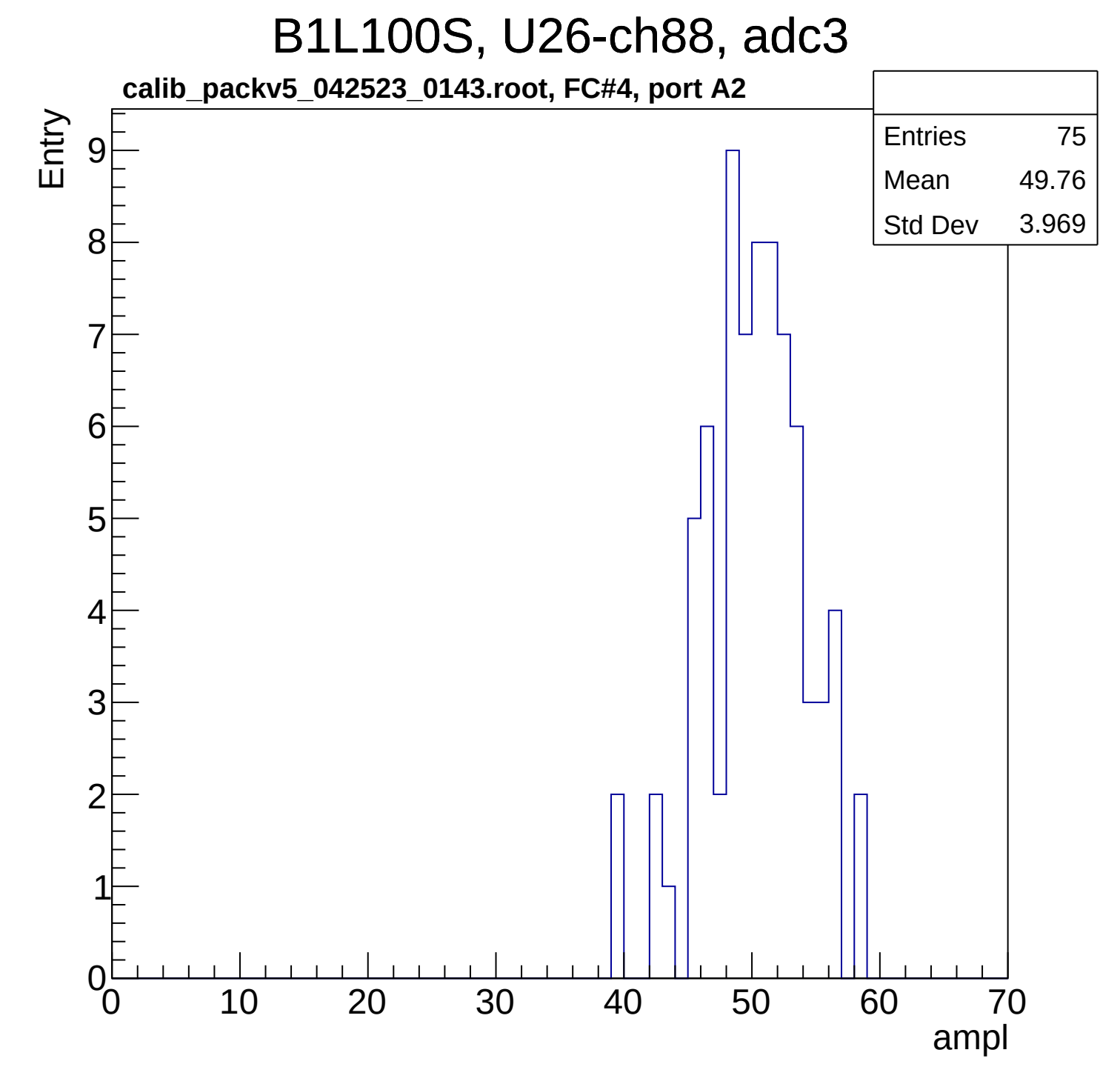
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.55
Std Dev 3.742
Gaus mean : 43.5660
Gaus Width: 3.2953
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 49.76
Std Dev 3.969
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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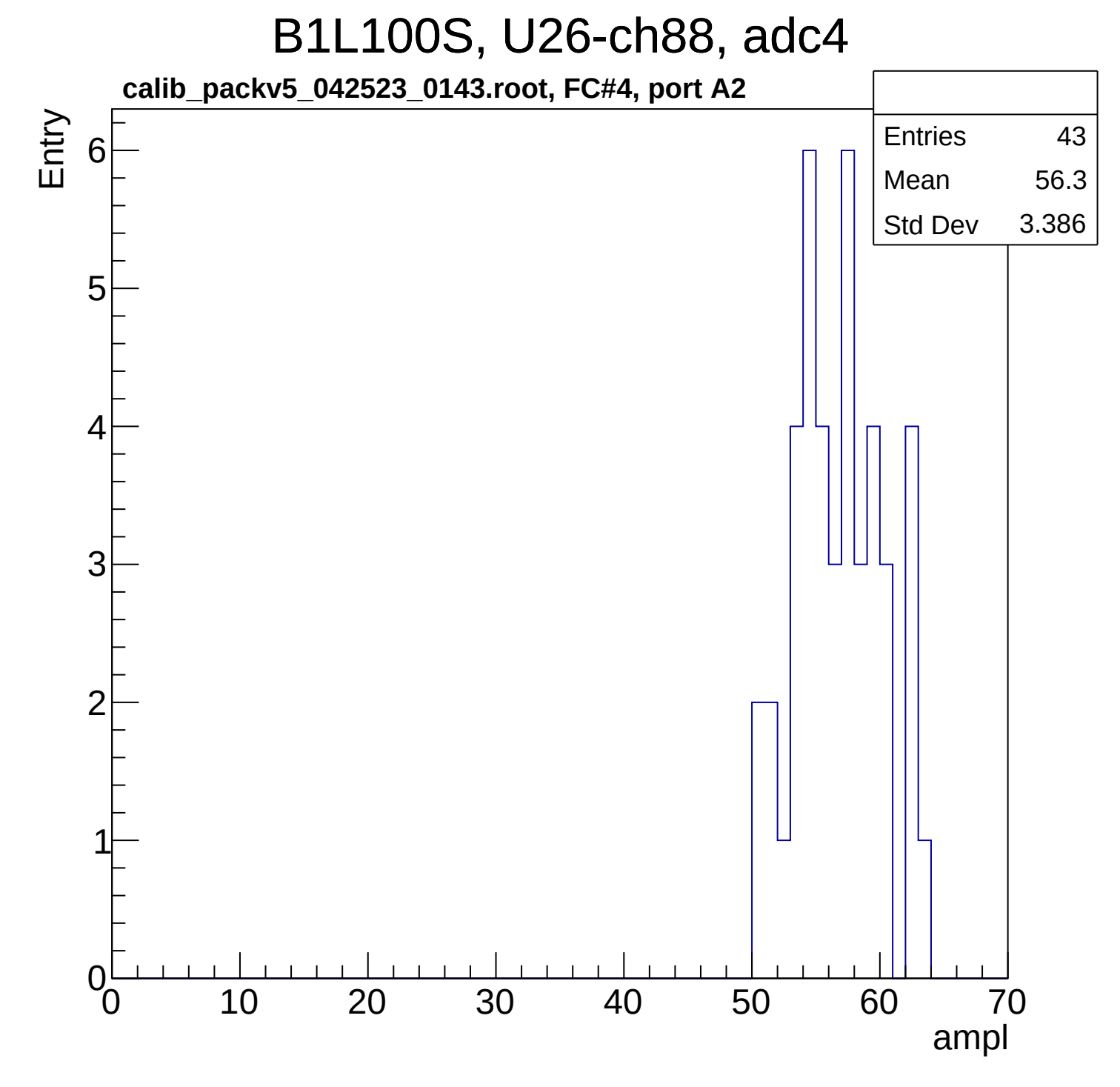
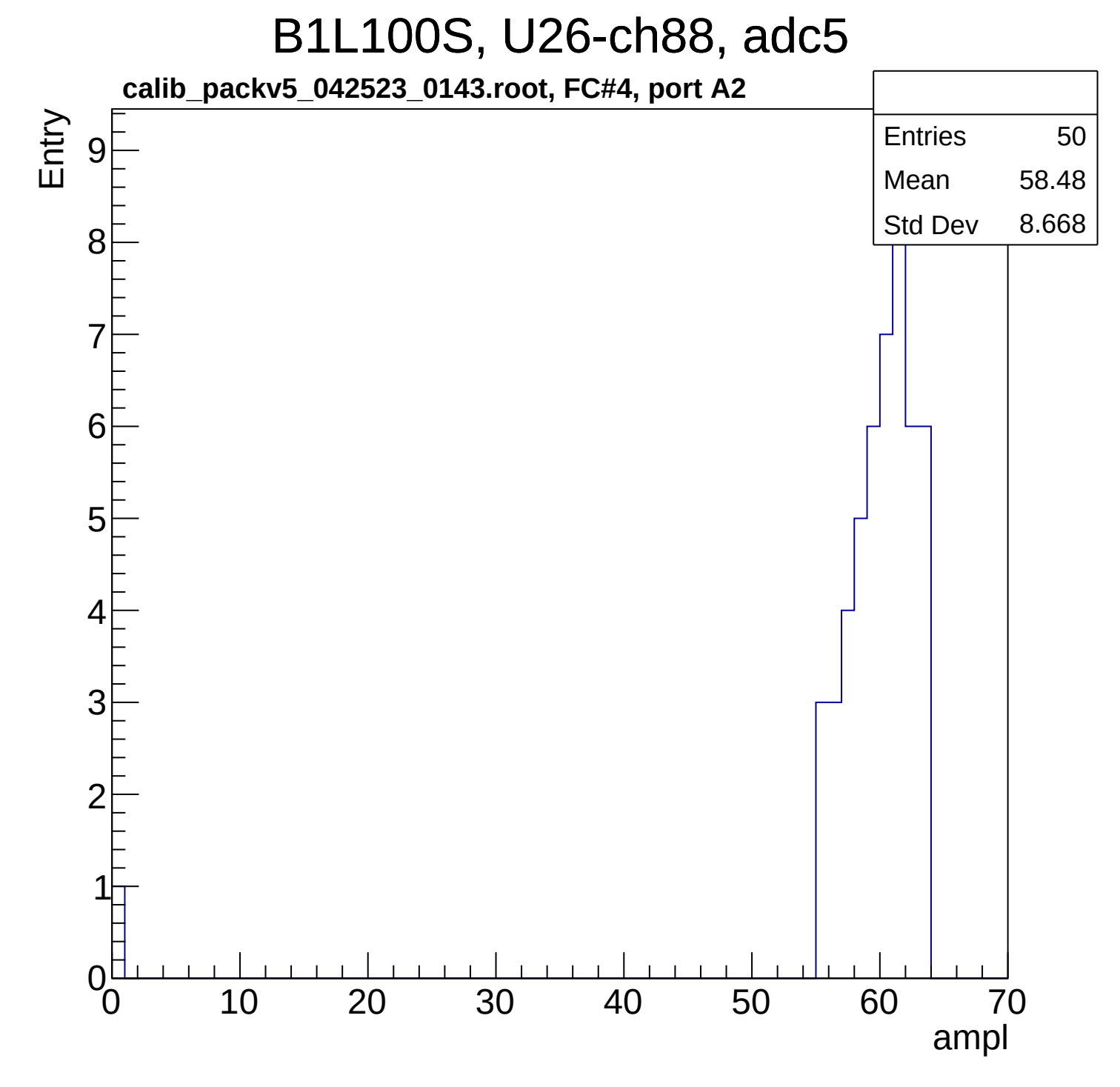
40
50
60
70


B1L100S, U26-ch88, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 56.3
Std Dev 3.386
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.48
Std Dev 8.668
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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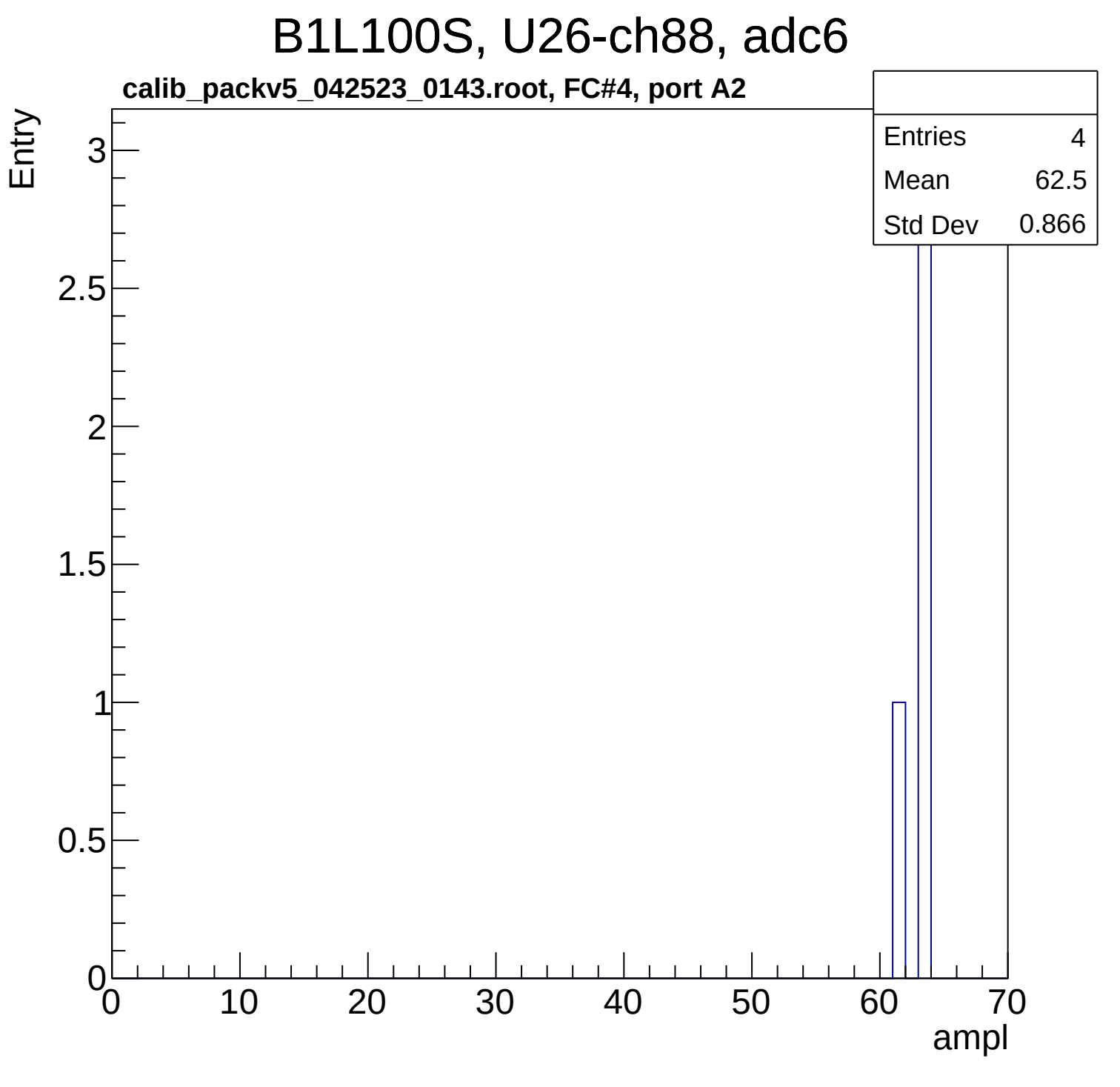
B1L100S, U26-ch88, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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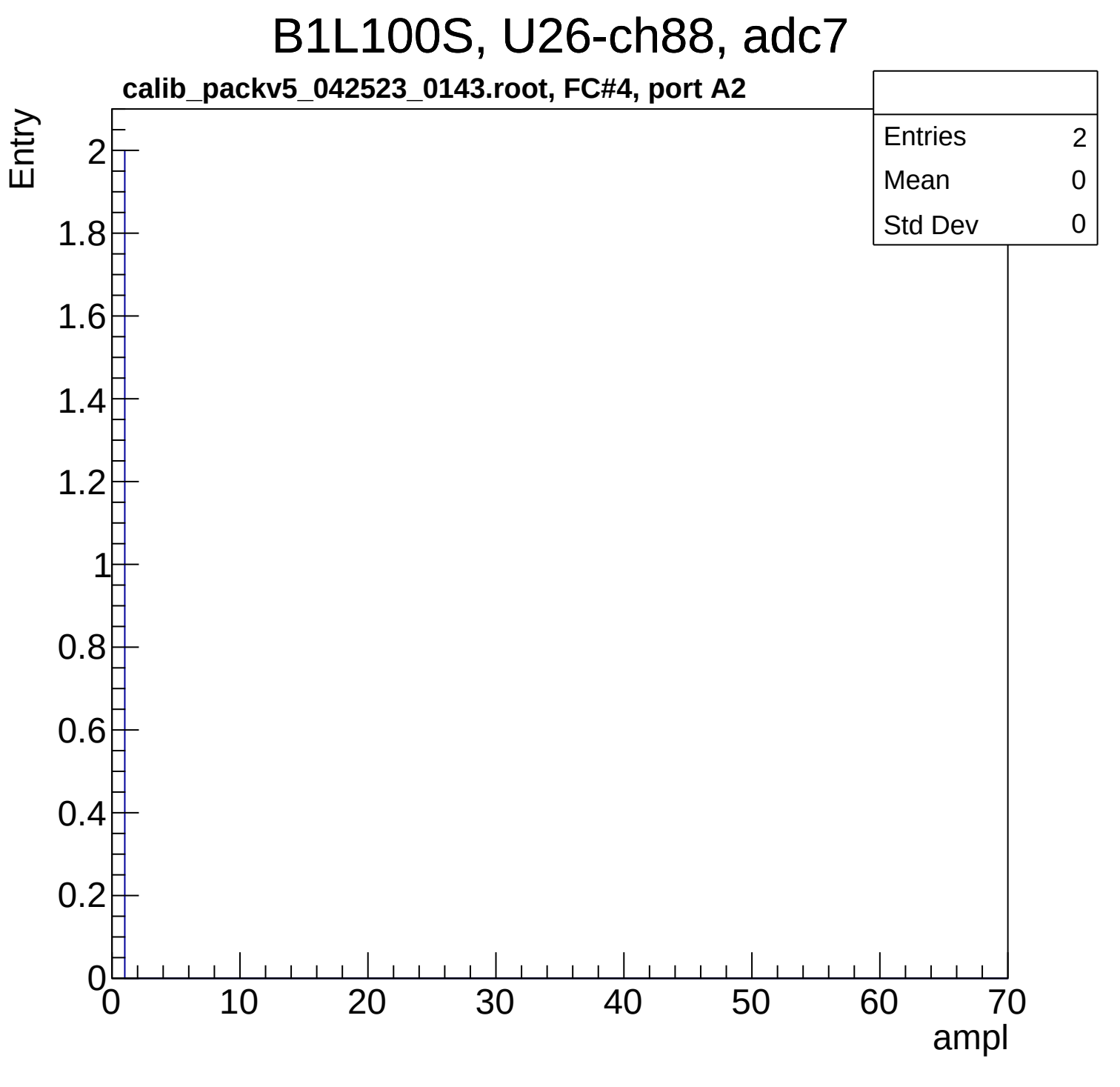
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch88, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
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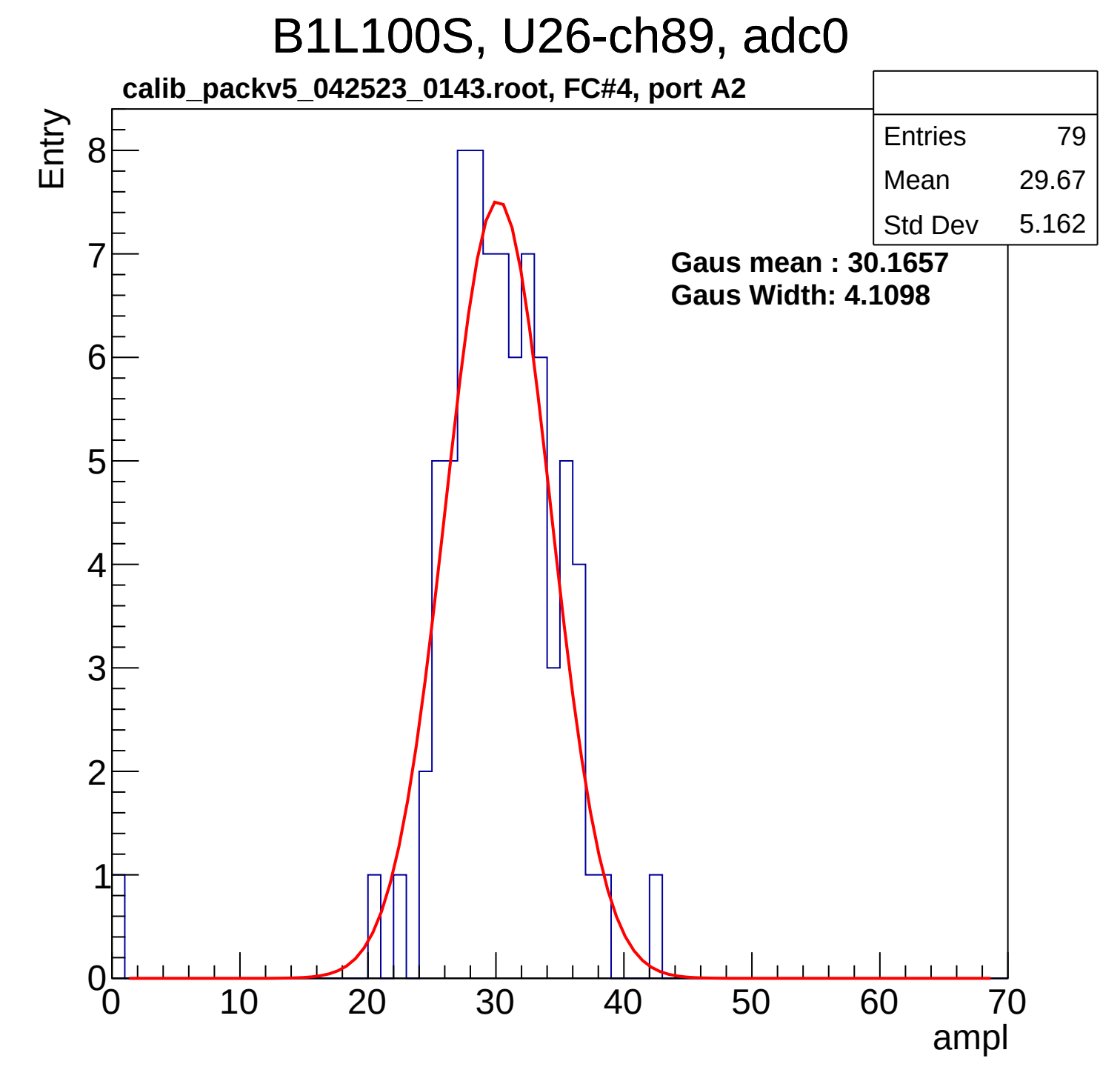
50
60
70
ampl


B1L100S, U26-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.67
Std Dev 5.162
Gaus mean : 30.1657
Gaus Width: 4.1098
Entry
ampl
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1
2
3
4
5
6
7
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0
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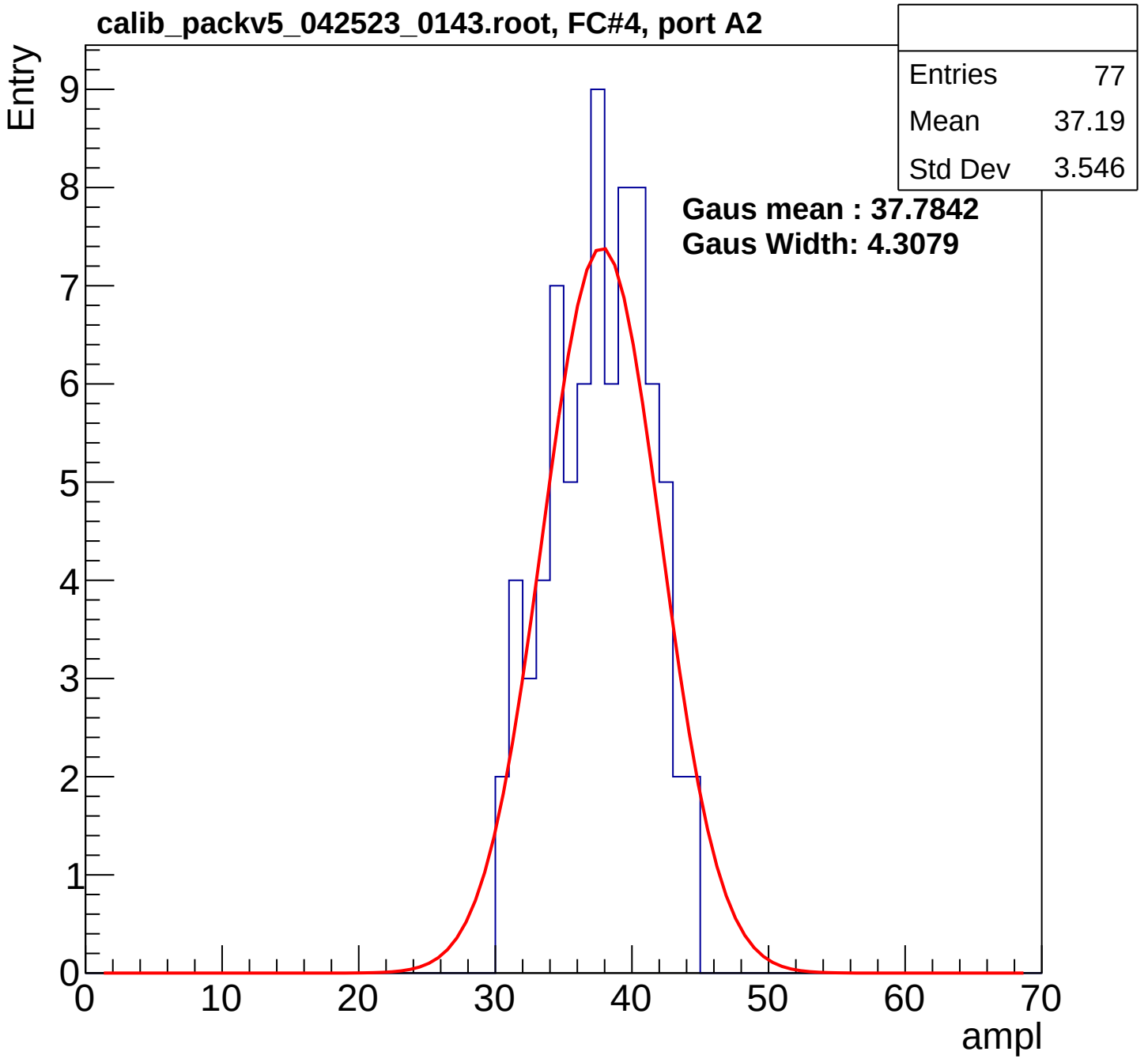
B1L100S, U26-ch89, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.19
Std Dev 3.546
Gaus mean : 37.7842
Gaus Width: 4.3079
Entry
ampl
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2
3
4
5
6
7
8
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0
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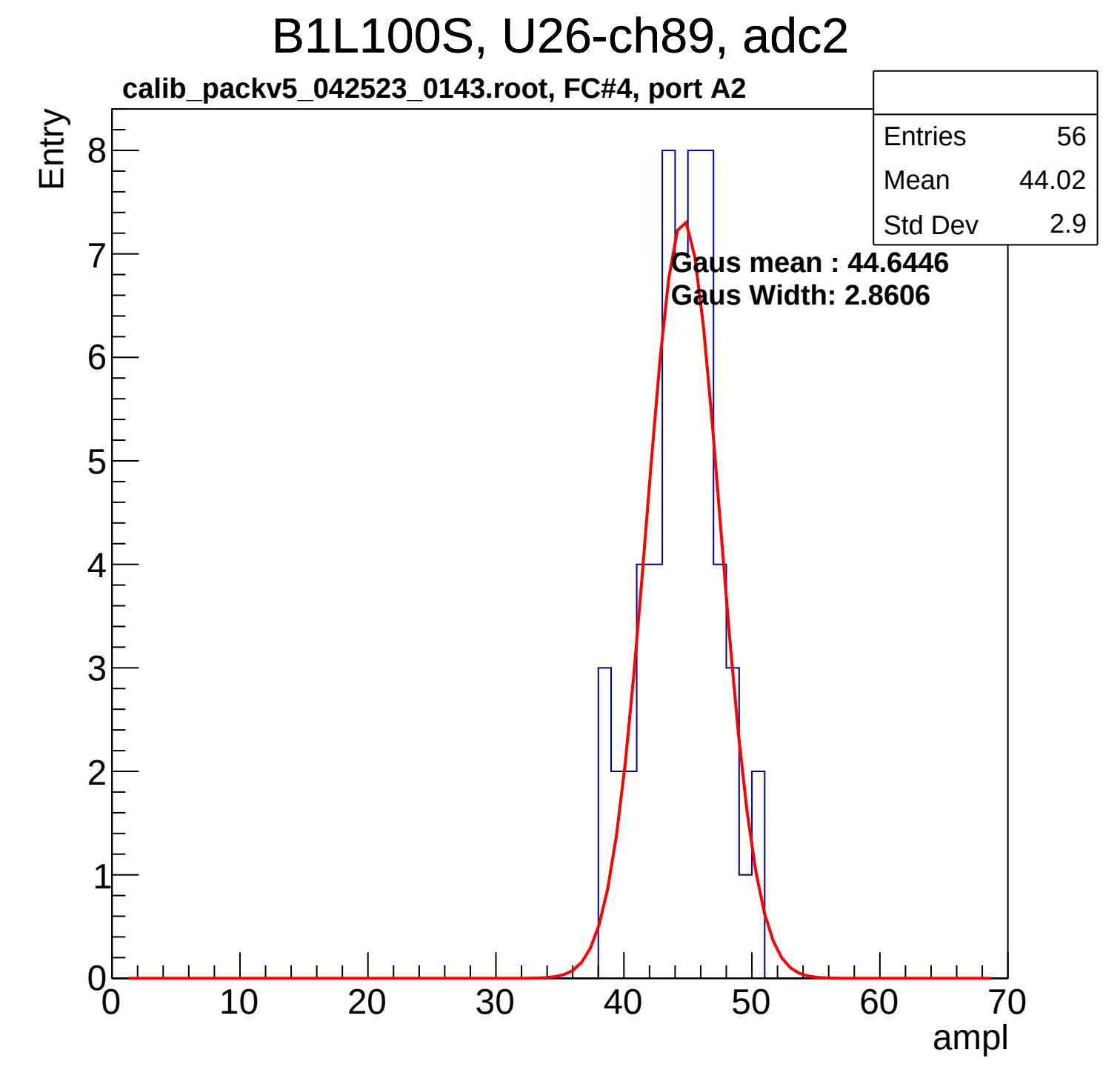
B1L100S, U26-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.02
Std Dev 2.9
Gaus mean : 44.6446
Gaus Width: 2.8606
Entry
ampl
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1
2
3
4
5
6
7
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0
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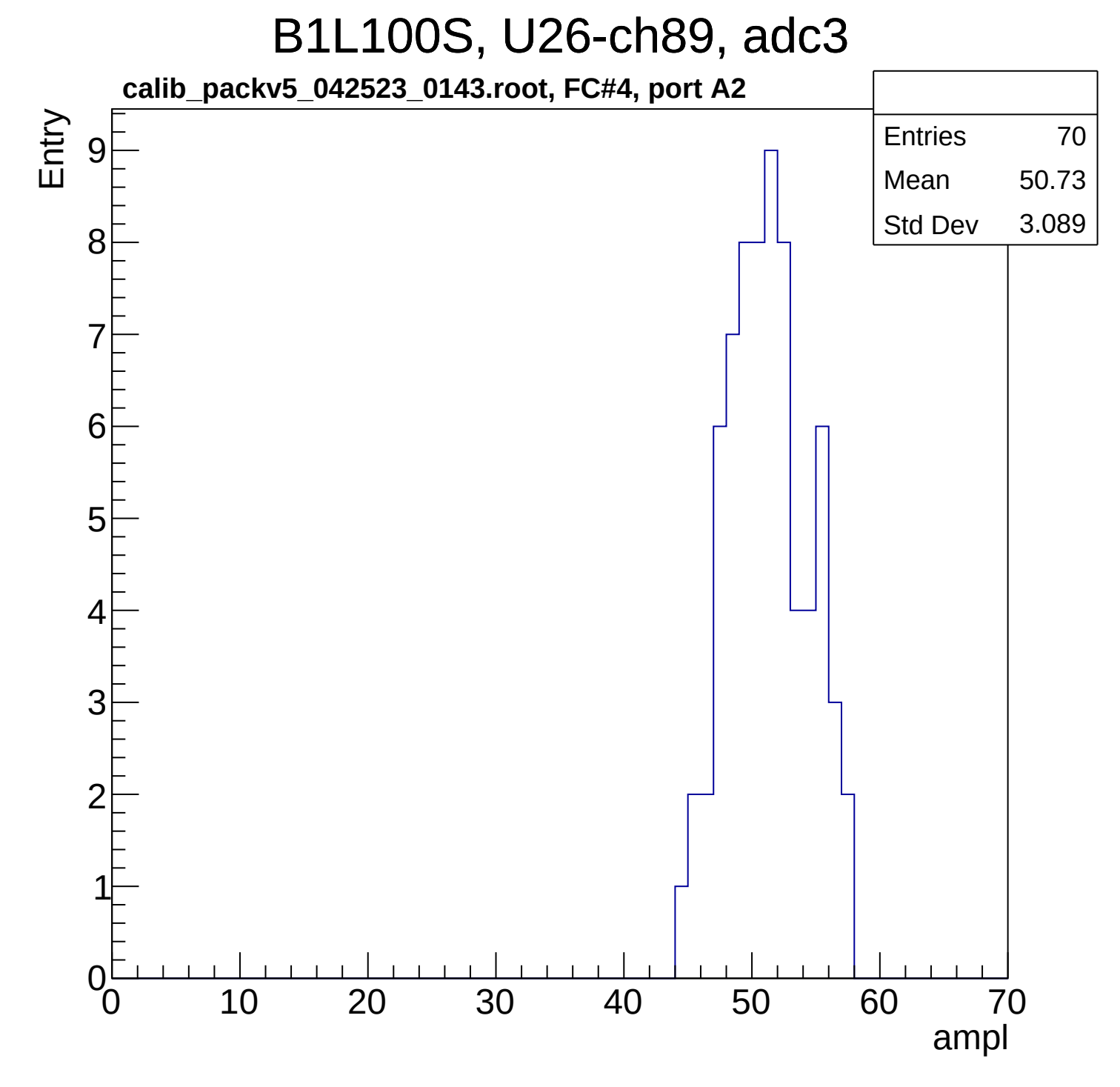
B1L100S, U26-ch89, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.73
Std Dev 3.089
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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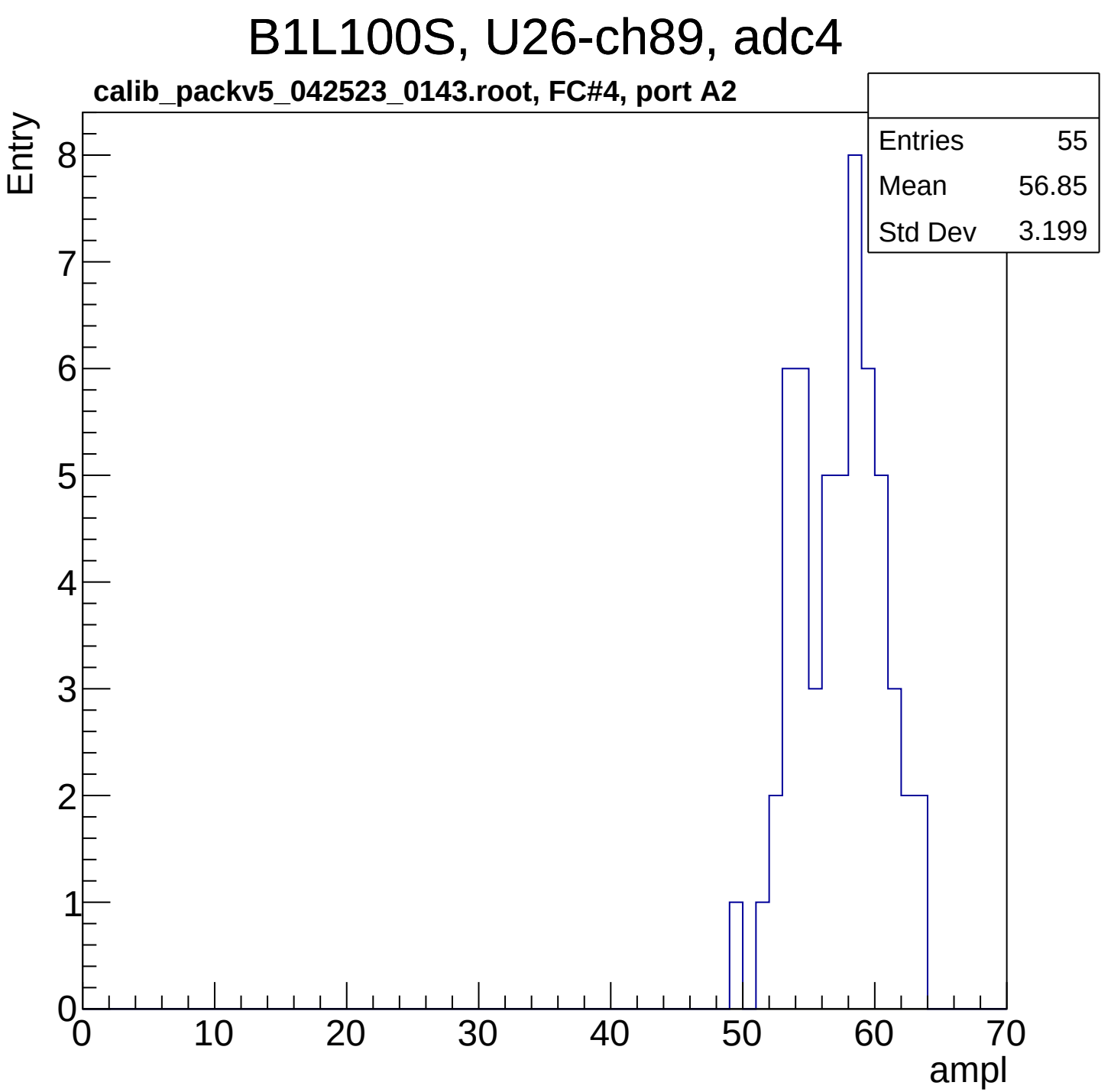
B1L100S, U26-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.85
Std Dev 3.199
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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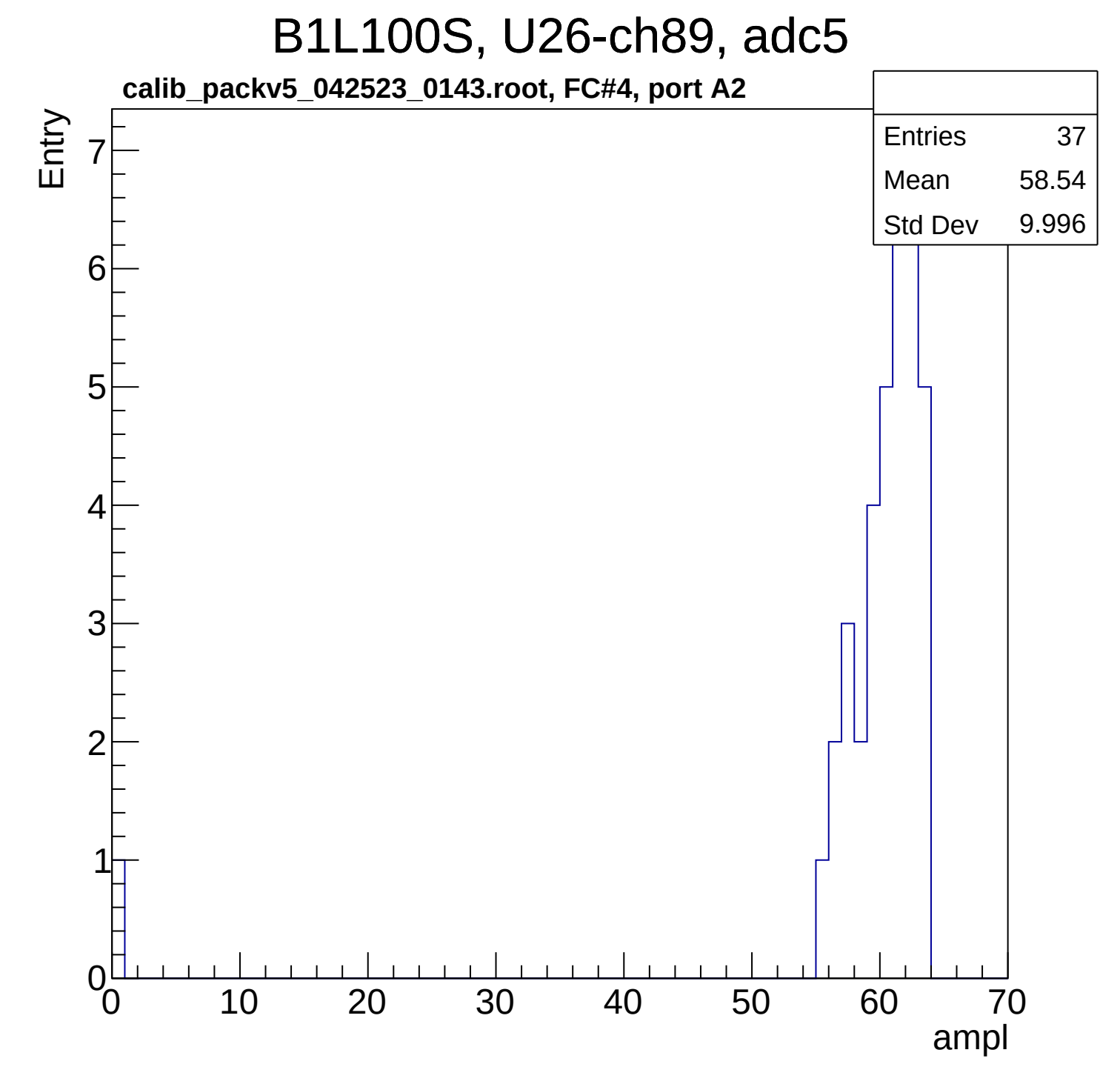
B1L100S, U26-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.54
Std Dev 9.996
Entry
ampl
0
1
2
3
4
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6
7
0
10
20
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60
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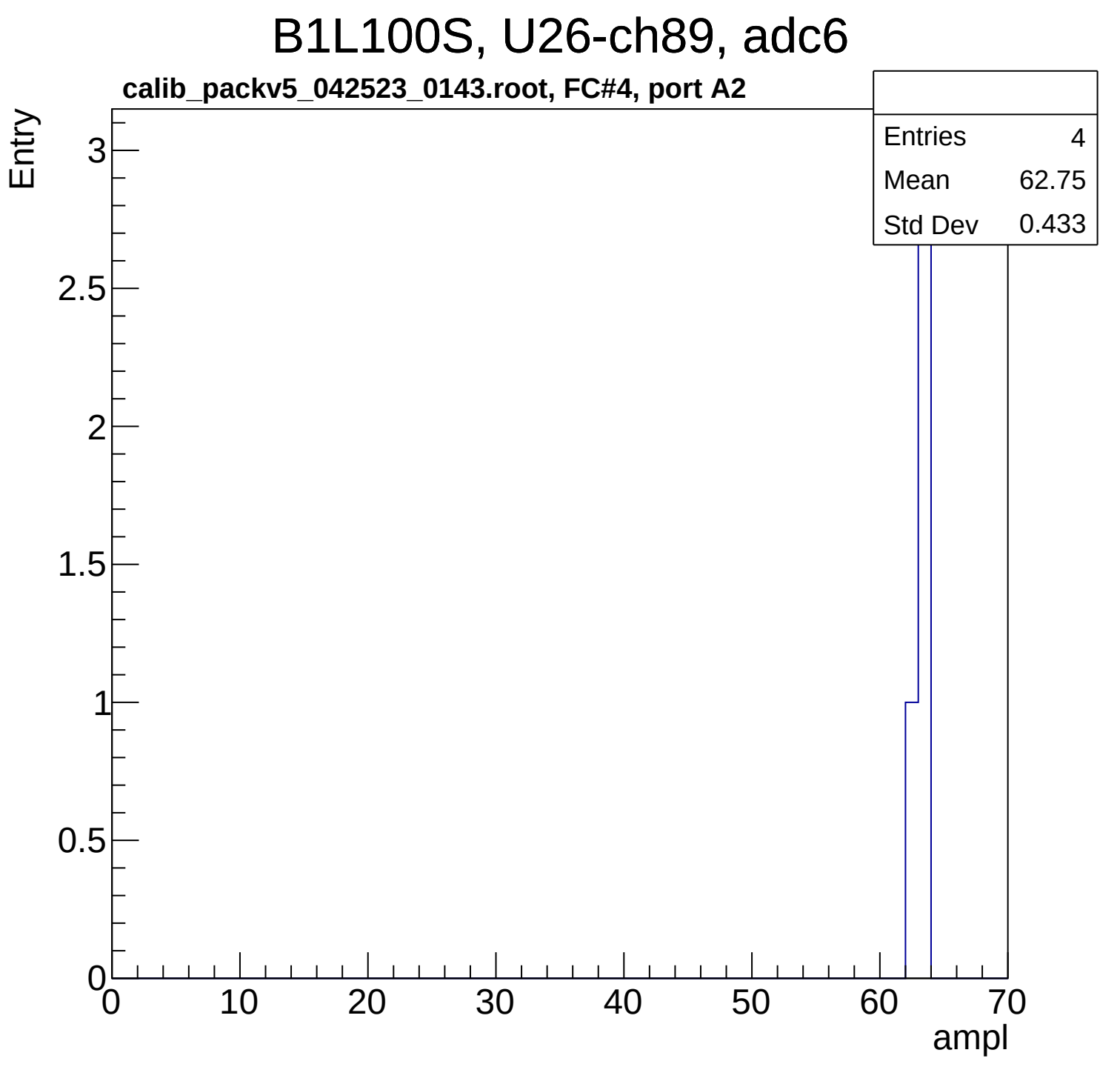
B1L100S, U26-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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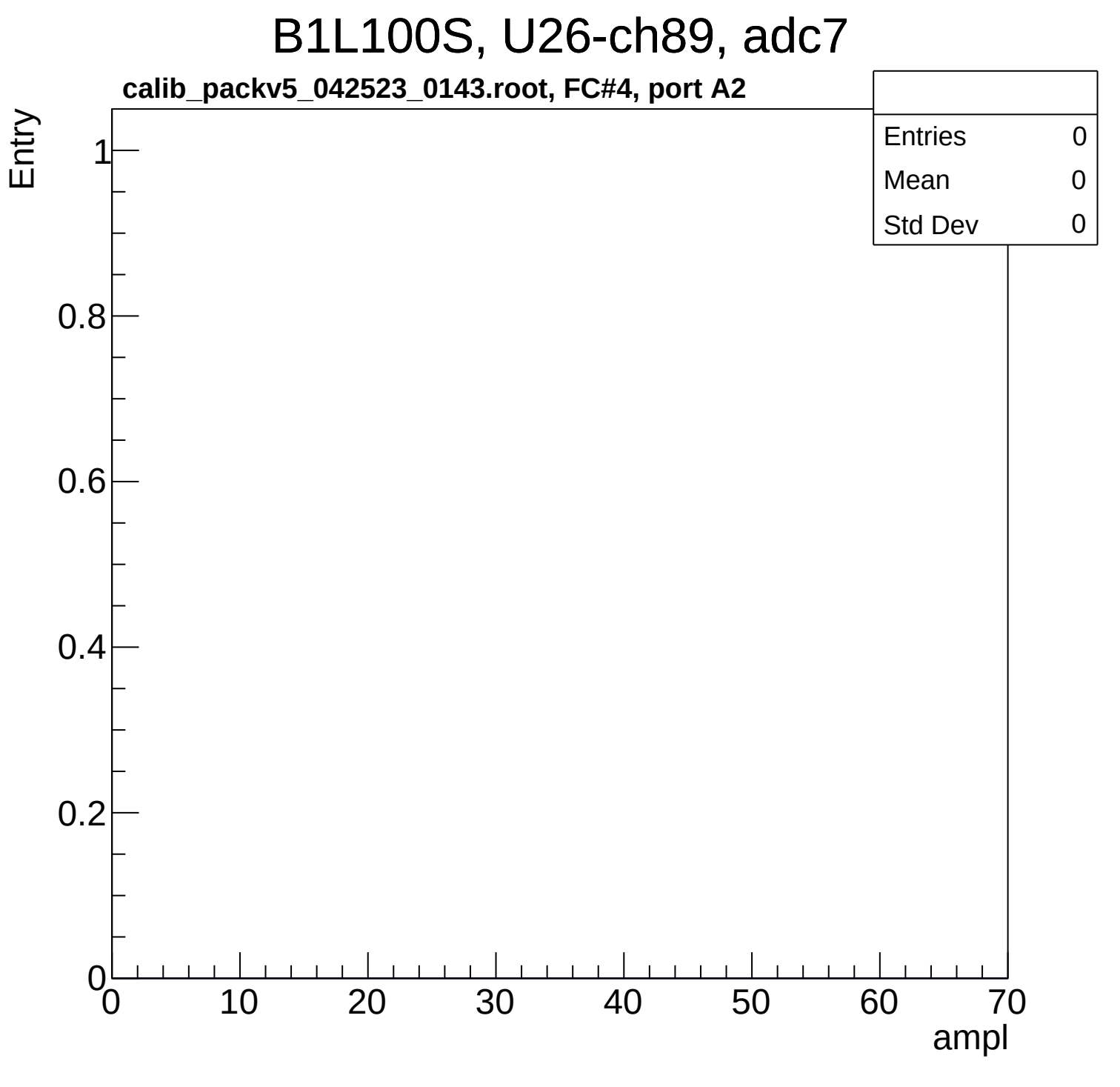
B1L100S, U26-ch89, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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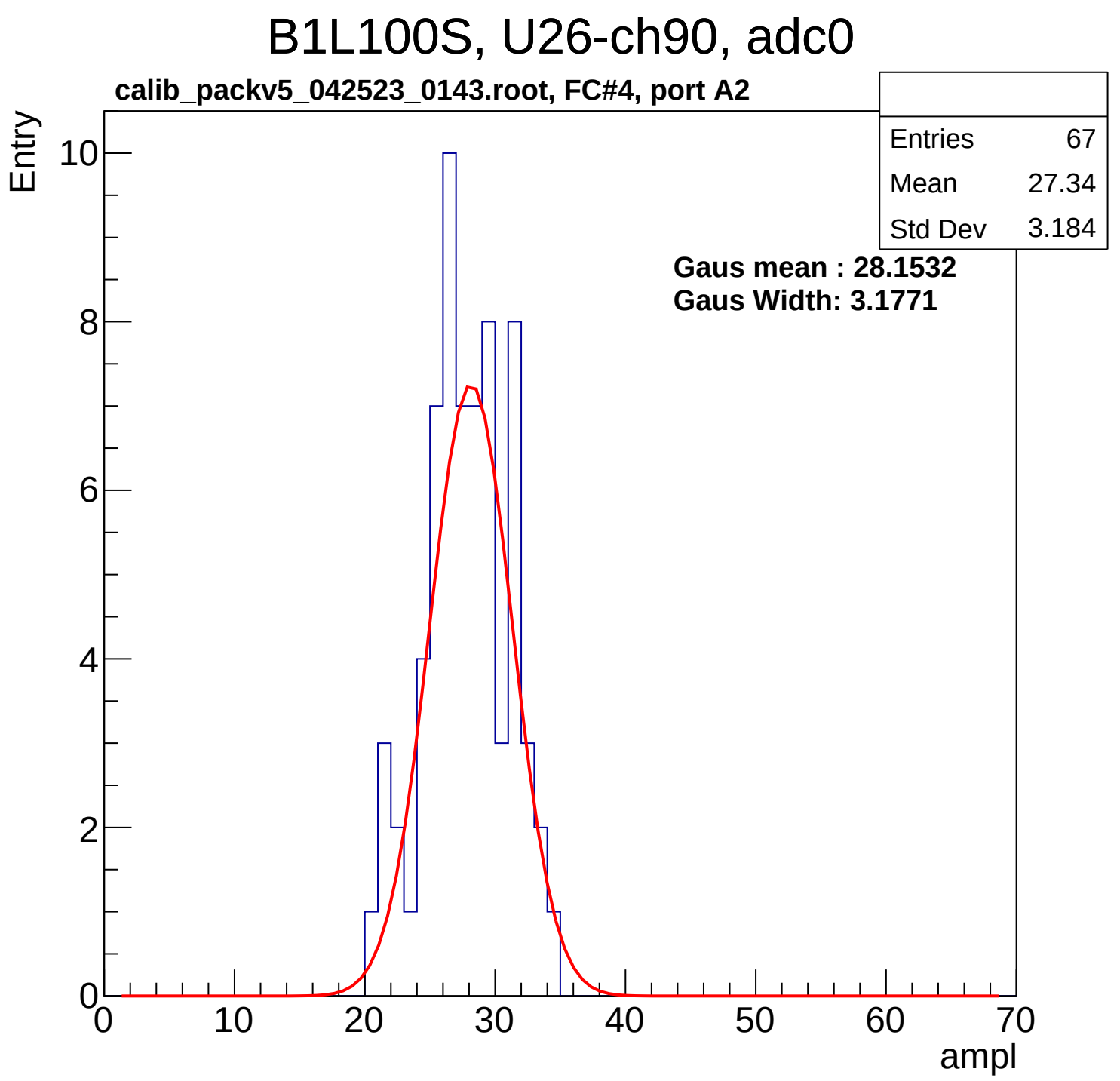
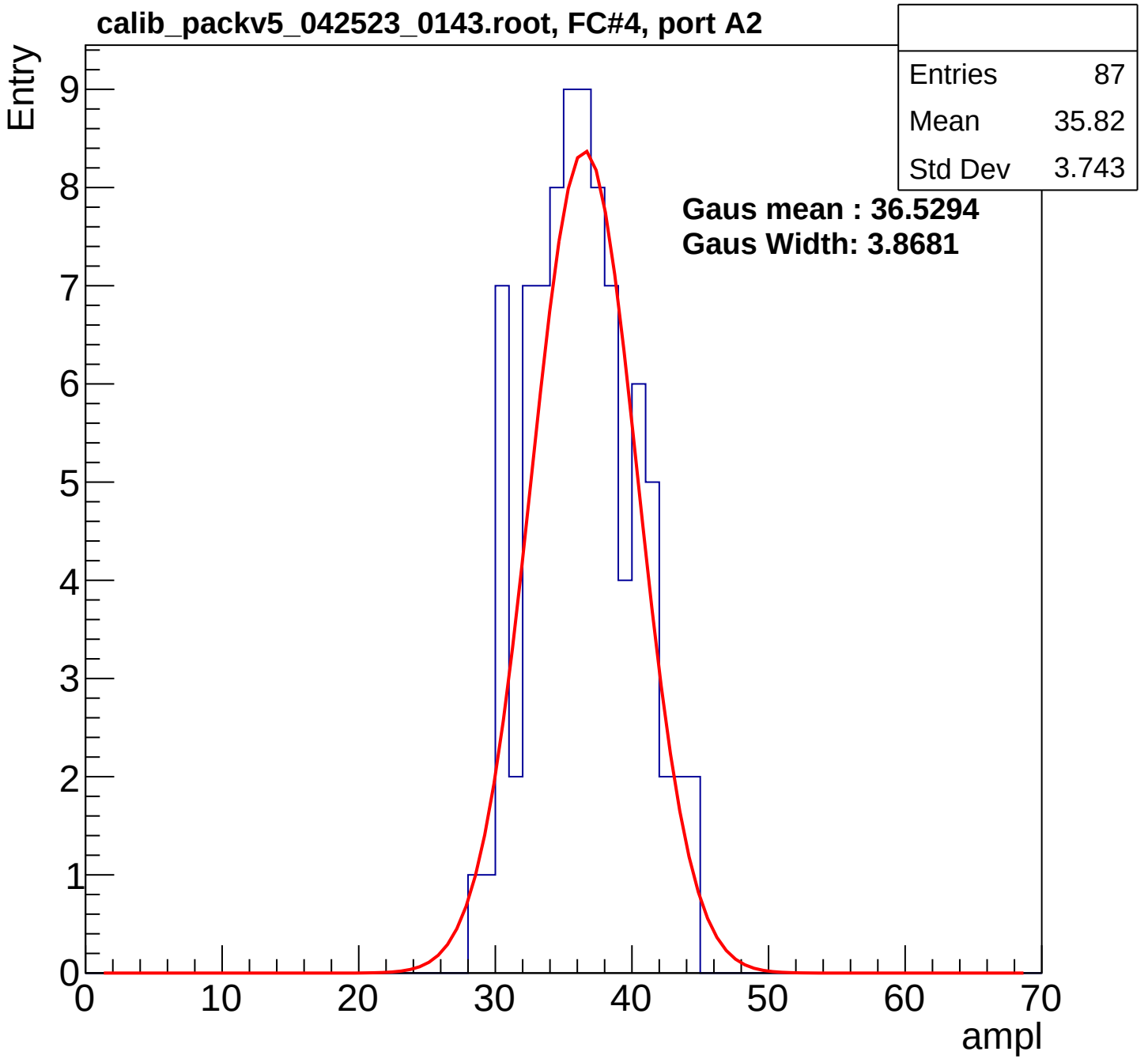
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.34
Std Dev 3.184
Gaus mean : 28.1532
Gaus Width: 3.1771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 35.82
Std Dev 3.743
Gaus mean : 36.5294
Gaus Width: 3.8681
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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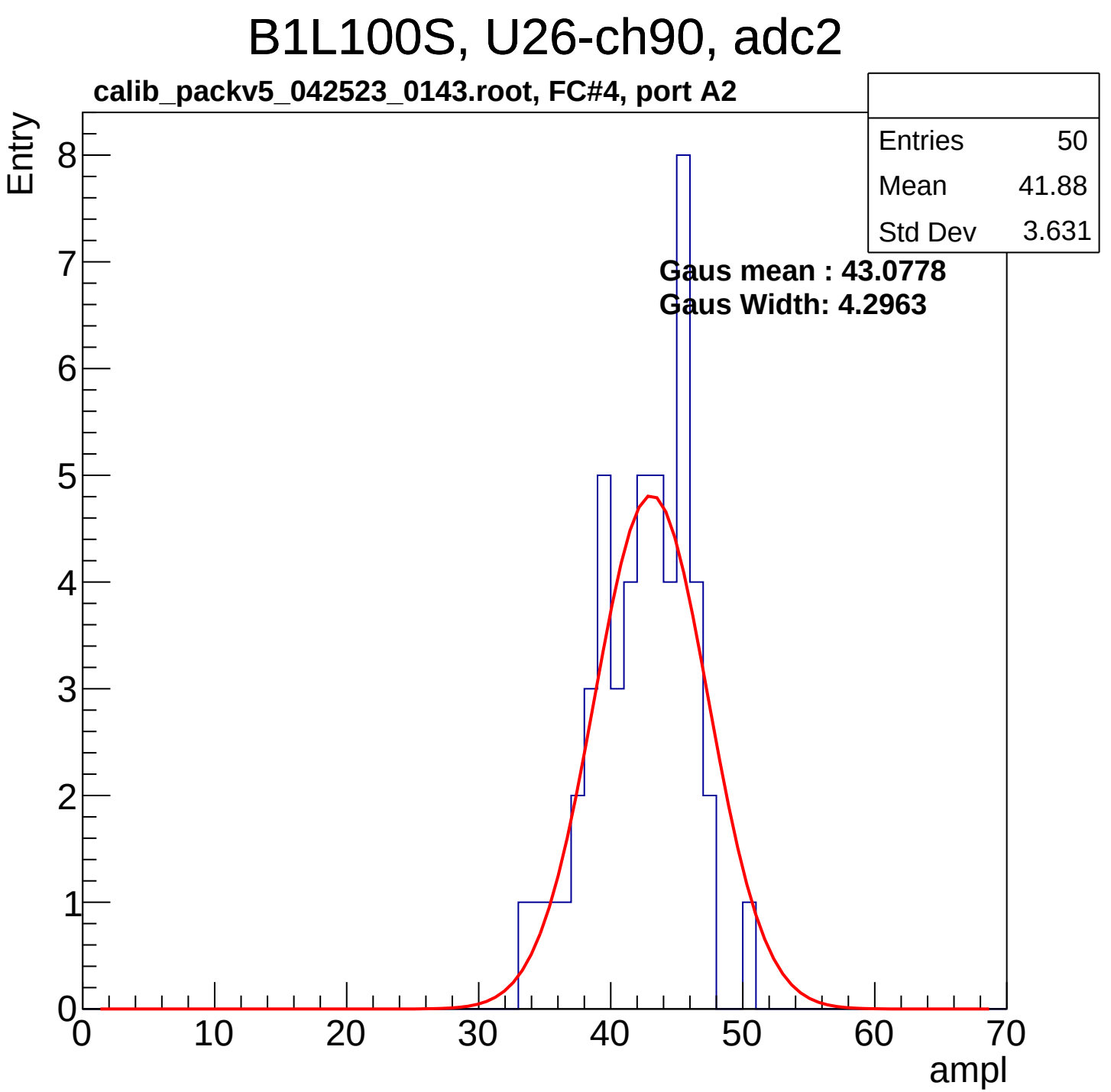
B1L100S, U26-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 41.88
Std Dev 3.631
Gaus mean : 43.0778
Gaus Width: 4.2963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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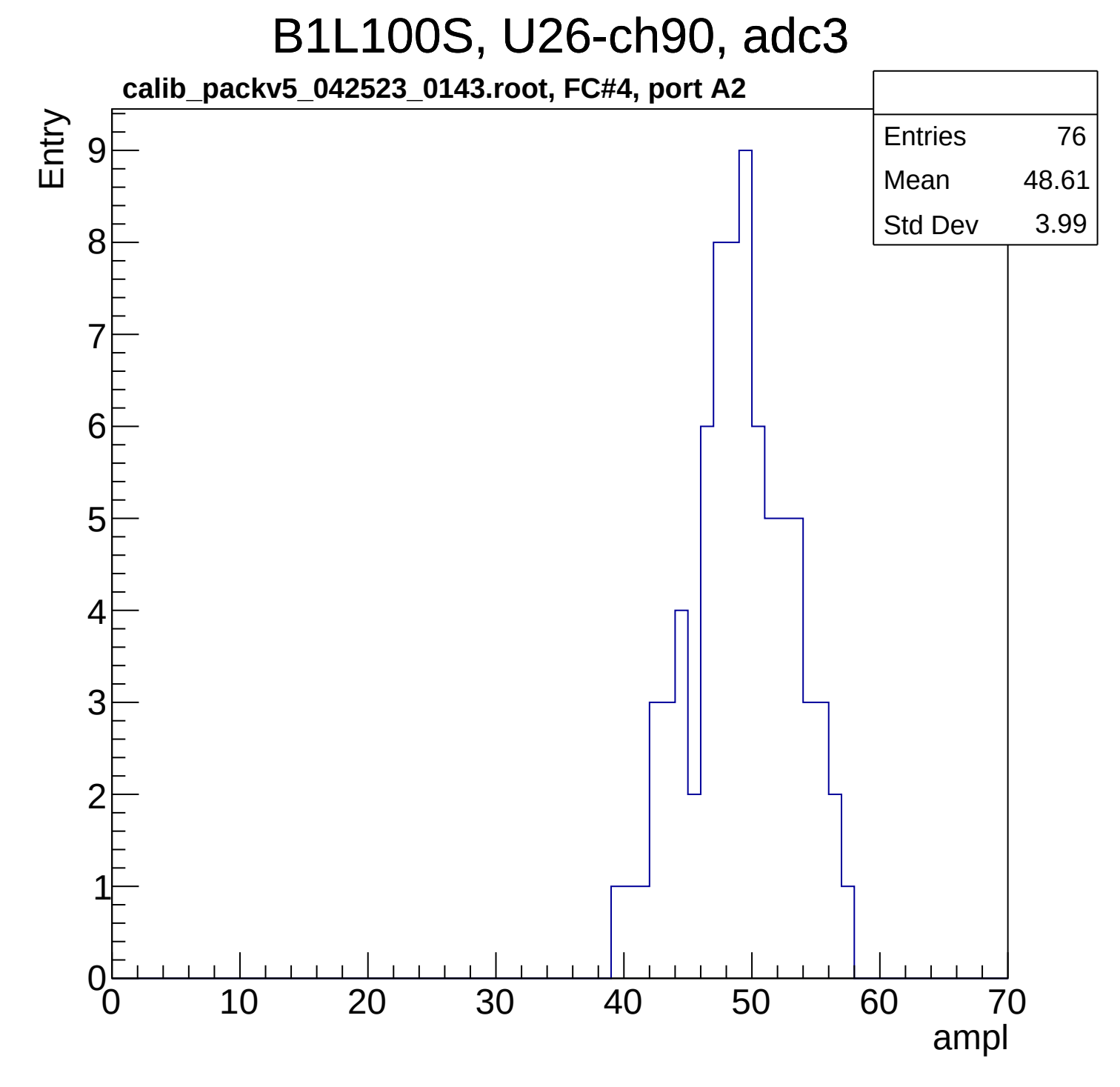
B1L100S, U26-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 48.61
Std Dev 3.99
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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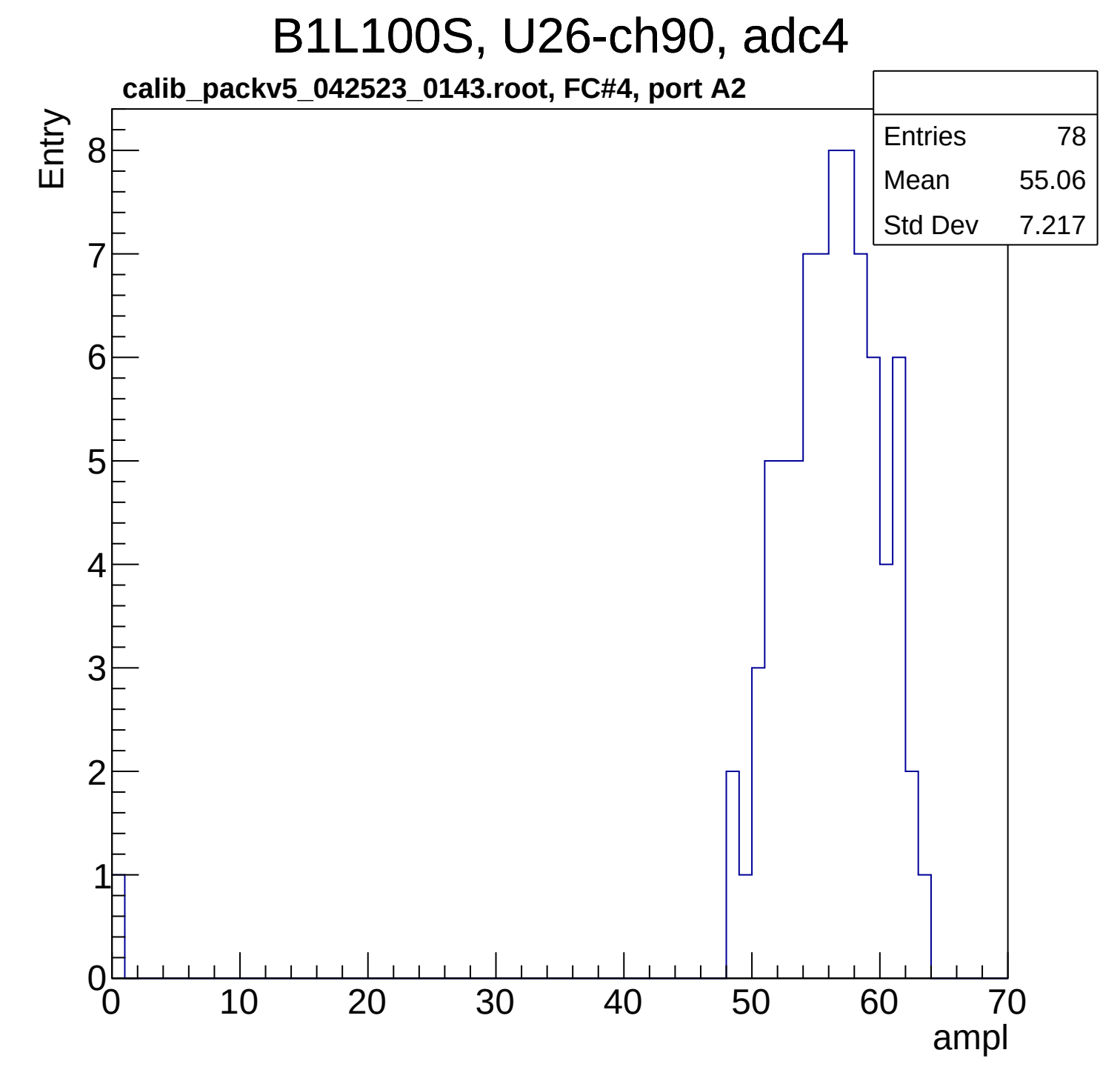
B1L100S, U26-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 55.06
Std Dev 7.217
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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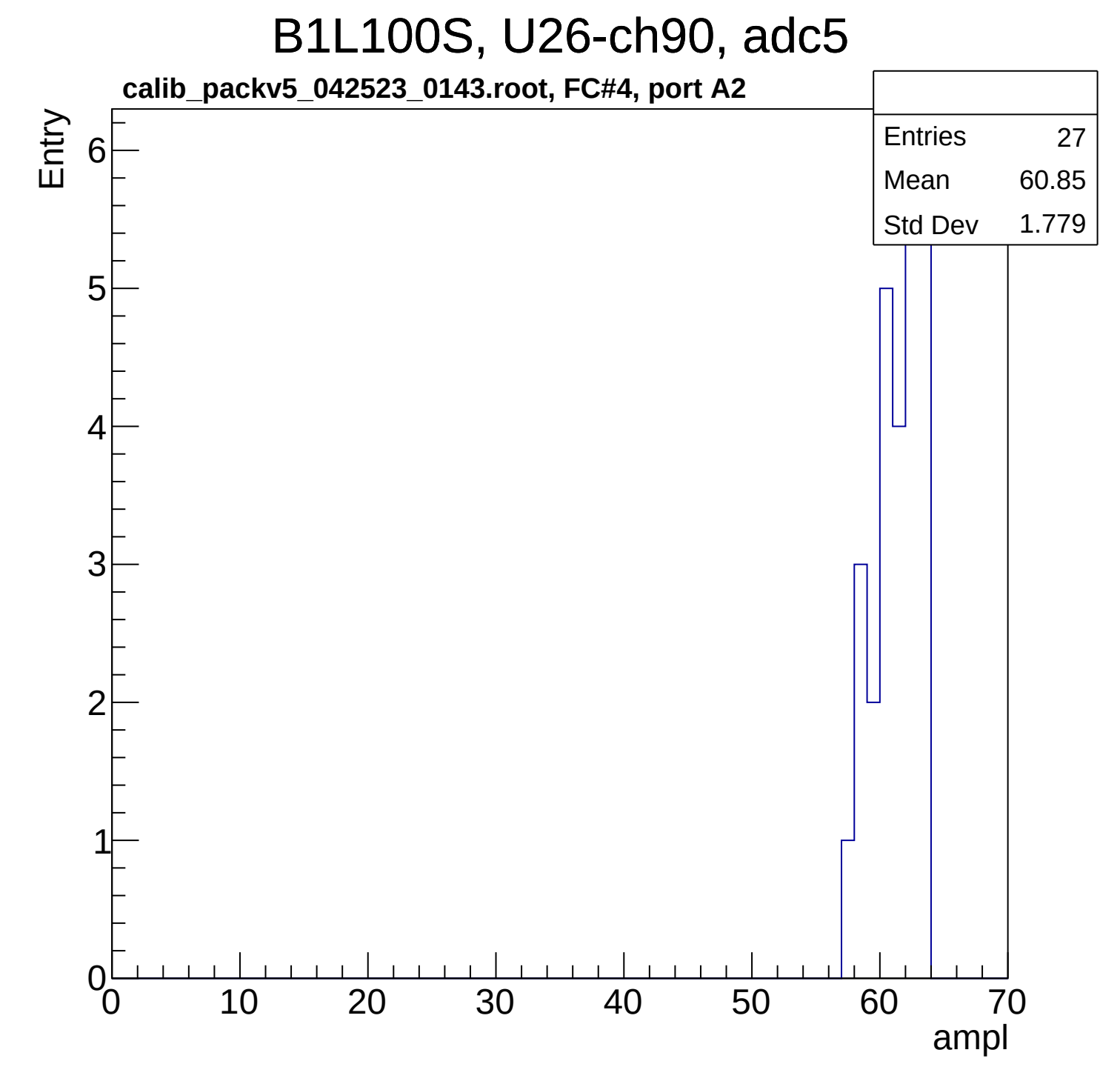
B1L100S, U26-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 60.85
Std Dev 1.779
Entry
ampl
0
1
2
3
4
5
6
0
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30
40
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60
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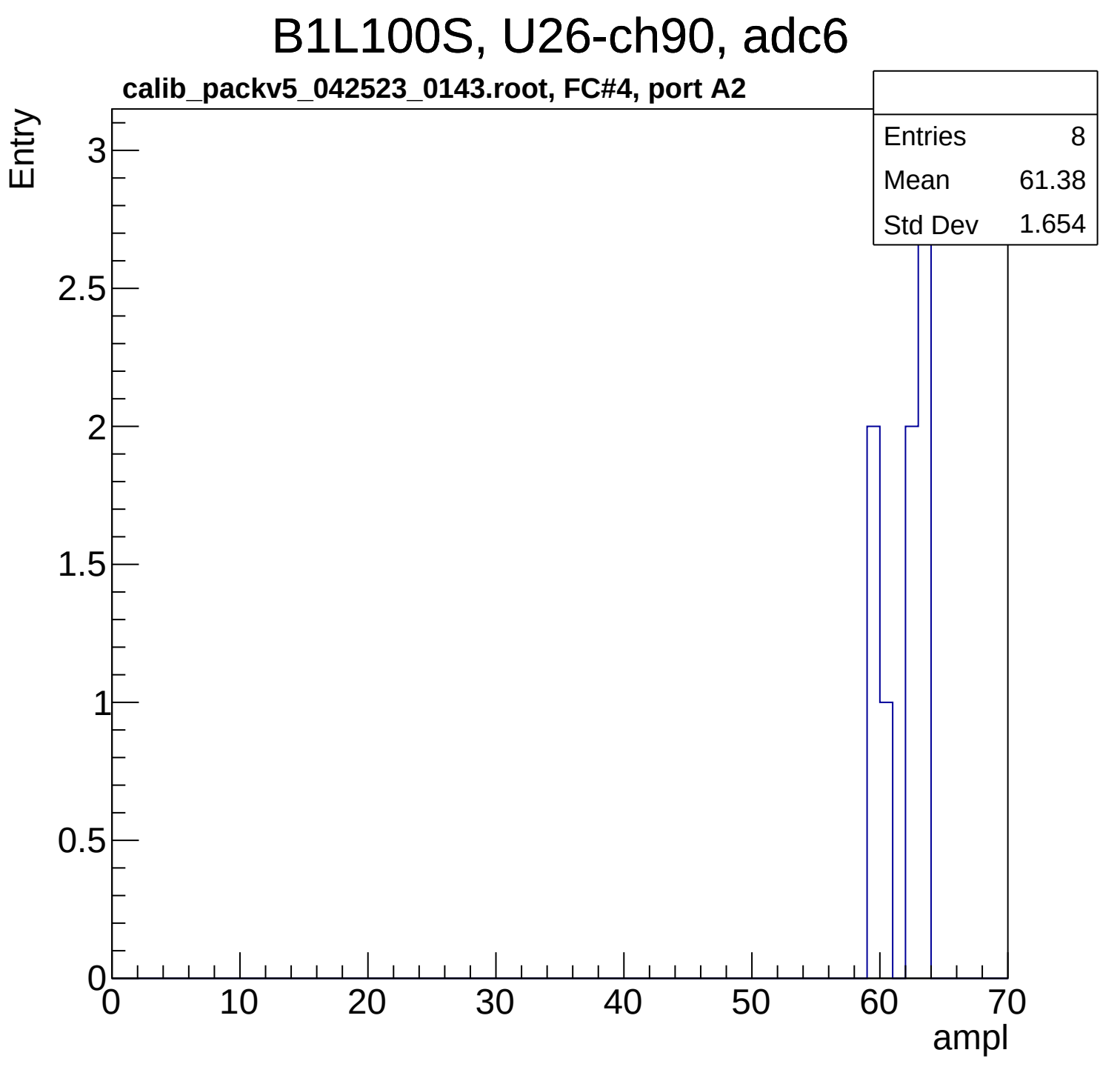
B1L100S, U26-ch90, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.38
Std Dev 1.654
Entry
ampl
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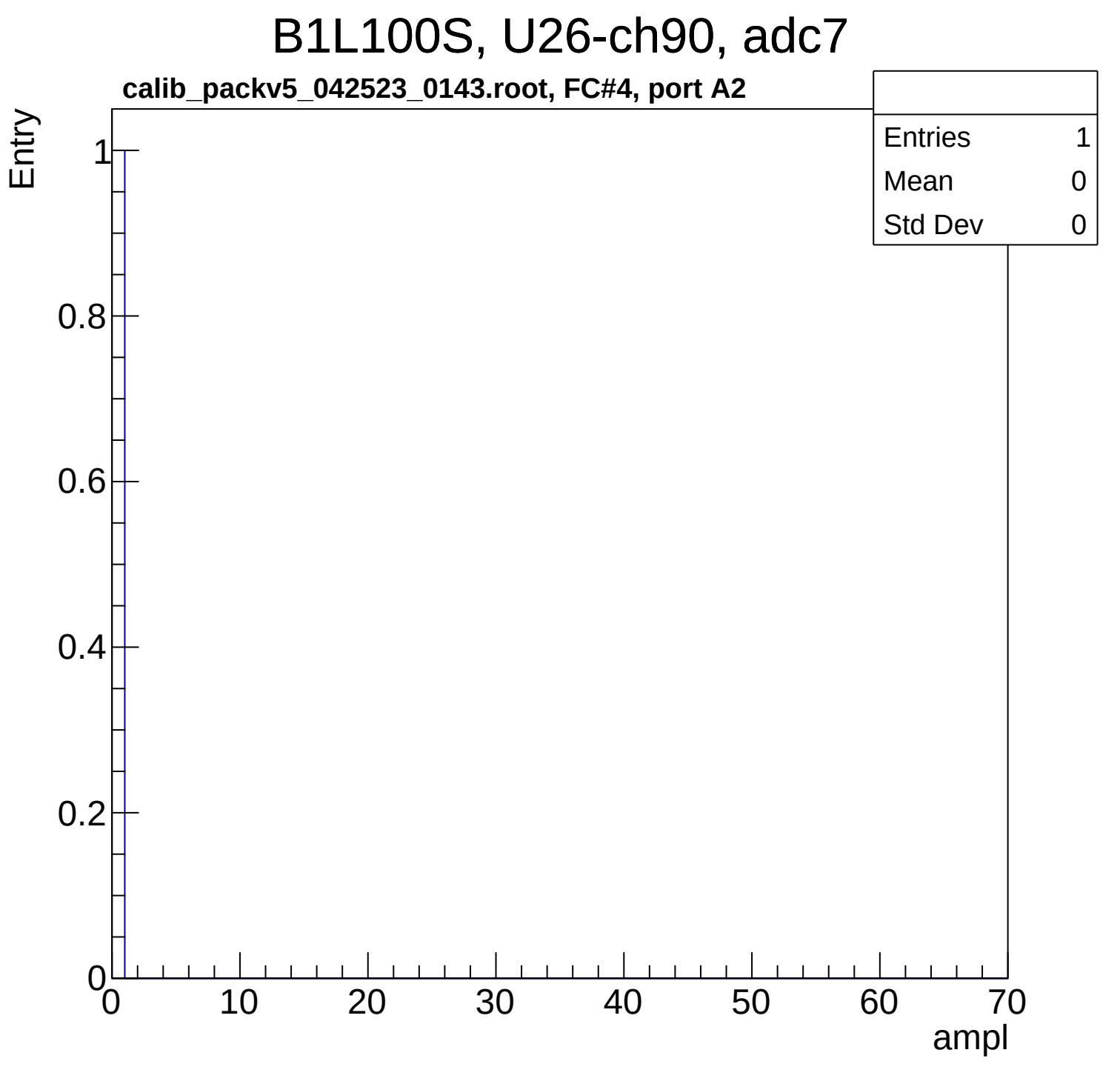
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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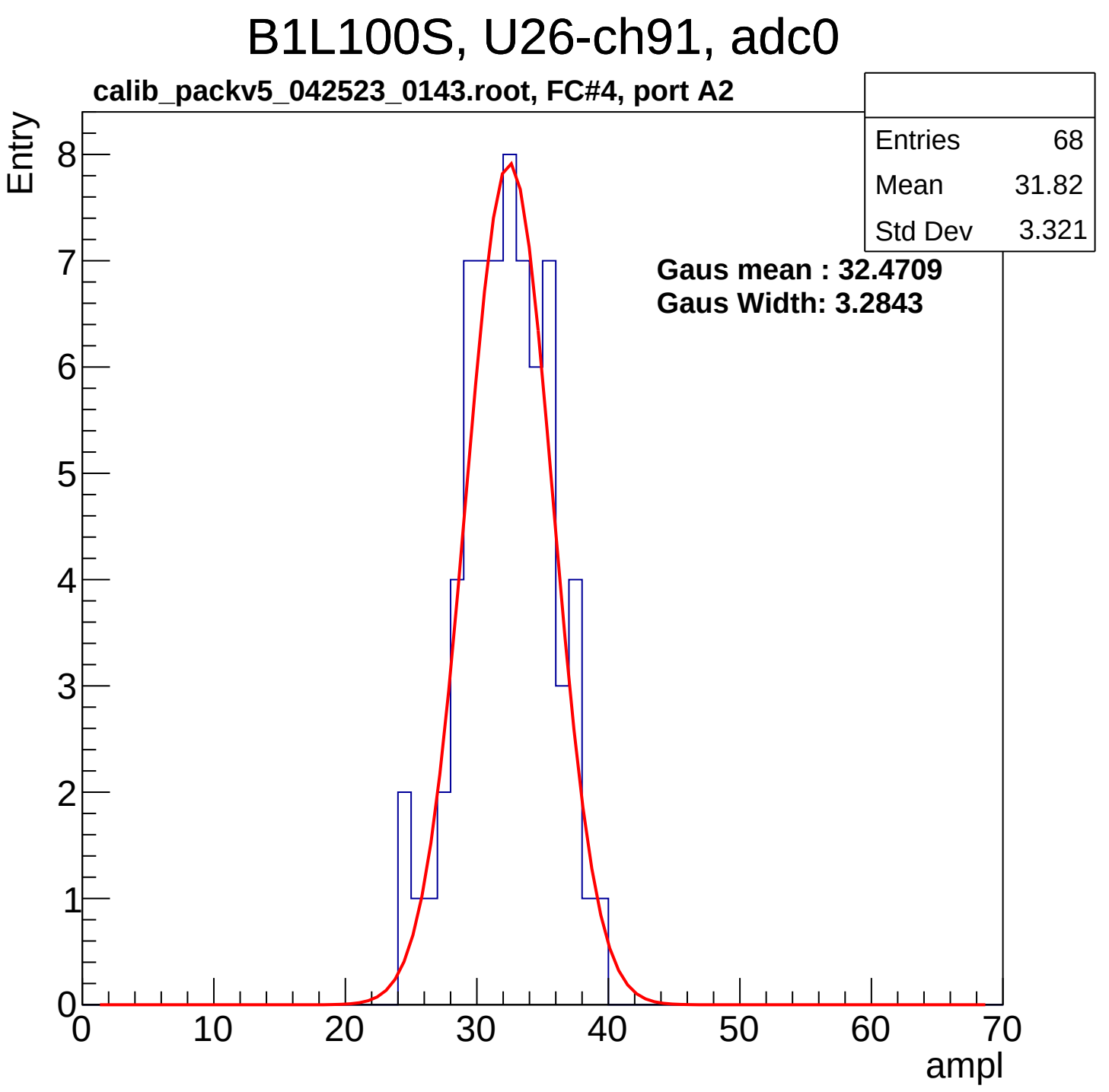
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 31.82
Std Dev 3.321
Gaus mean : 32.4709
Gaus Width: 3.2843
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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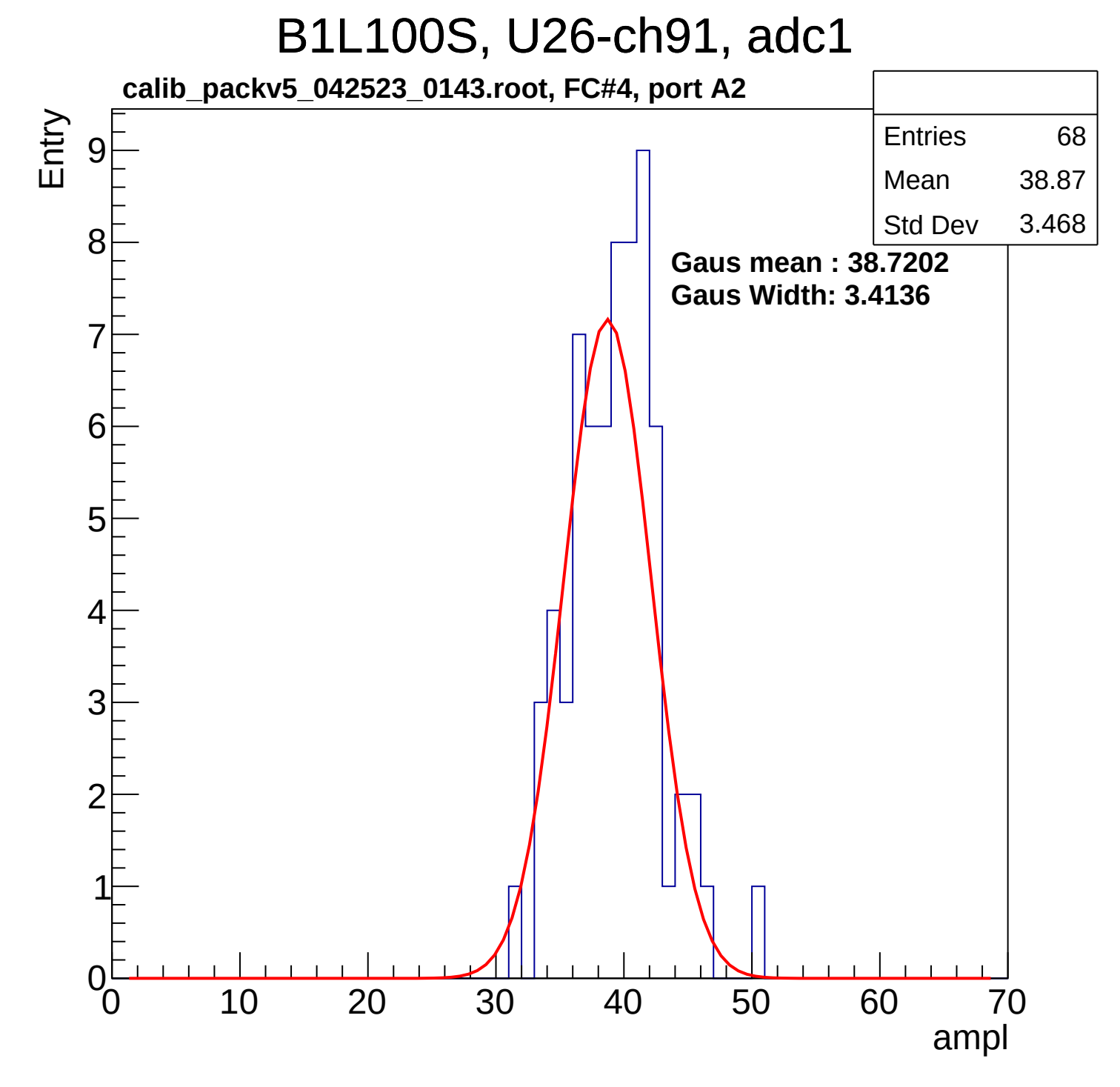
40
50
60
70


B1L100S, U26-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 38.87
Std Dev 3.468
Gaus mean : 38.7202
Gaus Width: 3.4136
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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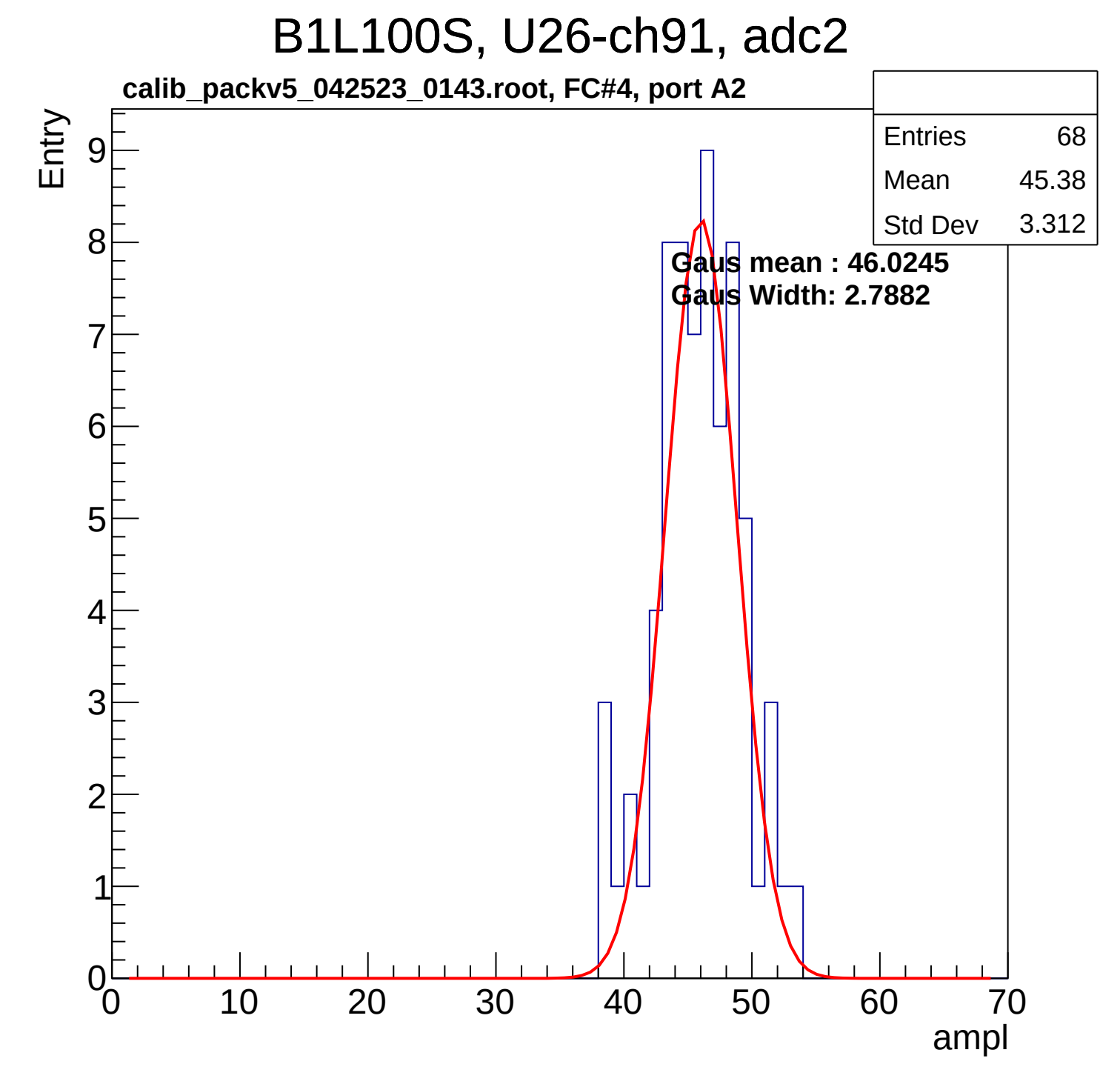
B1L100S, U26-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 45.38
Std Dev 3.312
Gaus mean : 46.0245
Gaus Width: 2.7882
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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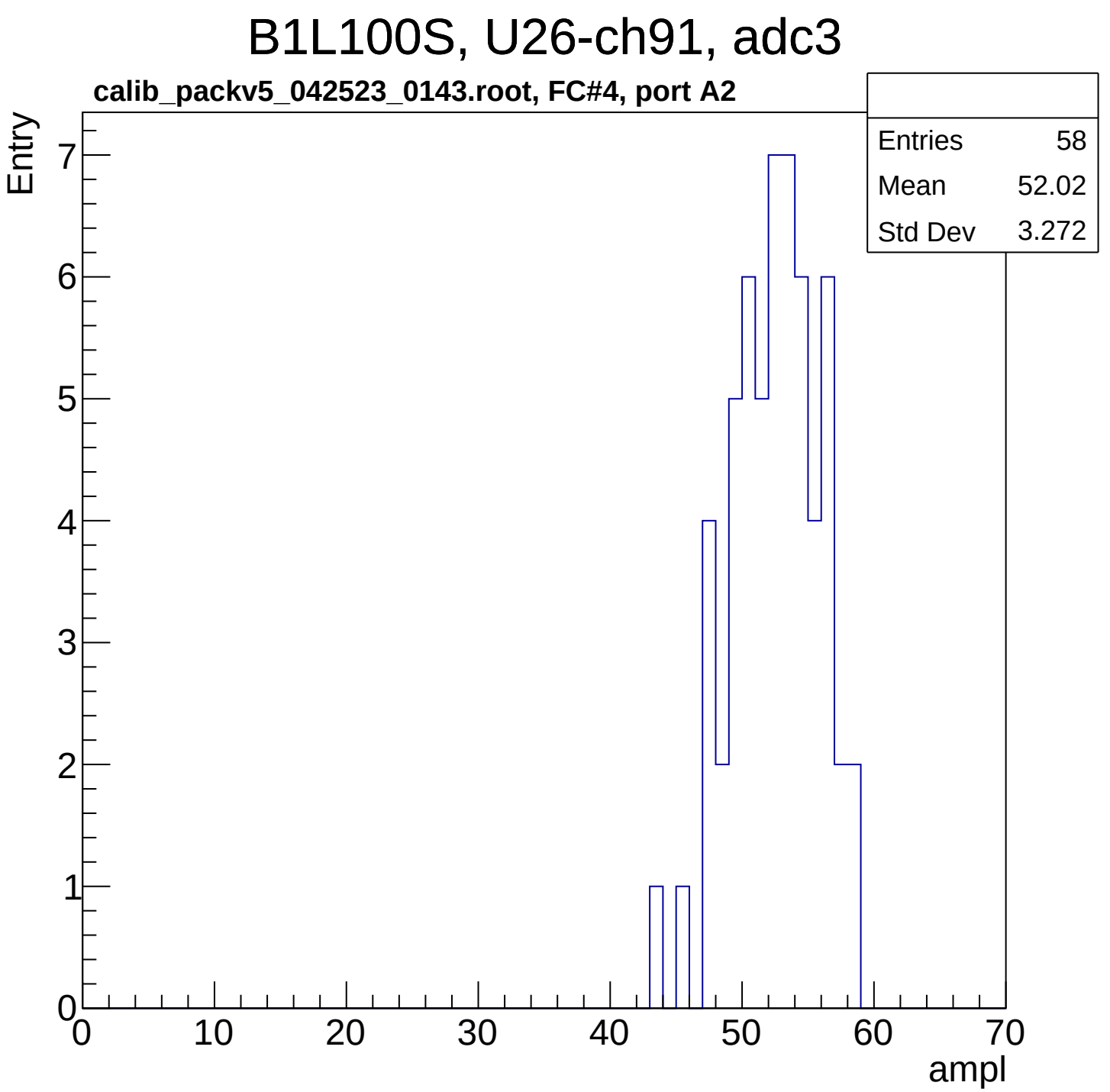
B1L100S, U26-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 52.02
Std Dev 3.272
Entry
ampl
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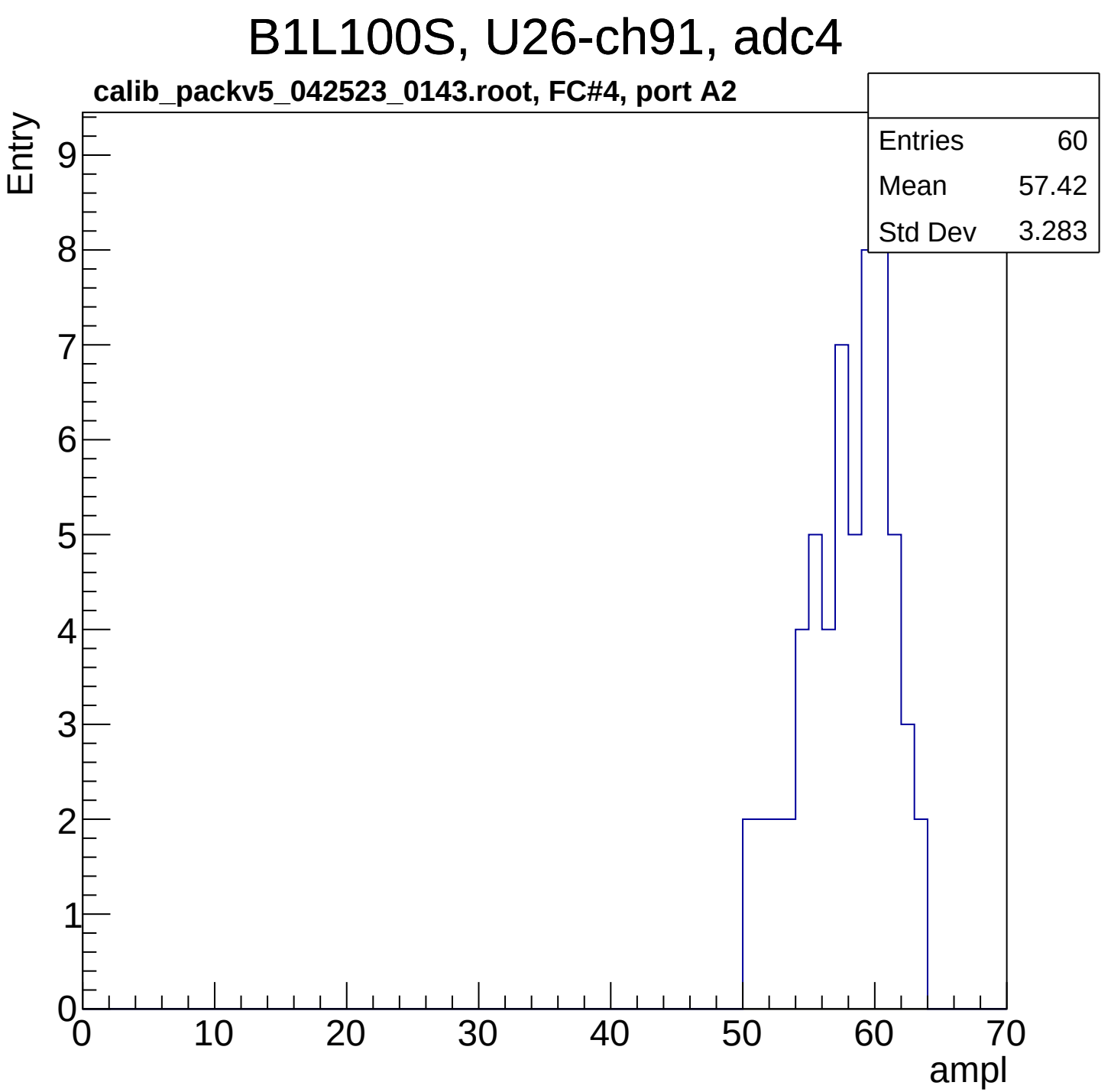
B1L100S, U26-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 57.42
Std Dev 3.283
Entry
ampl
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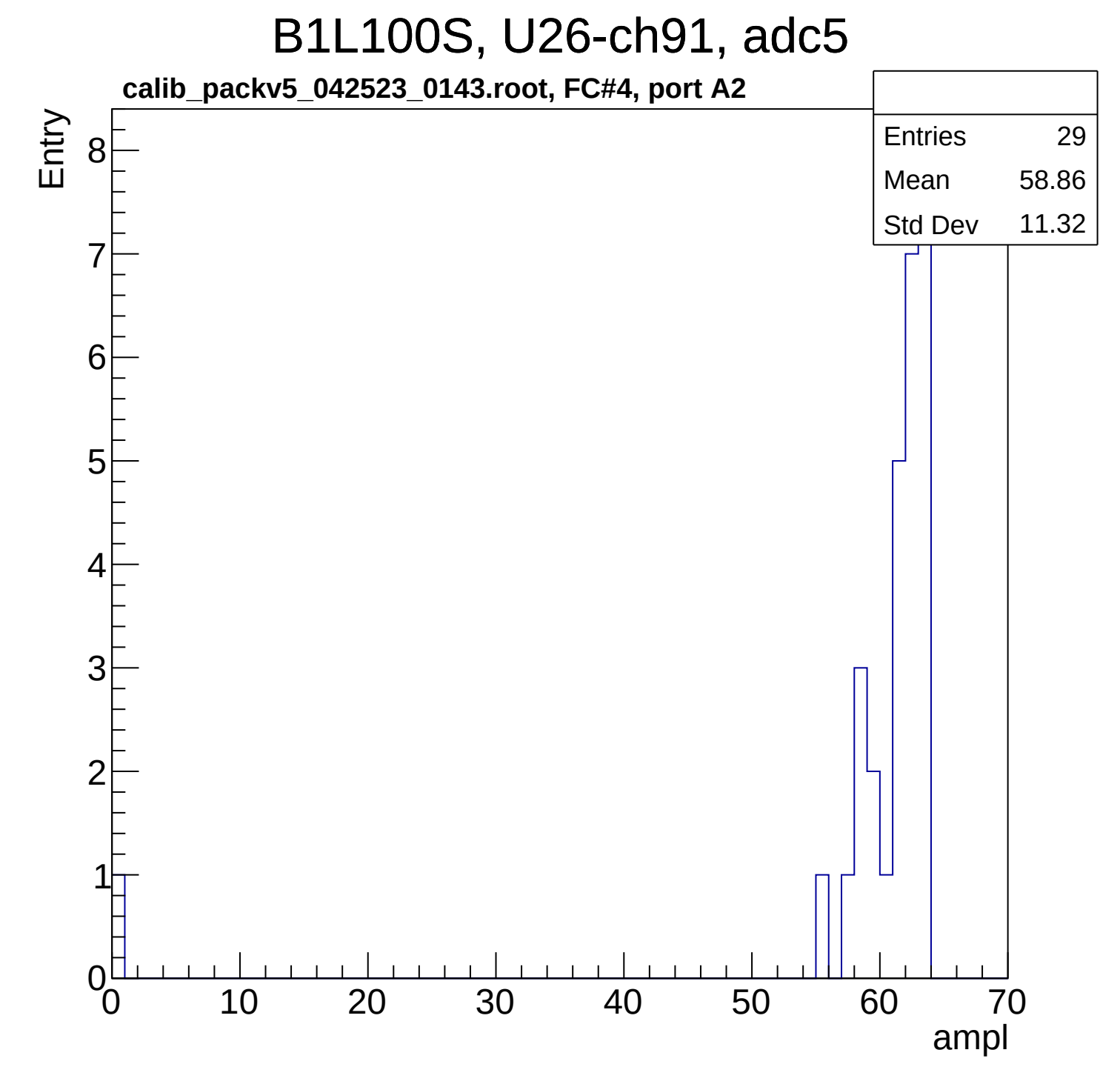
B1L100S, U26-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.86
Std Dev 11.32
Entry
ampl
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5
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0
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60
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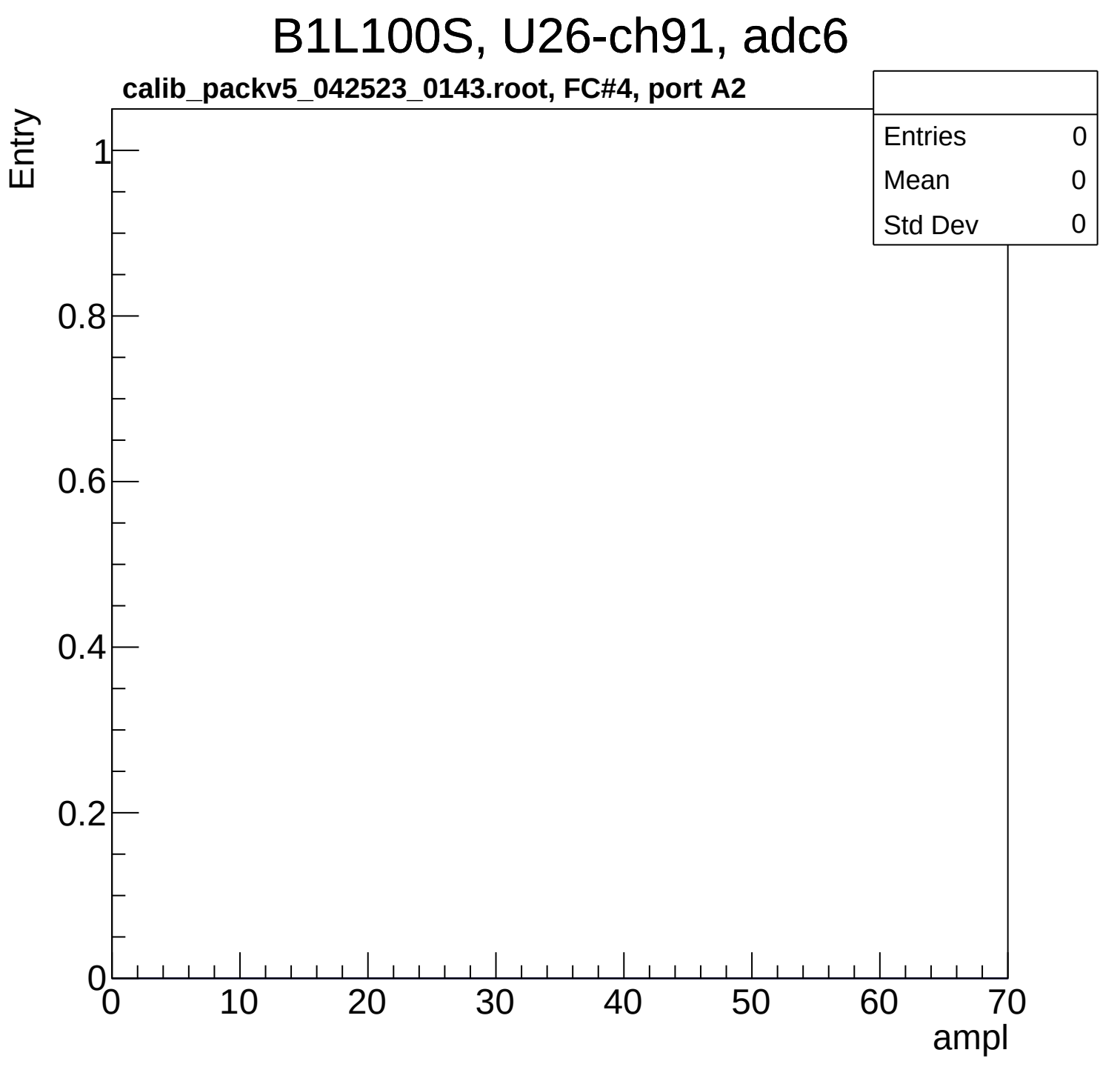
B1L100S, U26-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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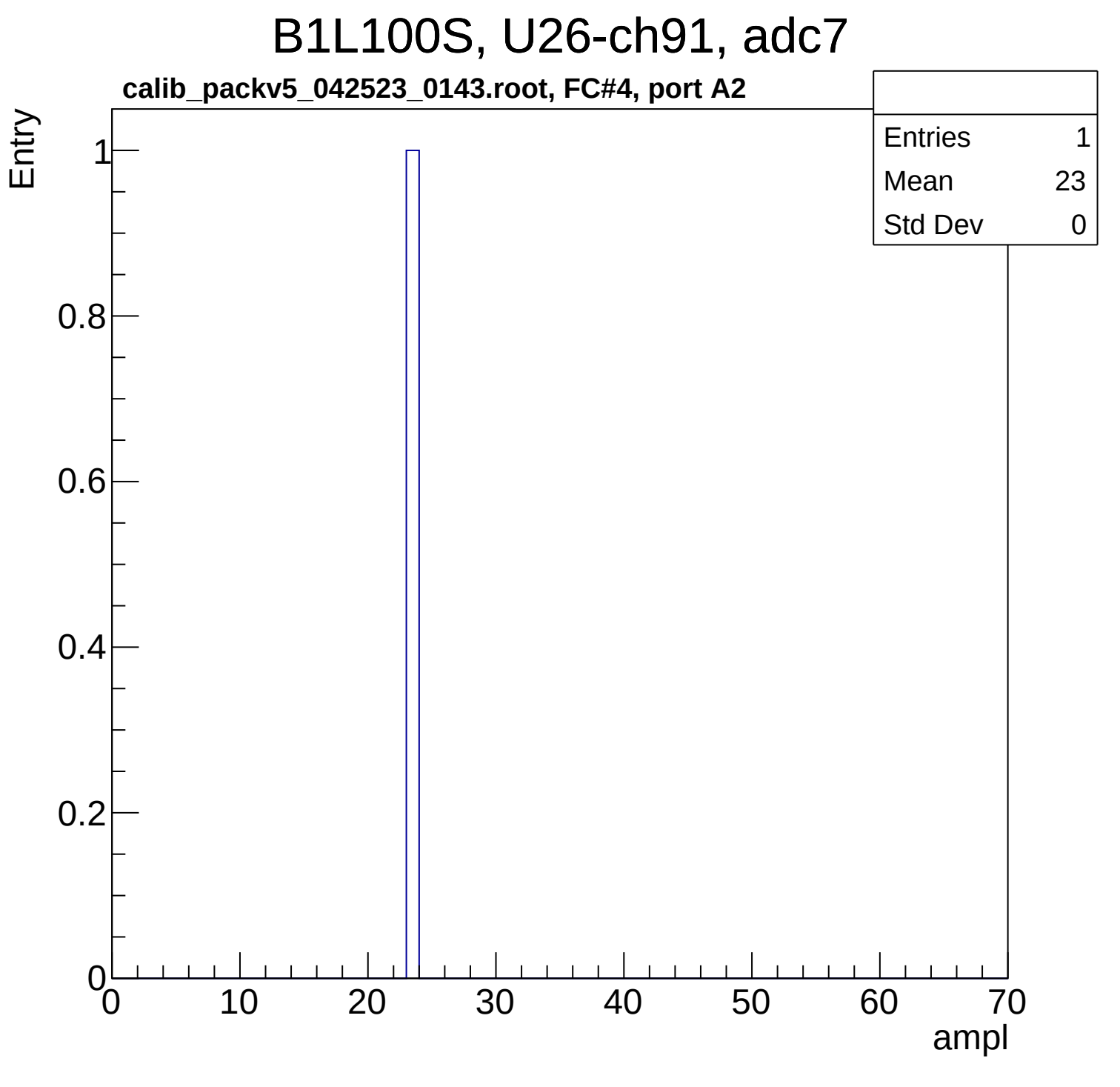
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
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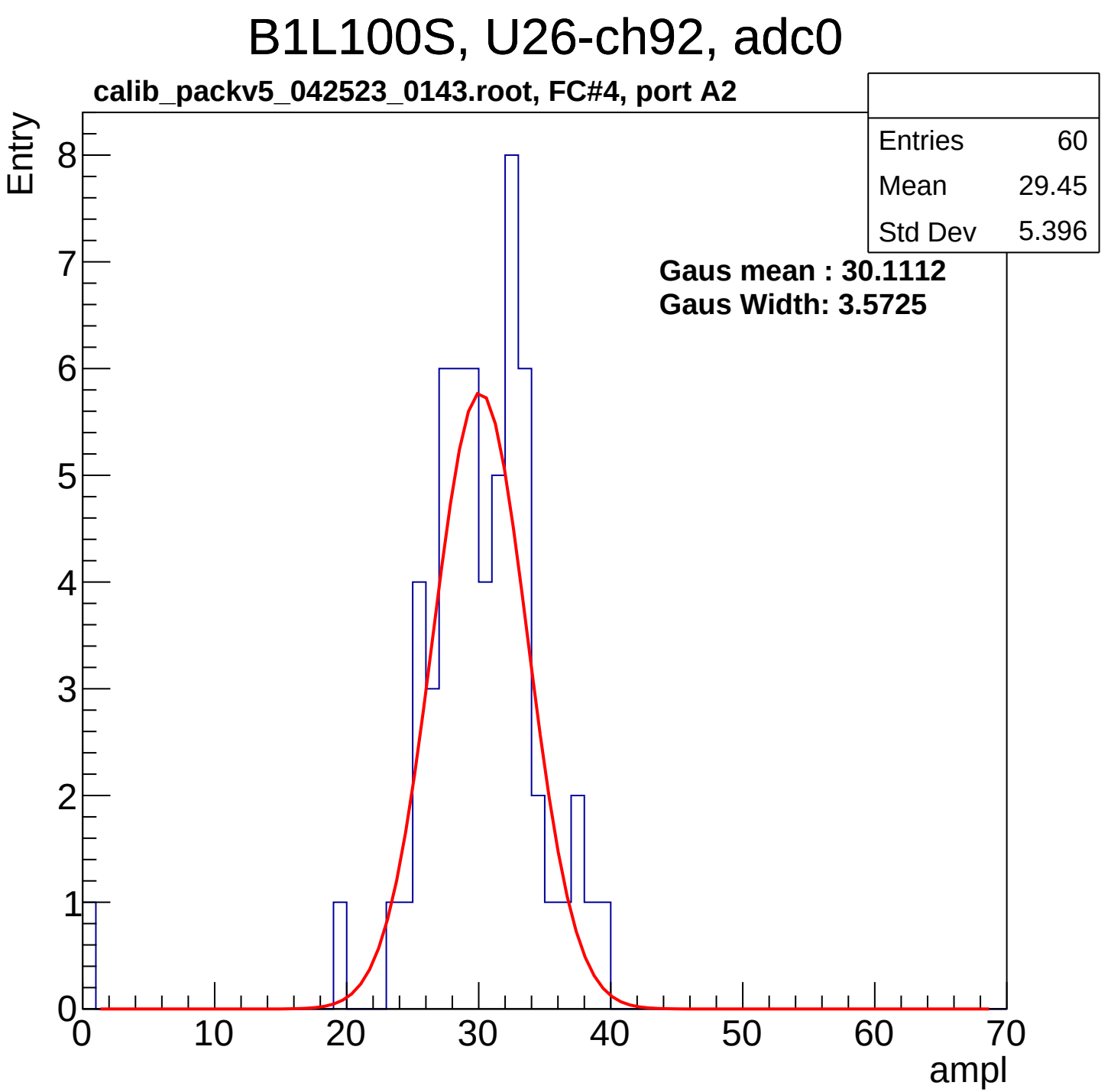
50
60
70
ampl


B1L100S, U26-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.45
Std Dev 5.396
Gaus mean : 30.1112
Gaus Width: 3.5725
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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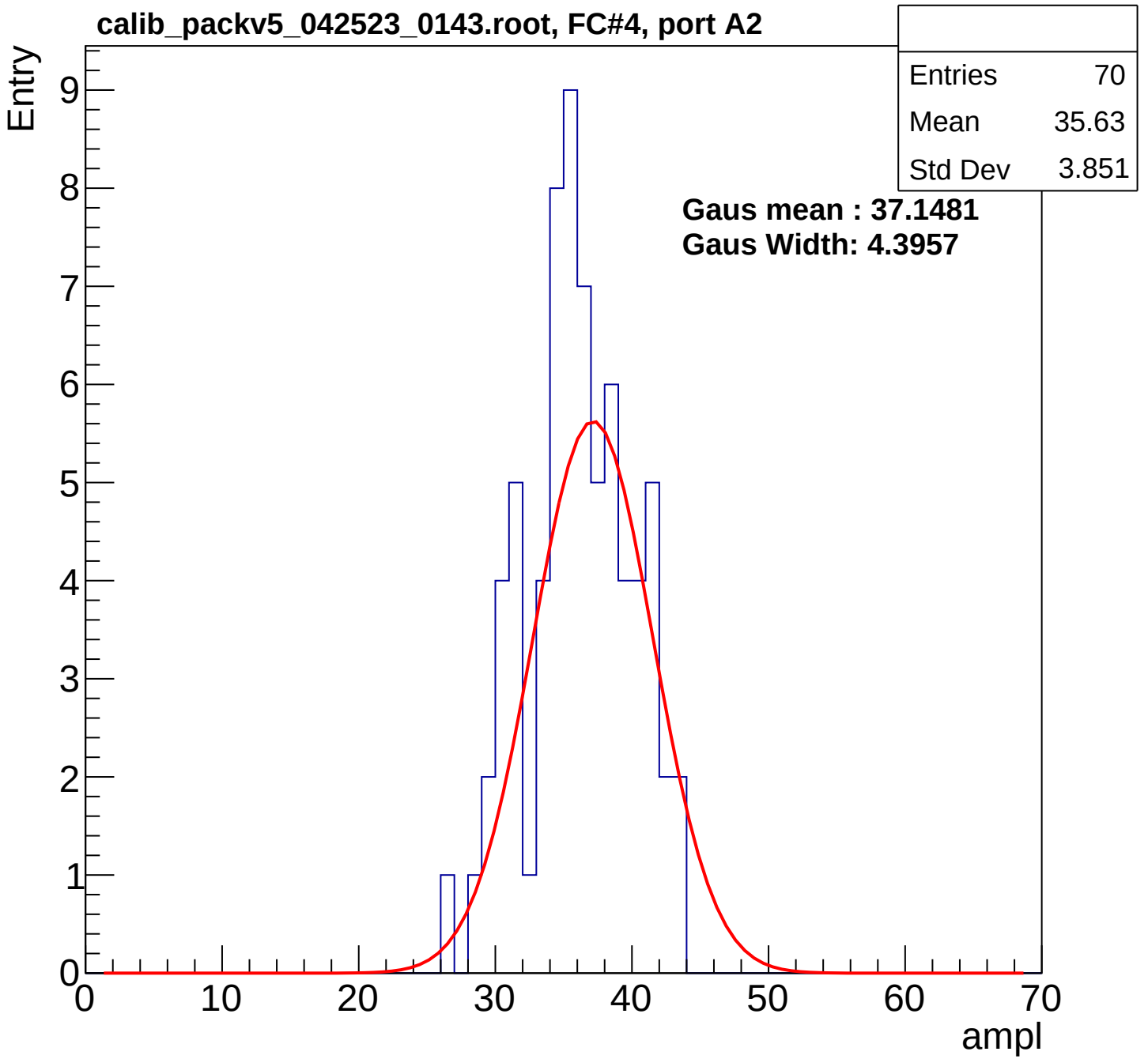
B1L100S, U26-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.63
Std Dev 3.851
Gaus mean : 37.1481
Gaus Width: 4.3957
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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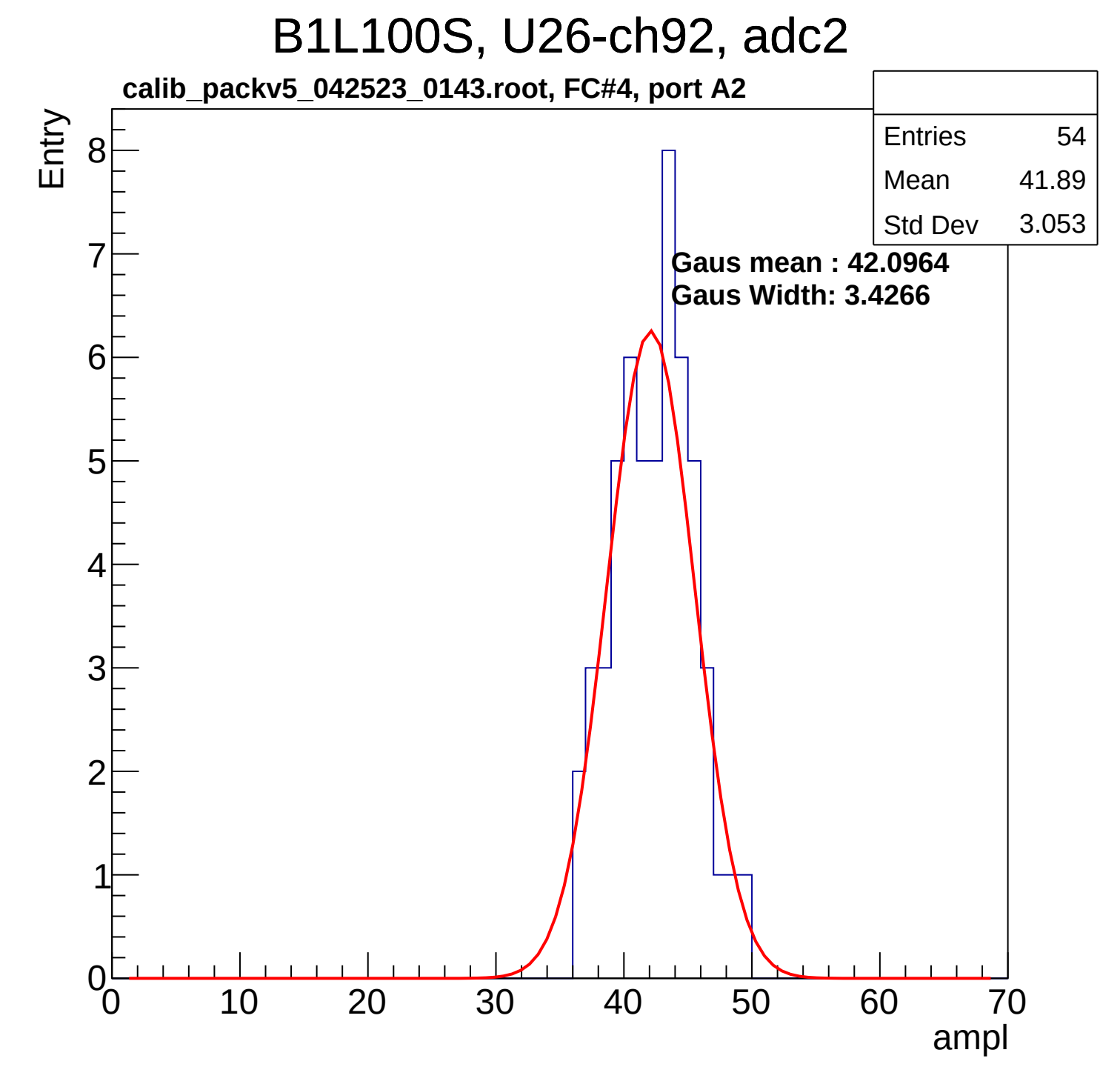
60
70


B1L100S, U26-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 41.89
Std Dev 3.053
Gaus mean : 42.0964
Gaus Width: 3.4266
Entry
ampl
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1
2
3
4
5
6
7
8
0
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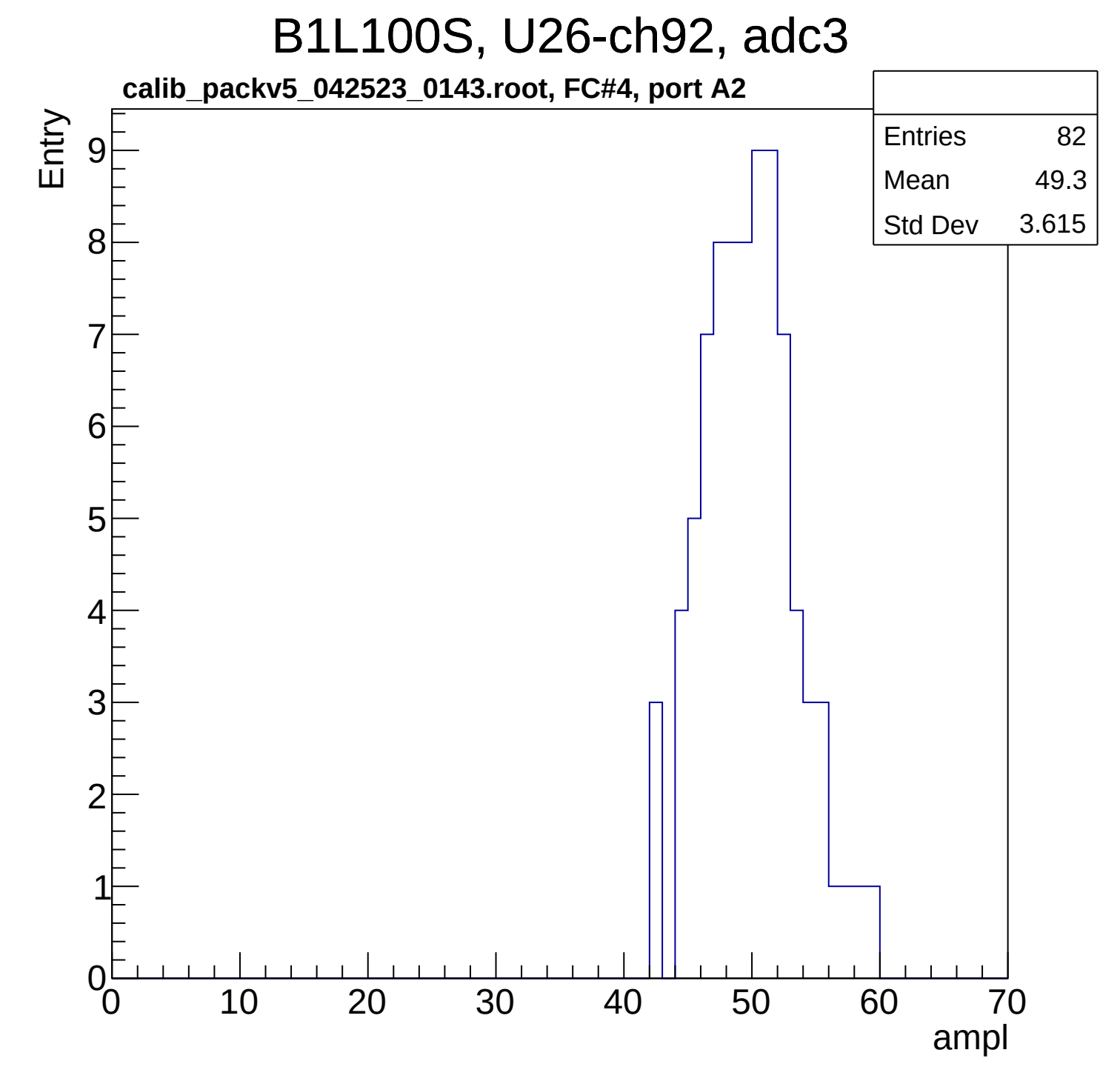
B1L100S, U26-ch92, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 49.3
Std Dev 3.615
Entry
ampl
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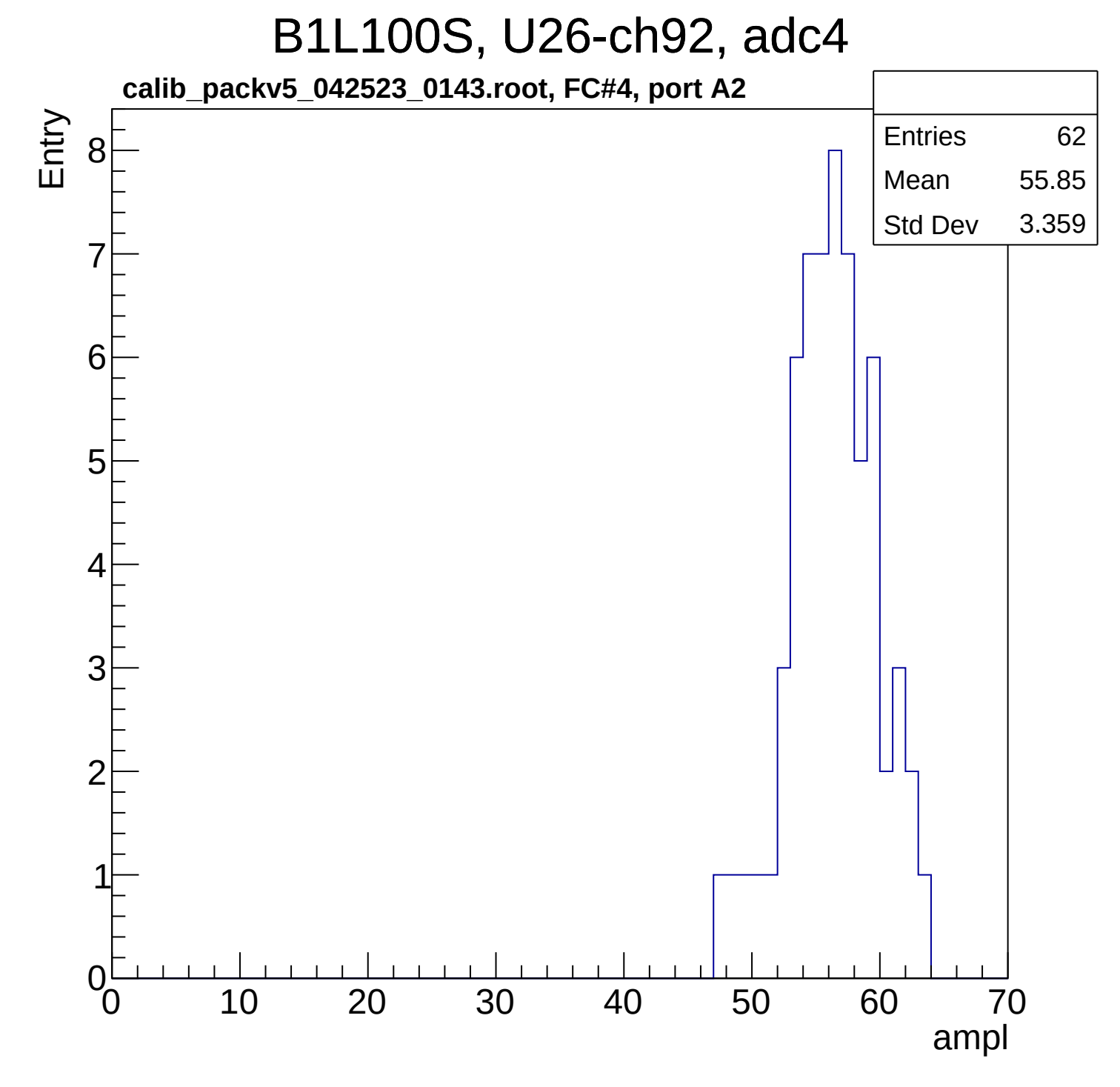
B1L100S, U26-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.85
Std Dev 3.359
Entry
ampl
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3
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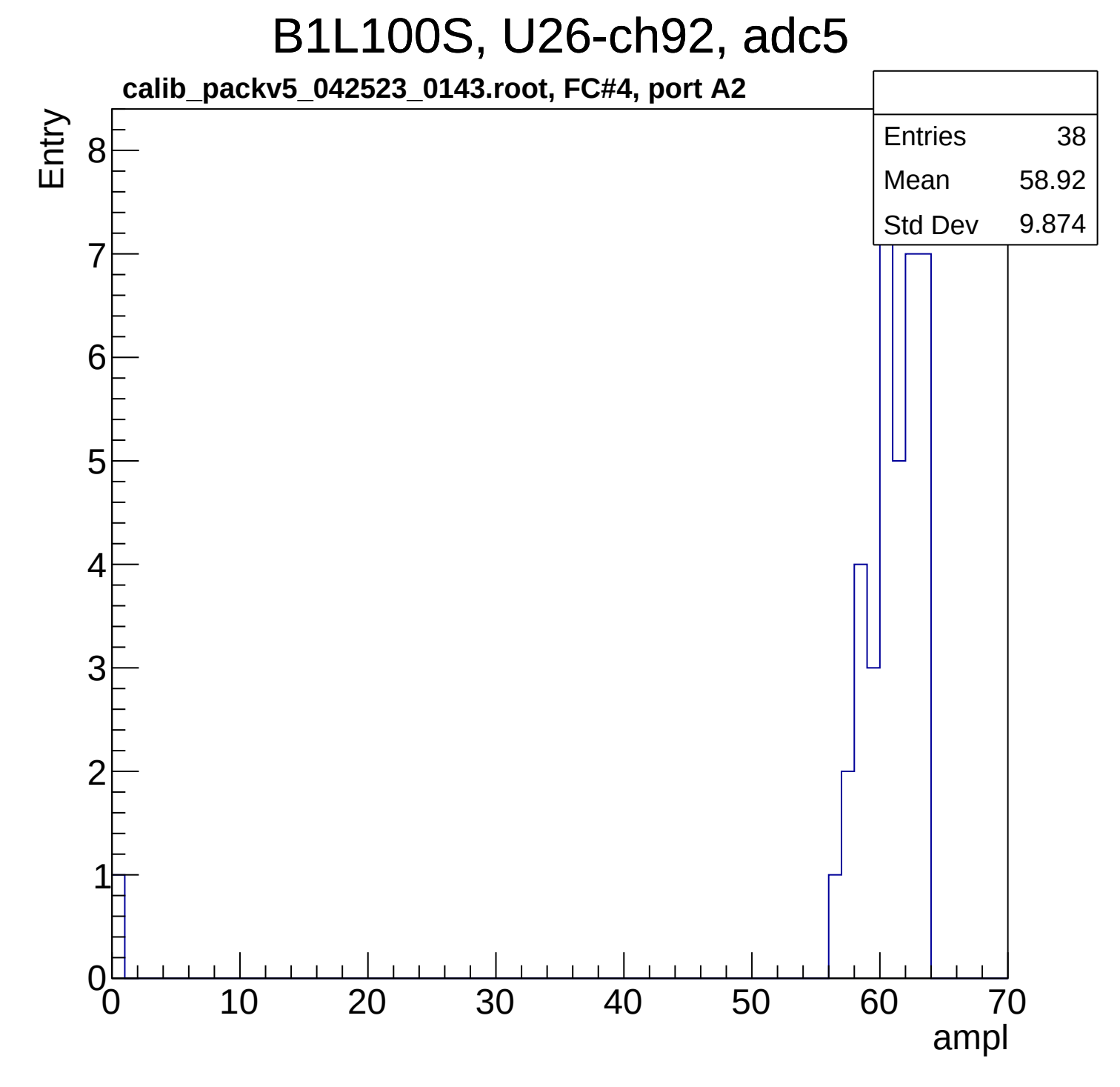
B1L100S, U26-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.92
Std Dev 9.874
Entry
ampl
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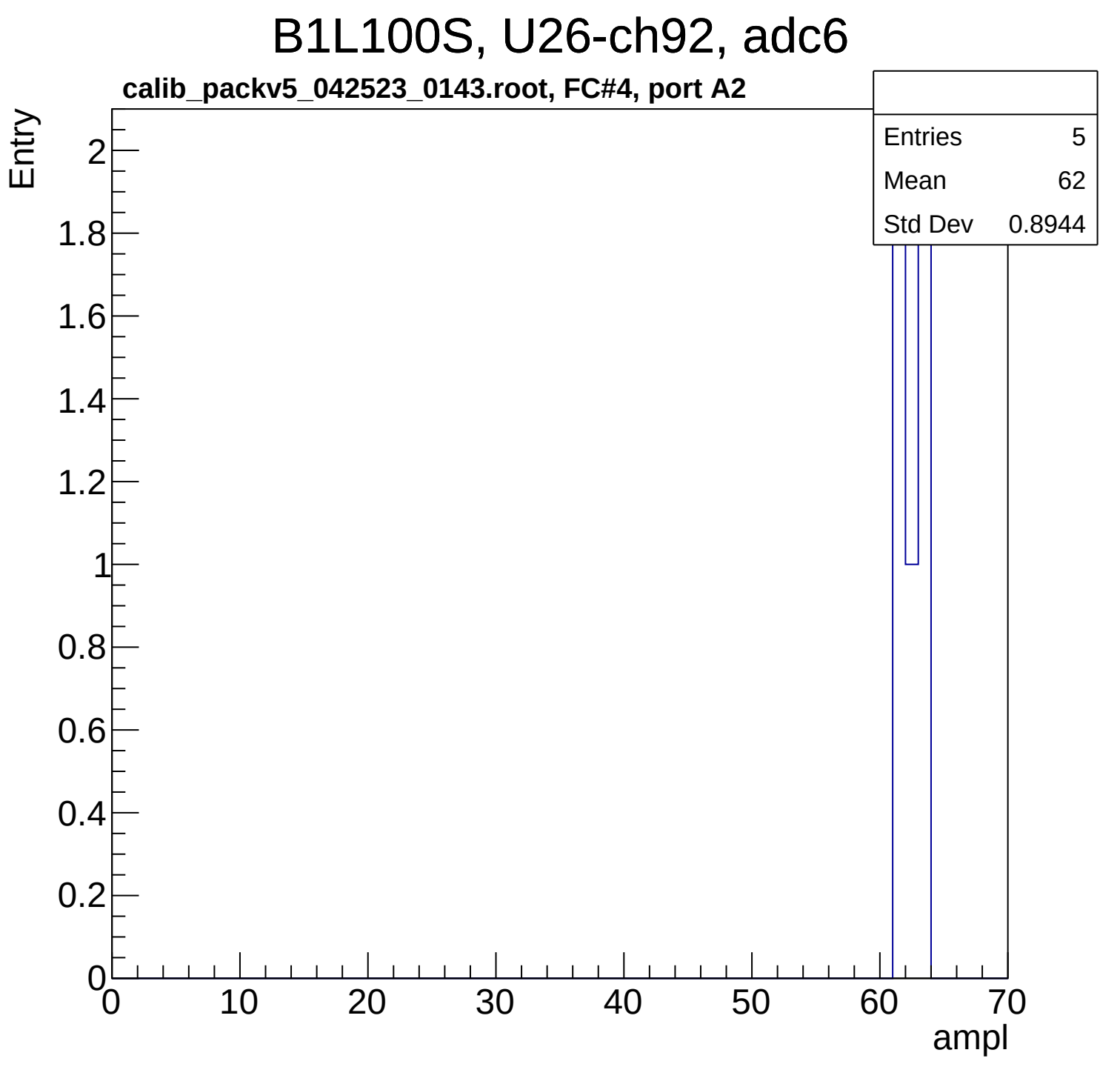
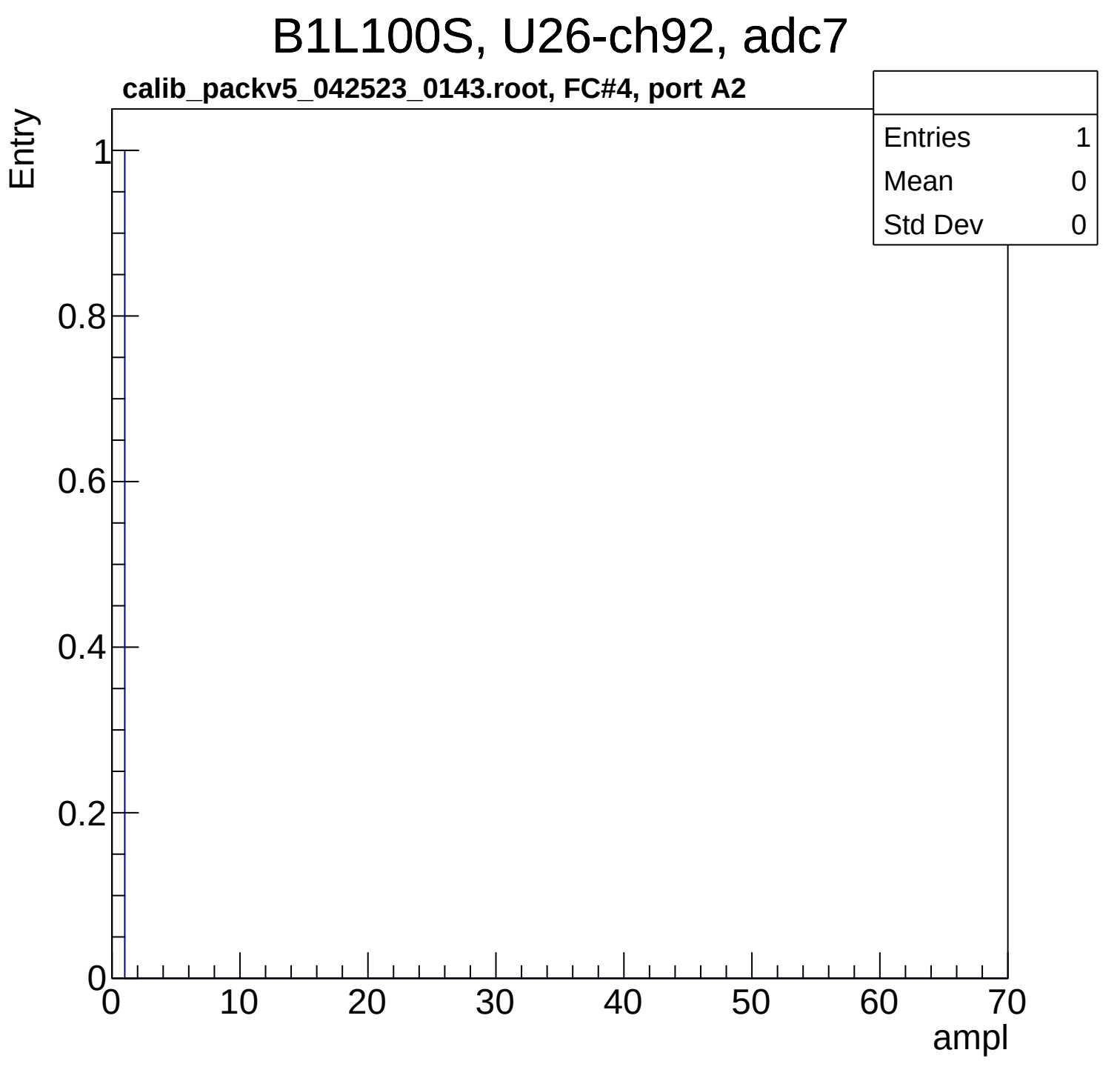
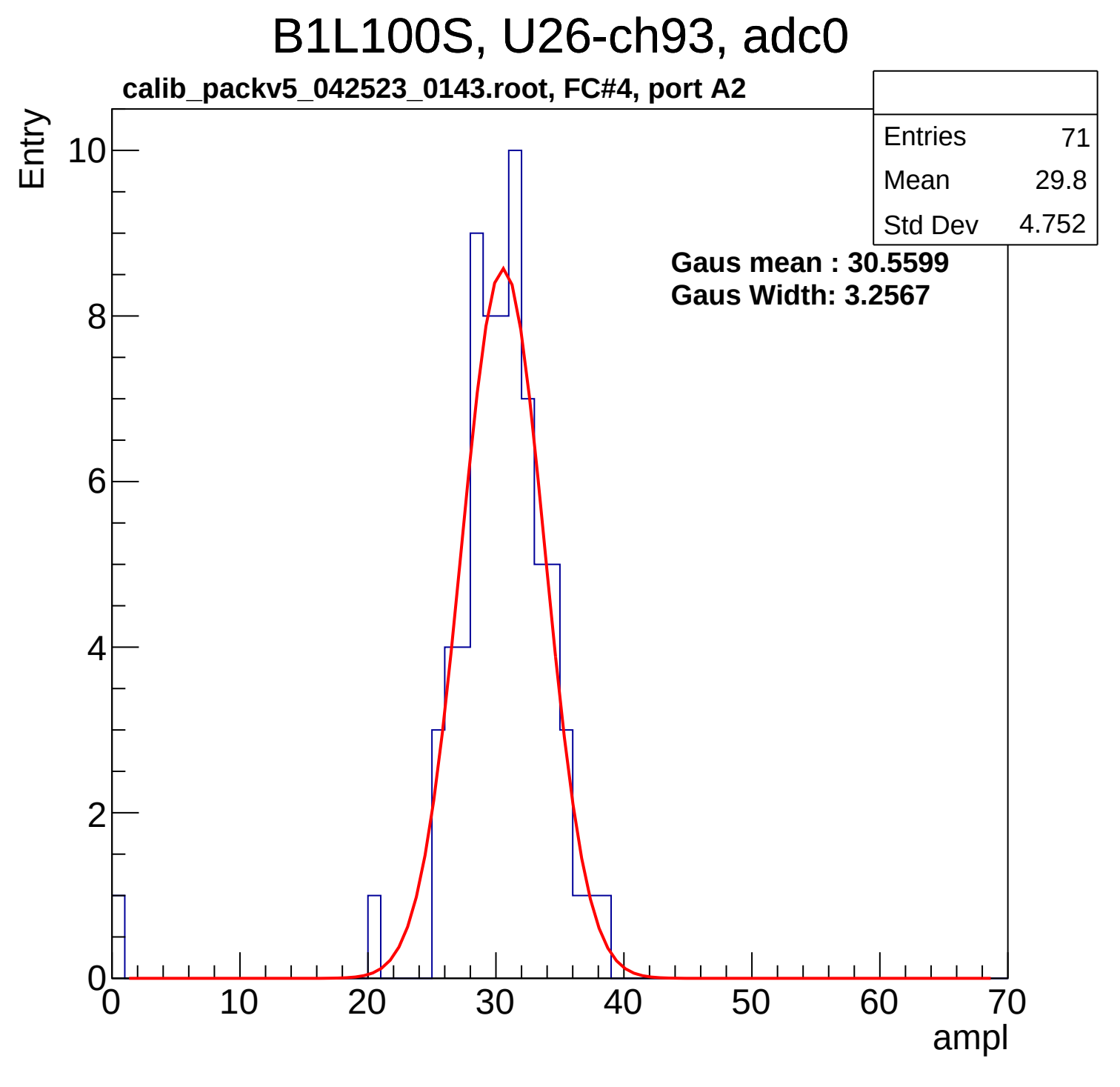

B1L100S, U26-ch92, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch93, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.8
Std Dev 4.752
Gaus mean : 30.5599
Gaus Width: 3.2567
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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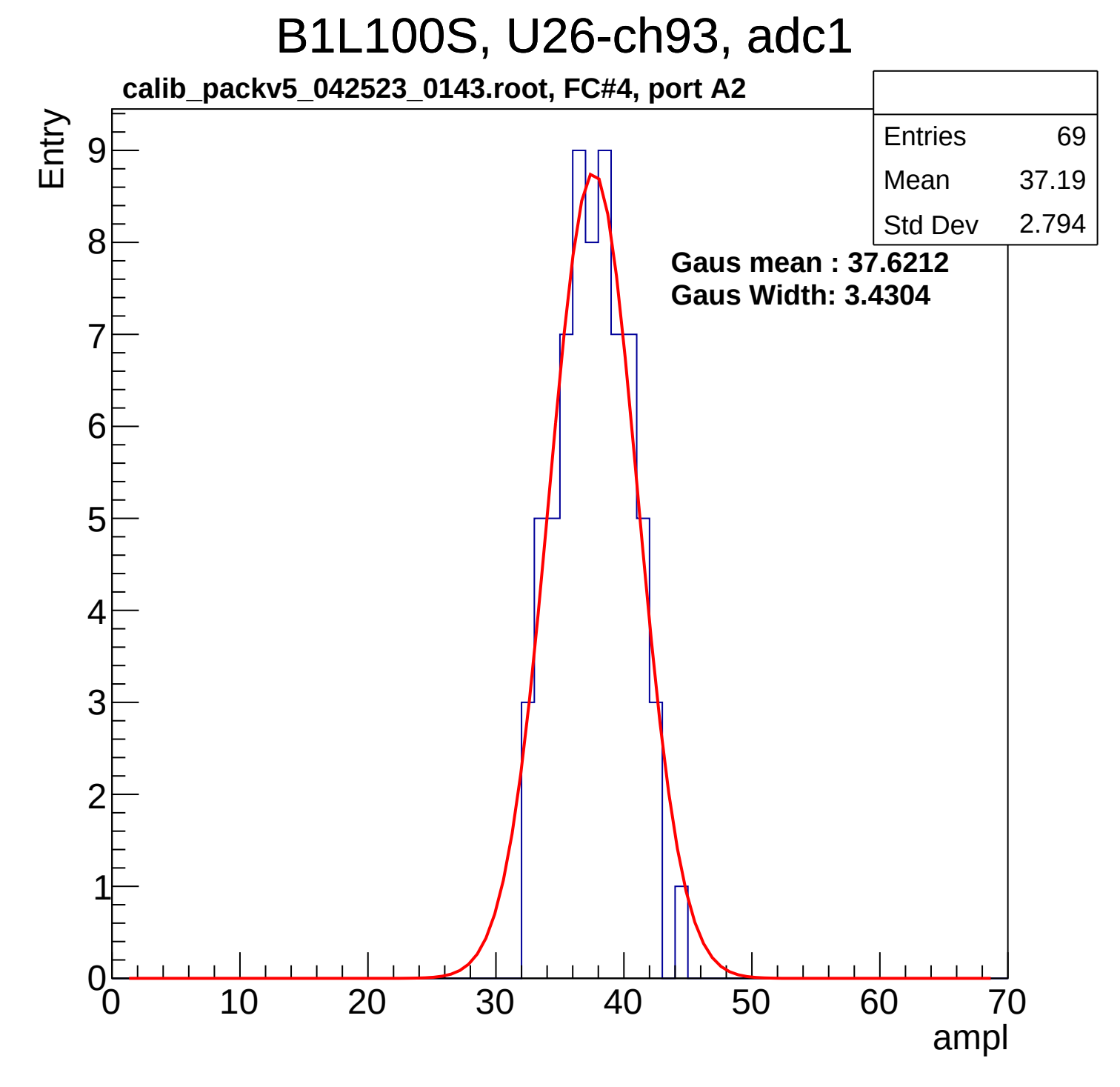
B1L100S, U26-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.19
Std Dev 2.794
Gaus mean : 37.6212
Gaus Width: 3.4304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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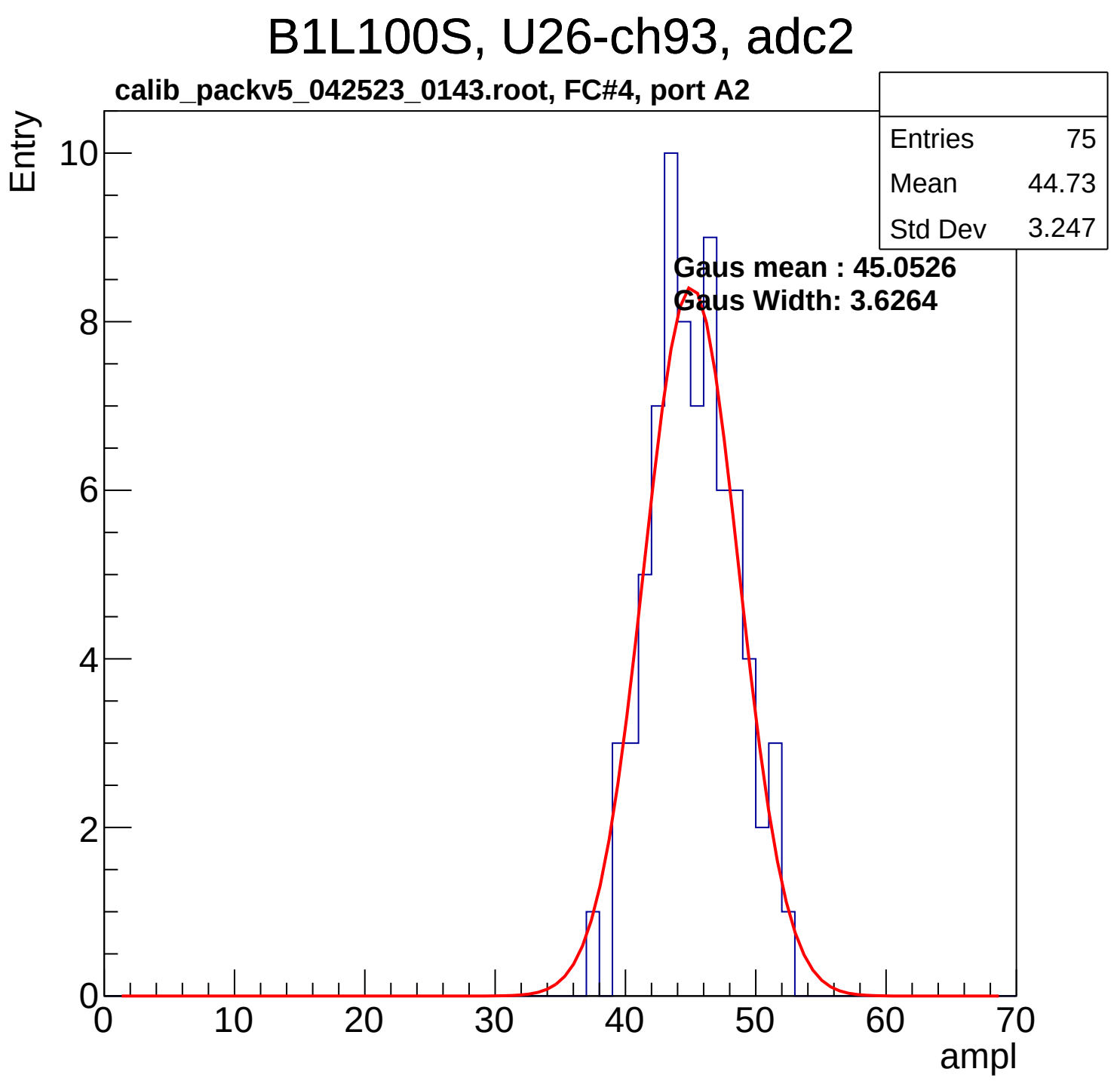
B1L100S, U26-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.73
Std Dev 3.247
Gaus mean : 45.0526
Gaus Width: 3.6264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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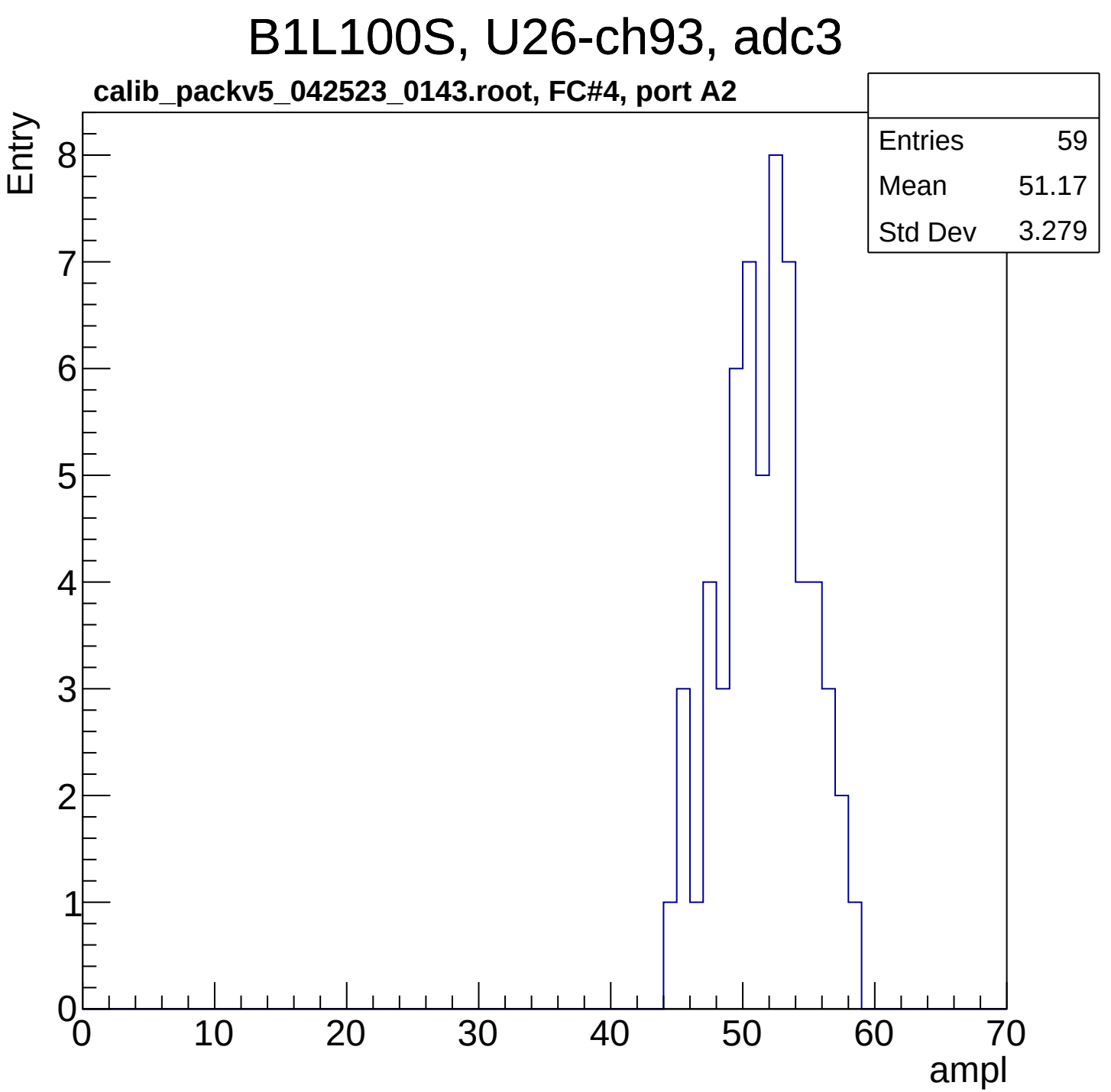
B1L100S, U26-ch93, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 51.17
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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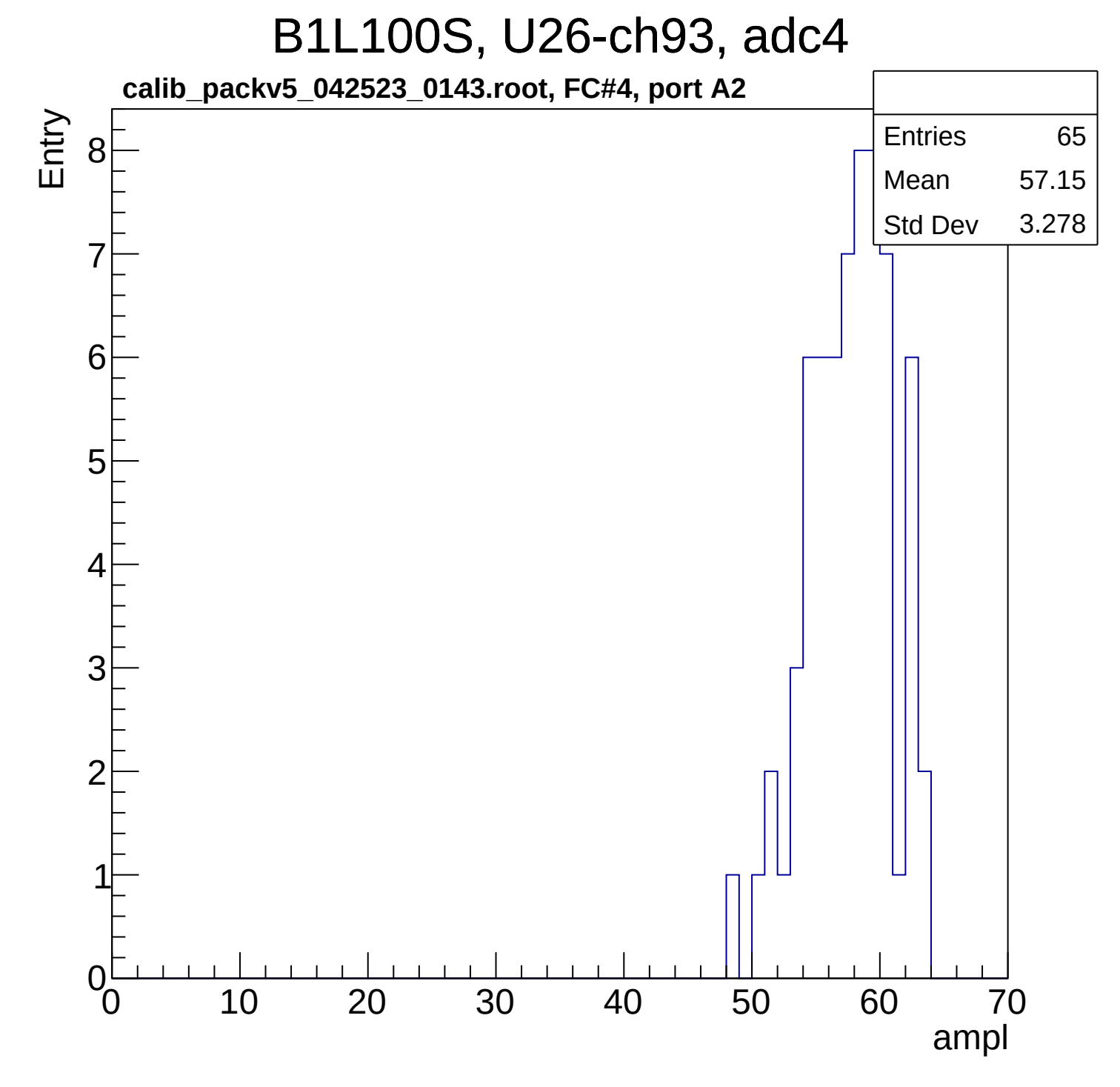
B1L100S, U26-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 57.15
Std Dev 3.278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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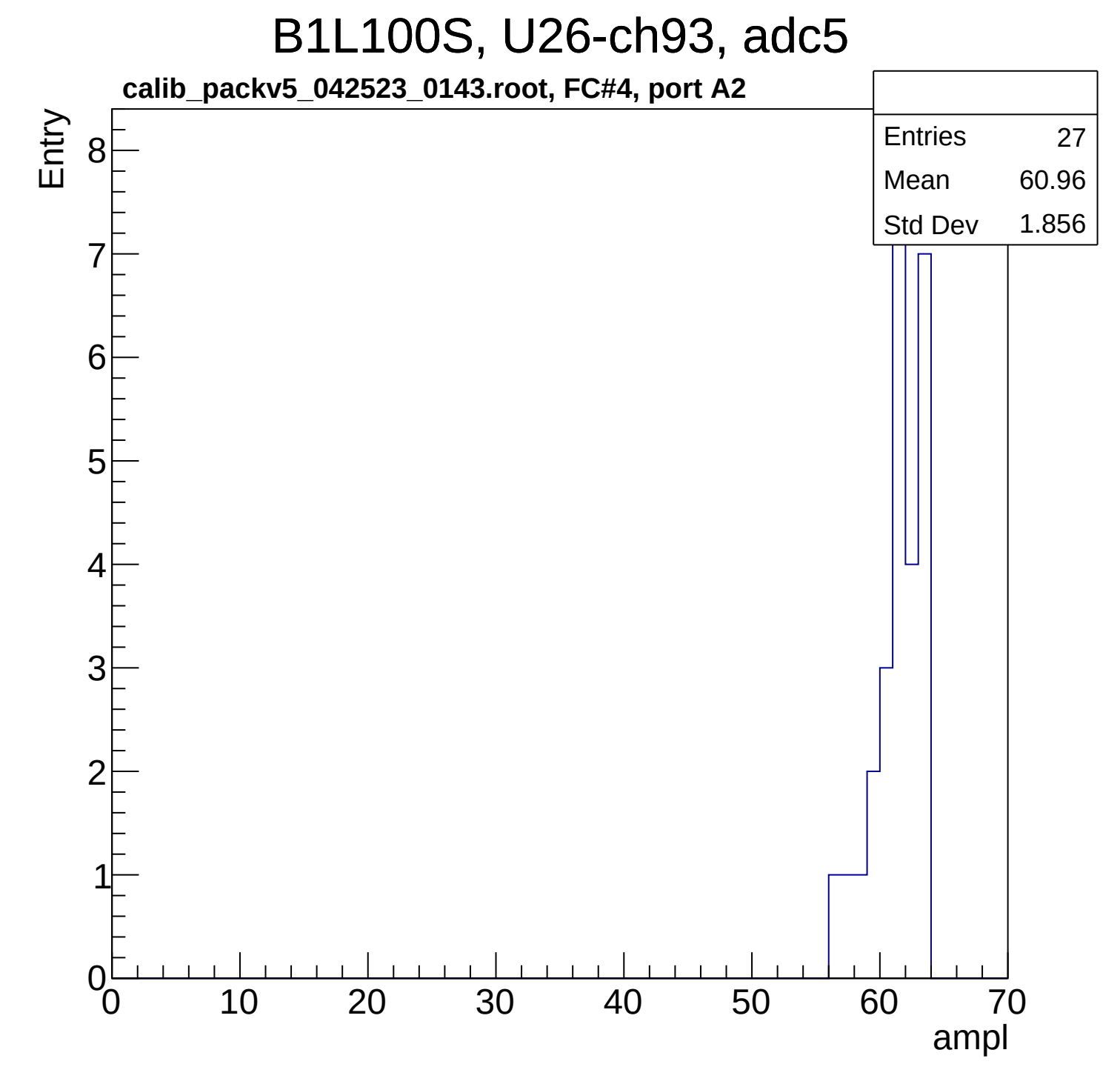
B1L100S, U26-ch93, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 60.96
Std Dev 1.856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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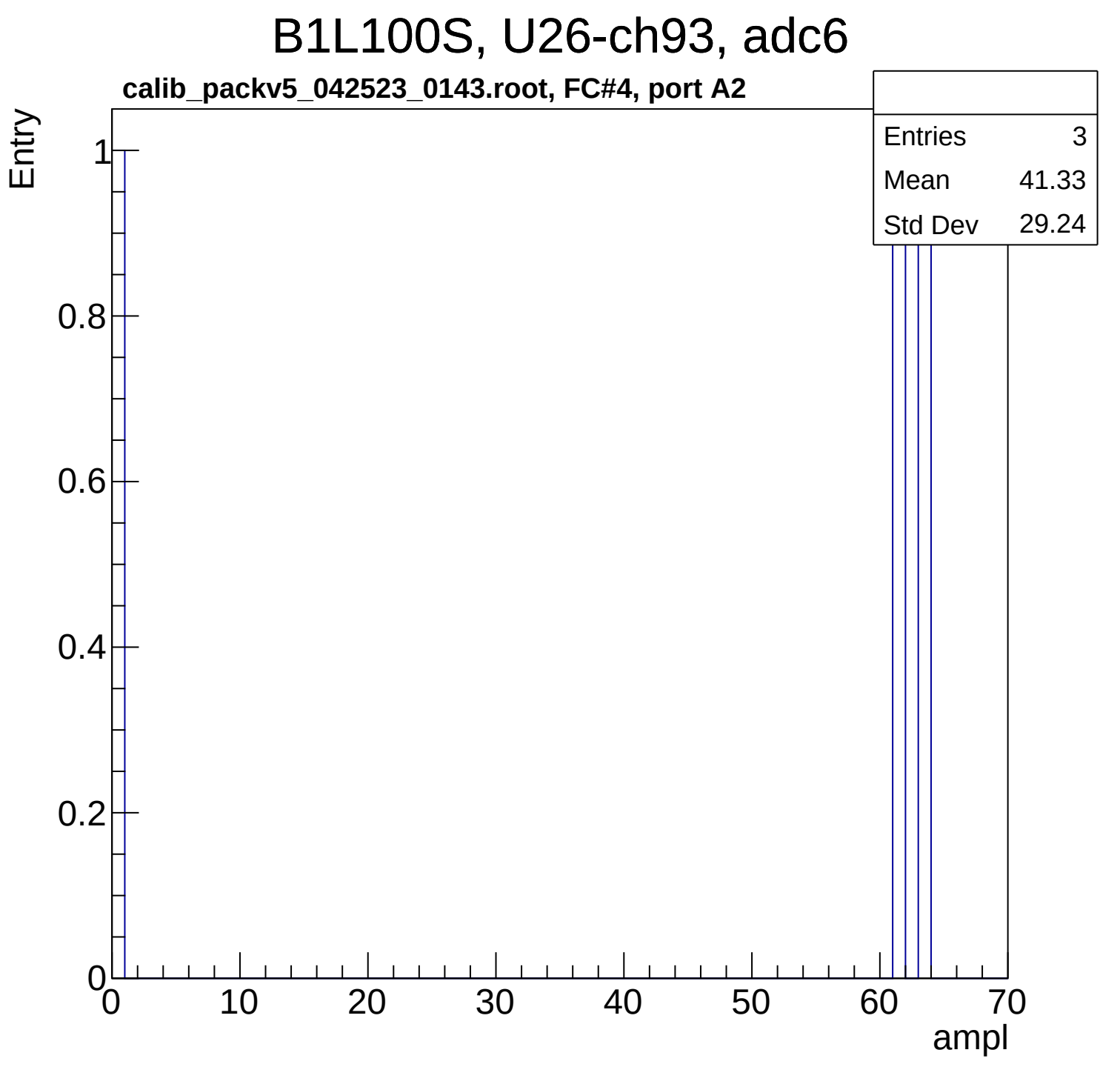
B1L100S, U26-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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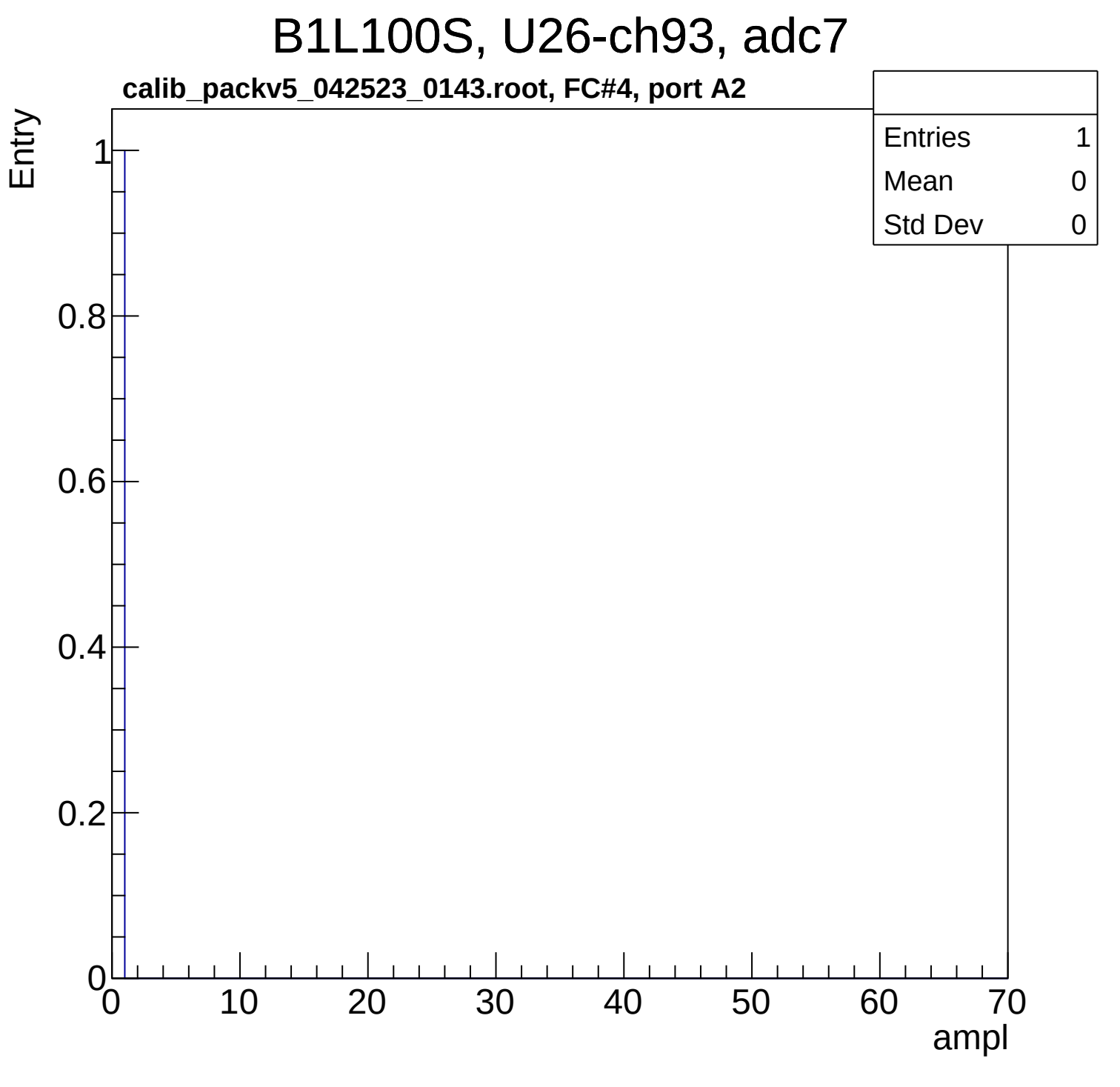
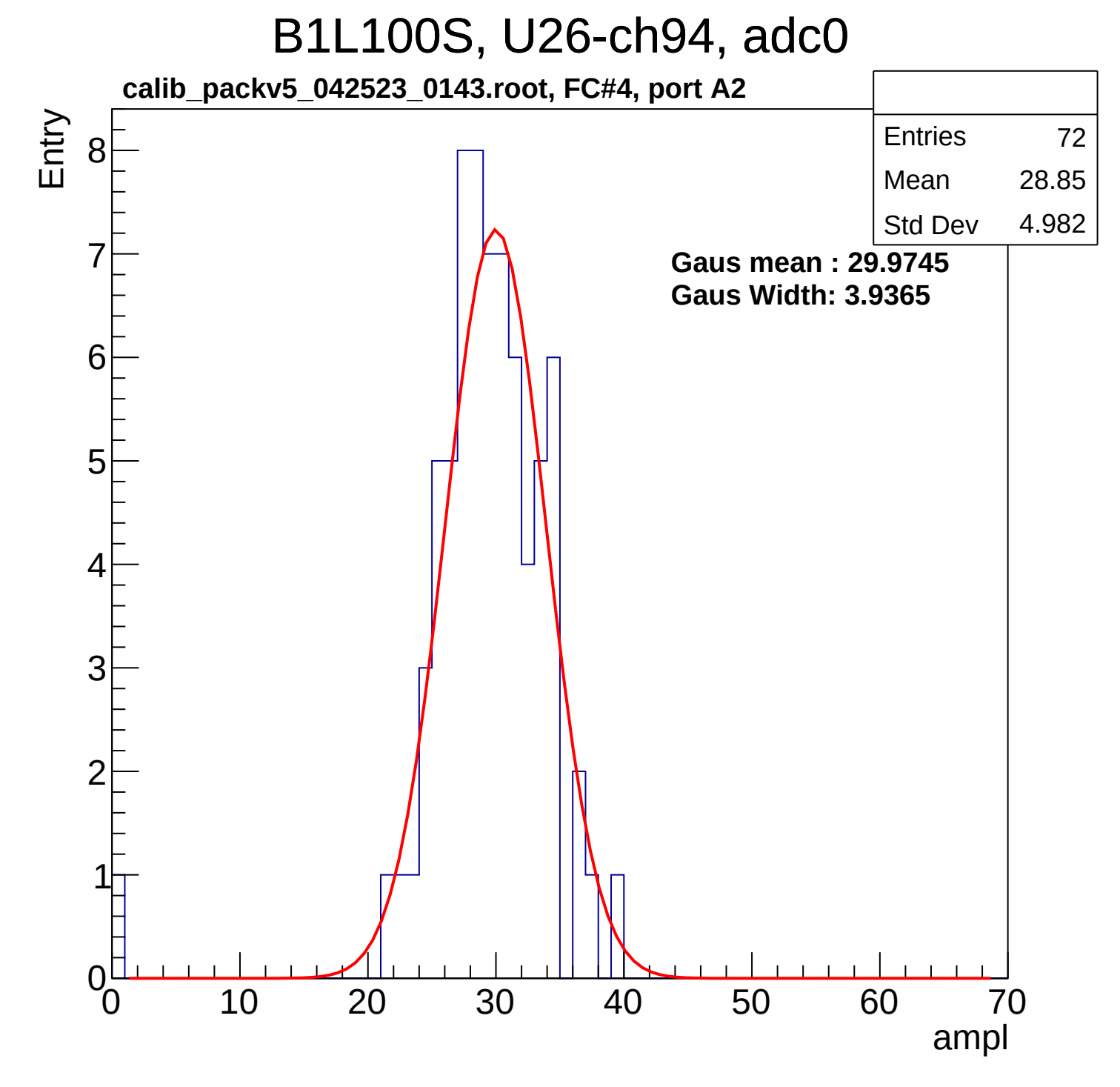
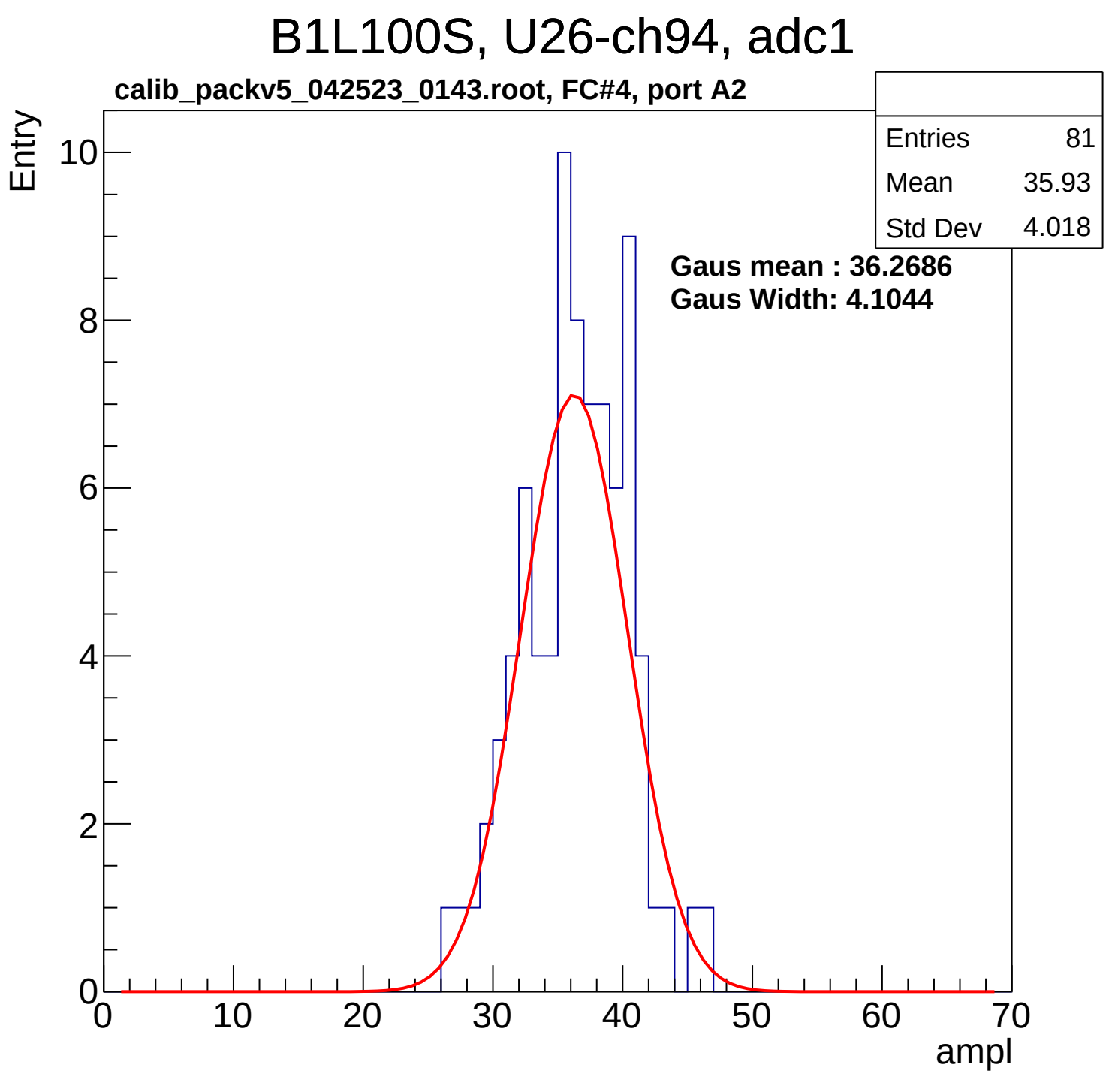

B1L100S, U26-ch93, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.85
Std Dev 4.982
Gaus mean : 29.9745
Gaus Width: 3.9365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.93
Std Dev 4.018
Gaus mean : 36.2686
Gaus Width: 4.1044
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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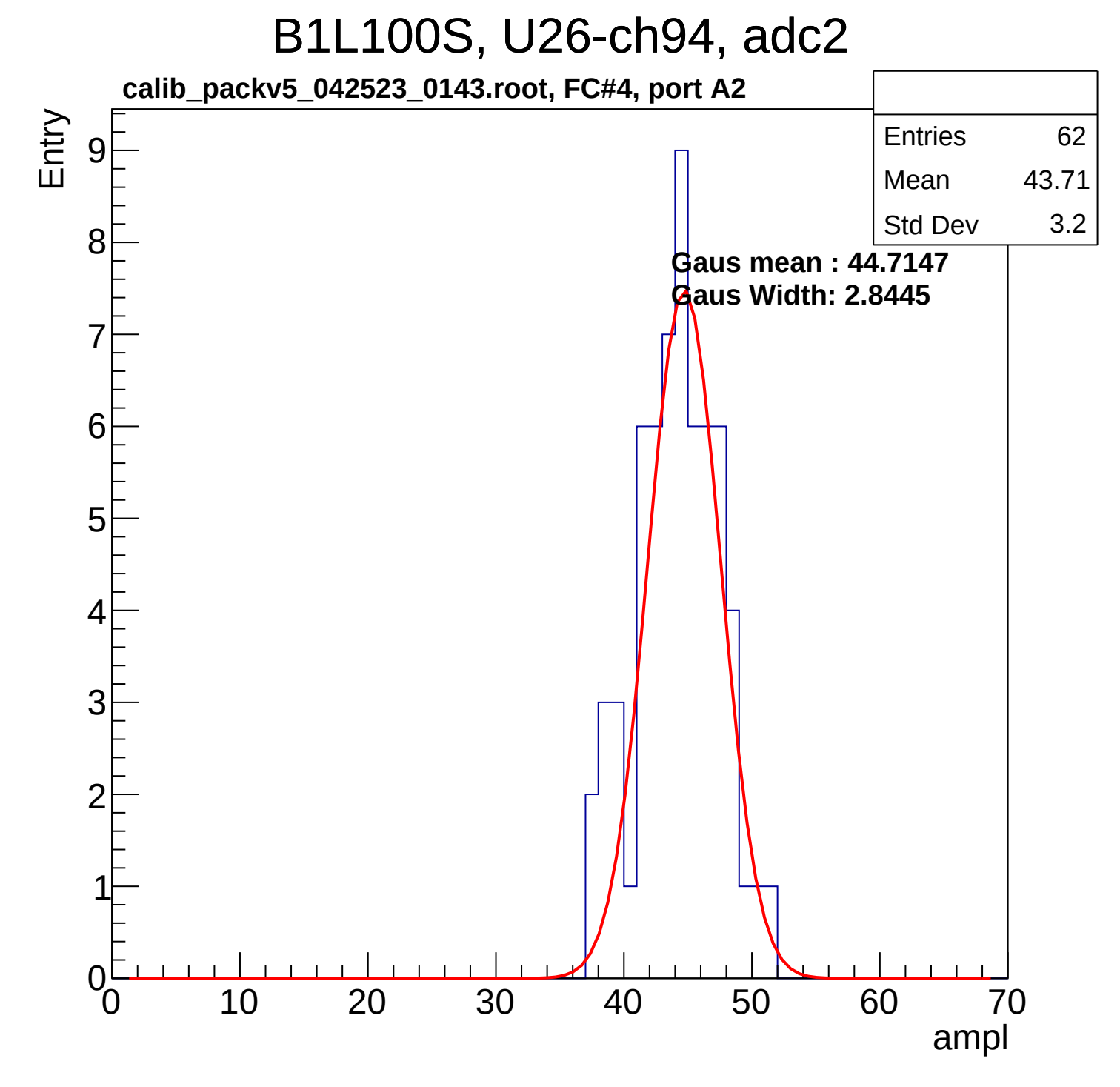
B1L100S, U26-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.71
Std Dev 3.2
Gaus mean : 44.7147
Gaus Width: 2.8445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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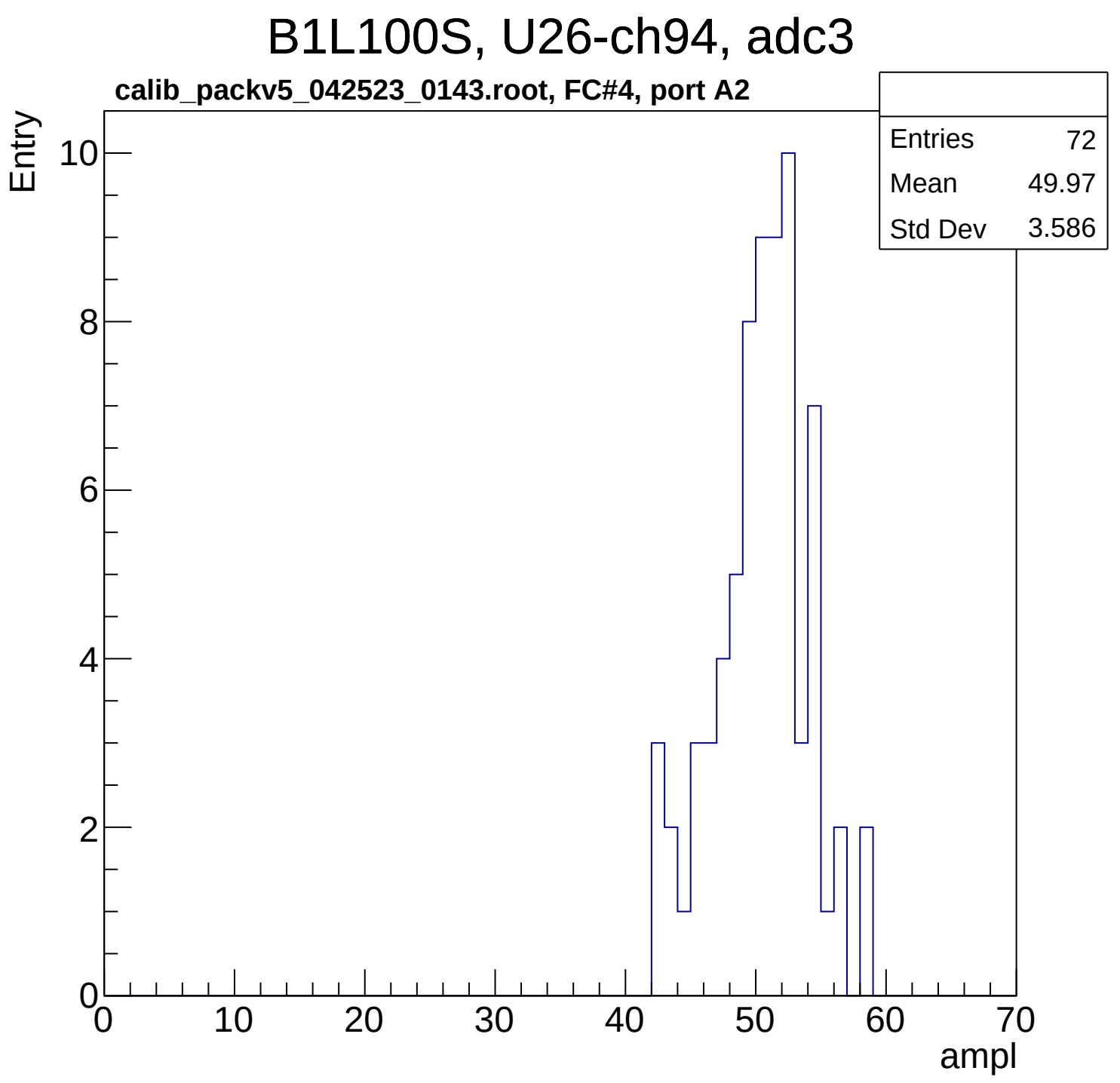
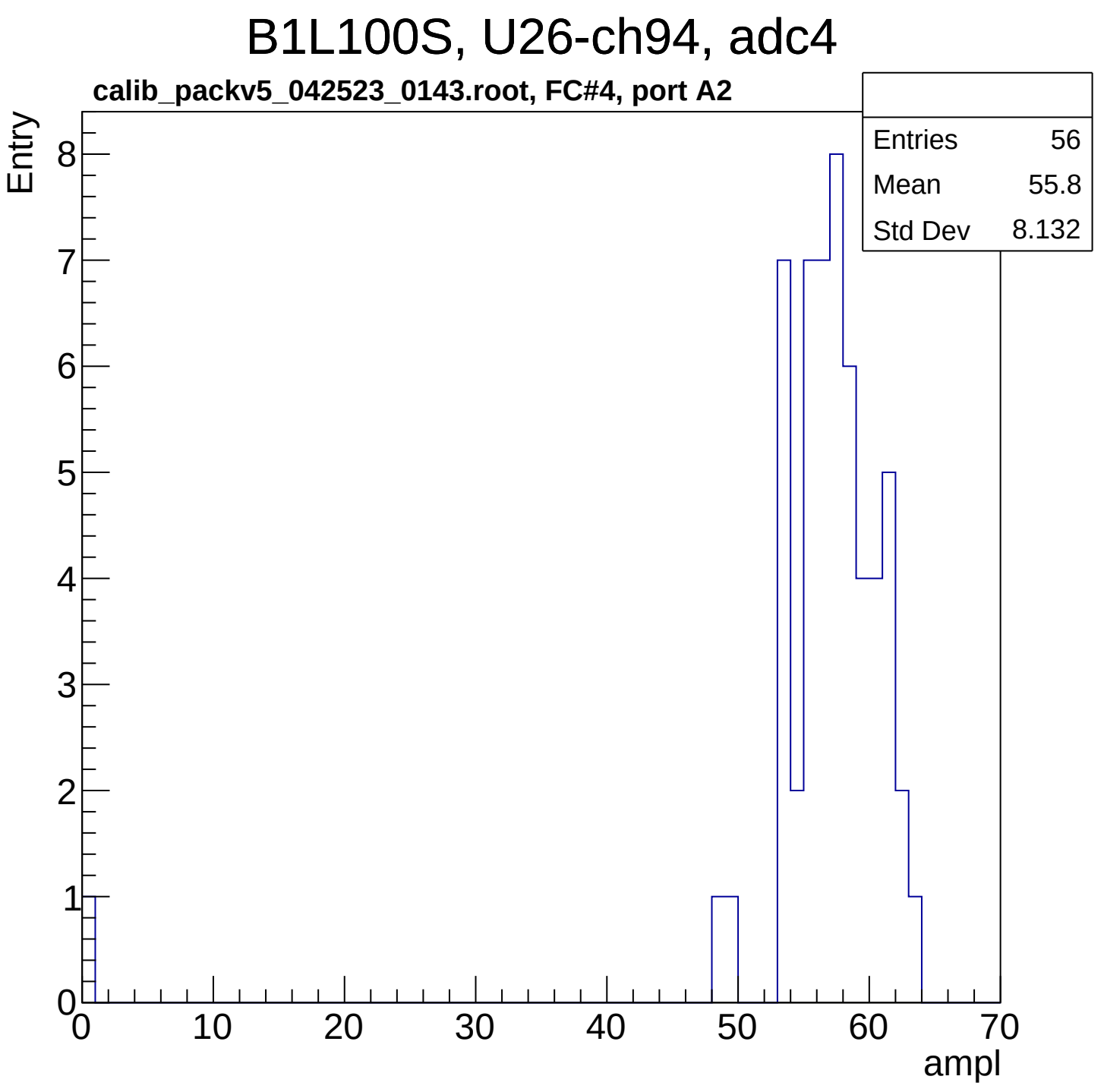

B1L100S, U26-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 49.97
Std Dev 3.586
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.8
Std Dev 8.132
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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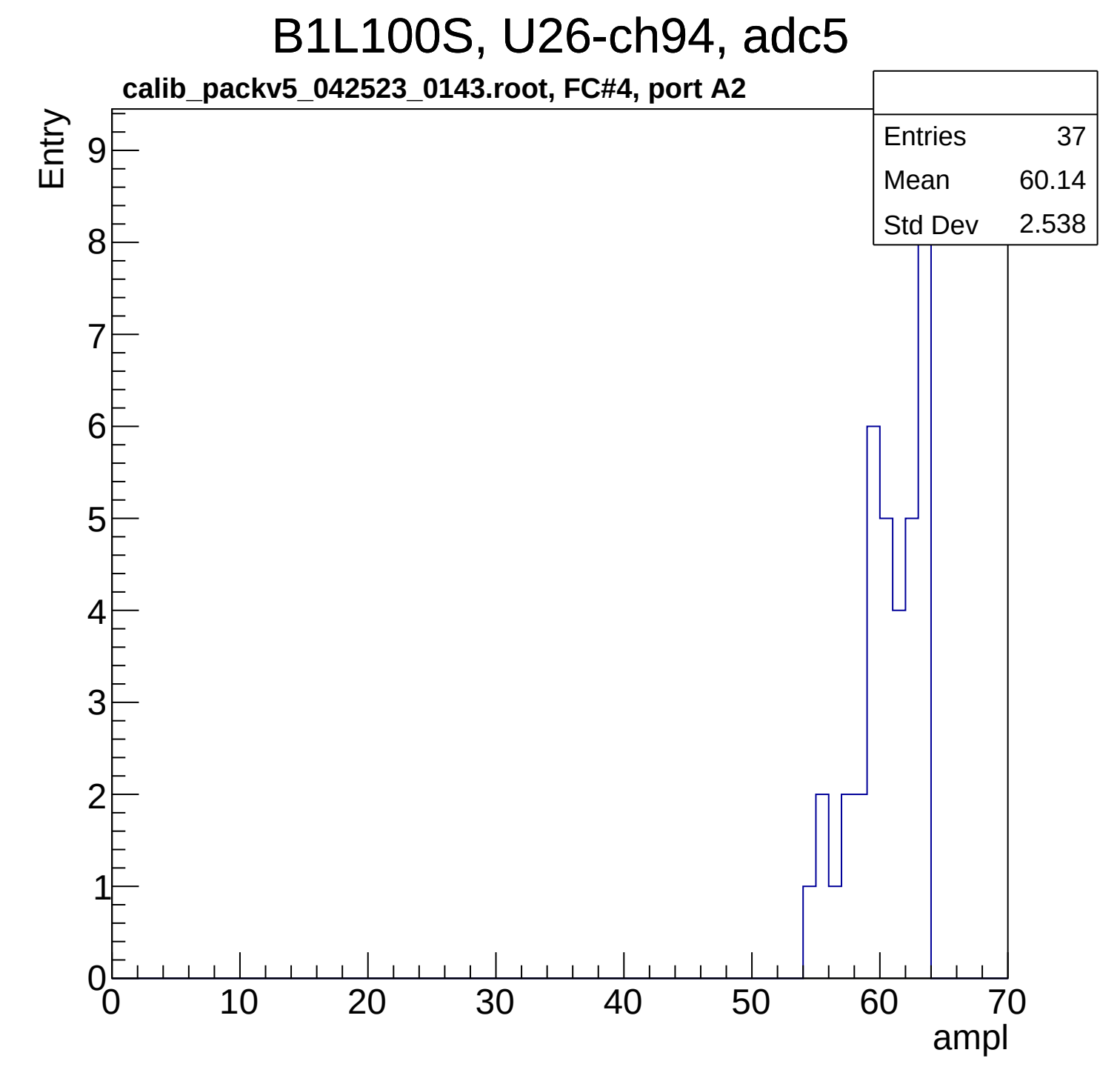
B1L100S, U26-ch94, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.14
Std Dev 2.538
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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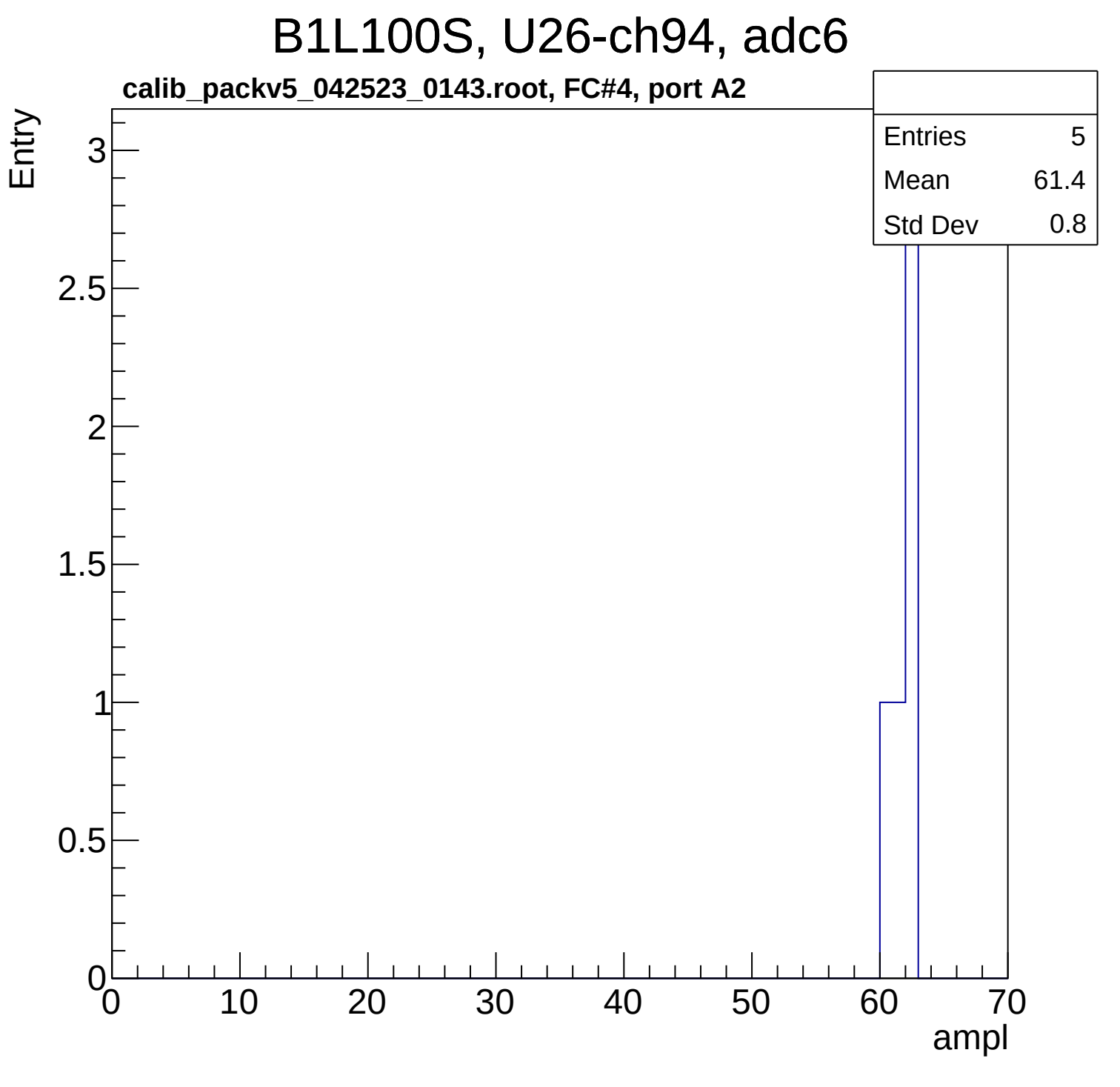
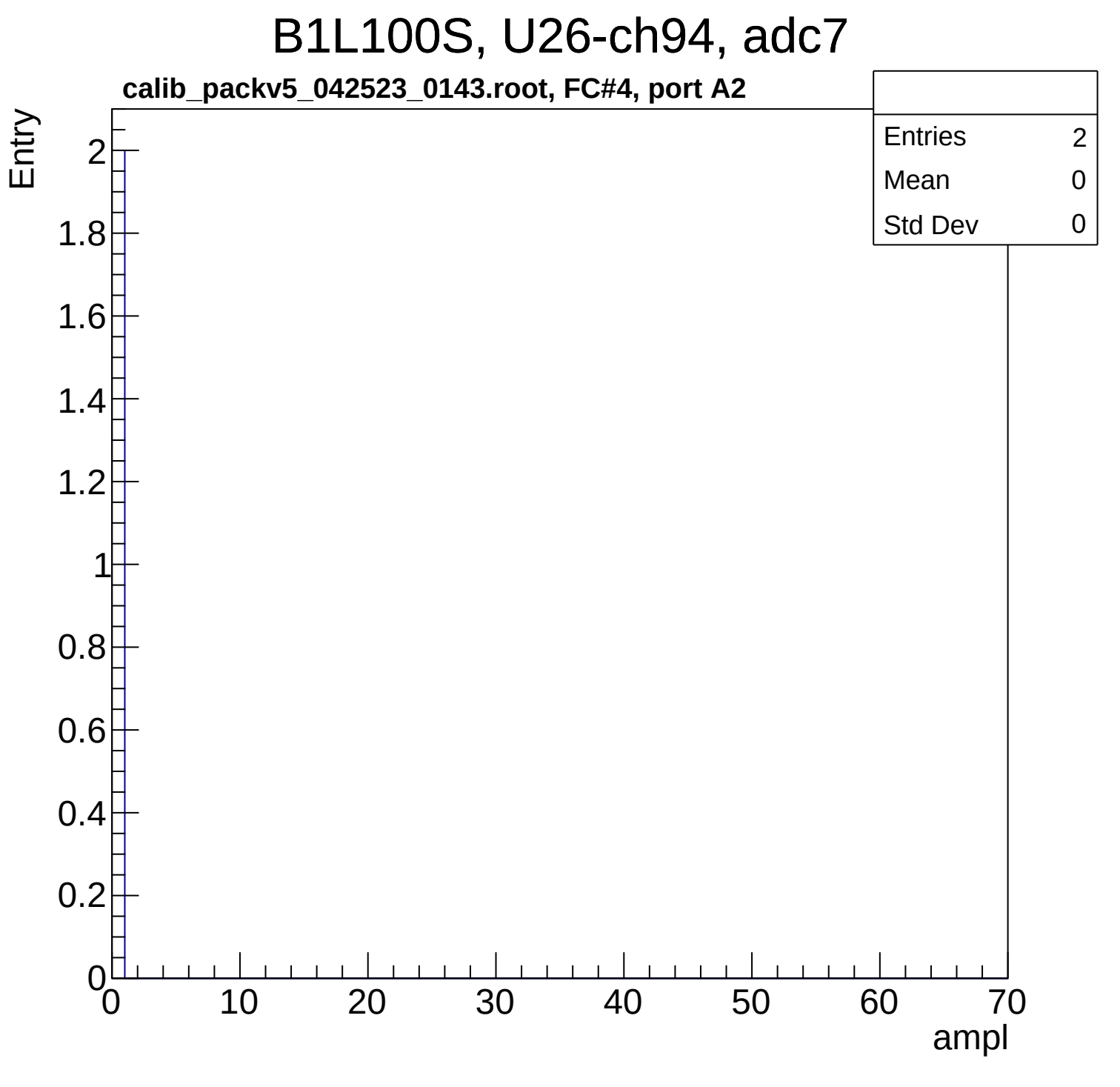
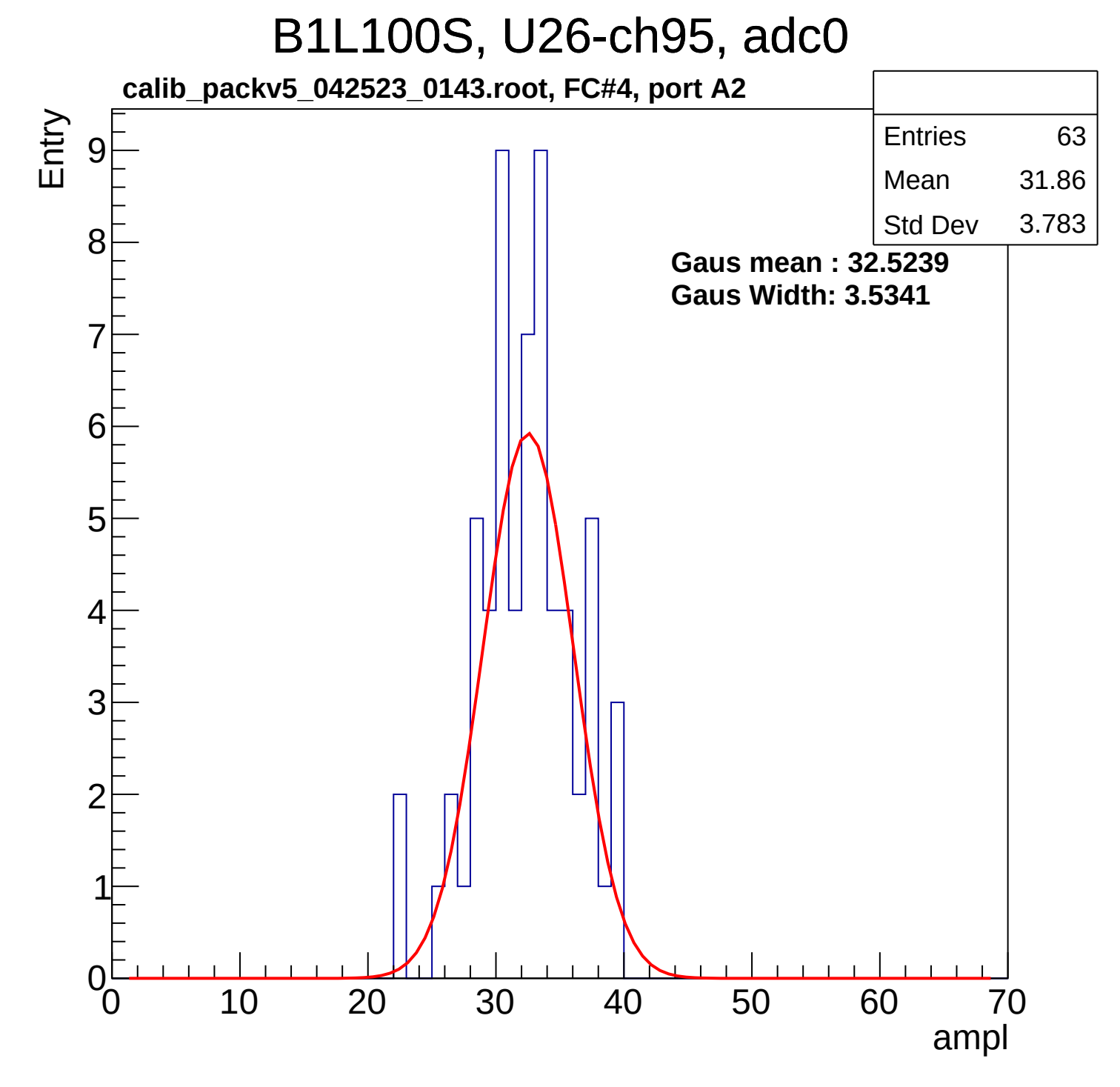

B1L100S, U26-ch94, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch94, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.86
Std Dev 3.783
Gaus mean : 32.5239
Gaus Width: 3.5341
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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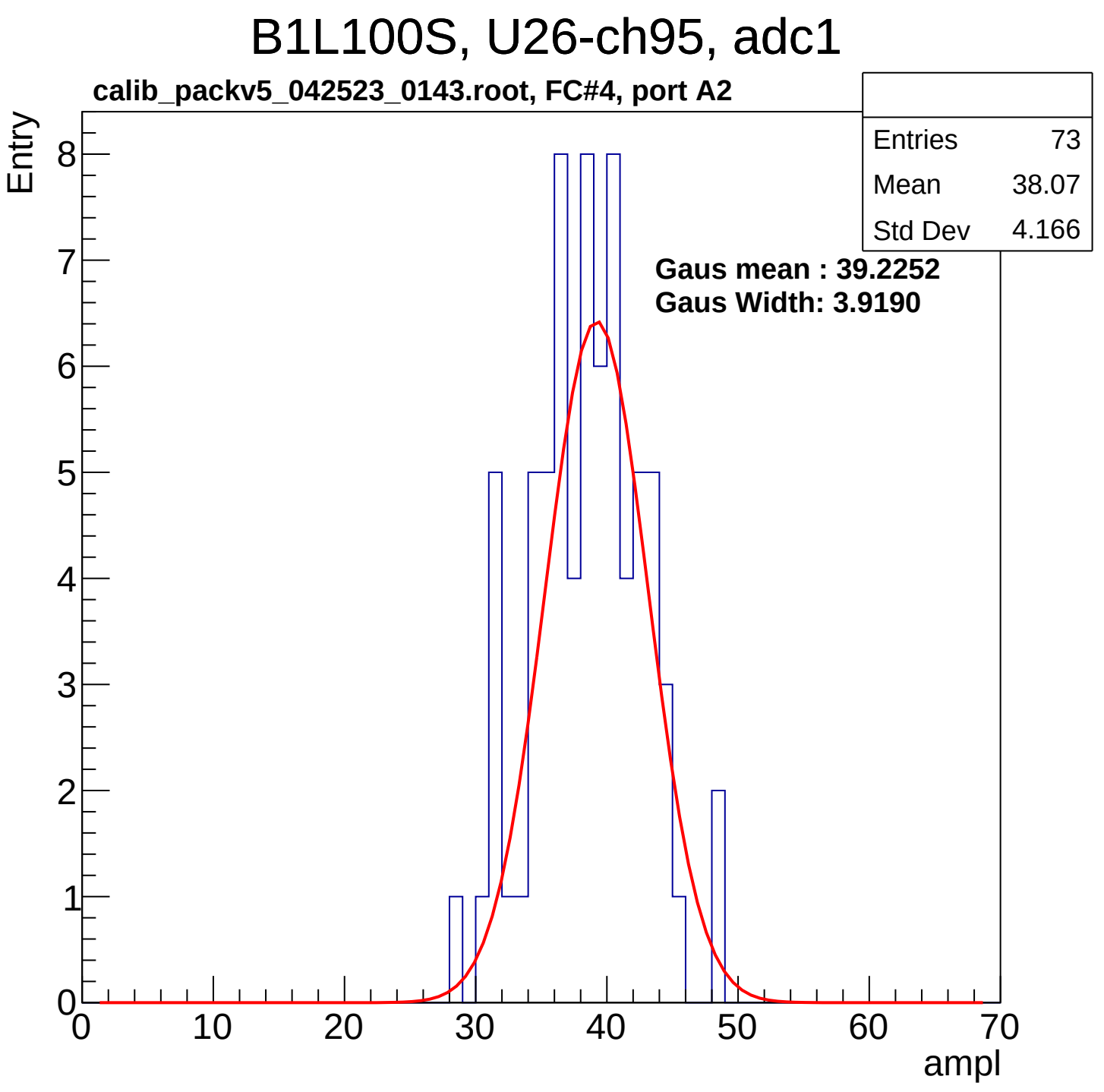
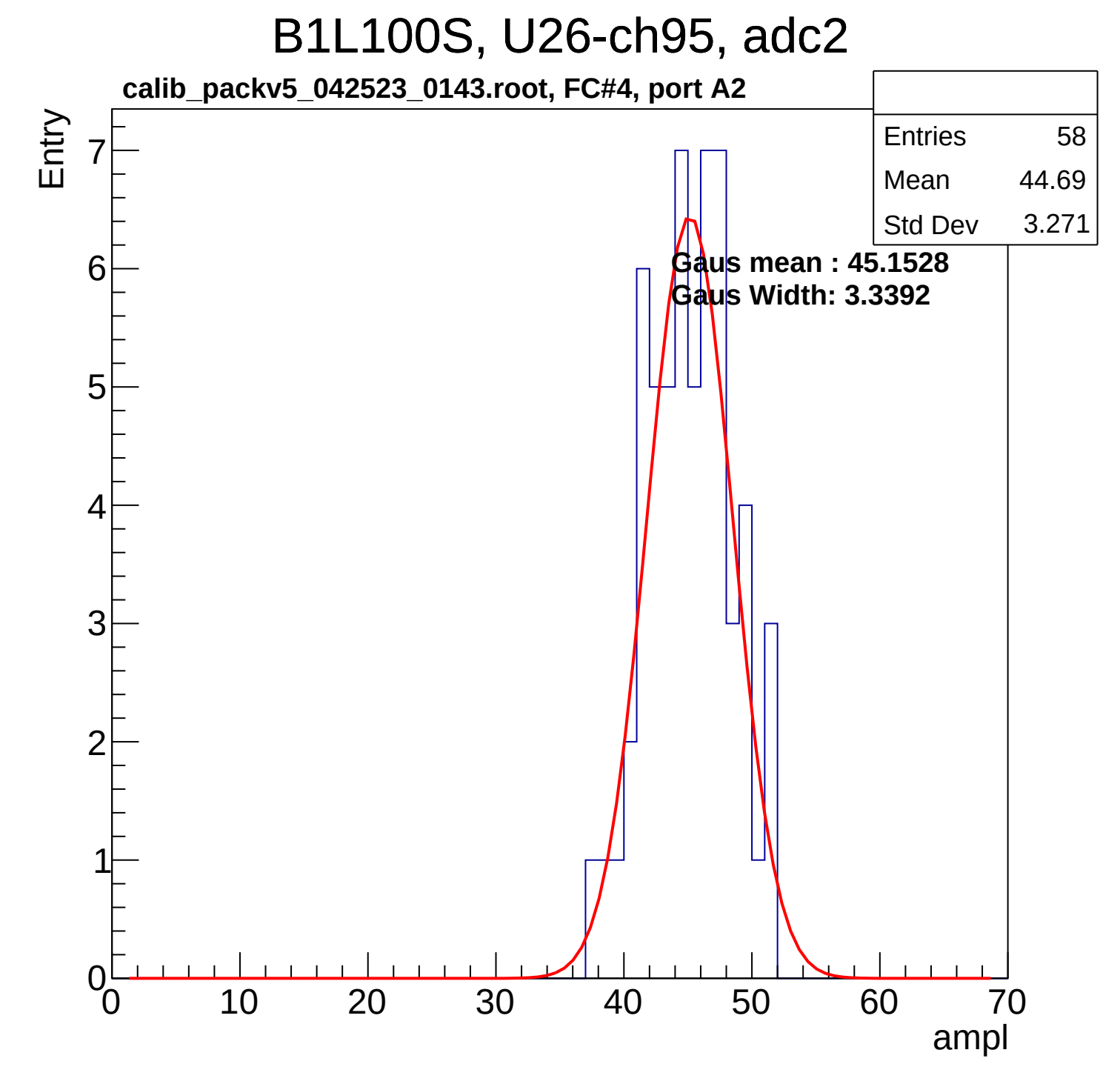

B1L100S, U26-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 38.07
Std Dev 4.166
Gaus mean : 39.2252
Gaus Width: 3.9190
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 44.69
Std Dev 3.271
Gaus mean : 45.1528
Gaus Width: 3.3392
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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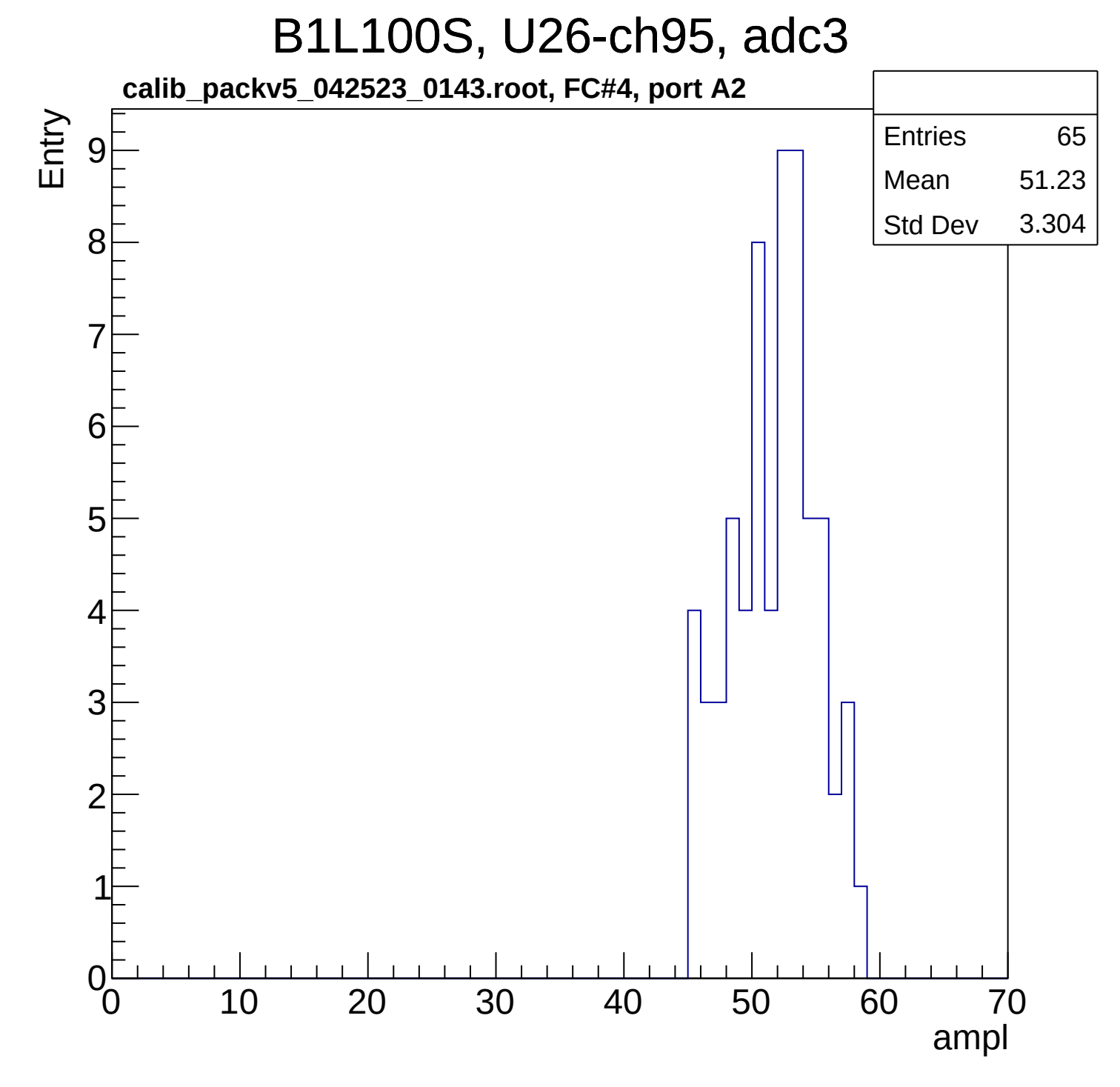
B1L100S, U26-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 51.23
Std Dev 3.304
Entry
ampl
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3
4
5
6
7
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0
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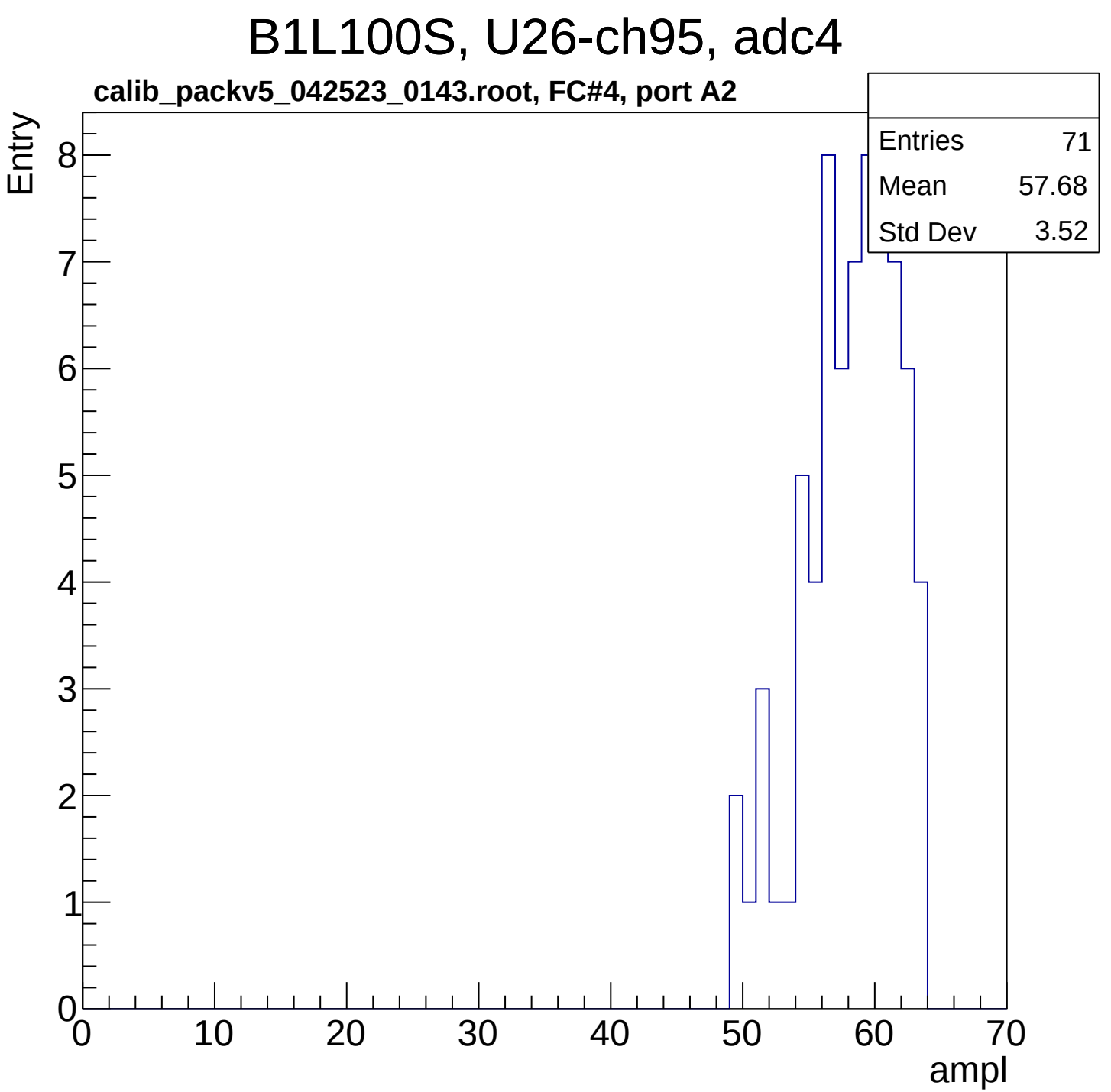
B1L100S, U26-ch95, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 57.68
Std Dev 3.52
Entry
ampl
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1
2
3
4
5
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7
8
0
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20
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40
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60
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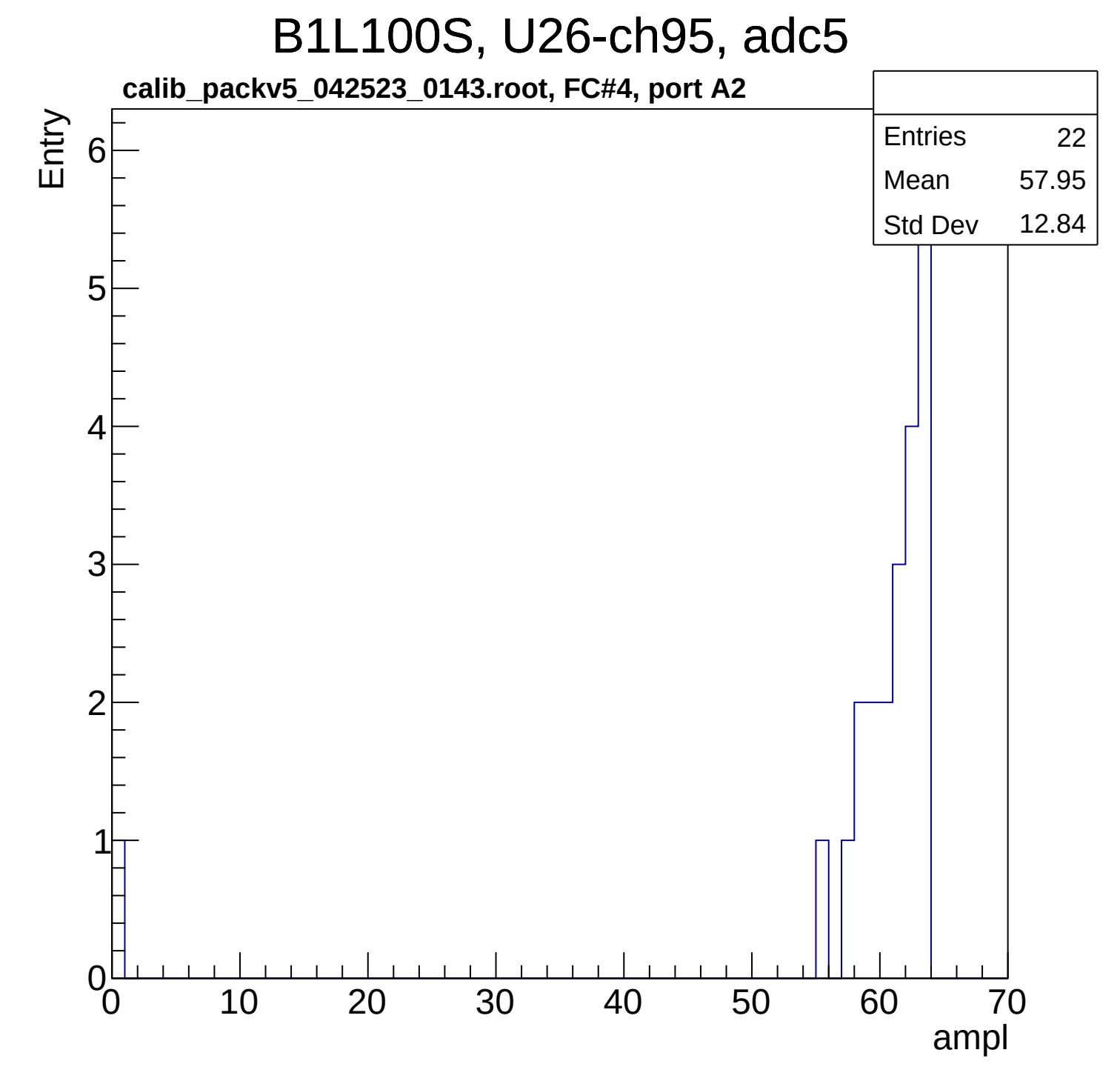
B1L100S, U26-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 57.95
Std Dev 12.84
Entry
ampl
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3
4
5
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0
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20
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60
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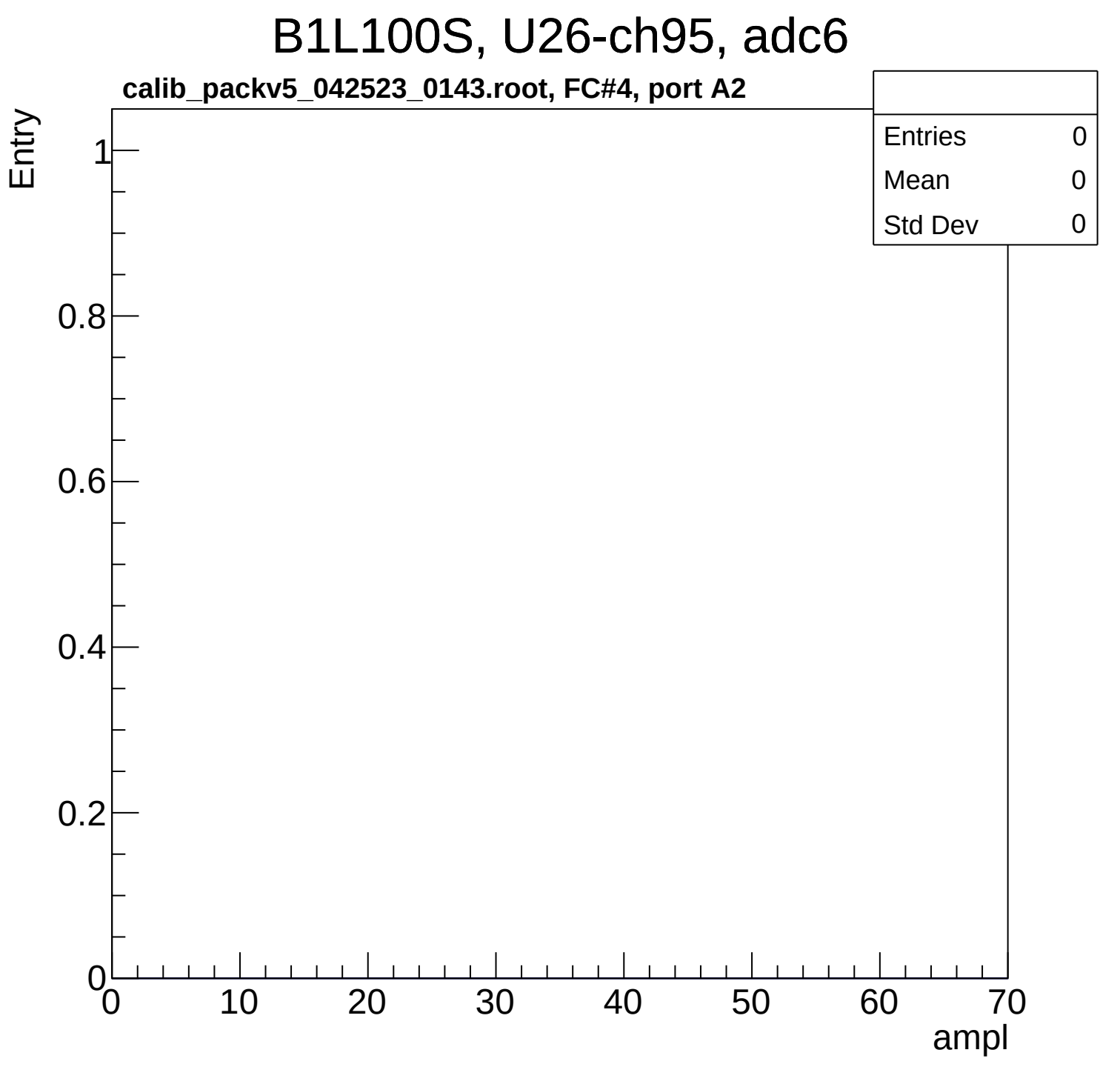
B1L100S, U26-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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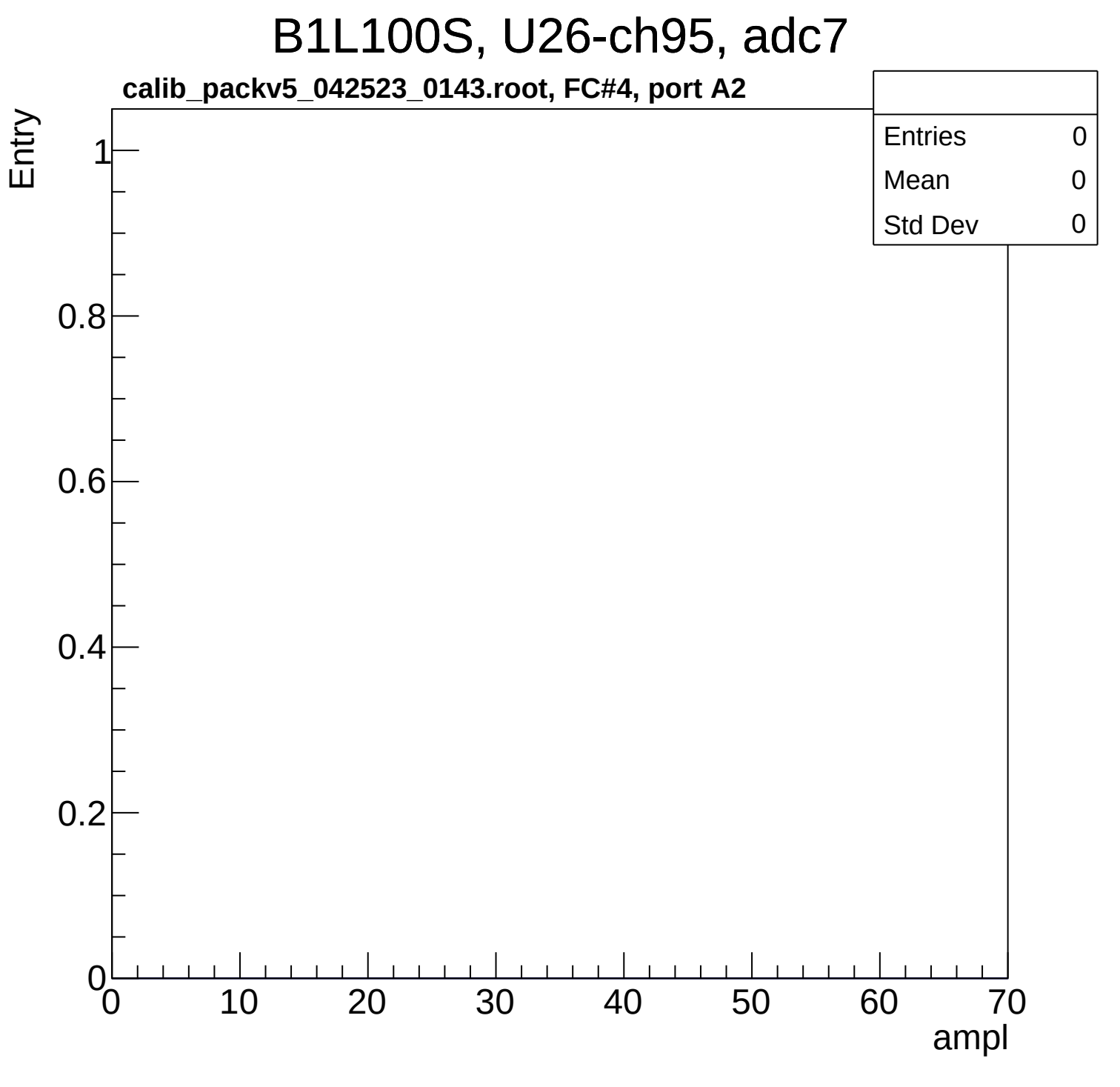
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch95, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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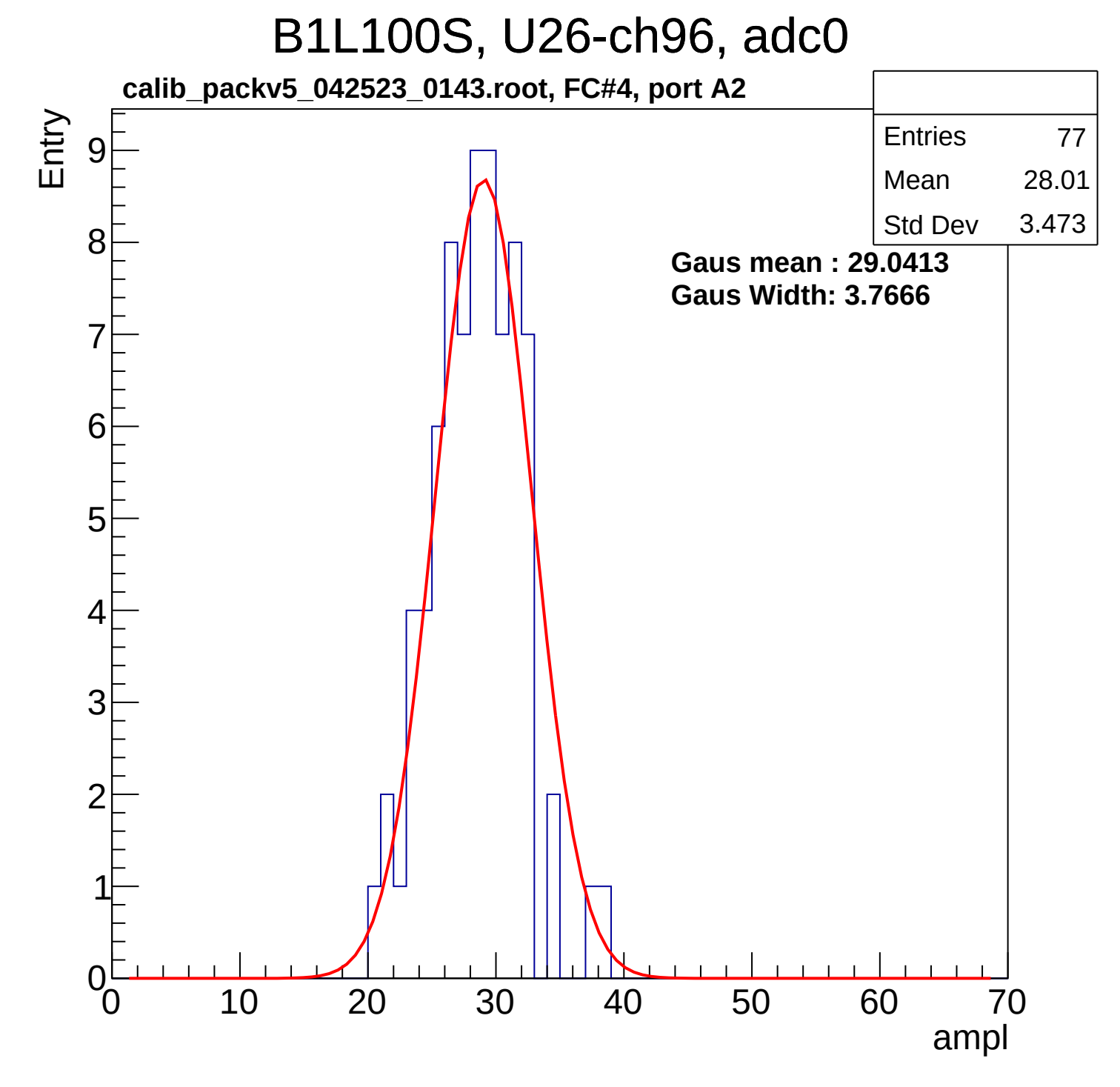
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.01
Std Dev 3.473
Gaus mean : 29.0413
Gaus Width: 3.7666
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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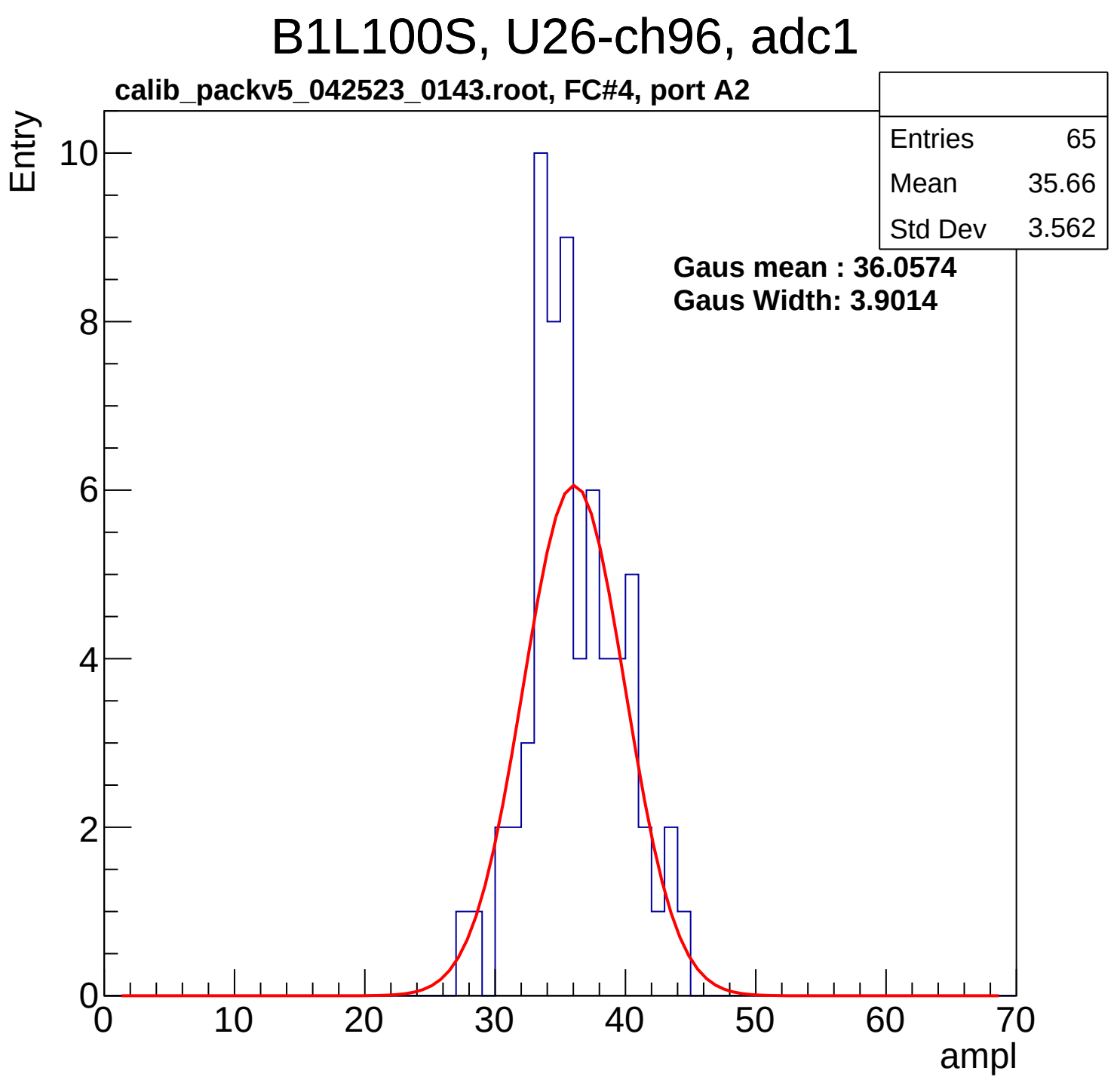
B1L100S, U26-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 35.66
Std Dev 3.562
Gaus mean : 36.0574
Gaus Width: 3.9014
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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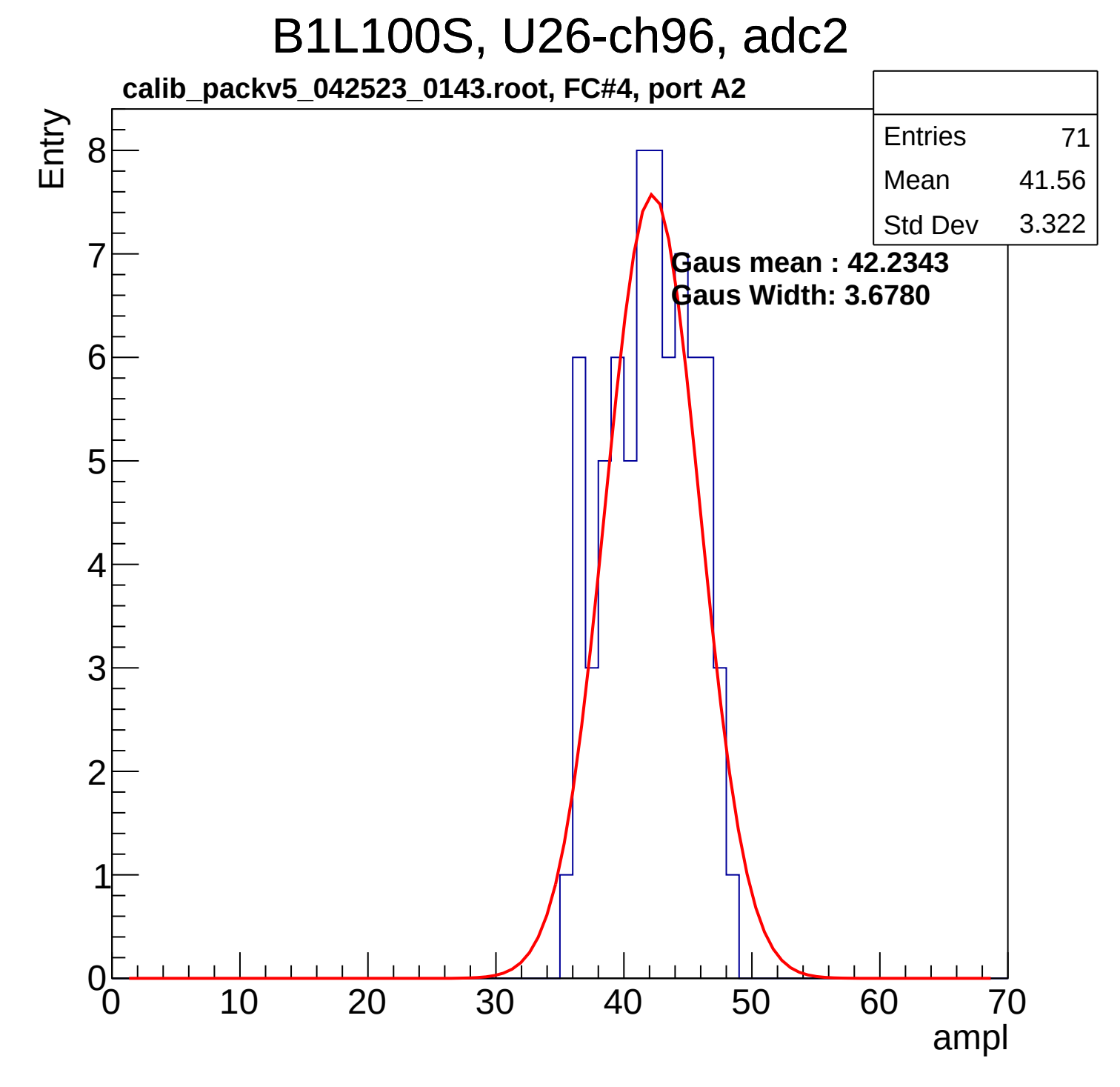
B1L100S, U26-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 41.56
Std Dev 3.322
Gaus mean : 42.2343
Gaus Width: 3.6780
Entry
ampl
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1
2
3
4
5
6
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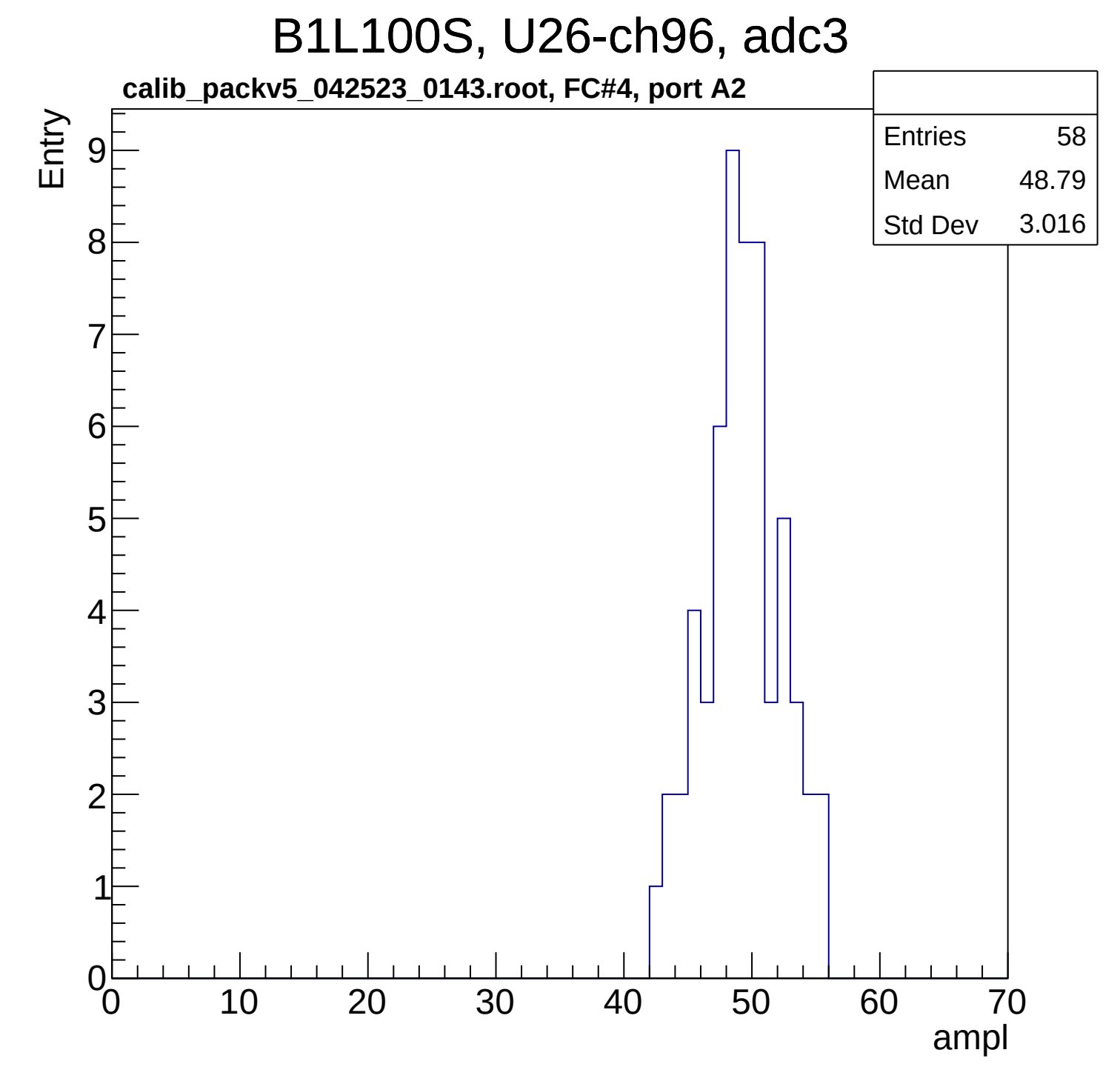
B1L100S, U26-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 48.79
Std Dev 3.016
Entry
ampl
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2
3
4
5
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7
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0
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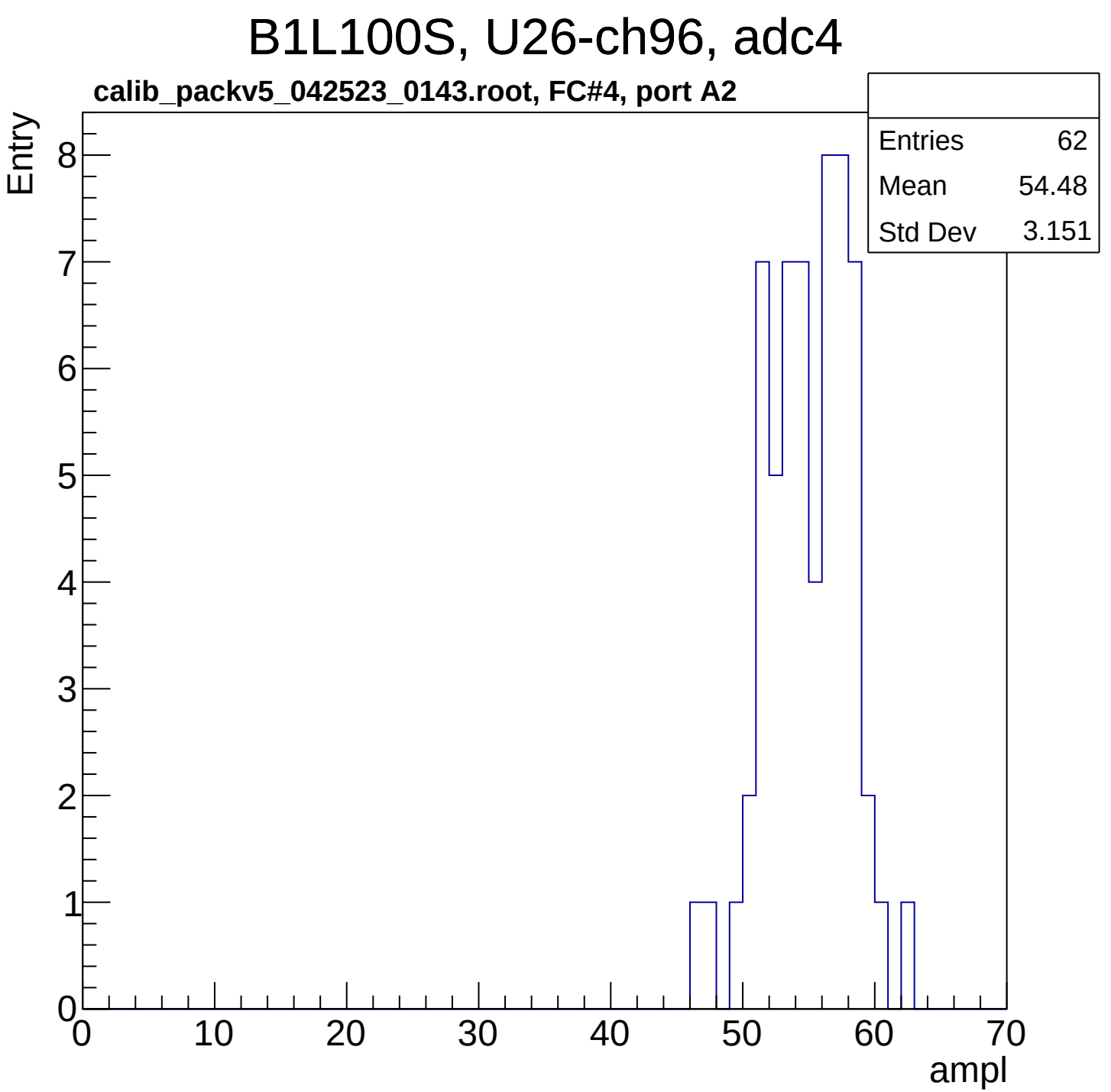
B1L100S, U26-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 54.48
Std Dev 3.151
Entry
ampl
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3
4
5
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7
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0
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20
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40
50
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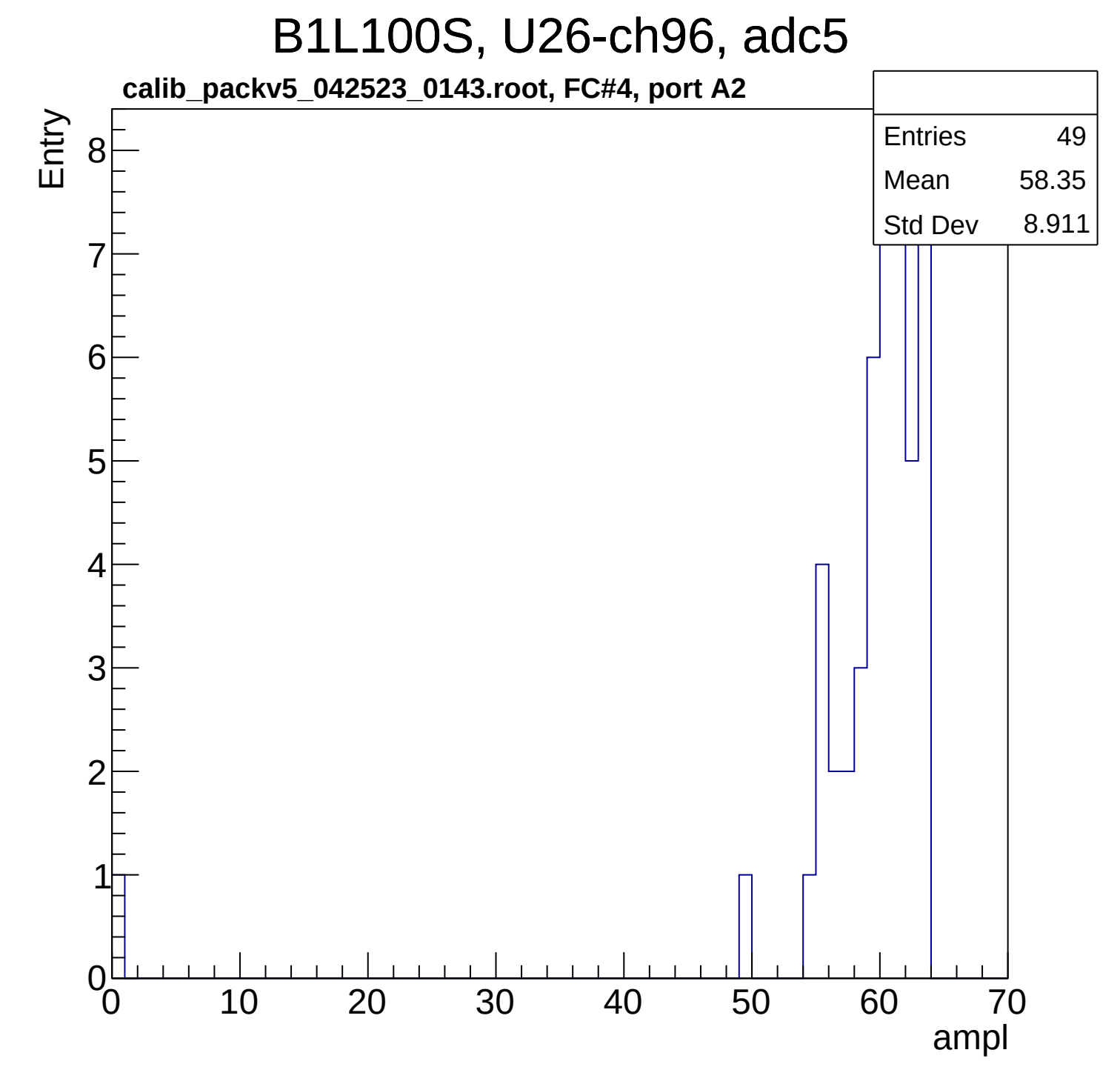
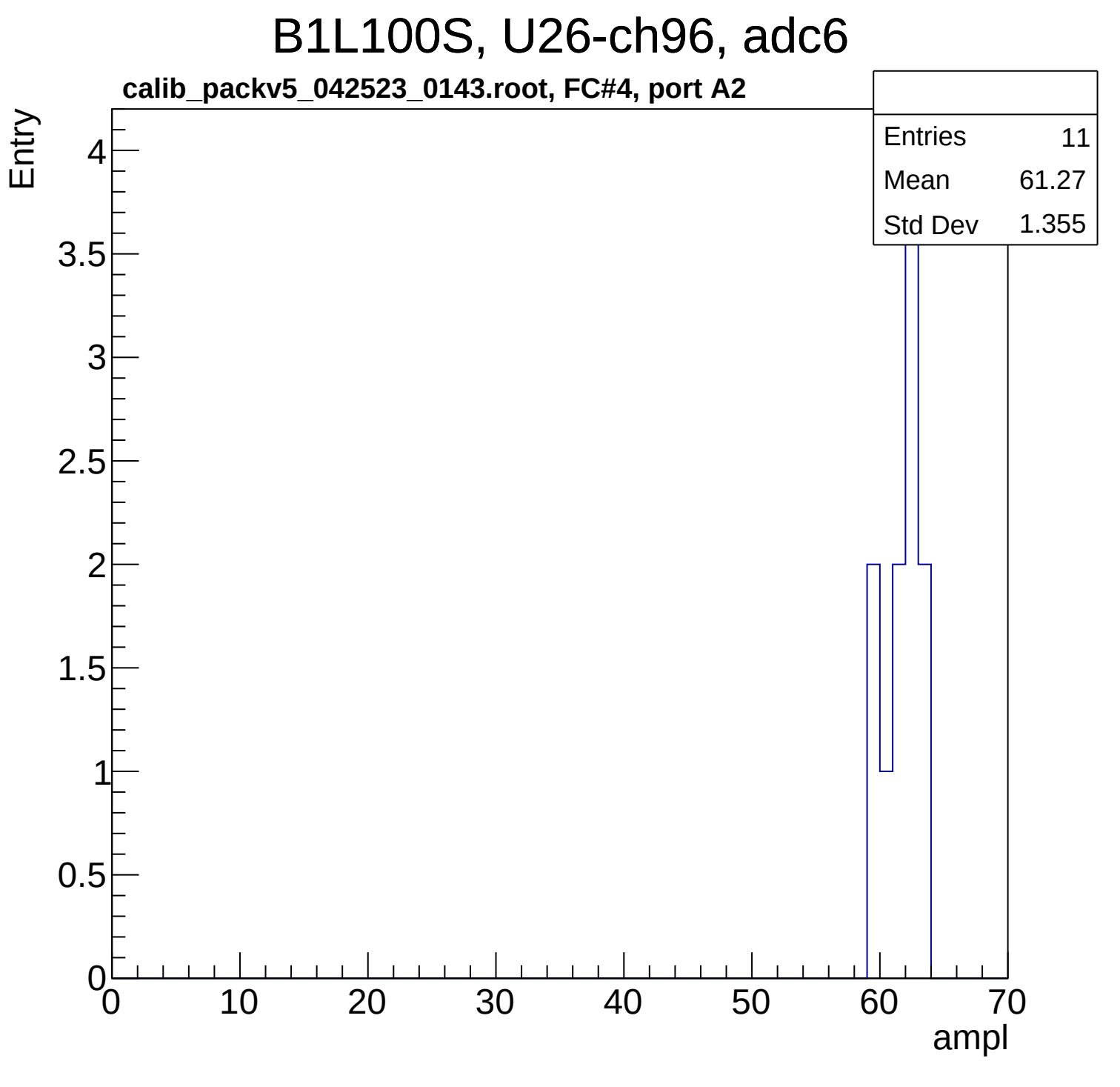

B1L100S, U26-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.35
Std Dev 8.911
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.27
Std Dev 1.355
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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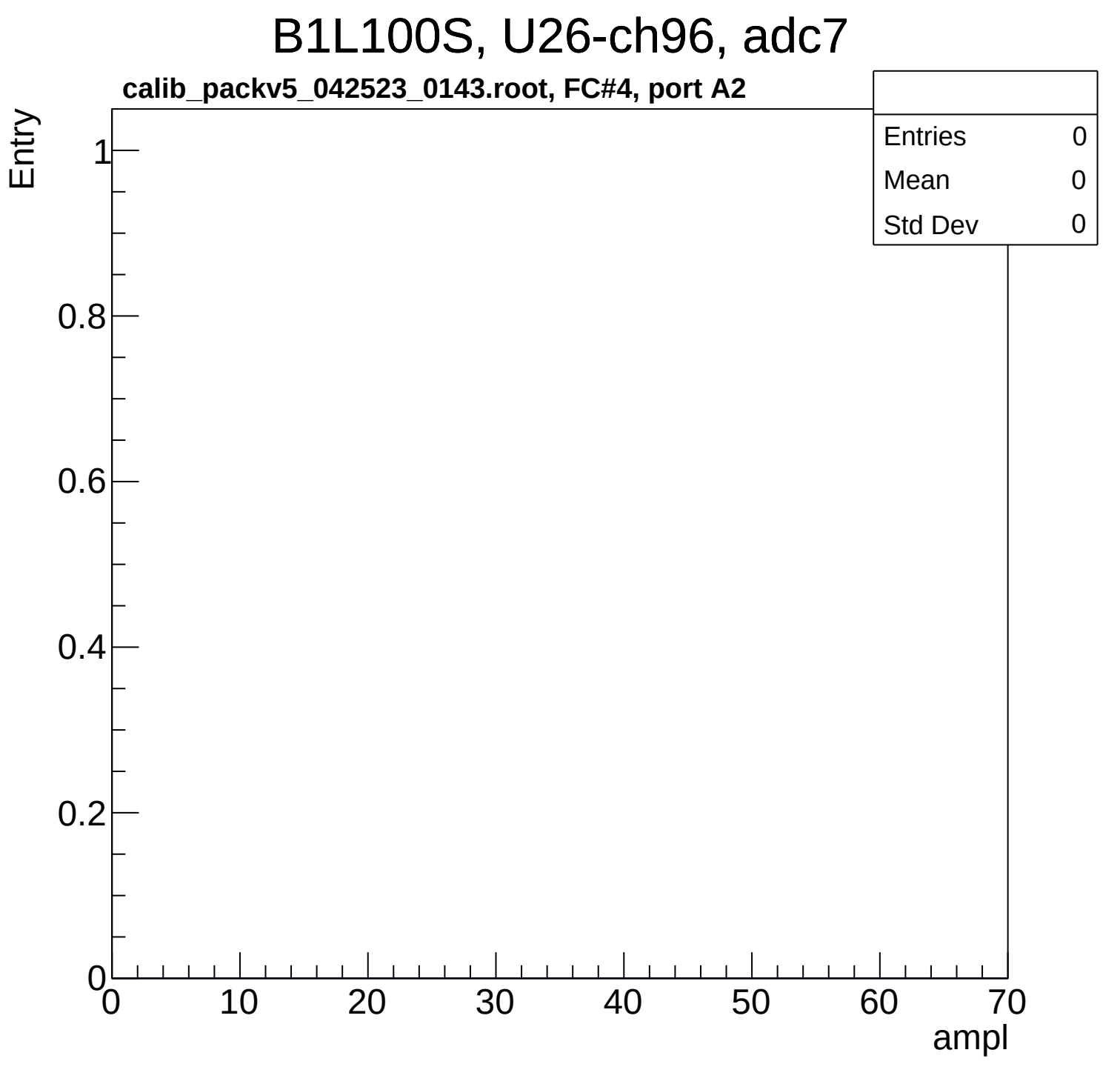
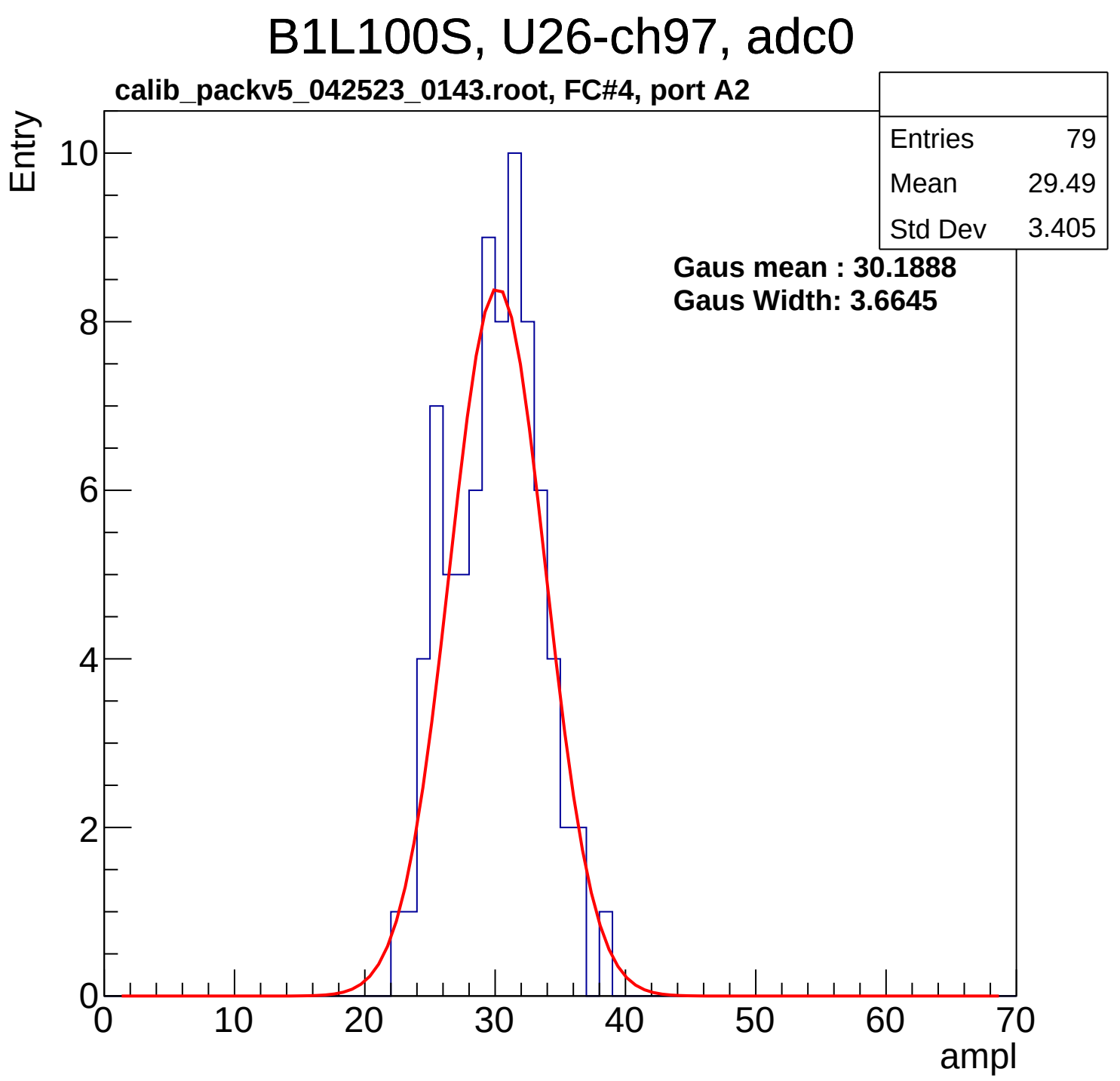
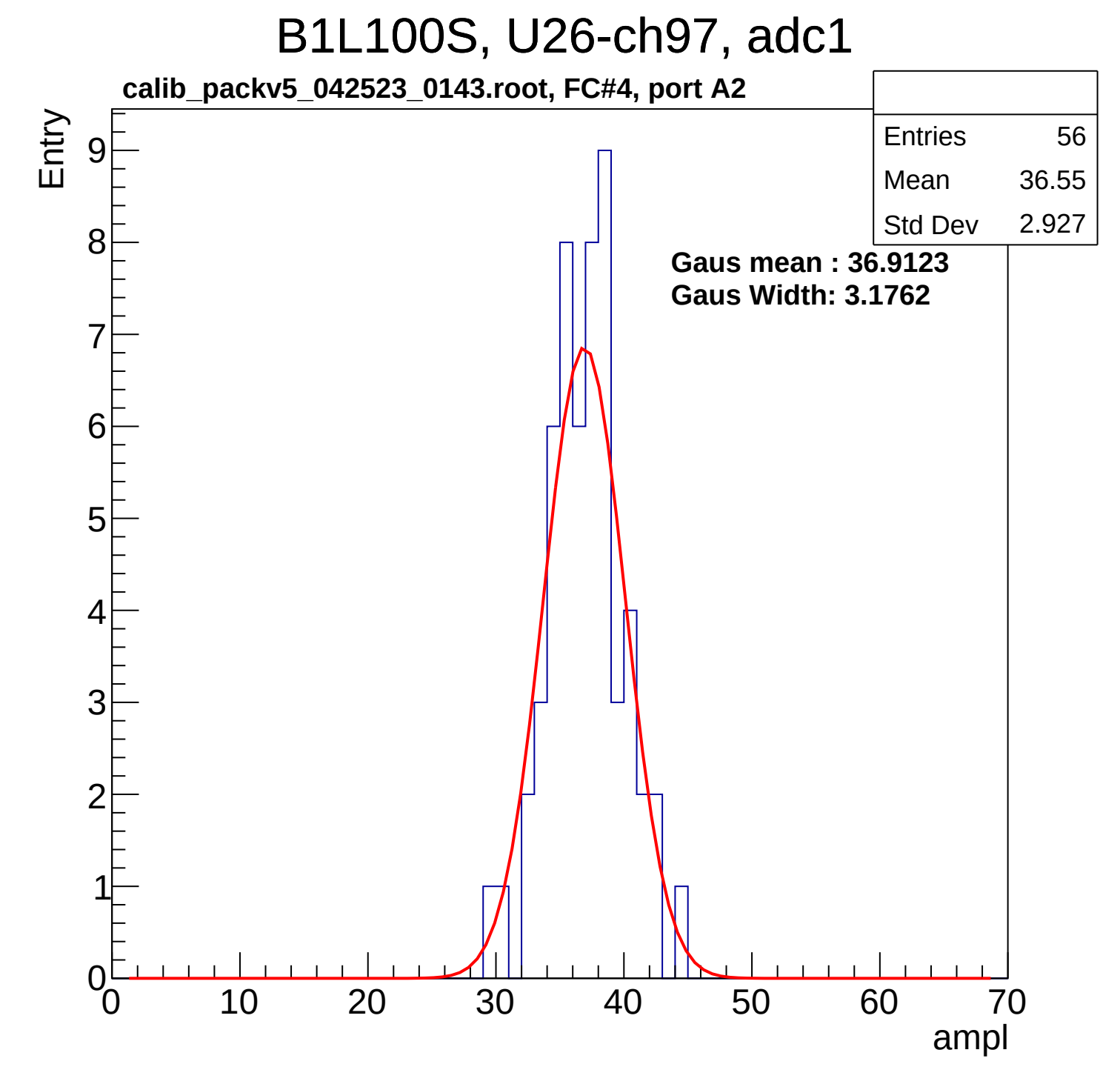

B1L100S, U26-ch96, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.49
Std Dev 3.405
Gaus mean : 30.1888
Gaus Width: 3.6645
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 36.55
Std Dev 2.927
Gaus mean : 36.9123
Gaus Width: 3.1762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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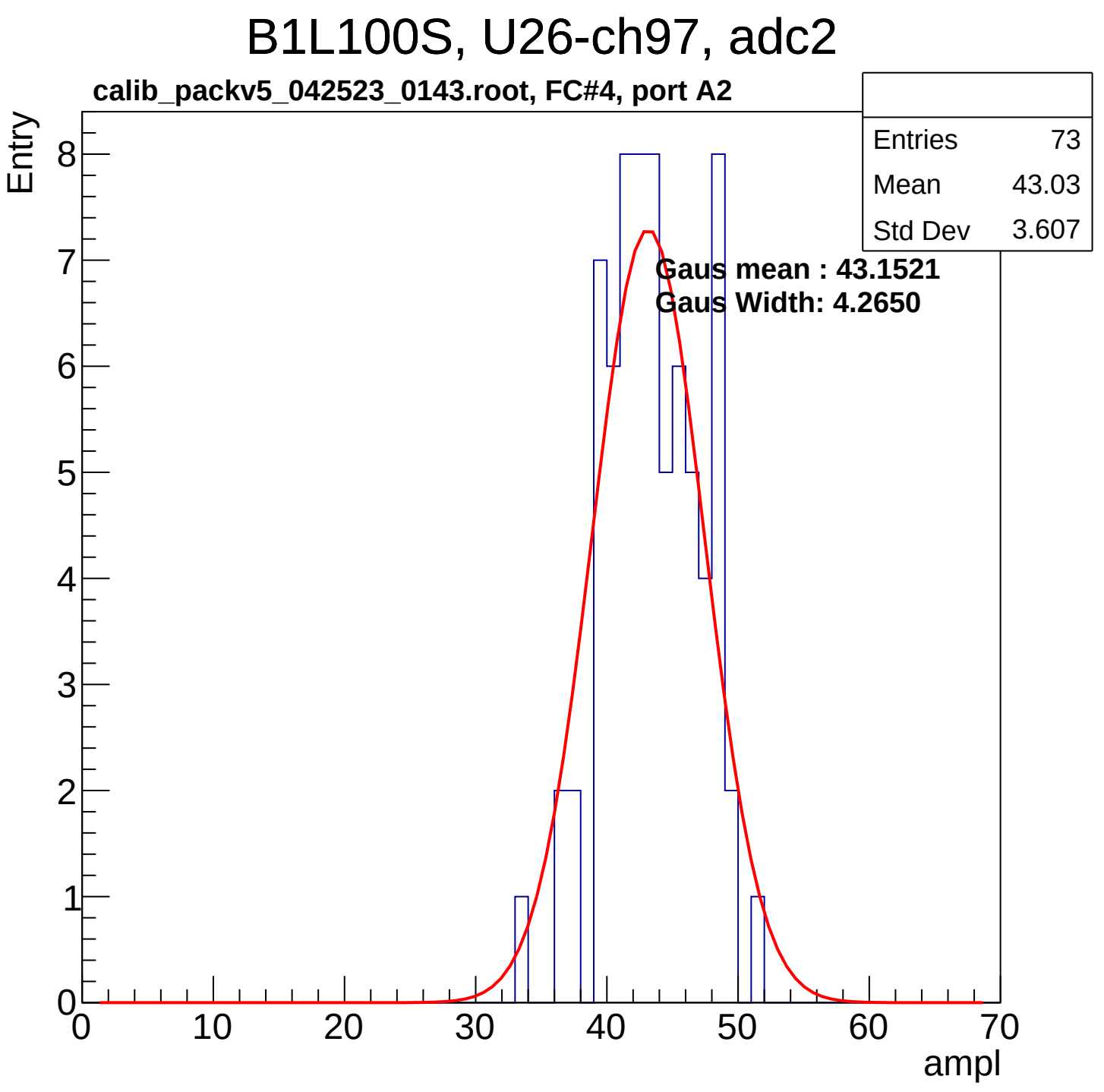
B1L100S, U26-ch97, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.03
Std Dev 3.607
Gaus mean : 43.1521
Gaus Width: 4.2650
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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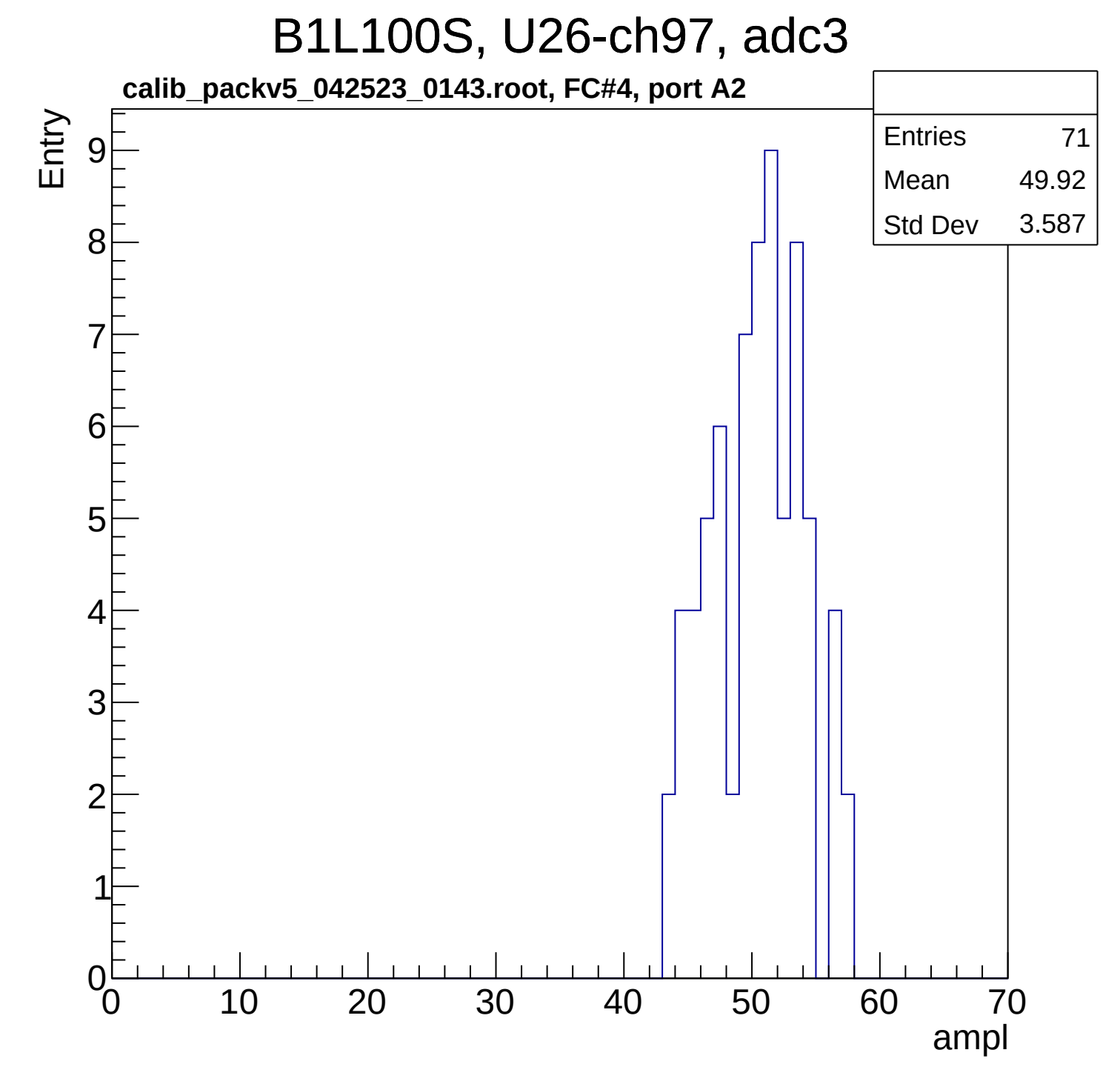
B1L100S, U26-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.92
Std Dev 3.587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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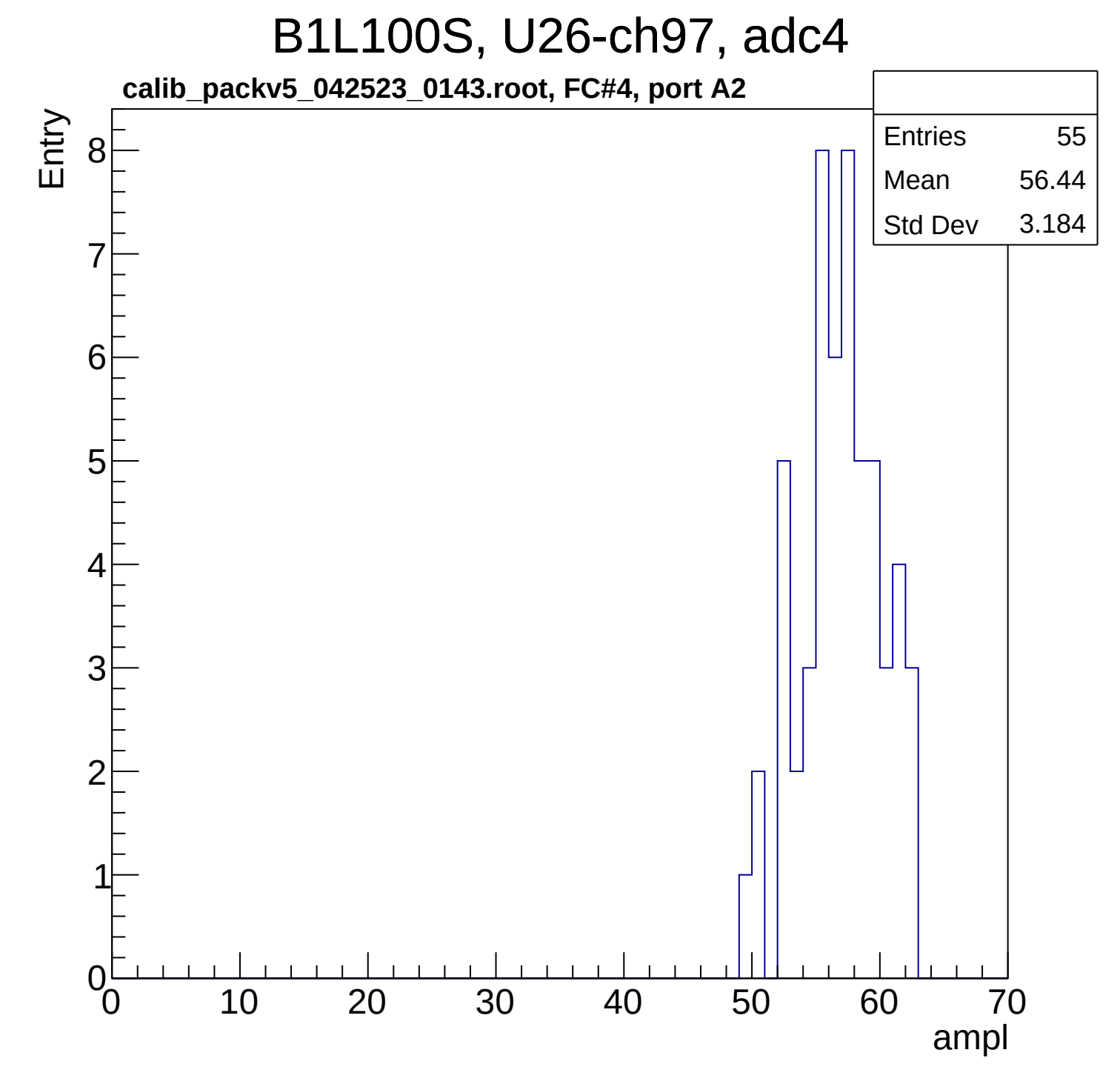
B1L100S, U26-ch97, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.44
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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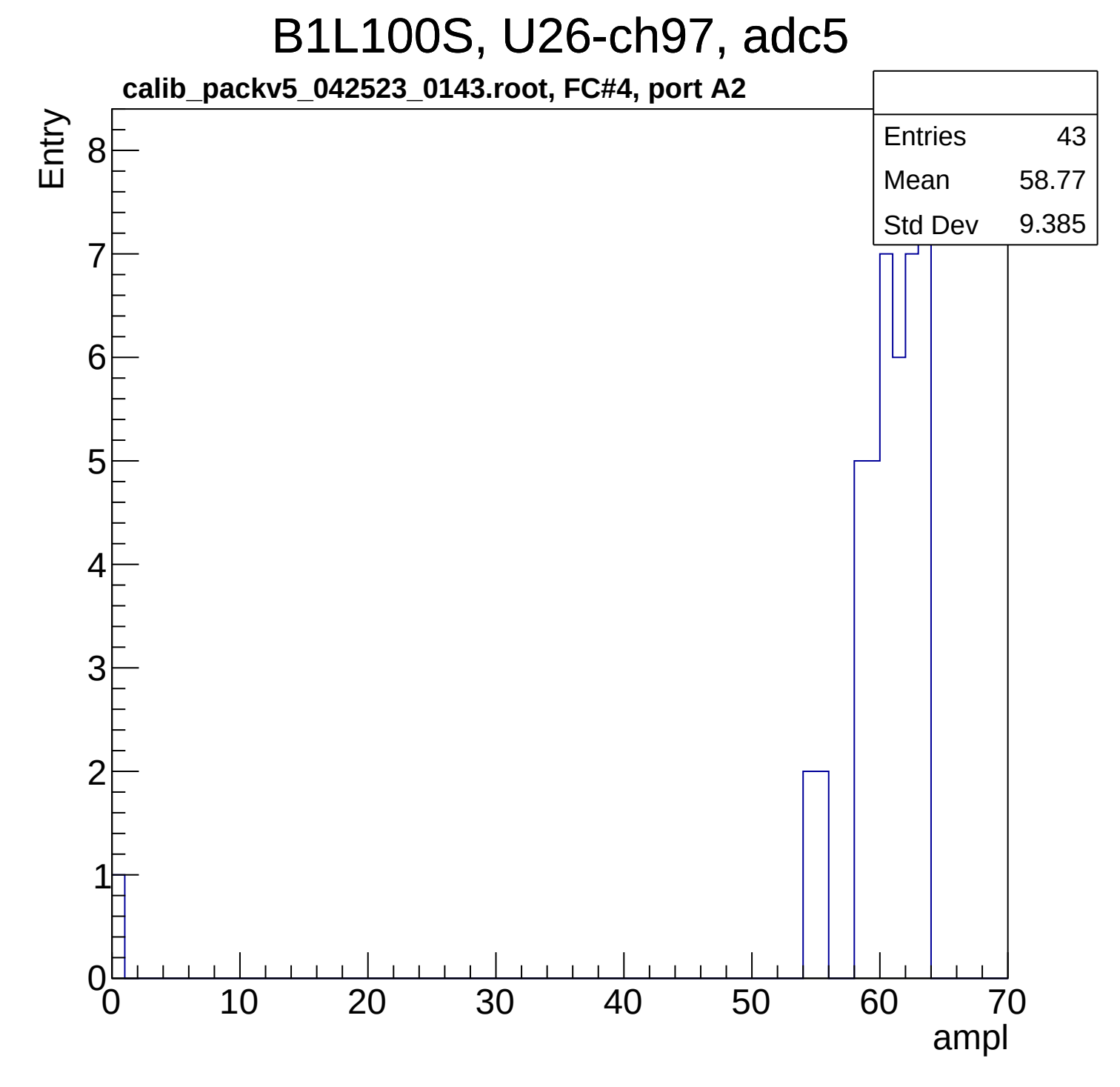
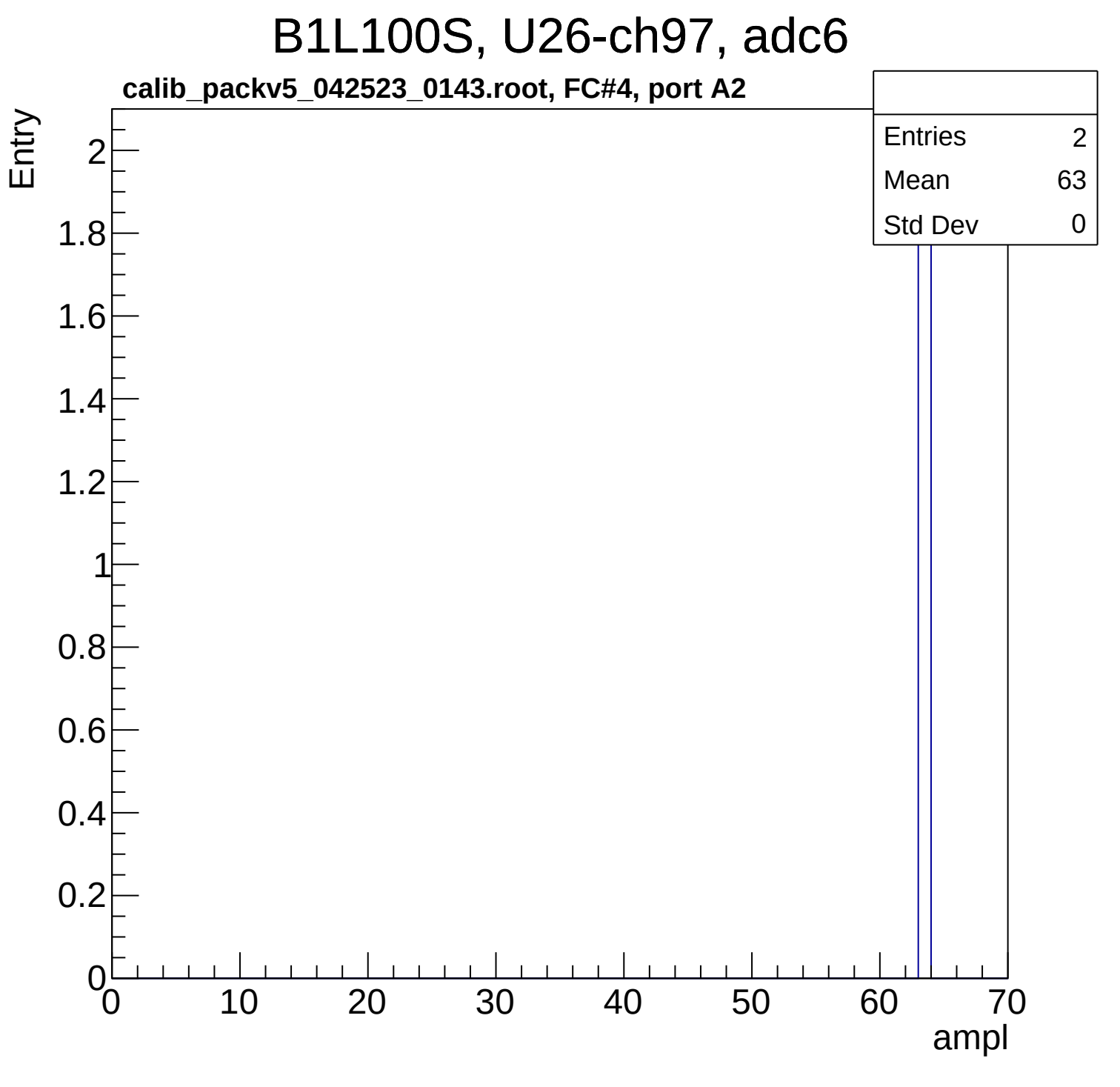
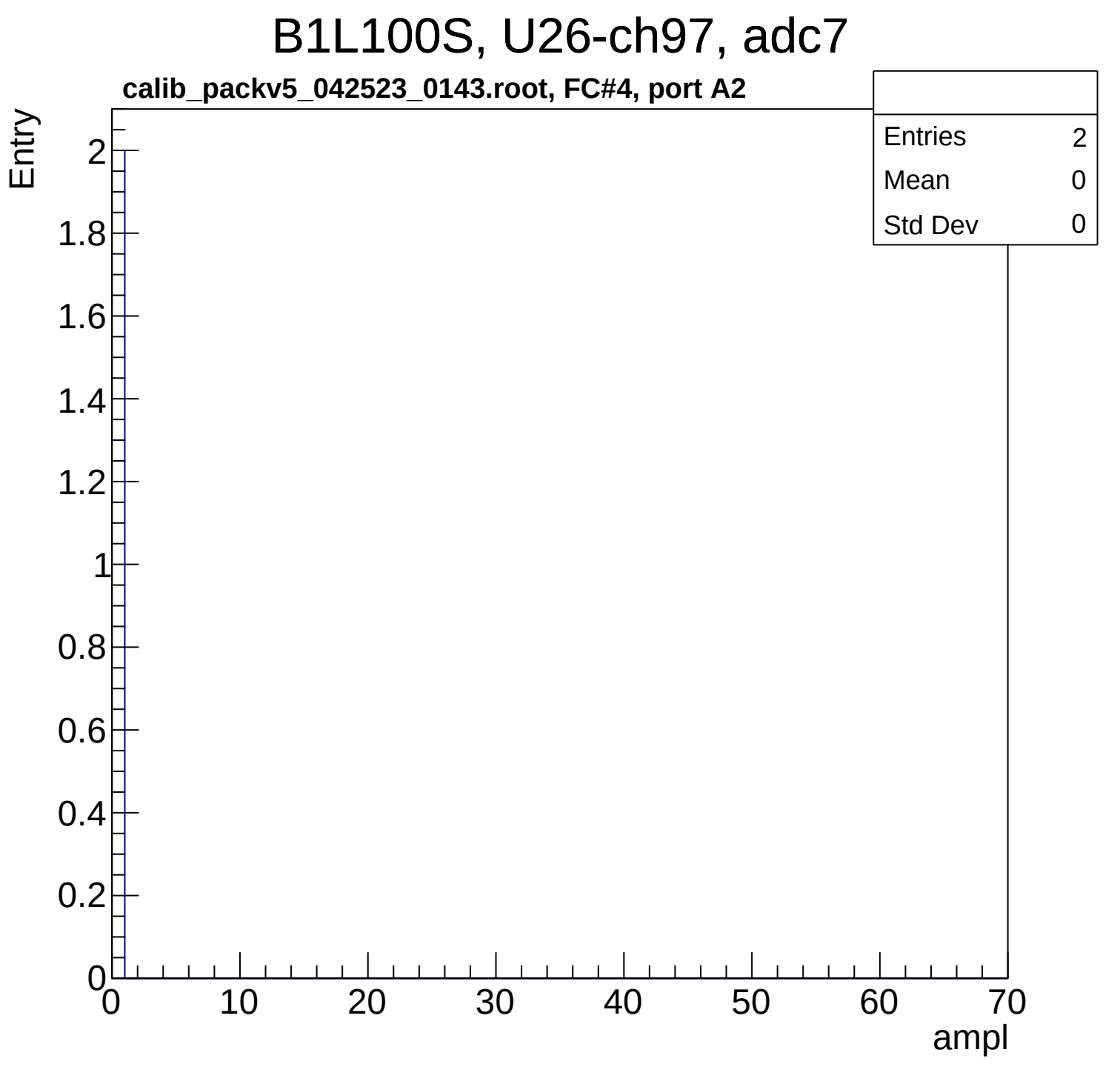
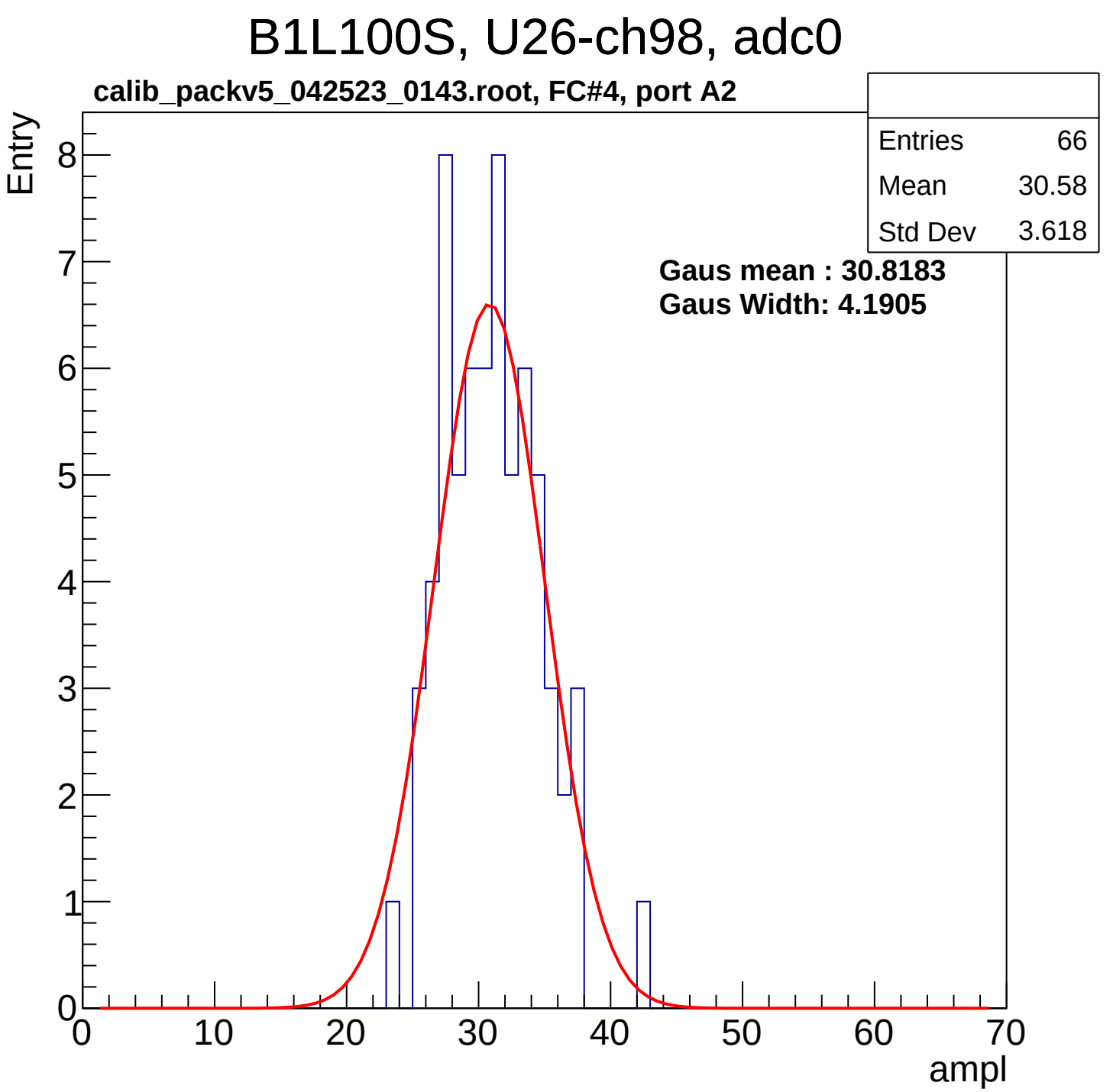

B1L100S, U26-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.77
Std Dev 9.385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.58
Std Dev 3.618
Gaus mean : 30.8183
Gaus Width: 4.1905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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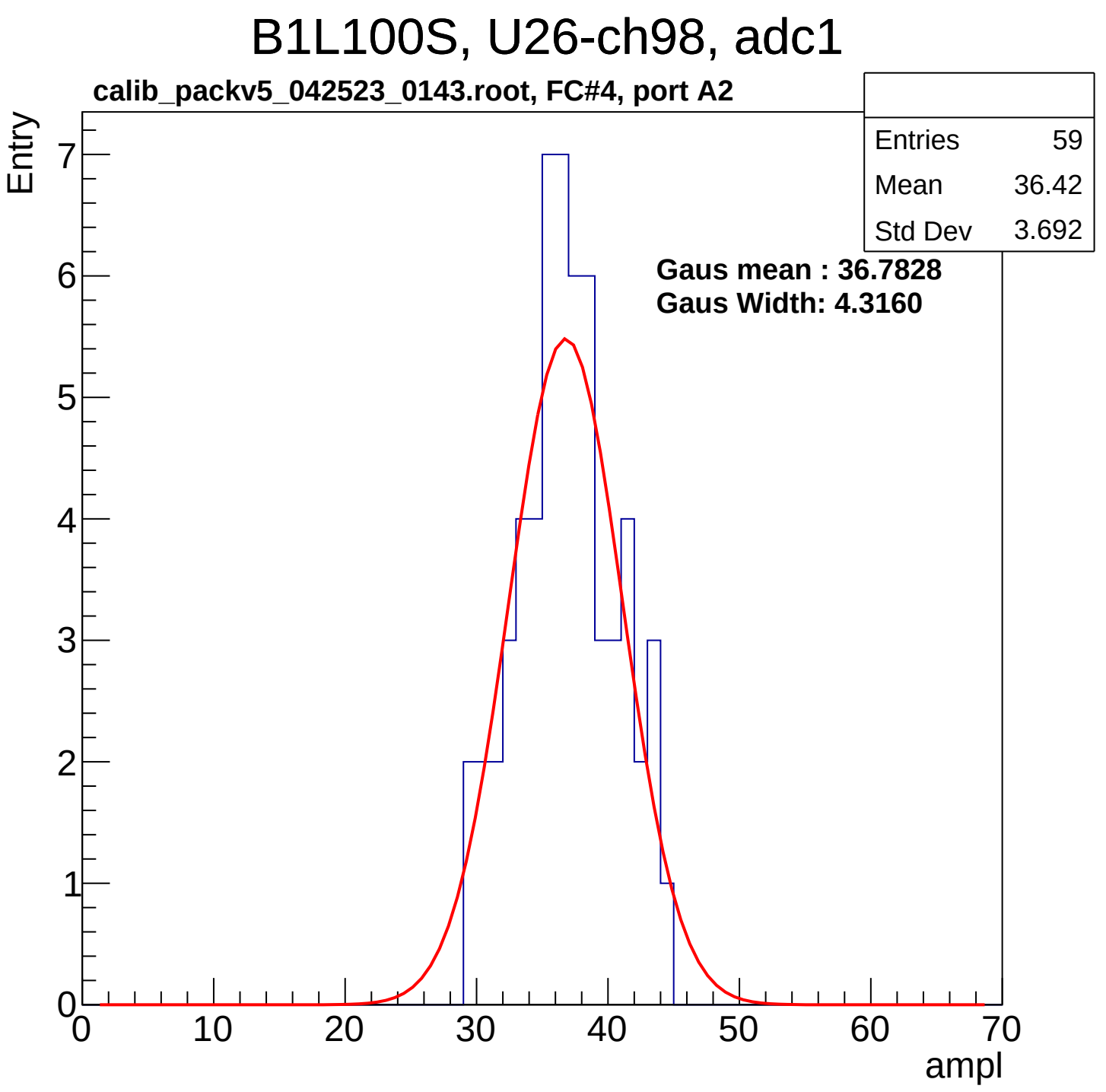
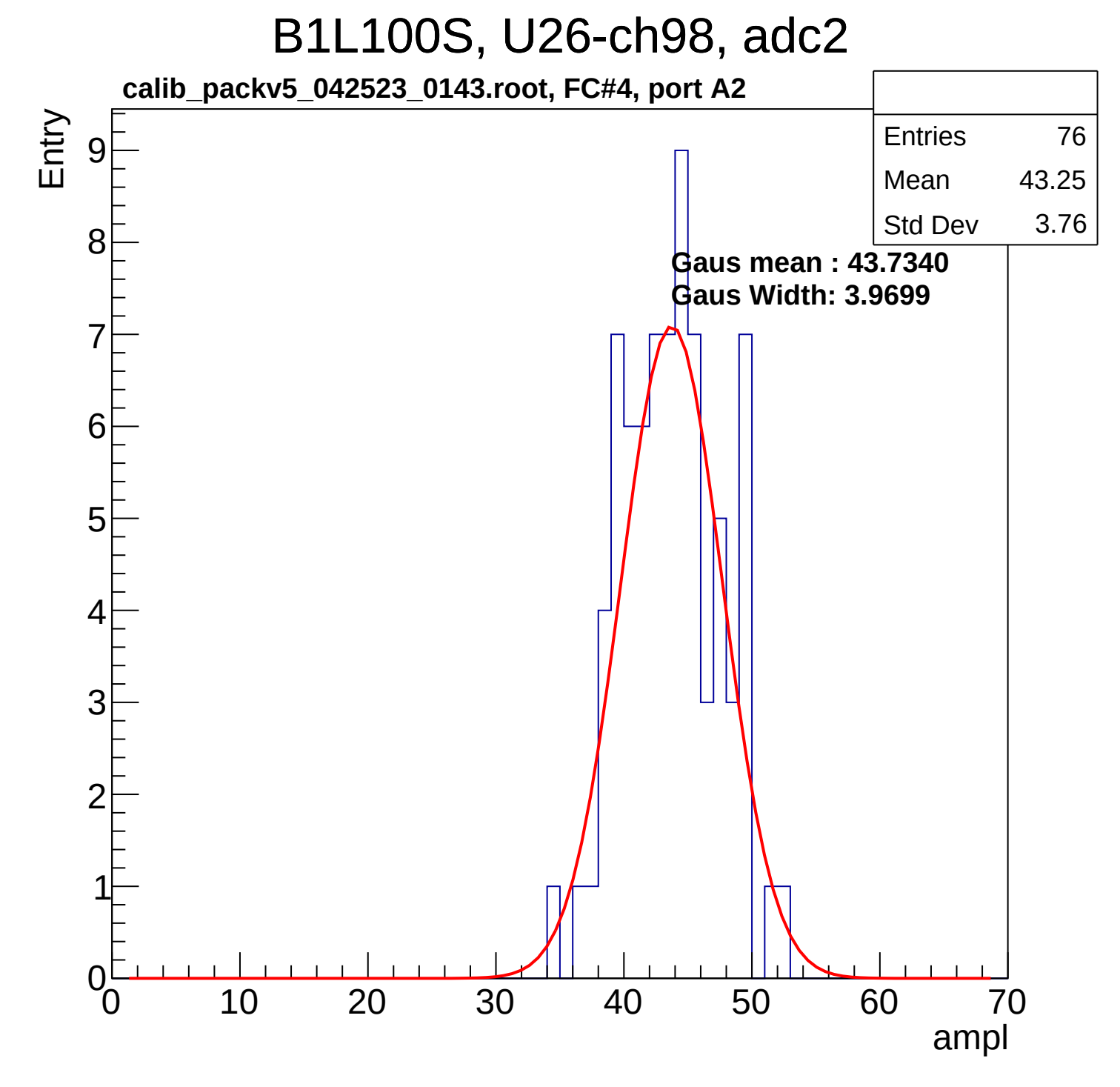
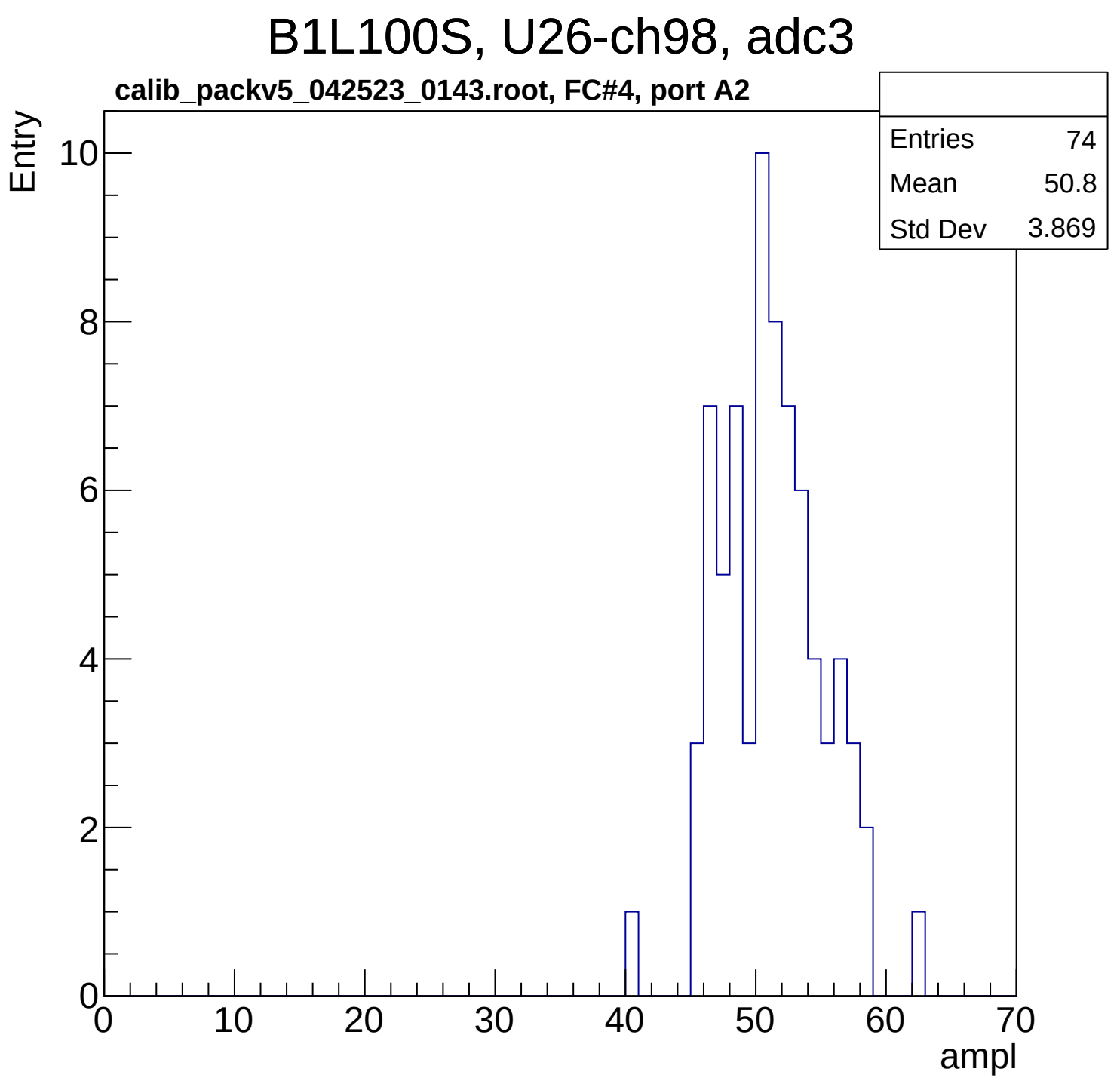
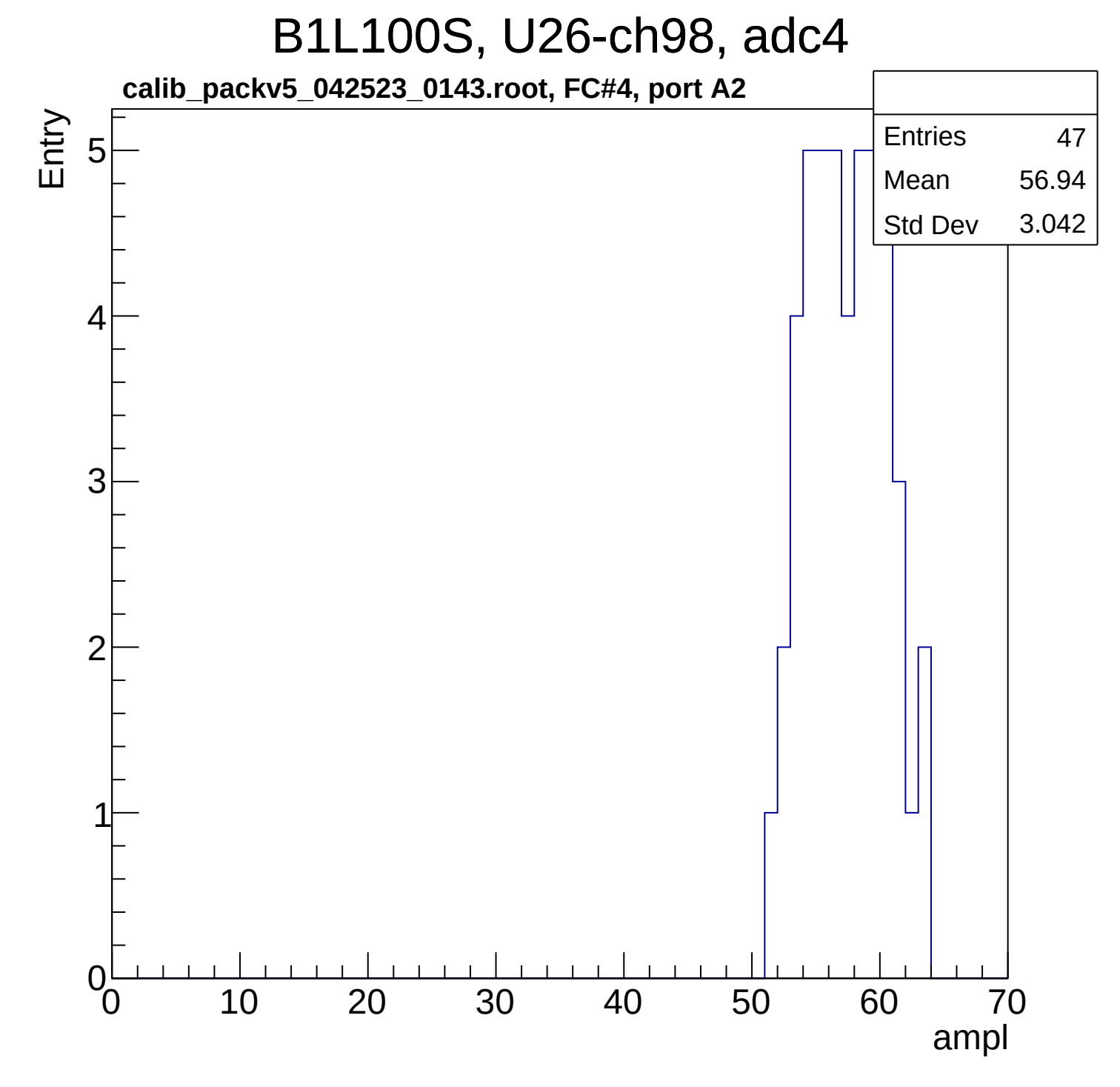
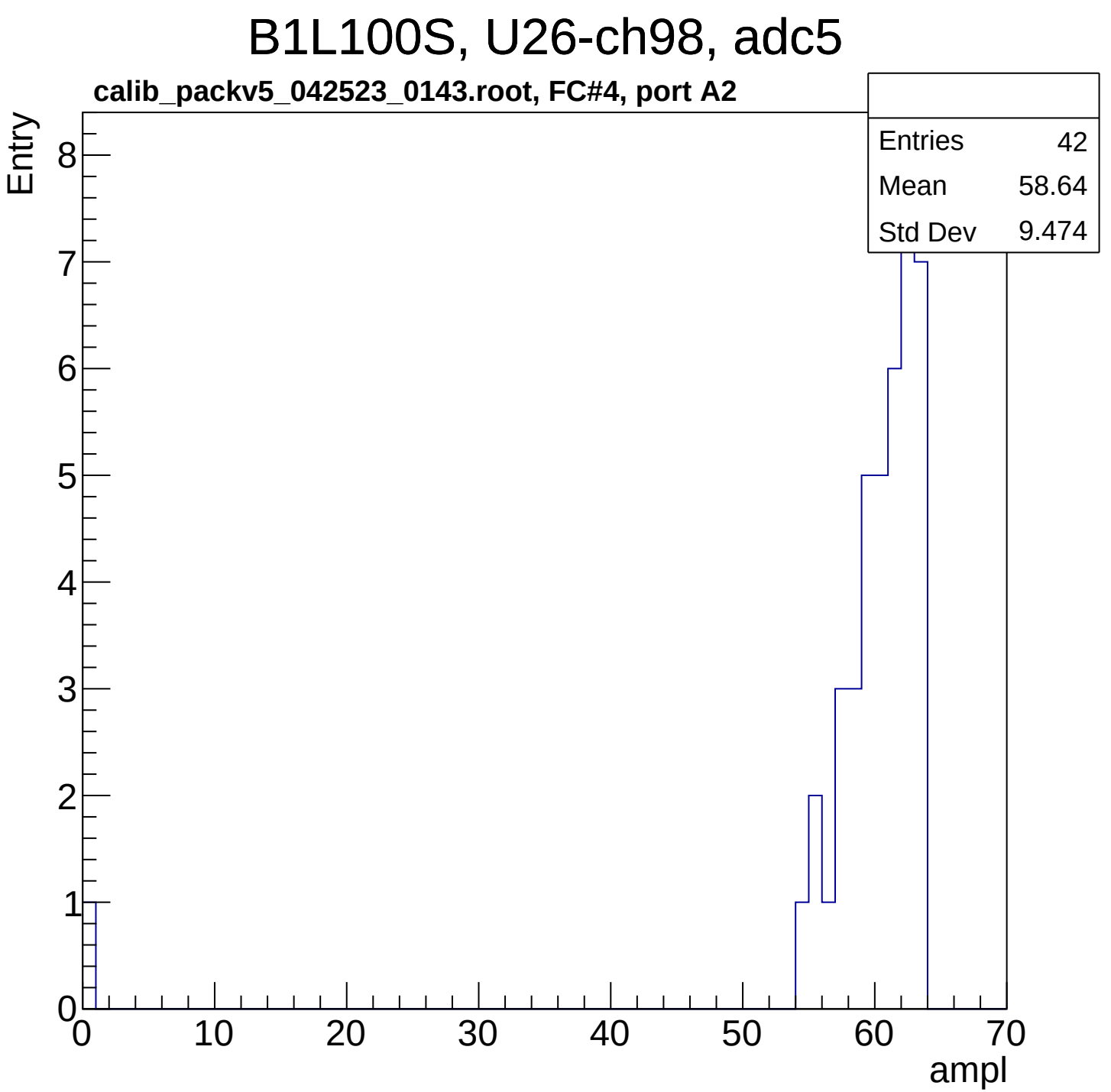
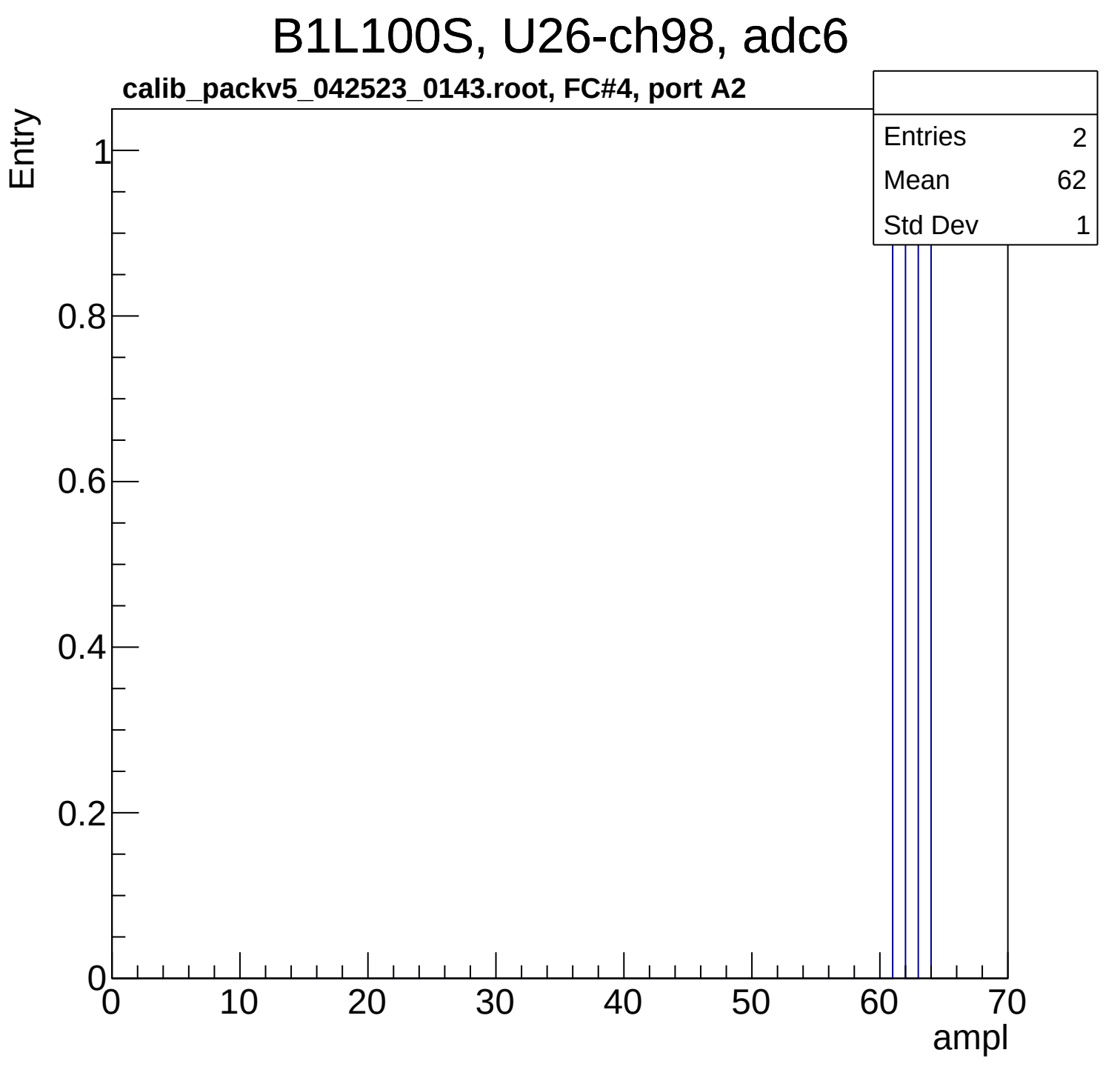

B1L100S, U26-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.42
Std Dev 3.692
Gaus mean : 36.7828
Gaus Width: 4.3160
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

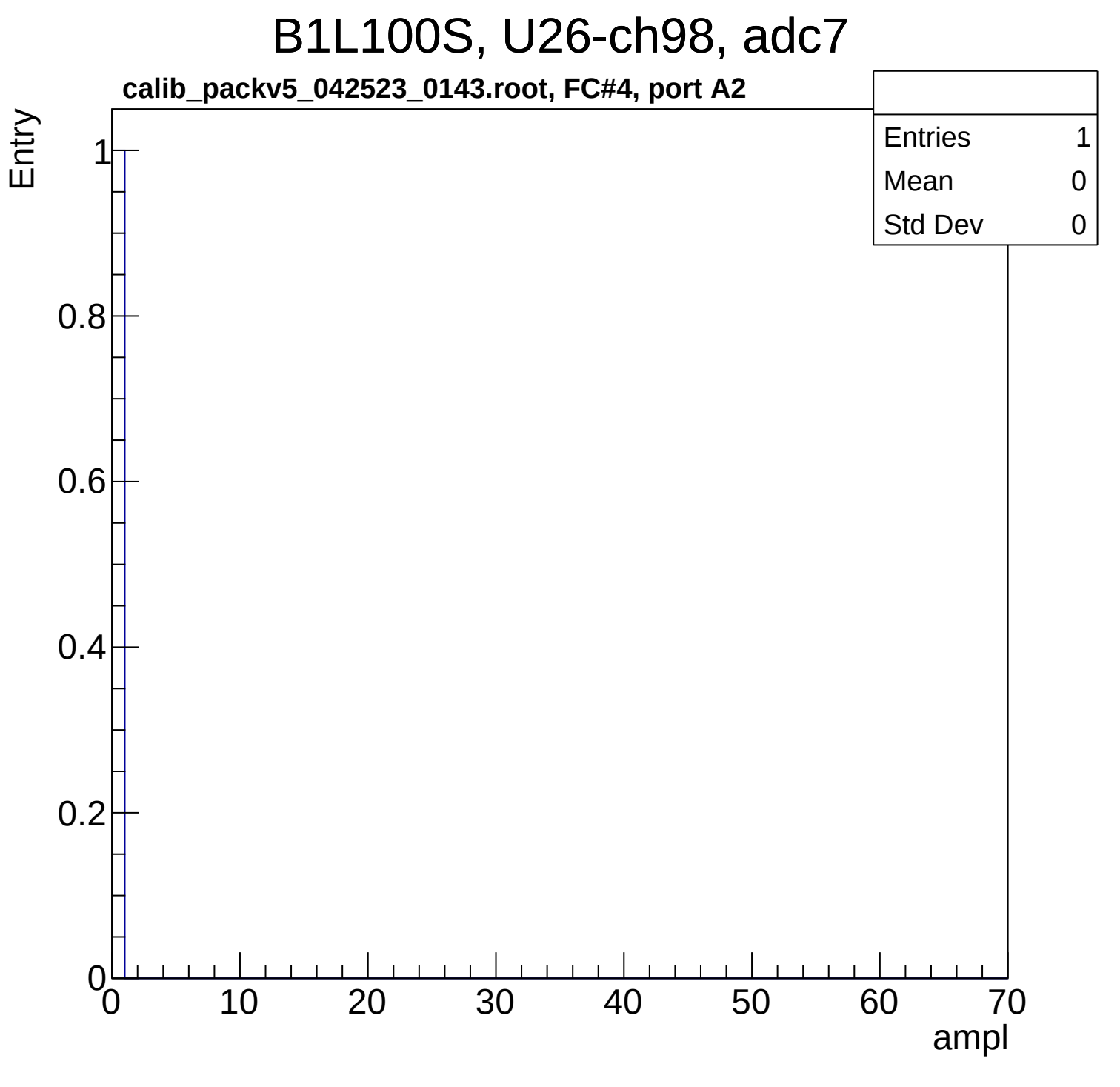

B1L100S, U26-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 43.25
Std Dev 3.76
Gaus mean : 43.7340
Gaus Width: 3.9699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch98, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.8
Std Dev 3.869
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 56.94
Std Dev 3.042
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U26-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.64
Std Dev 9.474
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch98, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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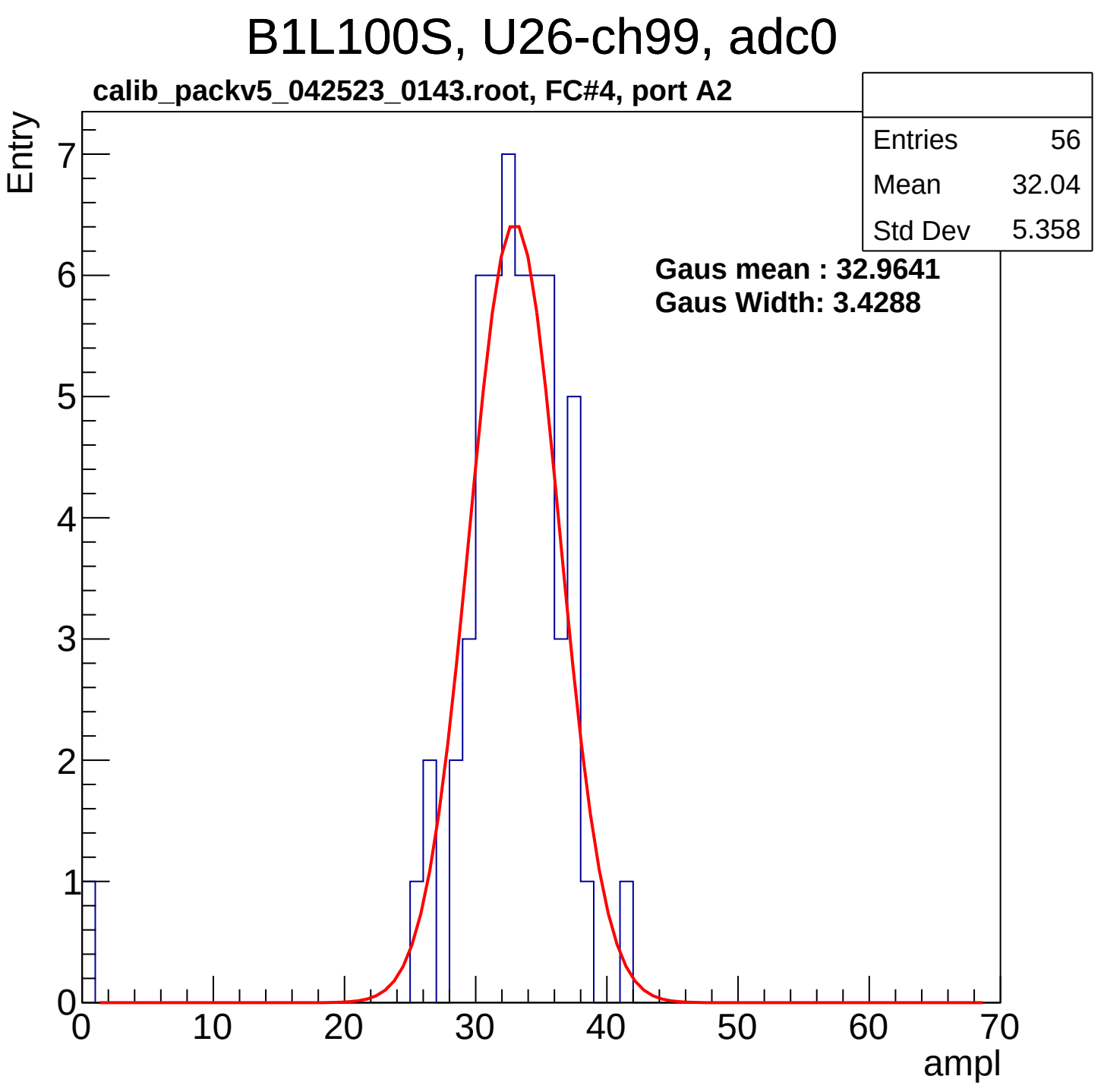
50
60
70
ampl


B1L100S, U26-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 32.04
Std Dev 5.358
Gaus mean : 32.9641
Gaus Width: 3.4288
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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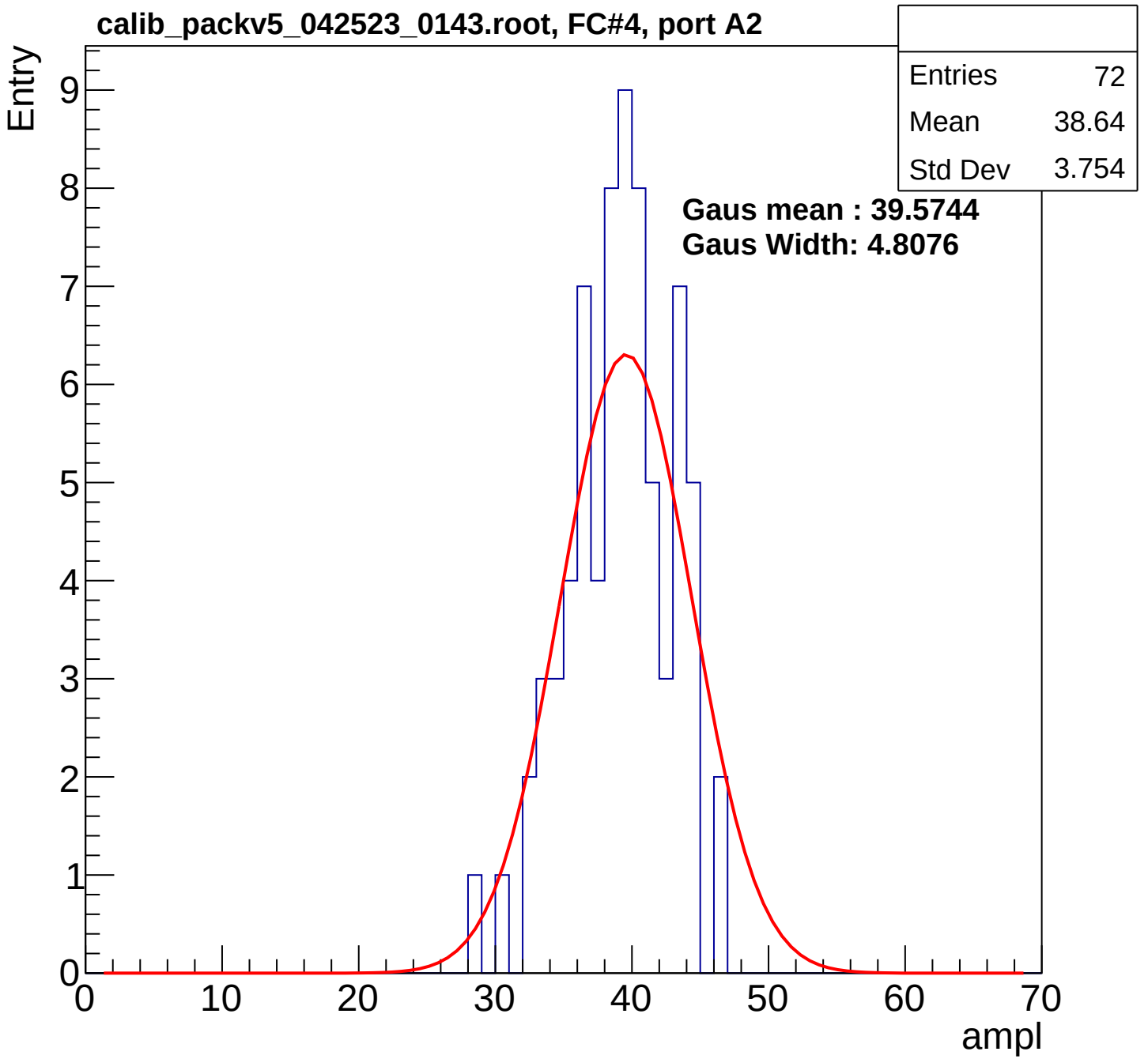
40
50
60
70


B1L100S, U26-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 38.64
Std Dev 3.754
Gaus mean : 39.5744
Gaus Width: 4.8076
Entry
ampl
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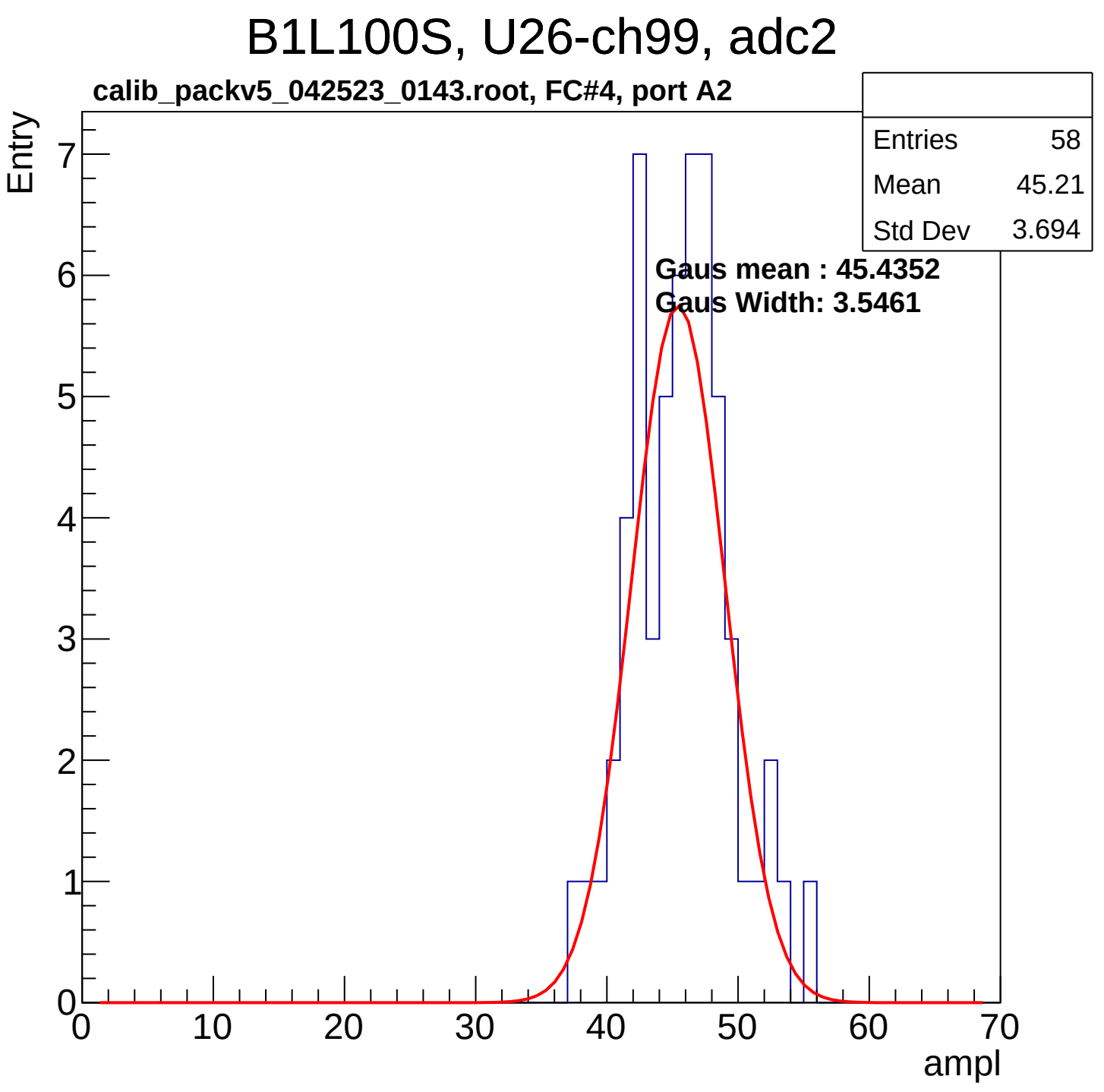
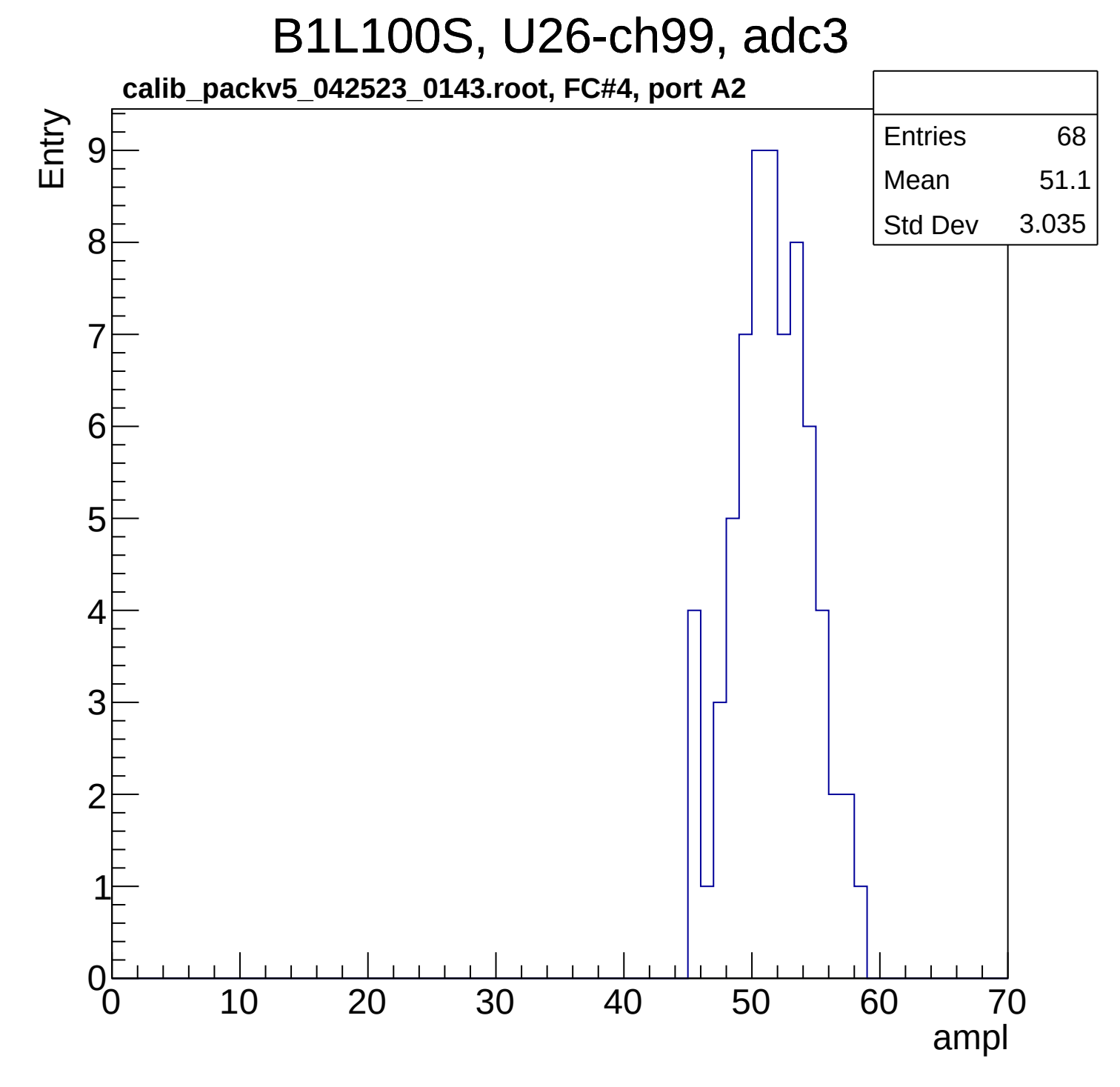
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 45.21
Std Dev 3.694
Gaus mean : 45.4352
Gaus Width: 3.5461
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch99, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 51.1
Std Dev 3.035
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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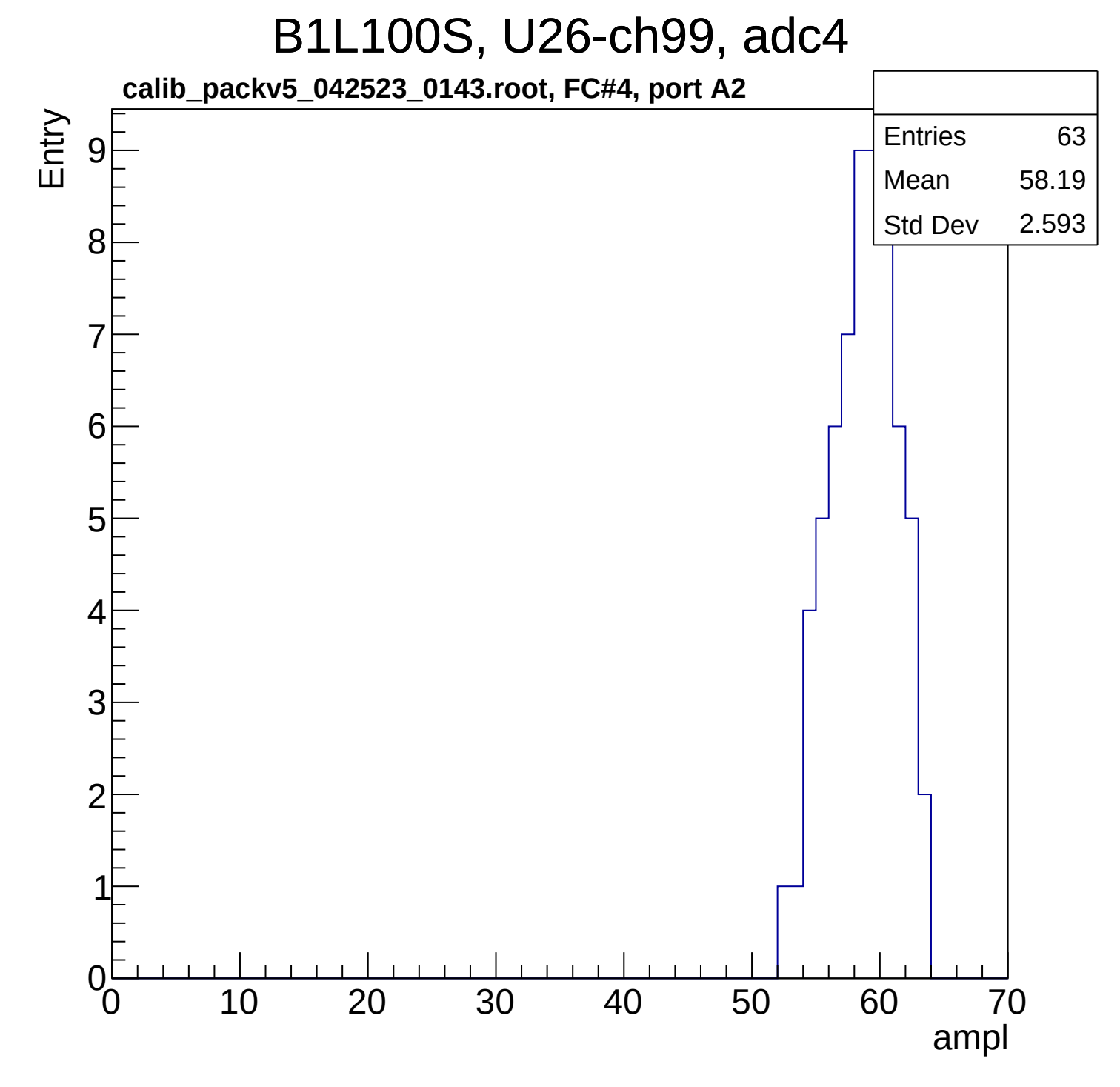
B1L100S, U26-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 58.19
Std Dev 2.593
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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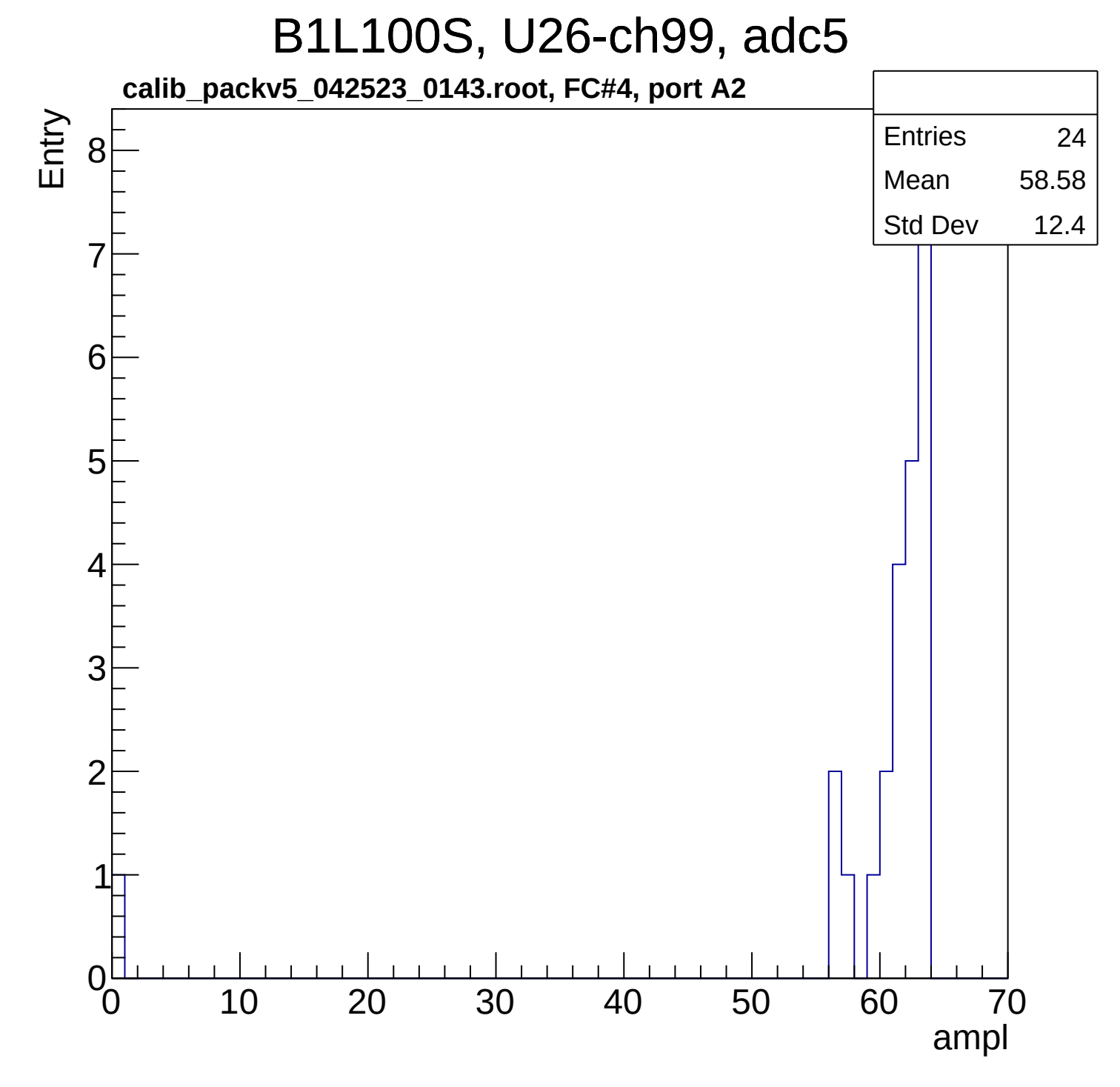
60
70


B1L100S, U26-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 24
Mean 58.58
Std Dev 12.4
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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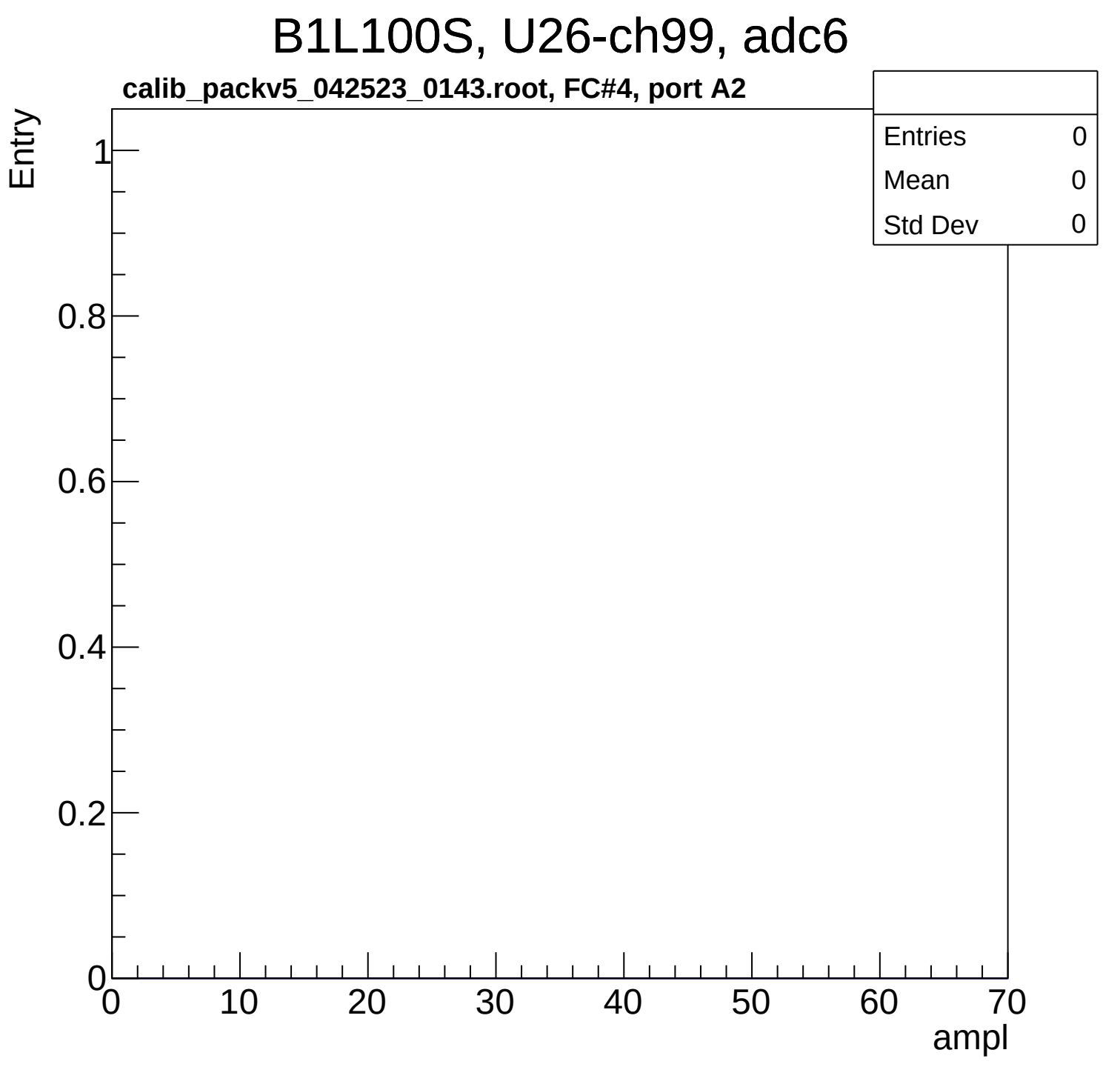
B1L100S, U26-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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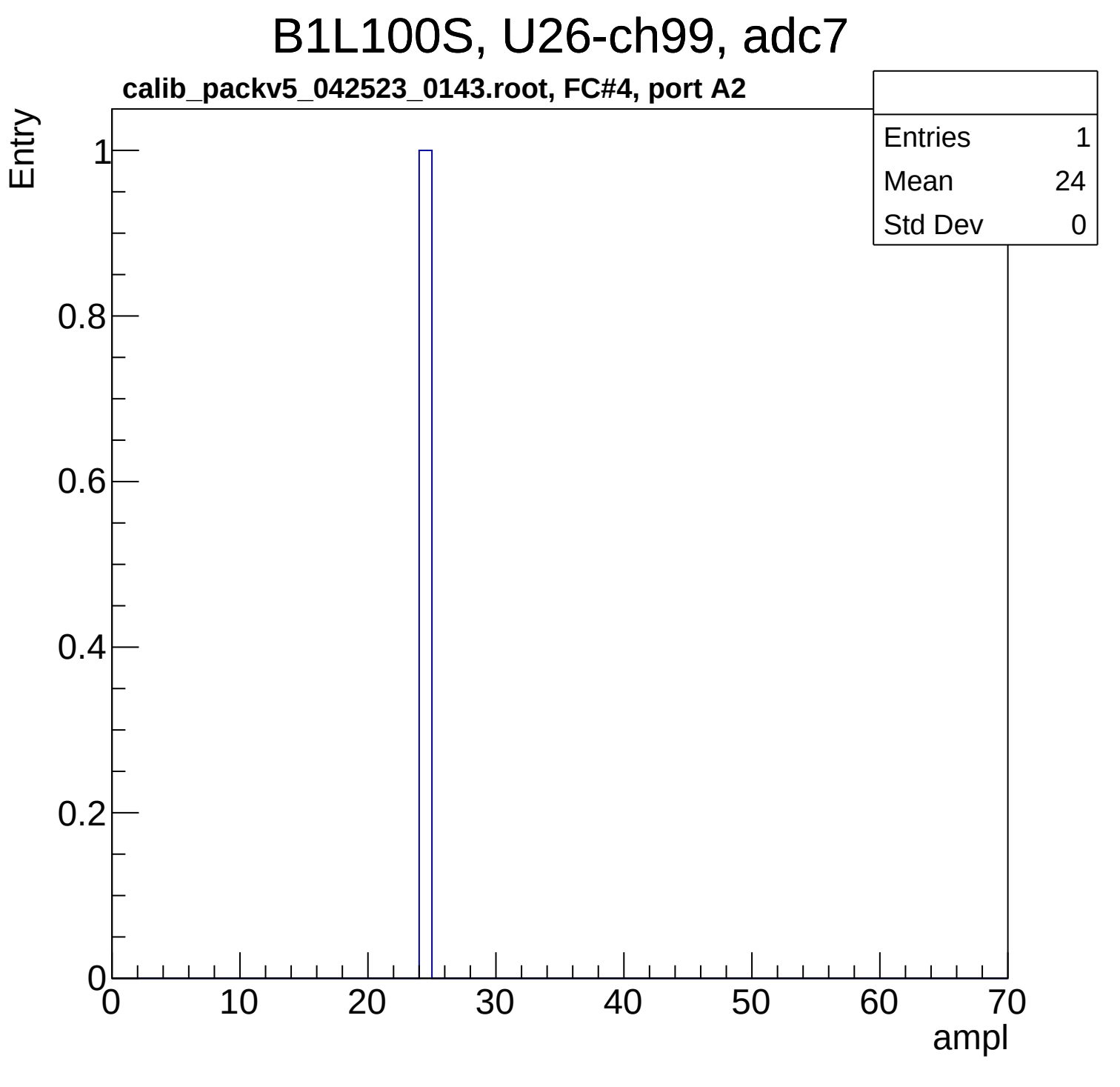
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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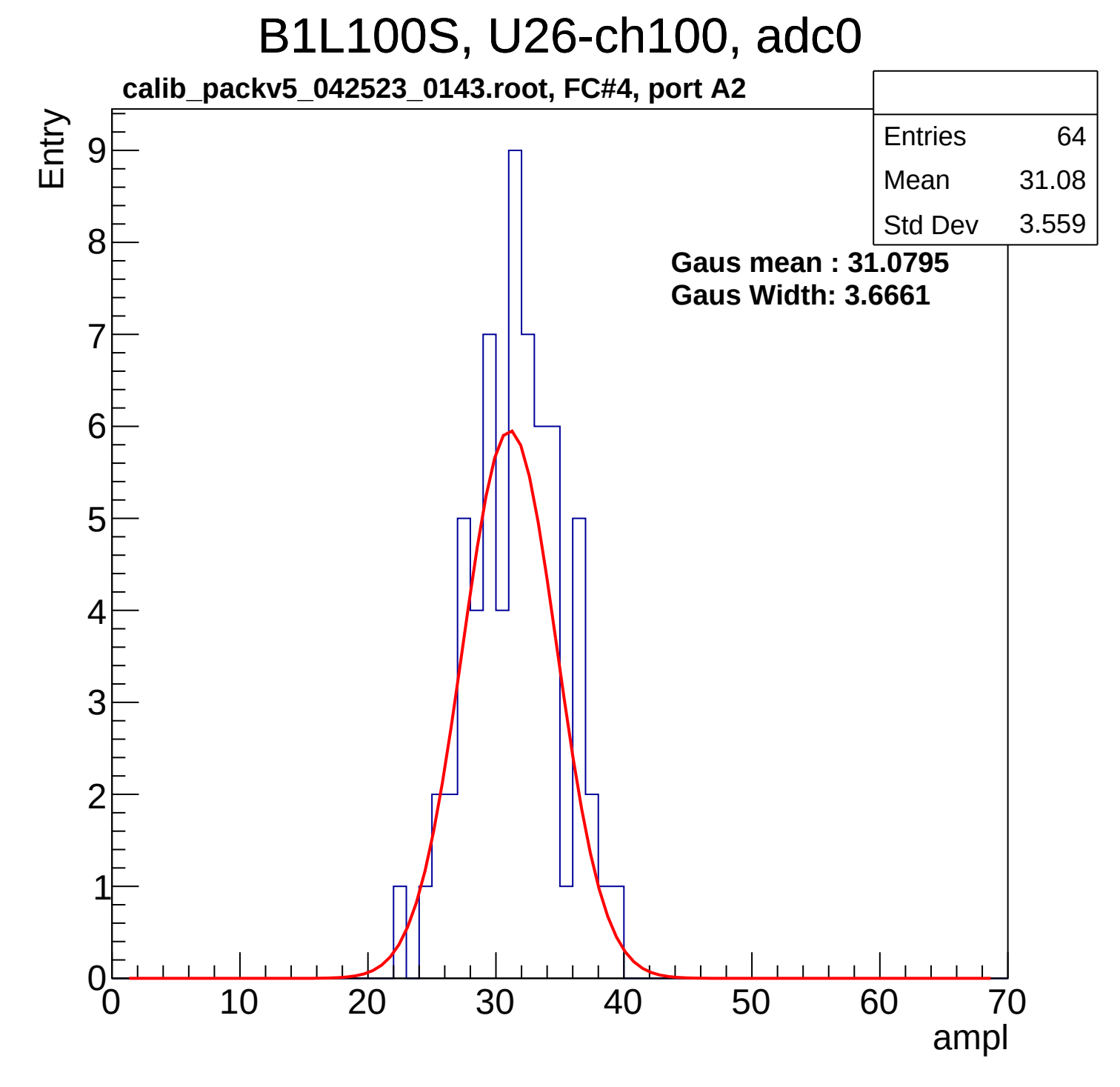
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch100, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 31.08
Std Dev 3.559
Gaus mean : 31.0795
Gaus Width: 3.6661
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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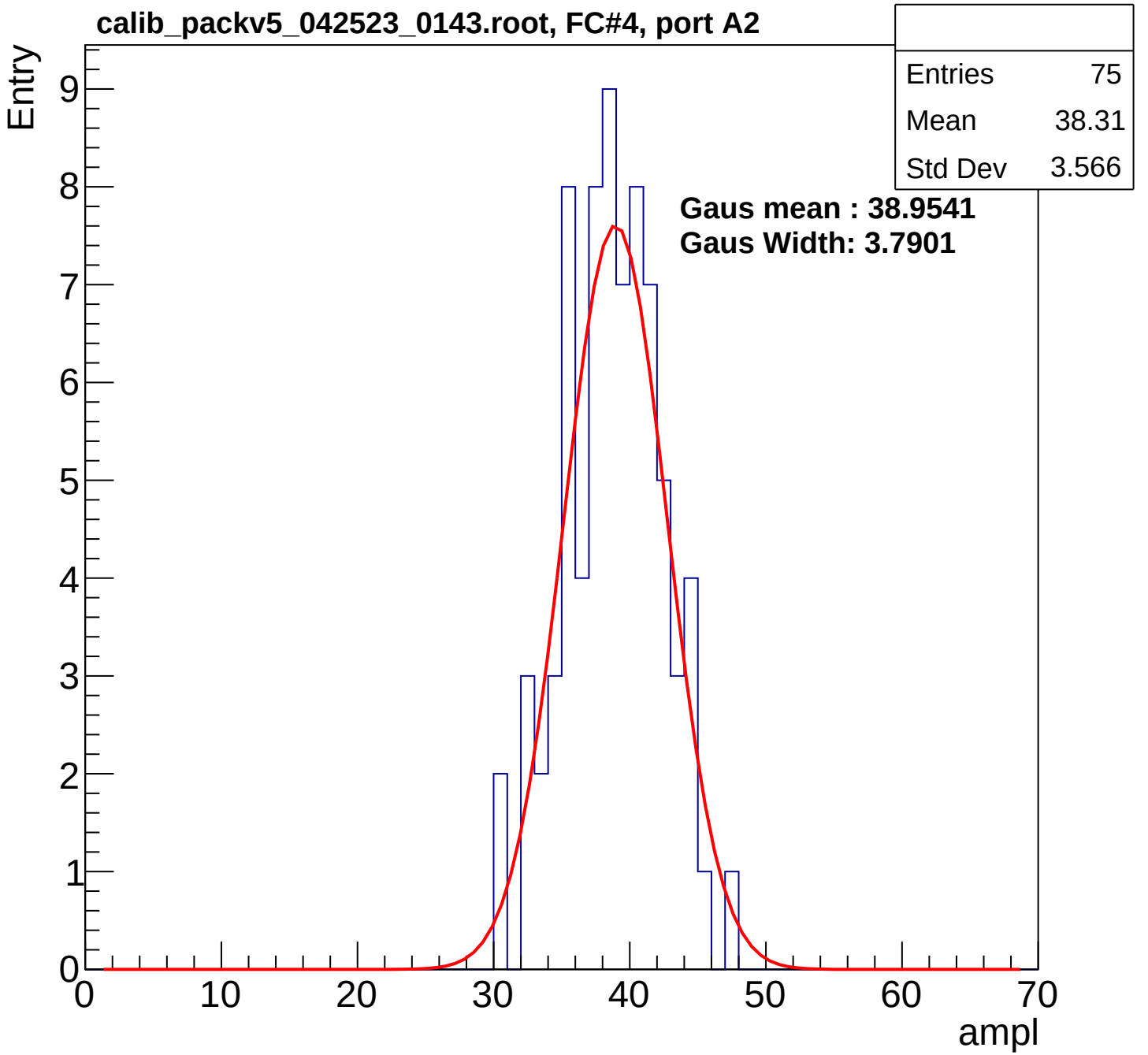
B1L100S, U26-ch100, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 38.31
Std Dev 3.566
Gaus mean : 38.9541
Gaus Width: 3.7901
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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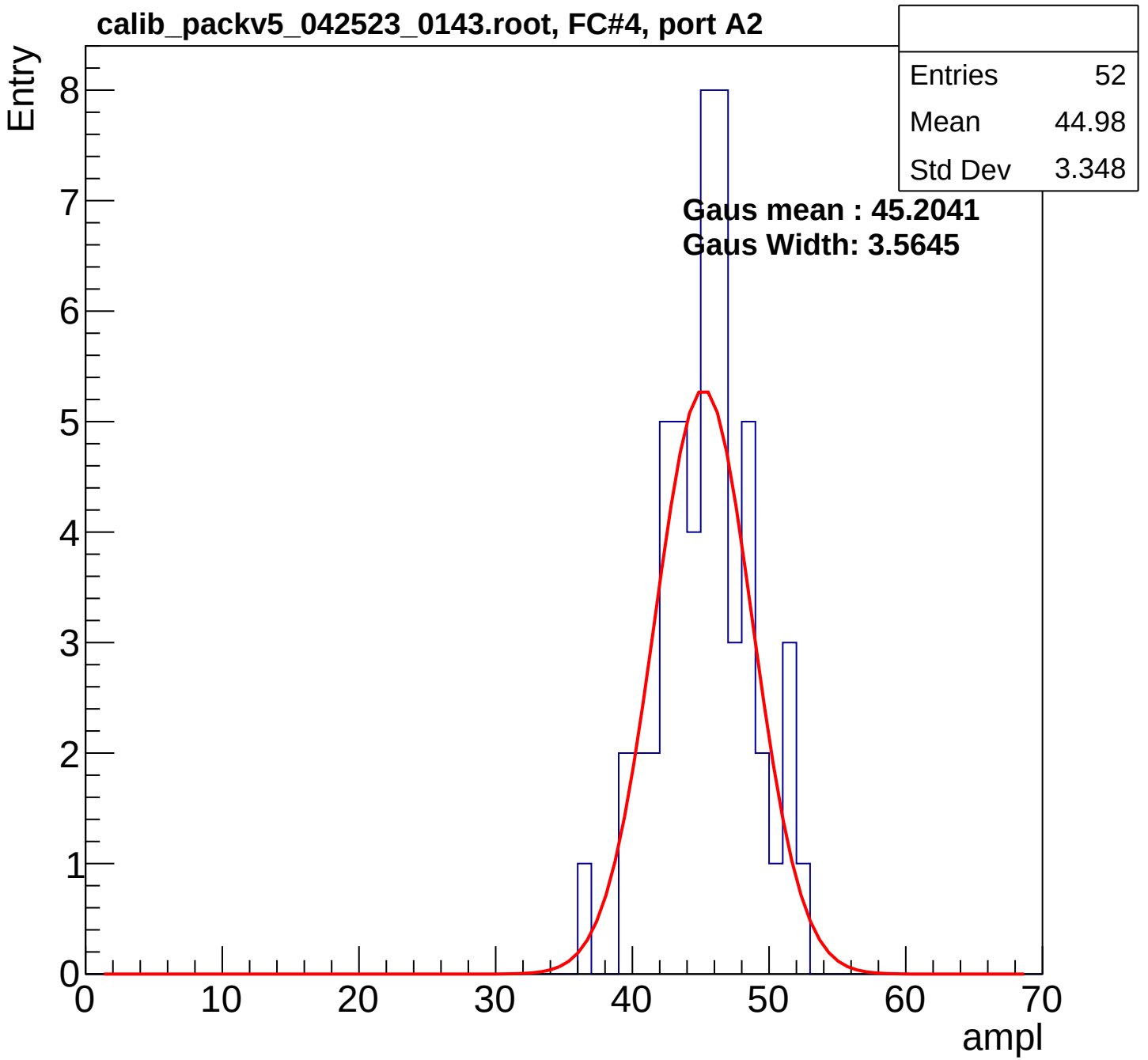
B1L100S, U26-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 44.98
Std Dev 3.348
Gaus mean : 45.2041
Gaus Width: 3.5645
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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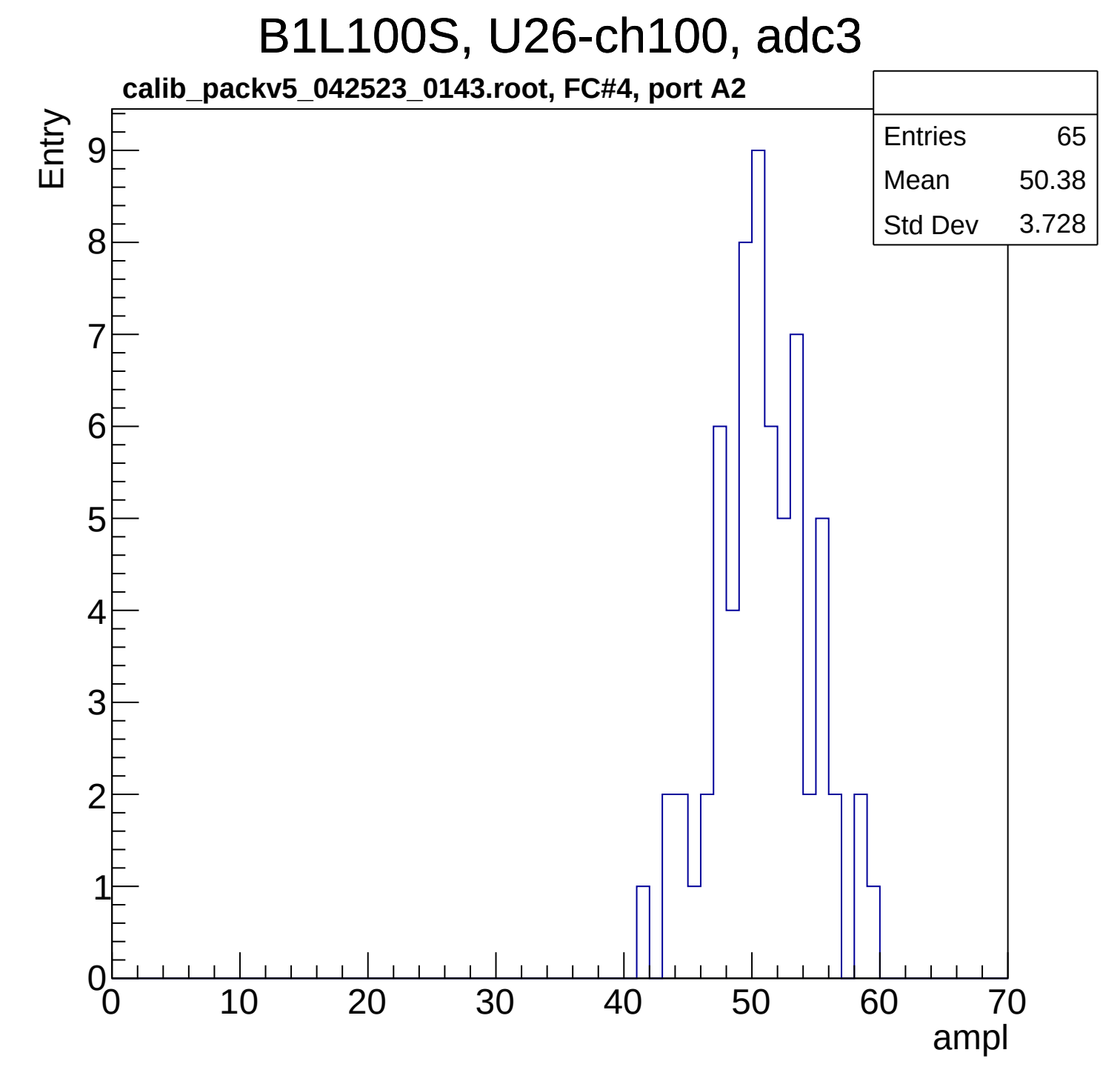
B1L100S, U26-ch100, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.38
Std Dev 3.728
Entry
ampl
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7
8
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0
10
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40
50
60
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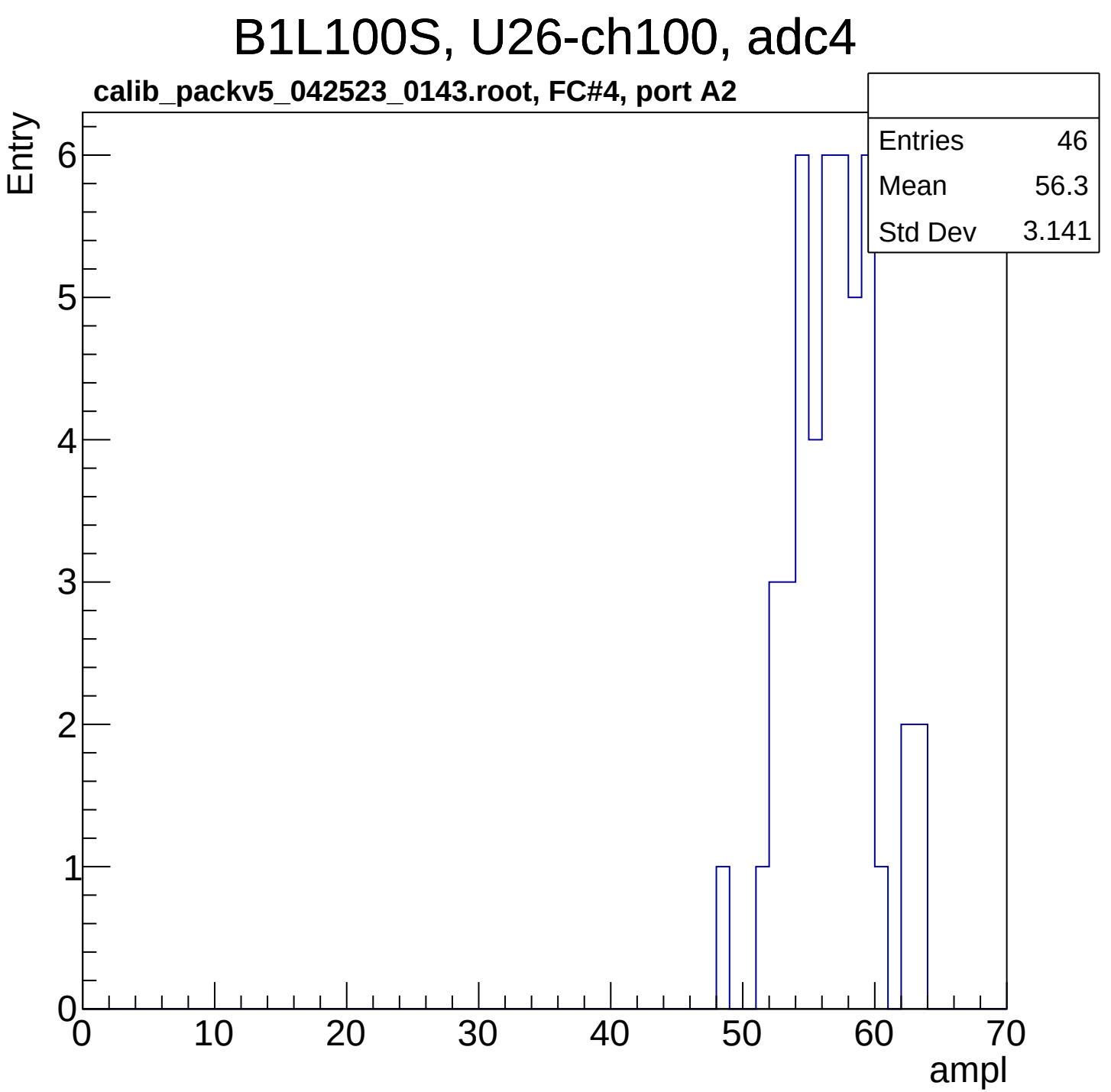
B1L100S, U26-ch100, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 56.3
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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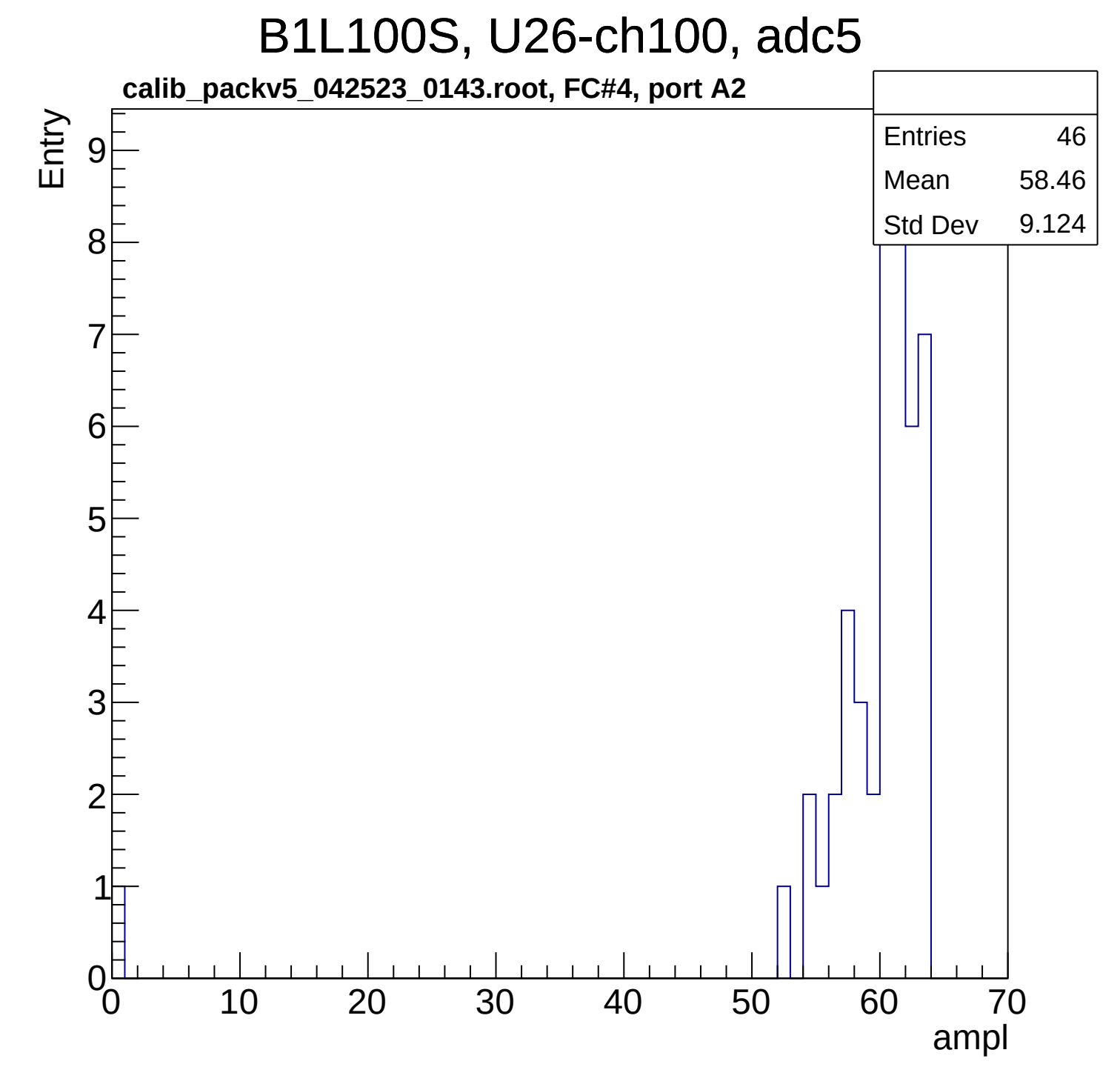
40
50
60
70


B1L100S, U26-ch100, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.46
Std Dev 9.124
Entry
ampl
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1
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3
4
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0
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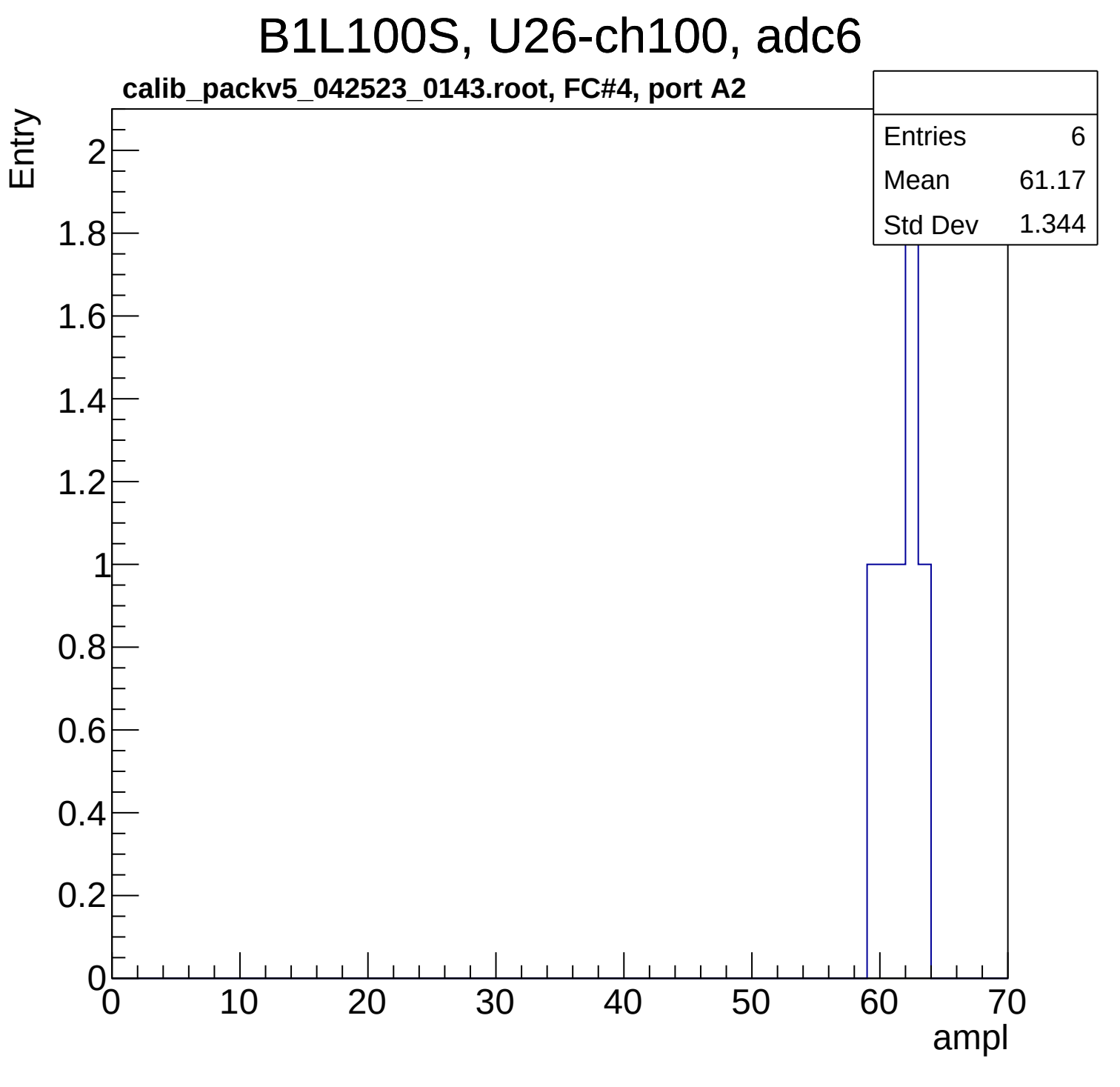
B1L100S, U26-ch100, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.17
Std Dev 1.344
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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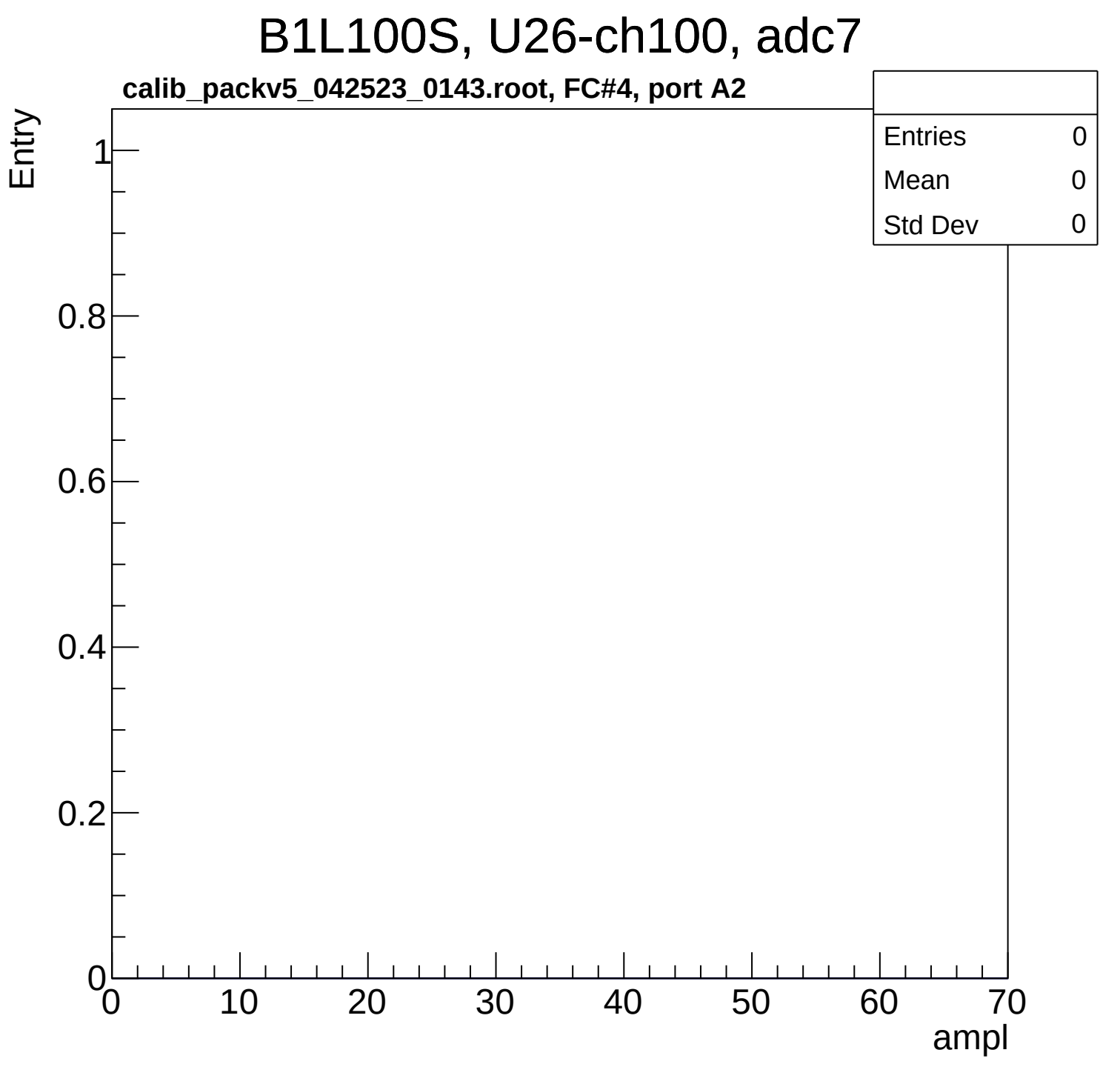
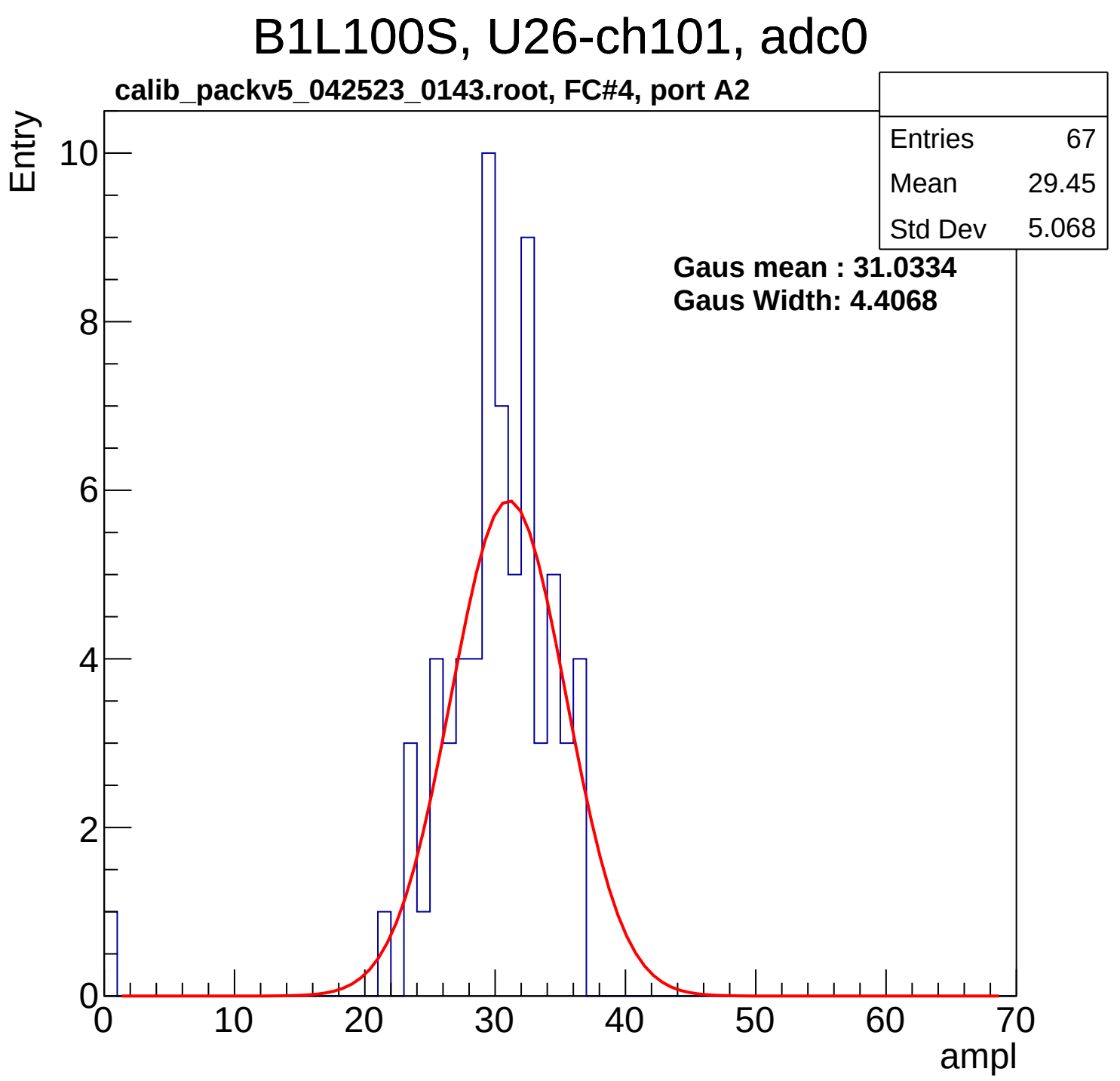

B1L100S, U26-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.45
Std Dev 5.068
Gaus mean : 31.0334
Gaus Width: 4.4068
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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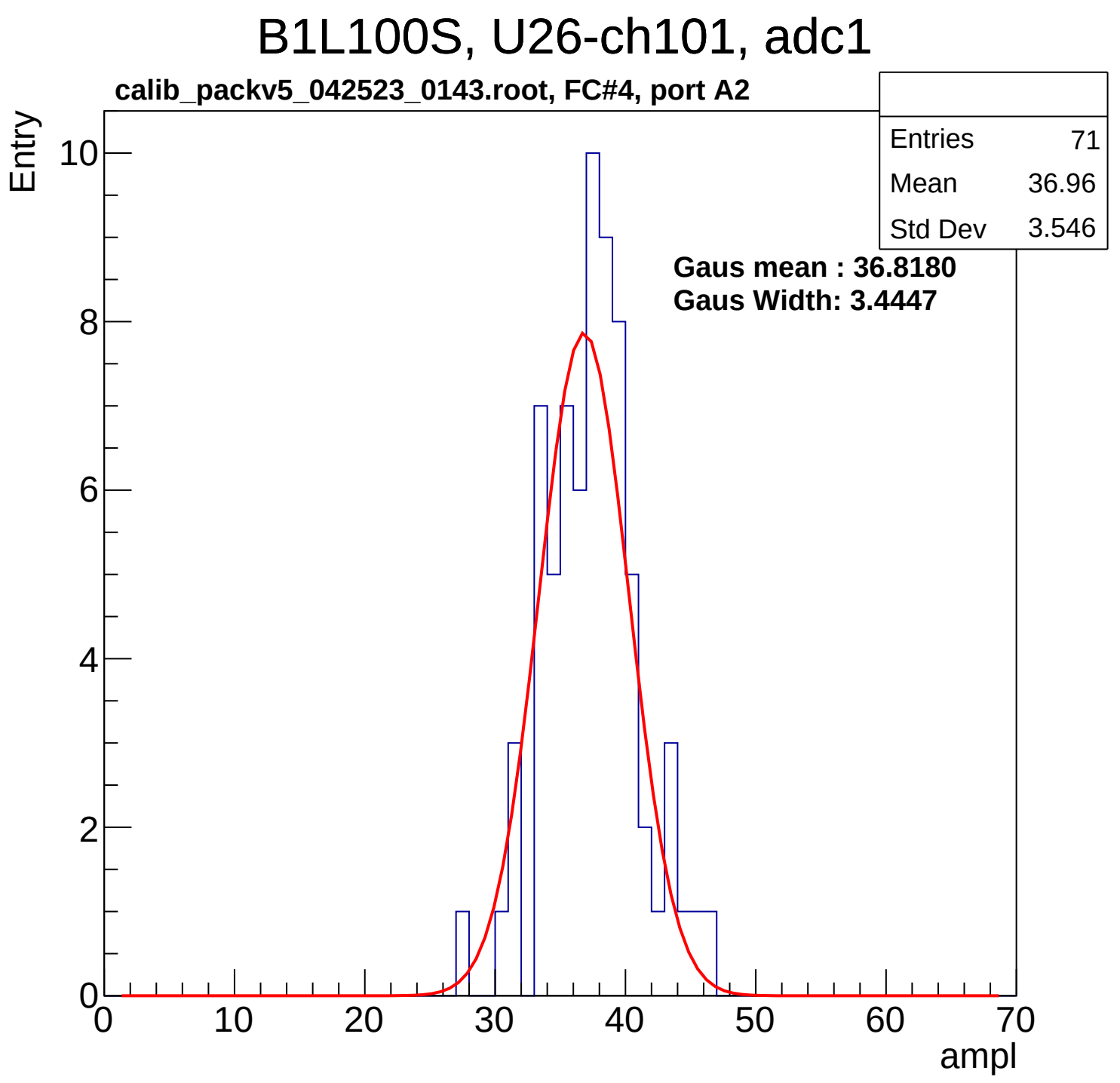
B1L100S, U26-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.96
Std Dev 3.546
Gaus mean : 36.8180
Gaus Width: 3.4447
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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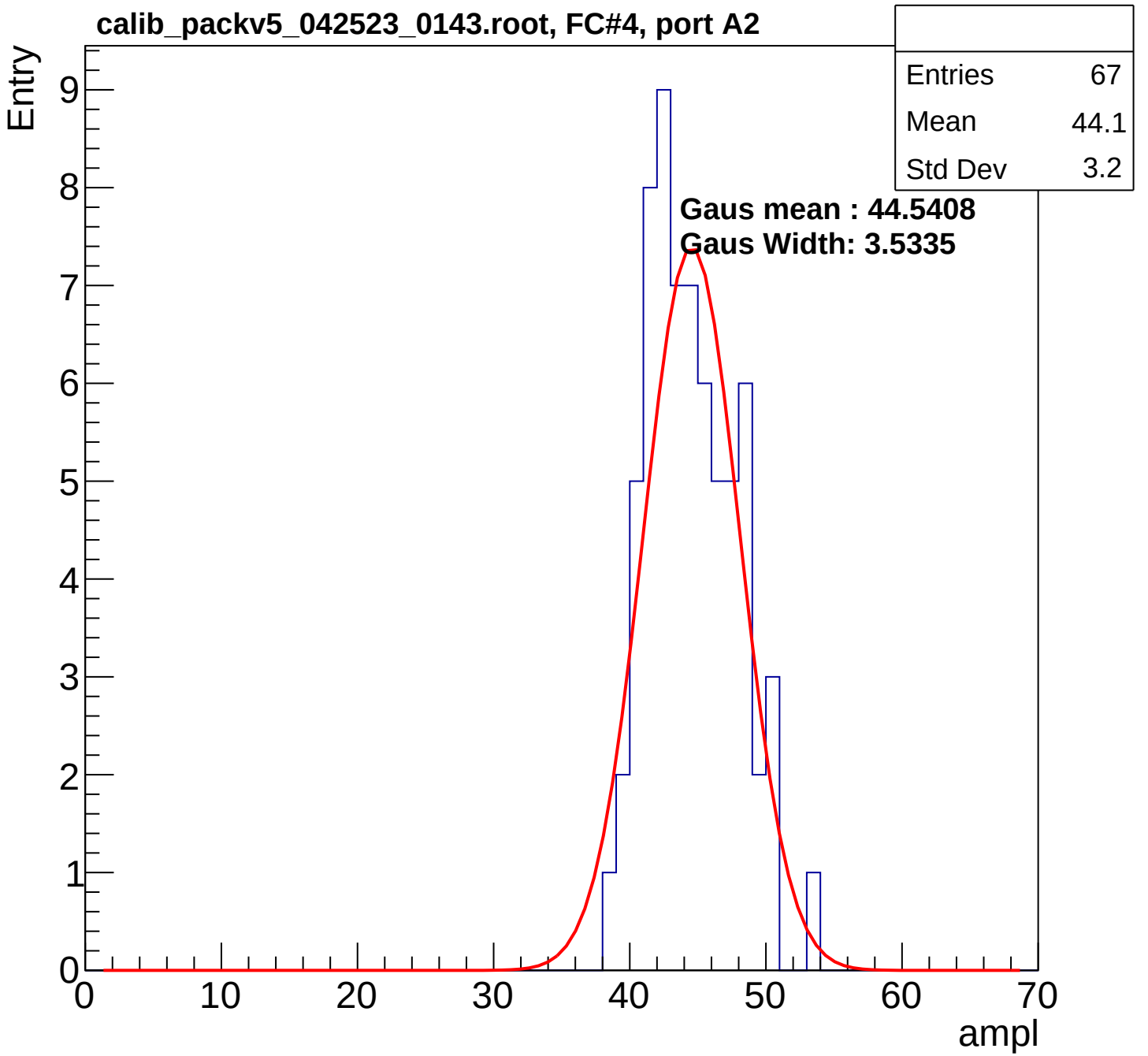
B1L100S, U26-ch101, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.1
Std Dev 3.2
Gaus mean : 44.5408
Gaus Width: 3.5335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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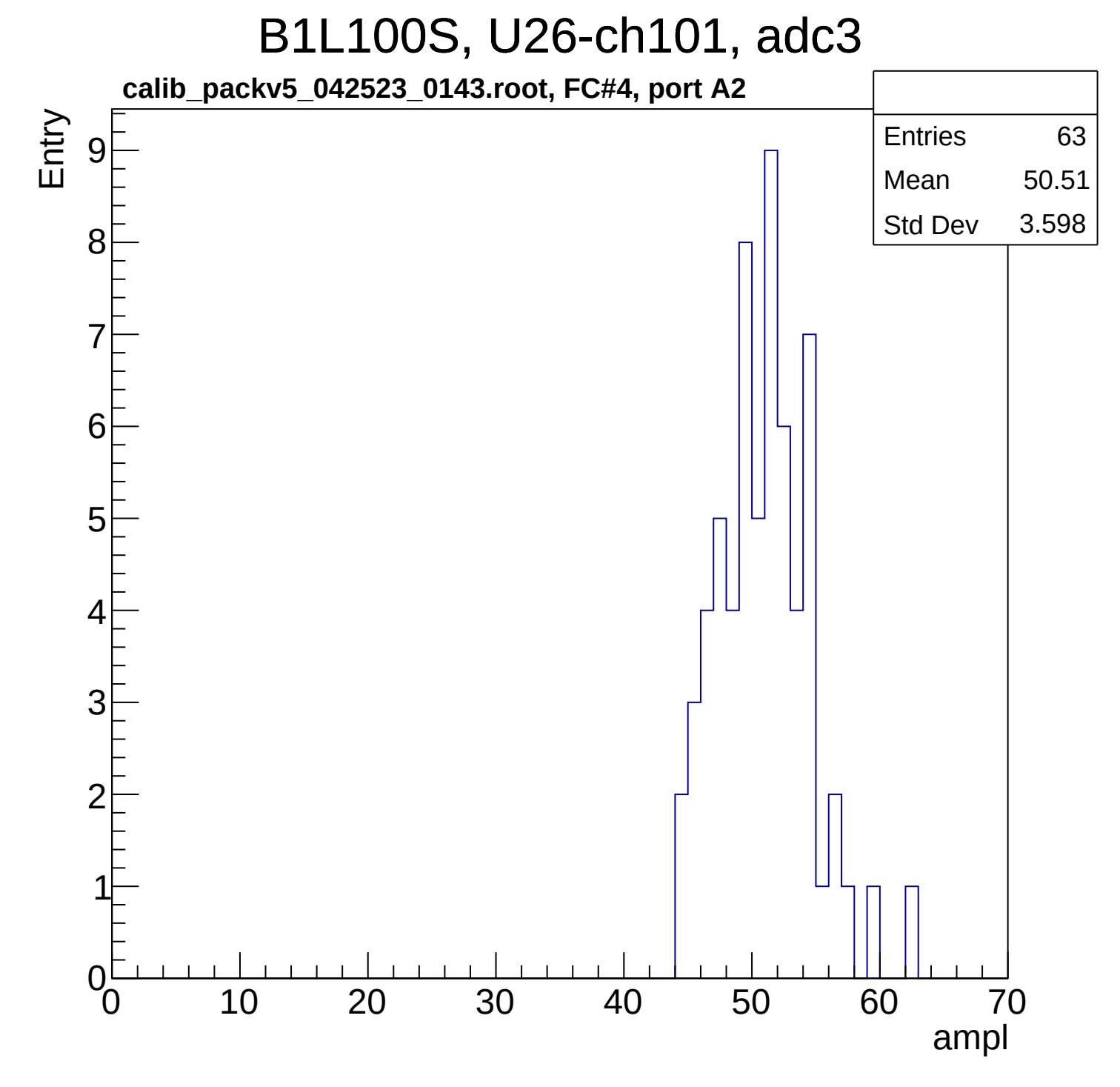
B1L100S, U26-ch101, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.51
Std Dev 3.598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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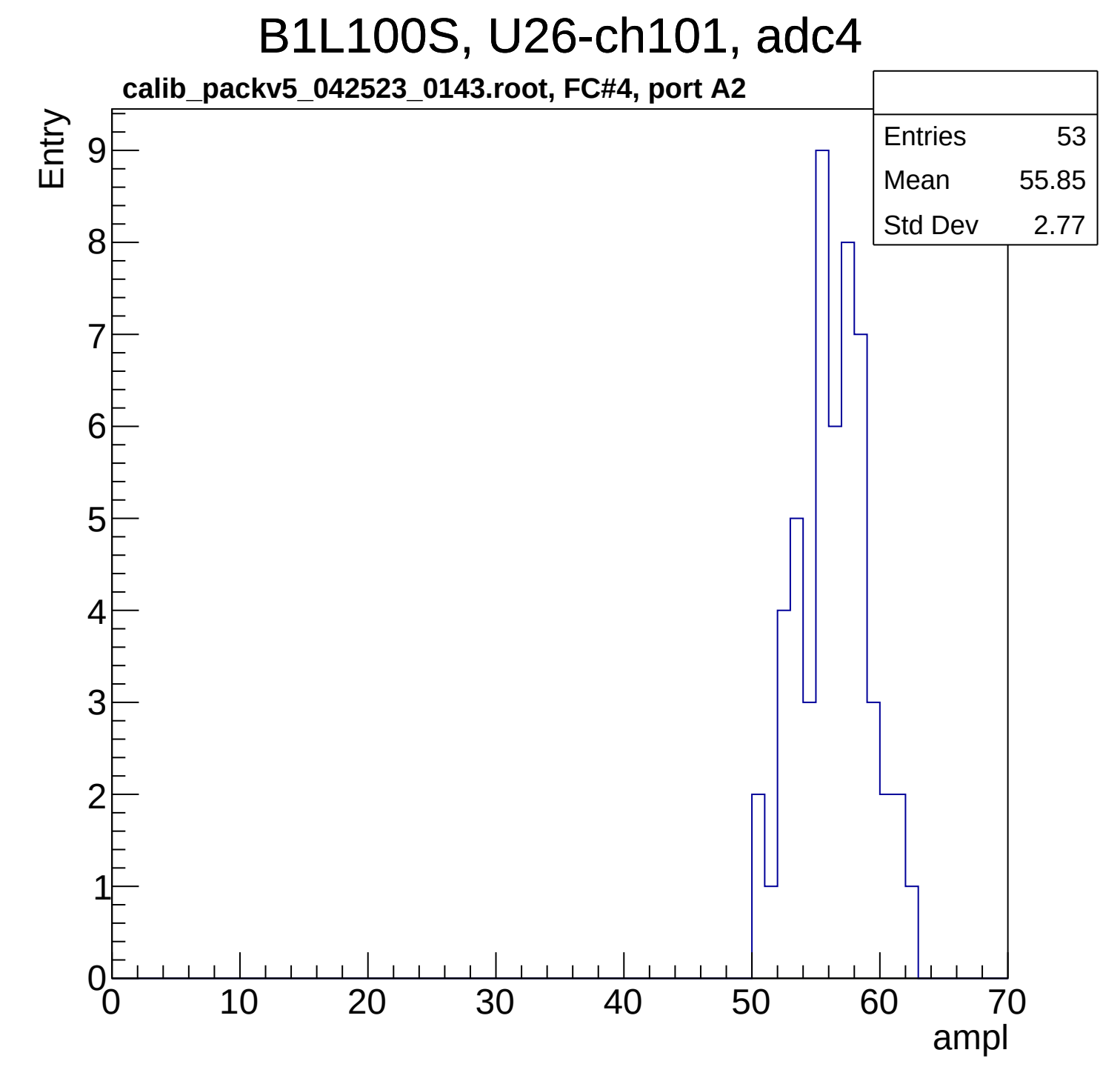
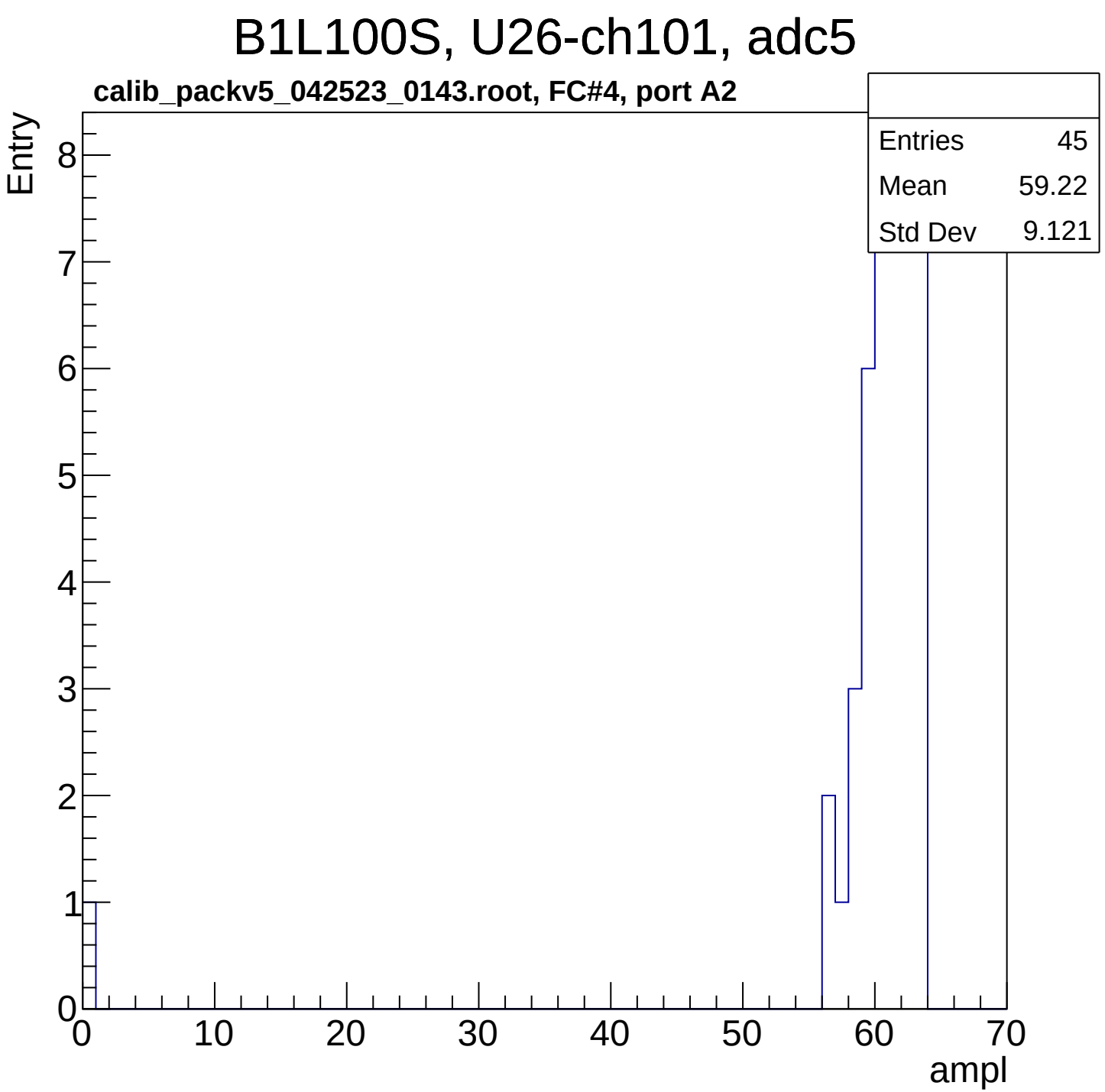

B1L100S, U26-ch101, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.85
Std Dev 2.77
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.22
Std Dev 9.121
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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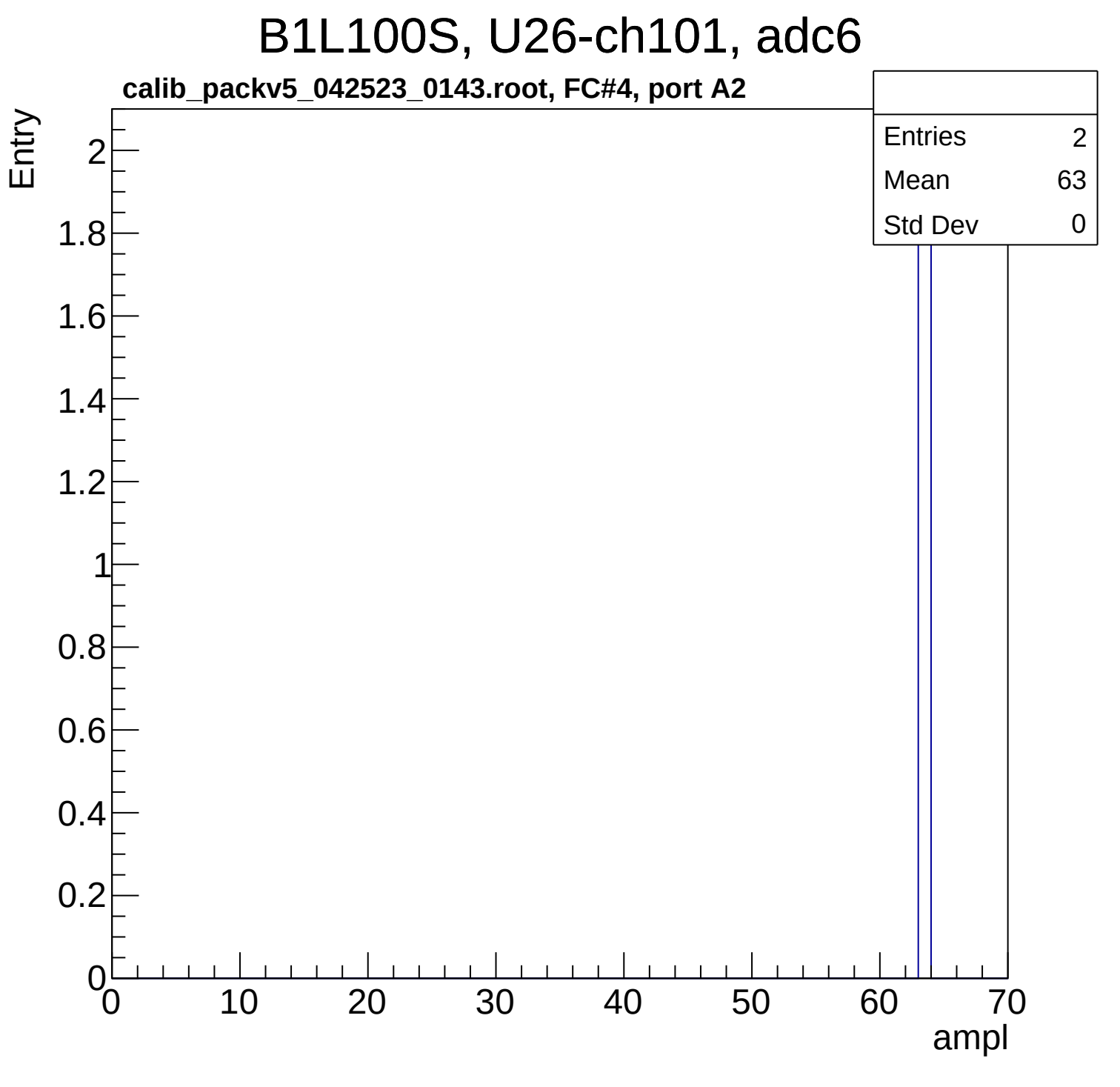
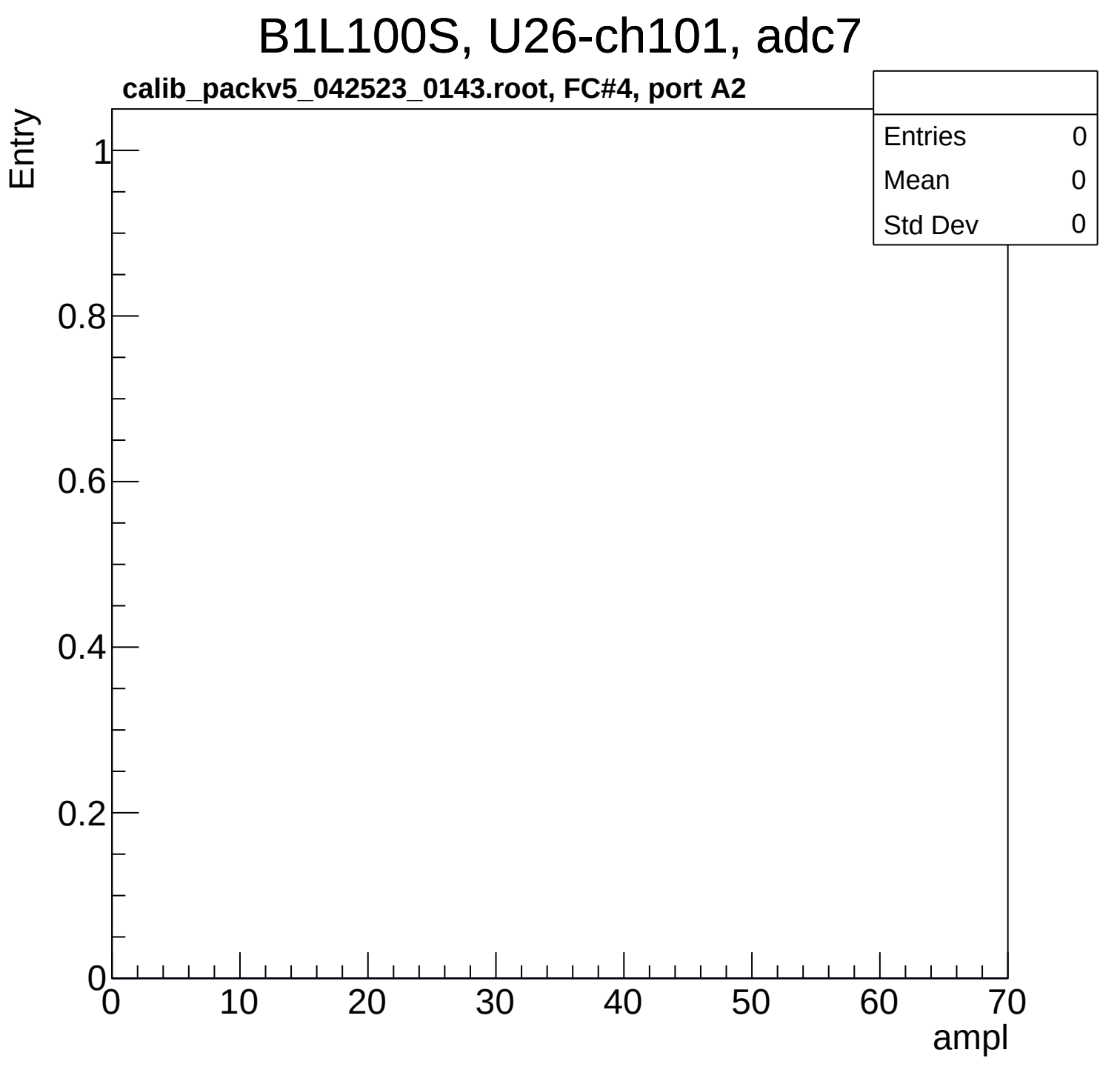
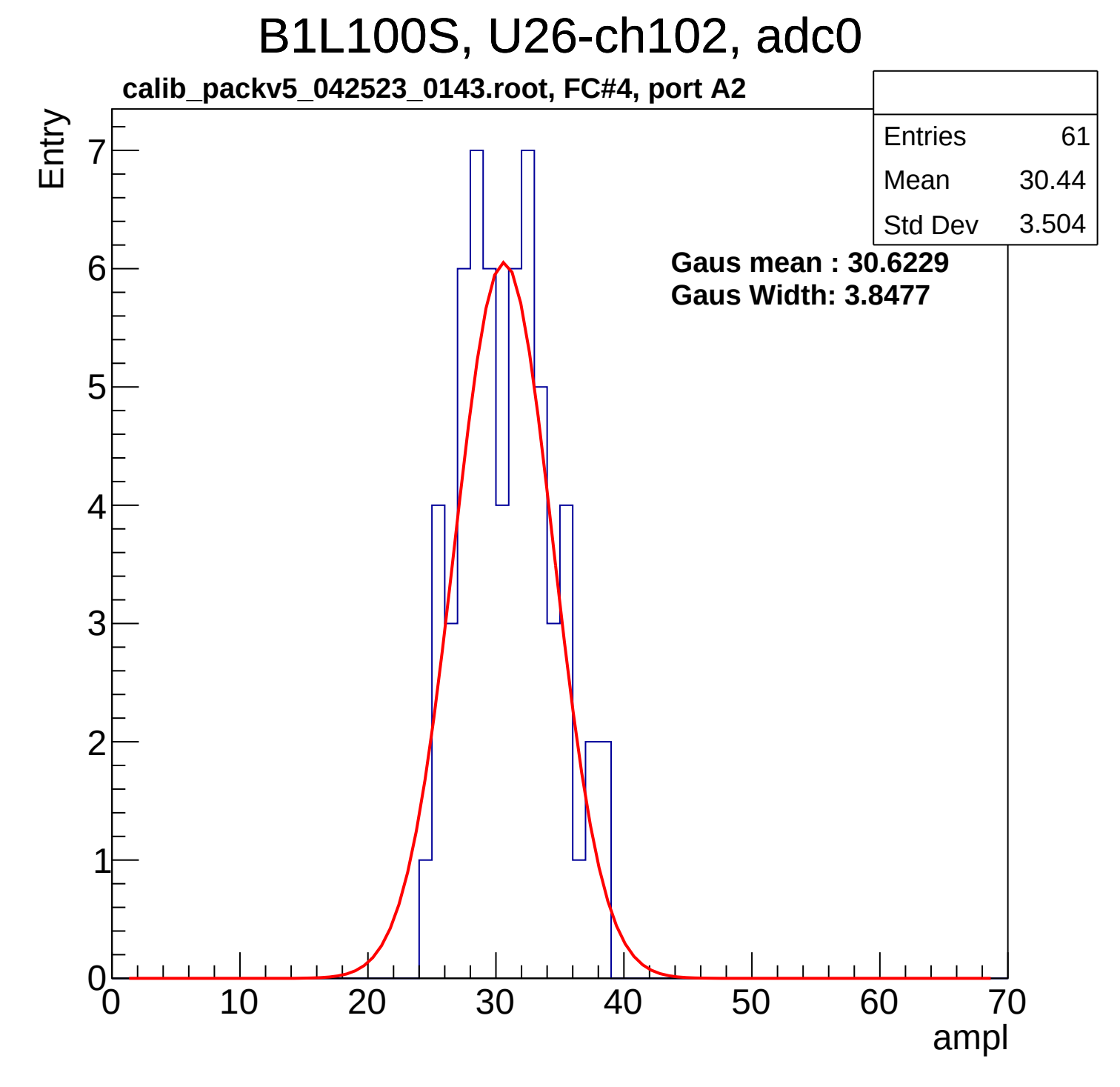
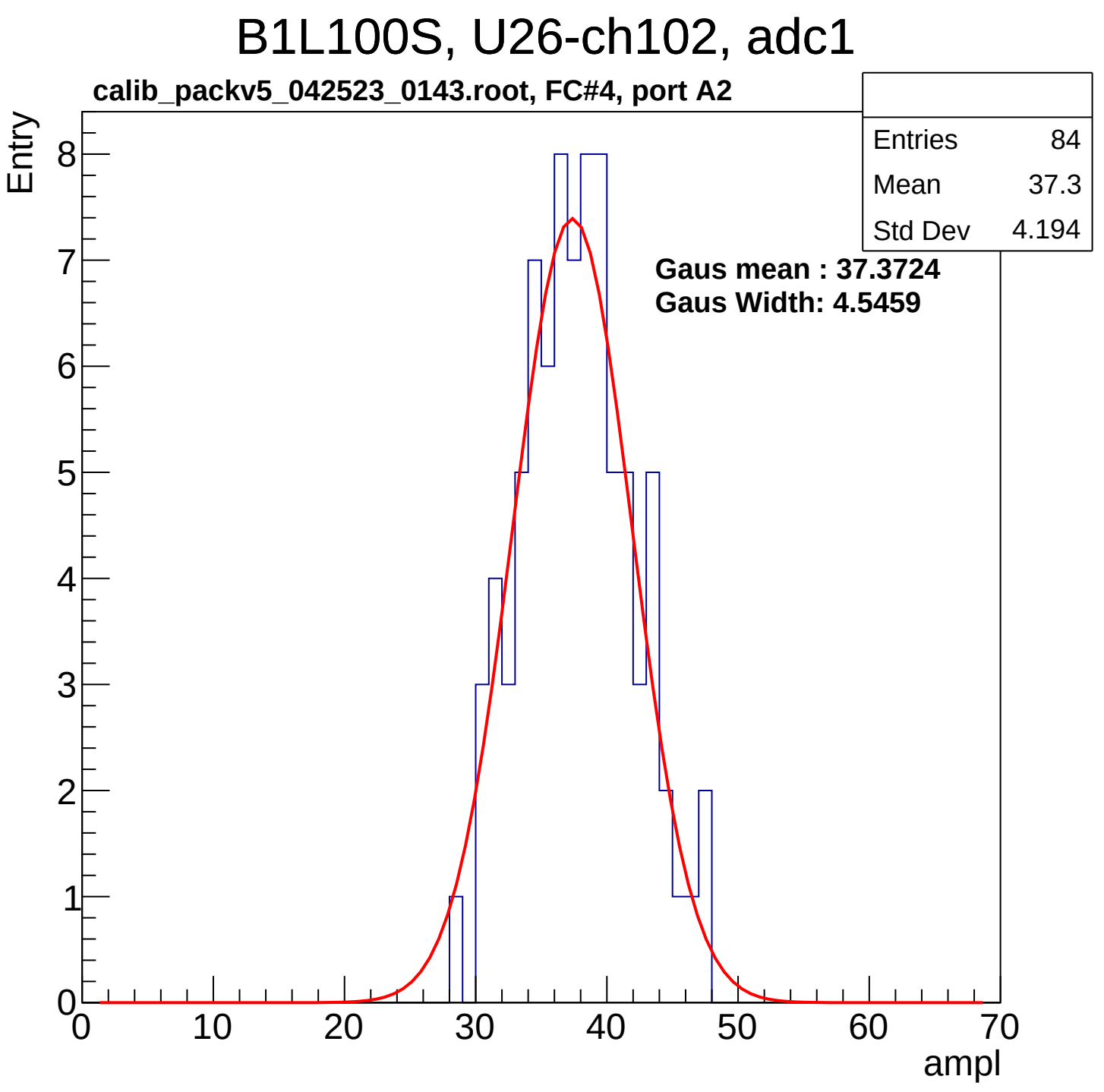

B1L100S, U26-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.44
Std Dev 3.504
Gaus mean : 30.6229
Gaus Width: 3.8477
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.3
Std Dev 4.194
Gaus mean : 37.3724
Gaus Width: 4.5459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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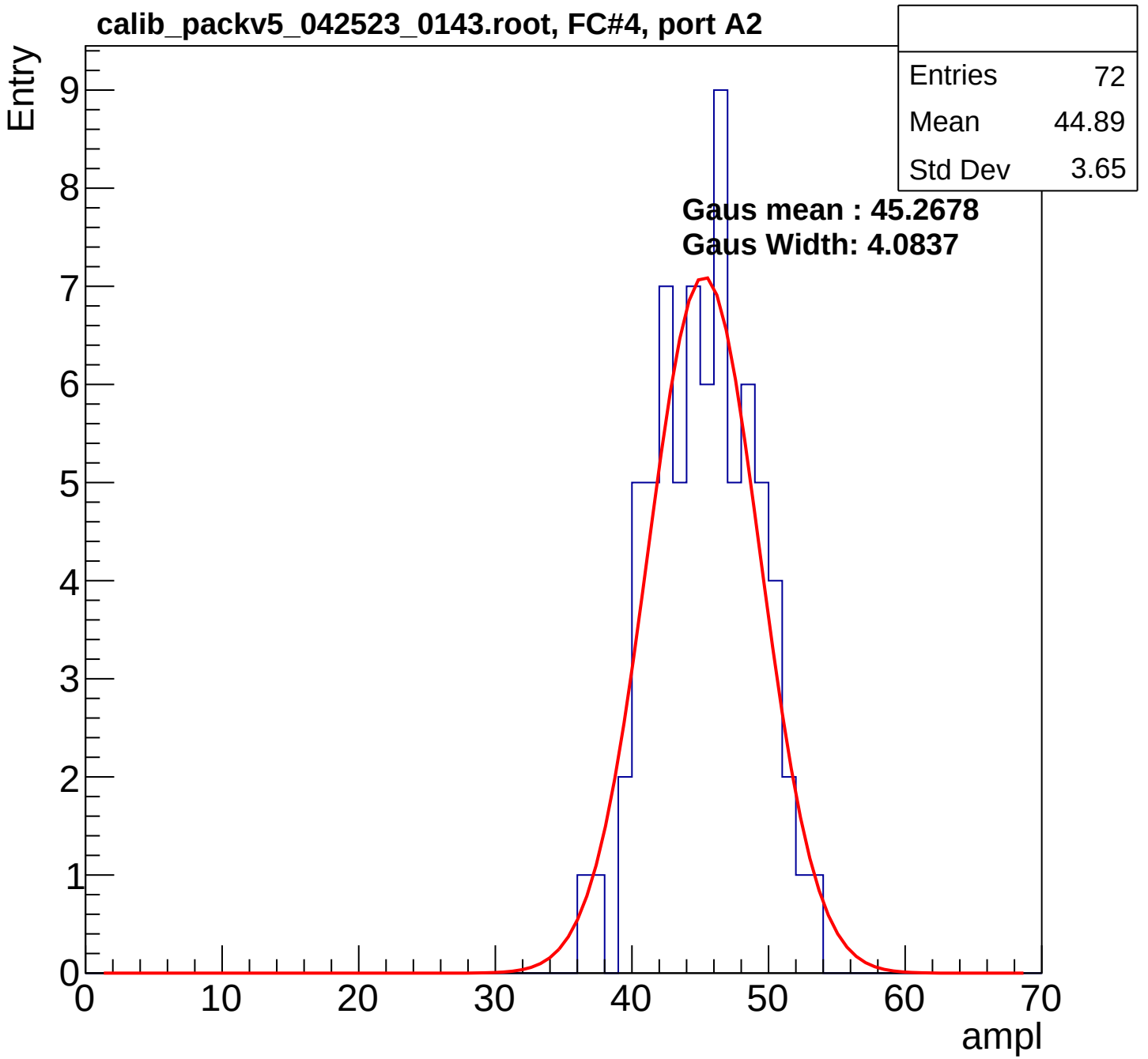
B1L100S, U26-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.89
Std Dev 3.65
Gaus mean : 45.2678
Gaus Width: 4.0837
Entry
ampl
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2
3
4
5
6
7
8
9
0
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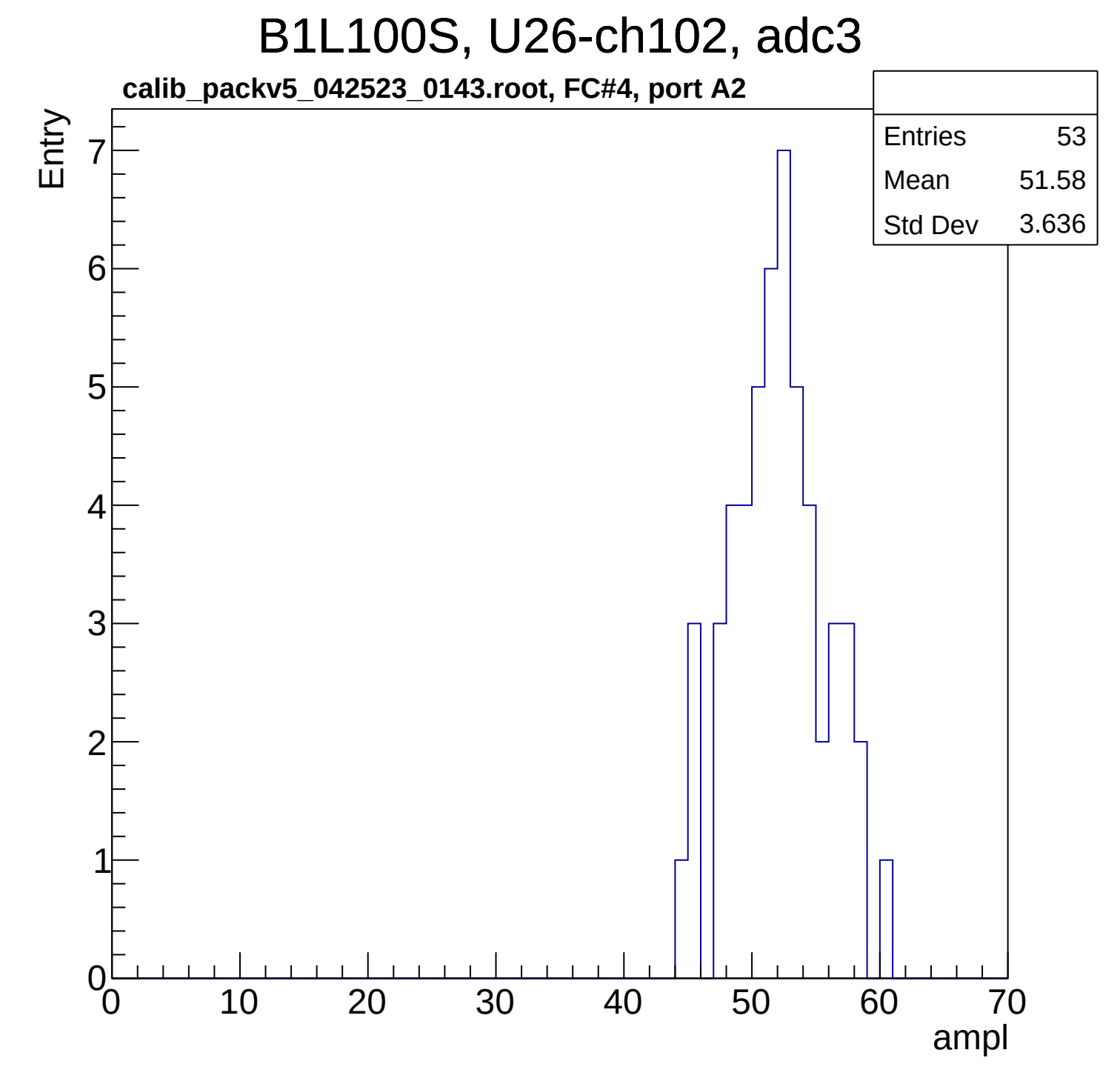
B1L100S, U26-ch102, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 51.58
Std Dev 3.636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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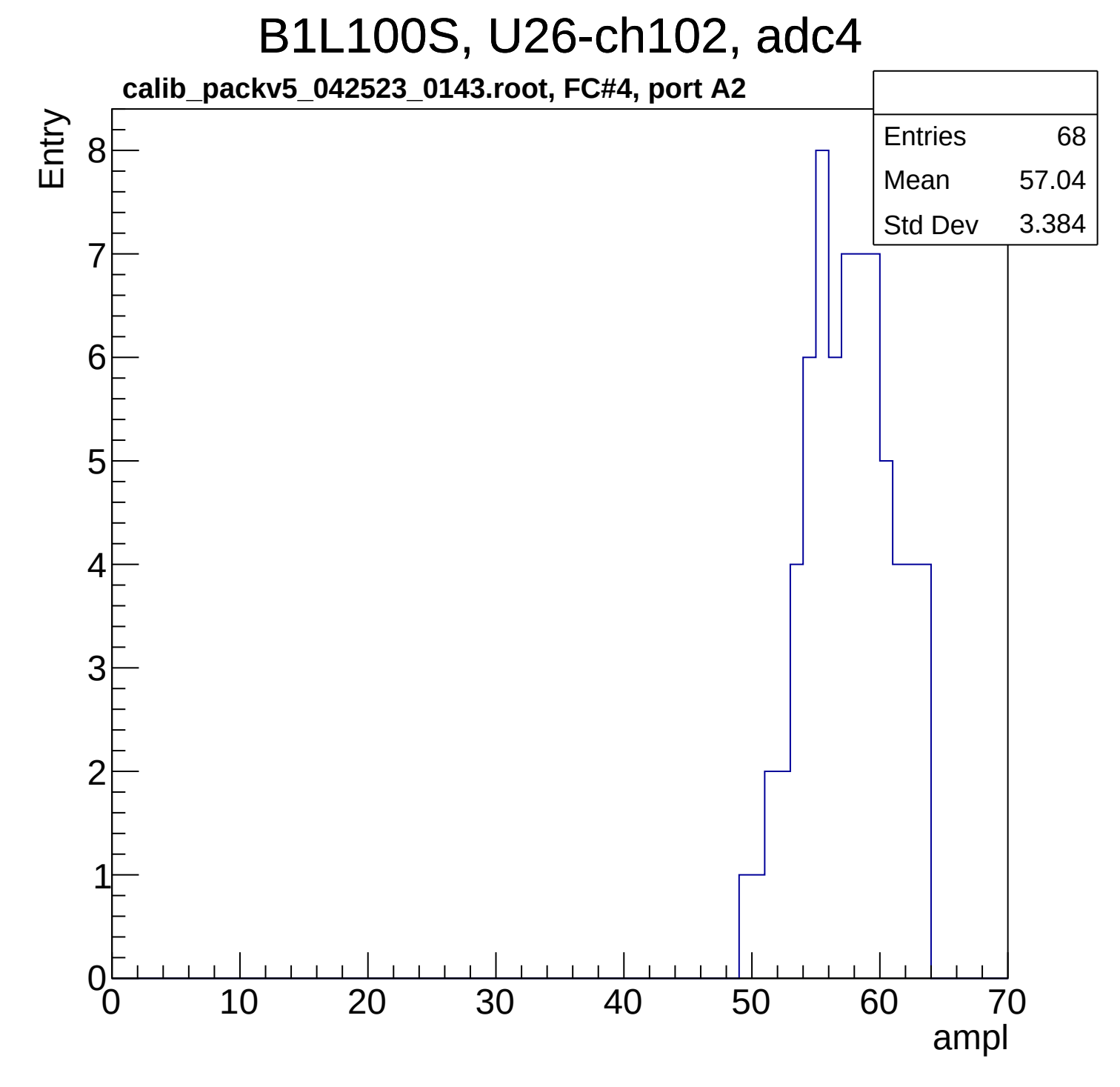
B1L100S, U26-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 57.04
Std Dev 3.384
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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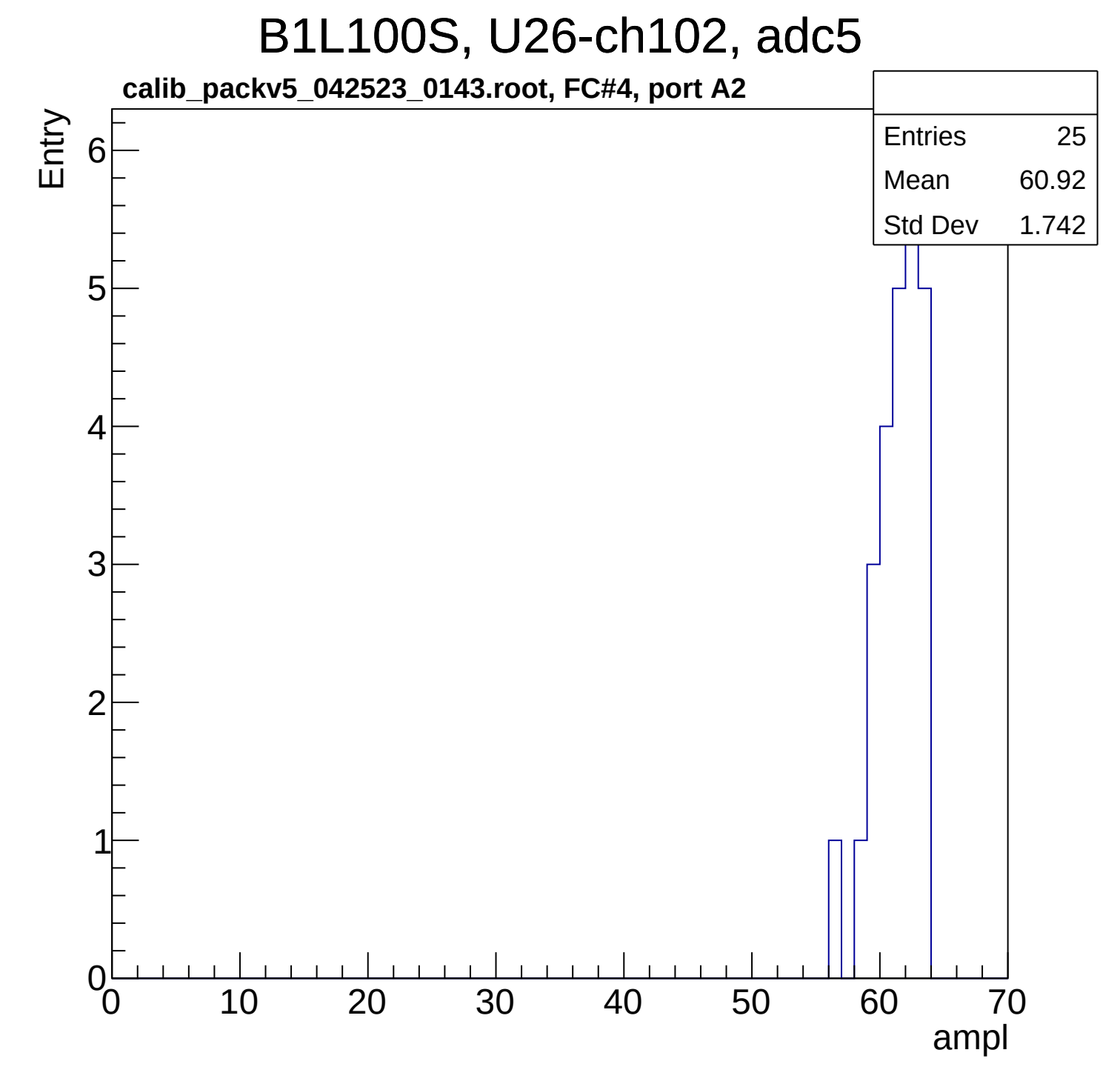
B1L100S, U26-ch102, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 60.92
Std Dev 1.742
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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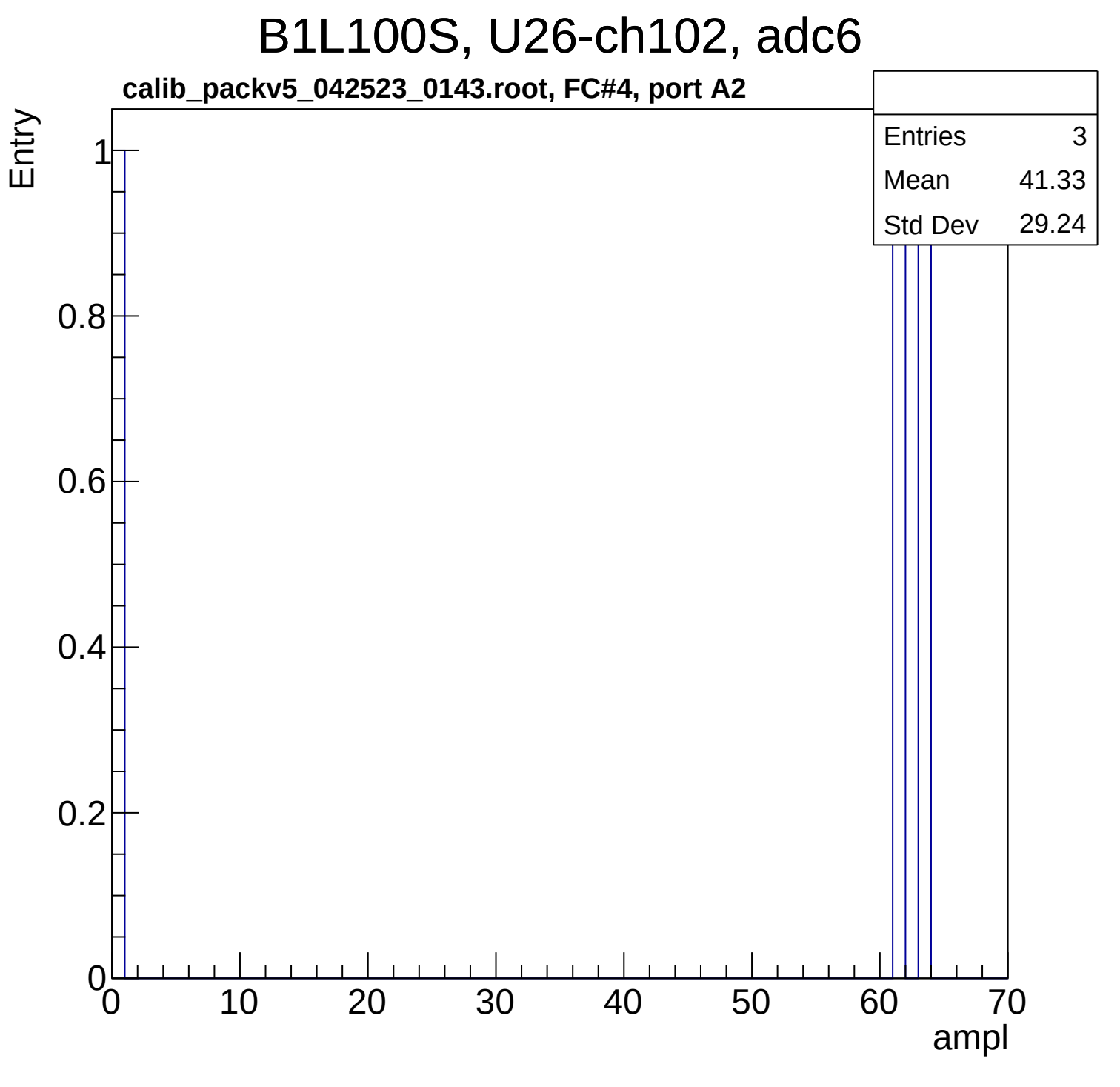
B1L100S, U26-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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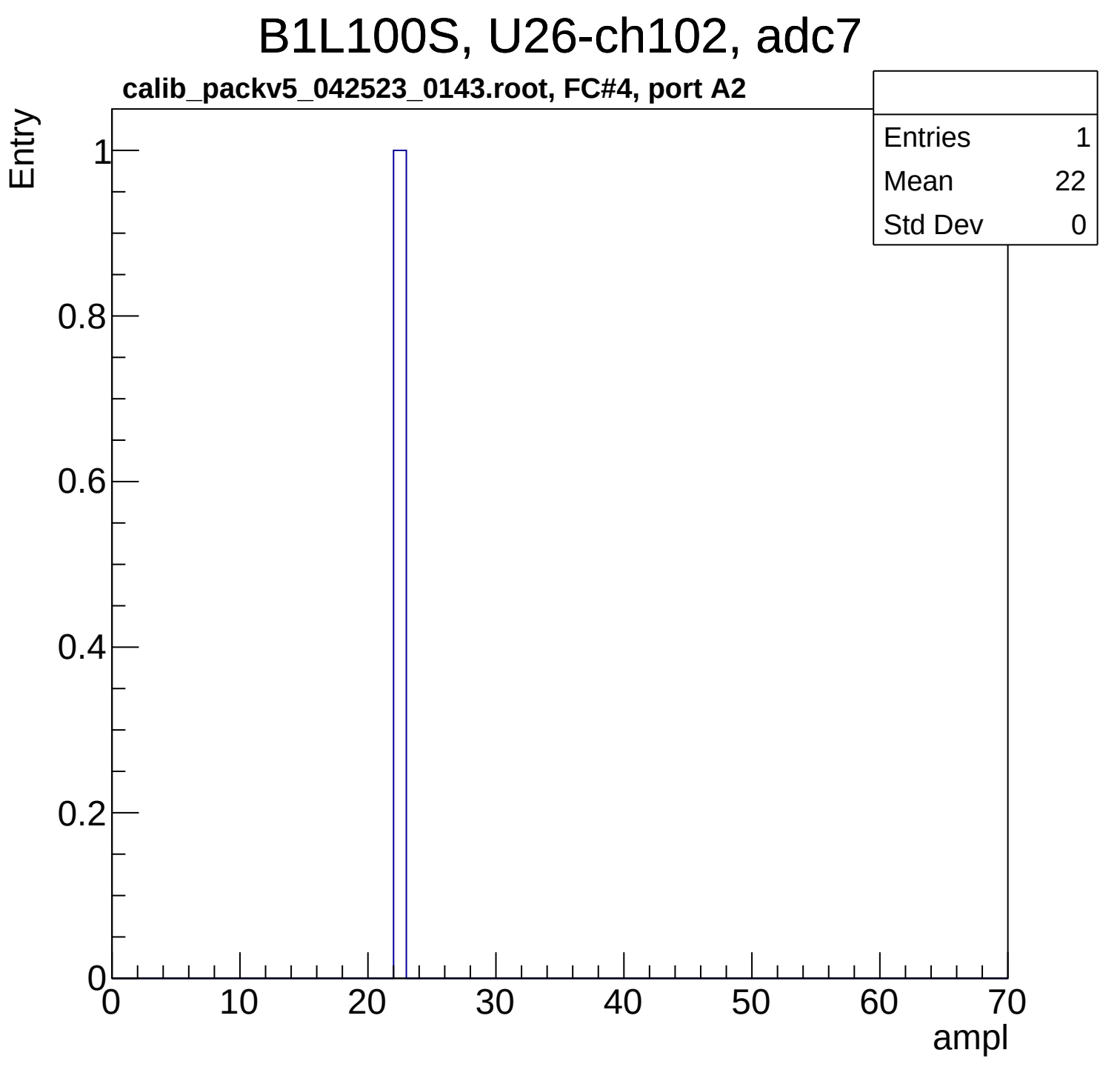
B1L100S, U26-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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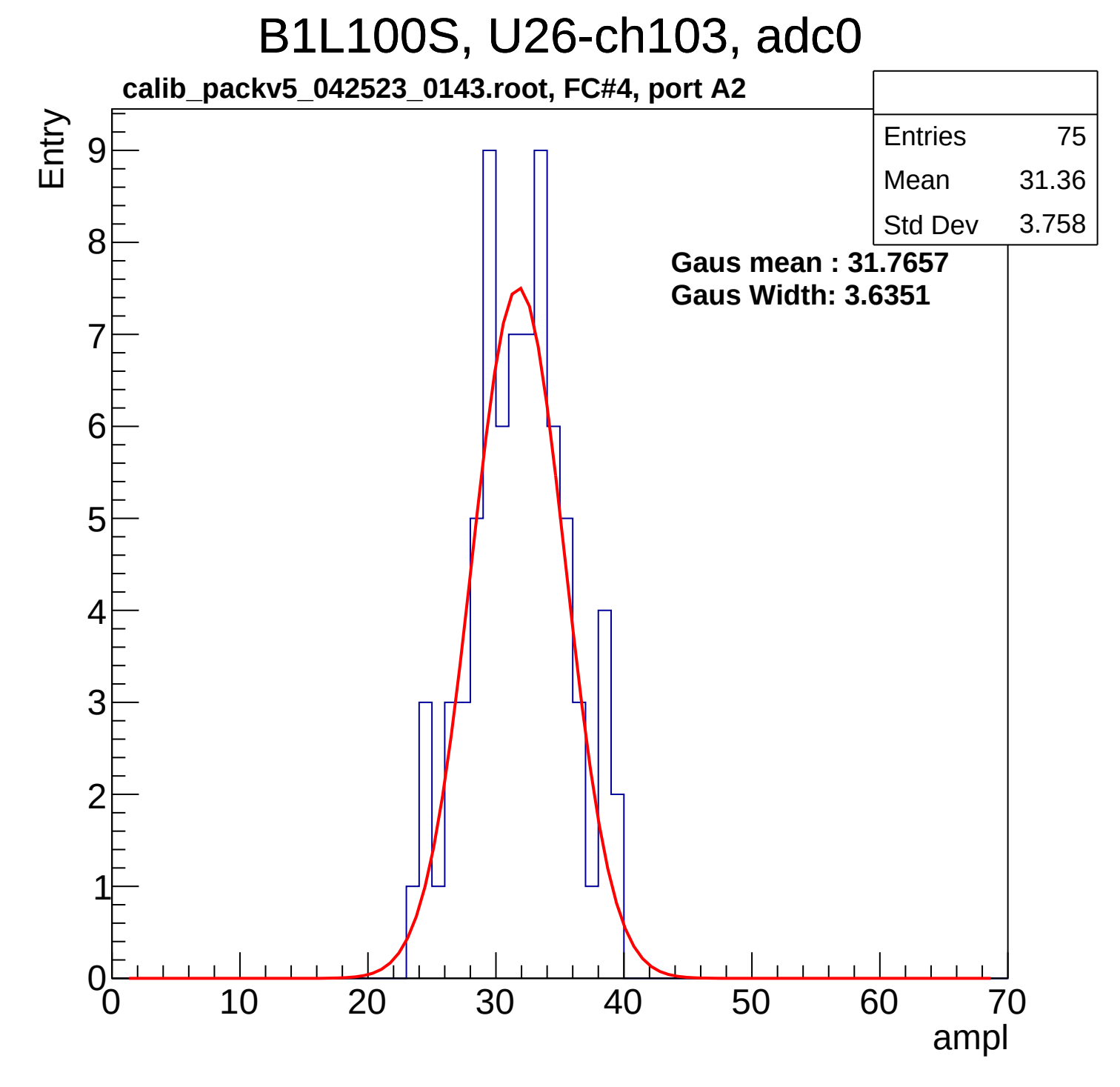
50
60
70
ampl


B1L100S, U26-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 31.36
Std Dev 3.758
Gaus mean : 31.7657
Gaus Width: 3.6351
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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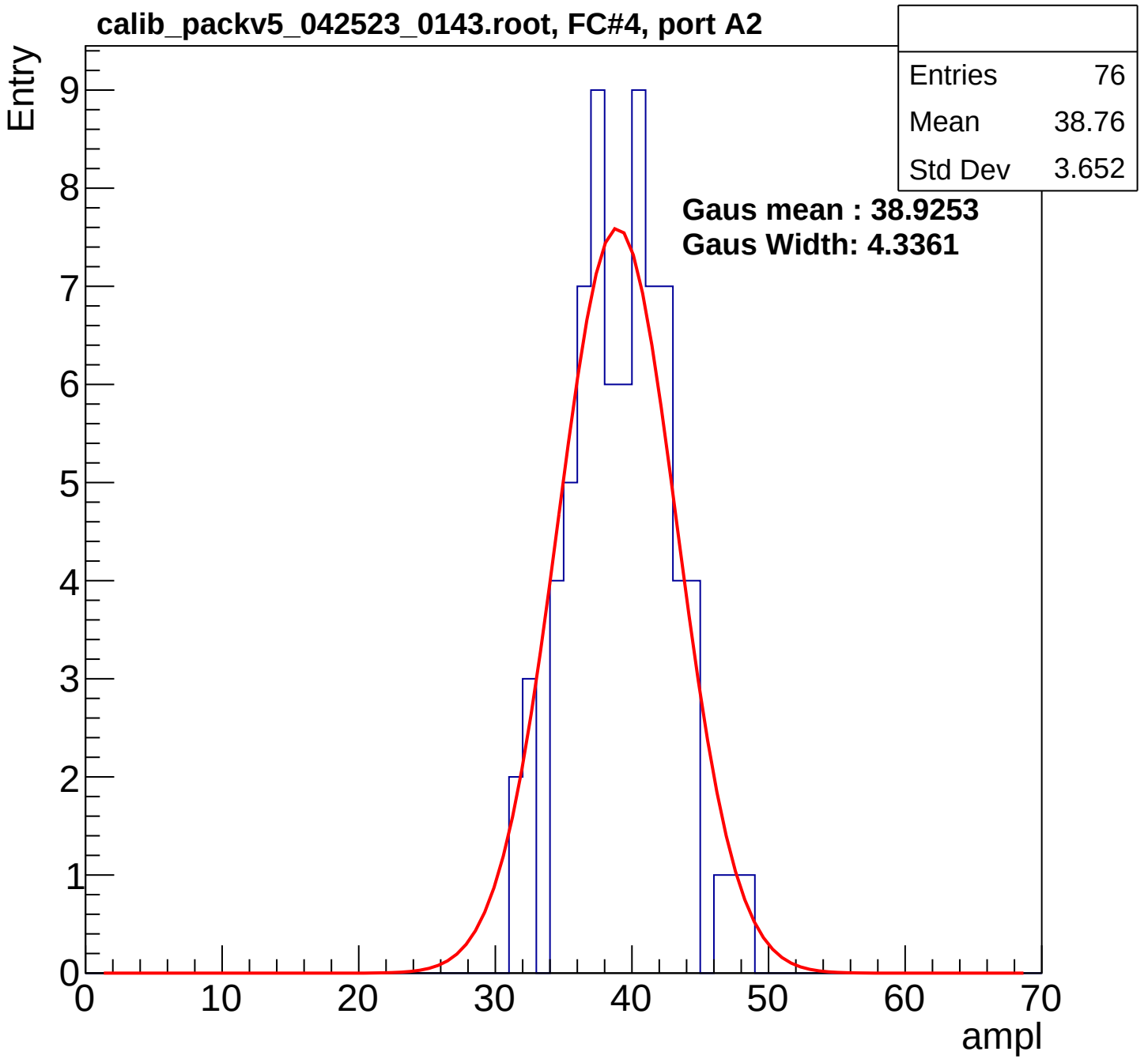
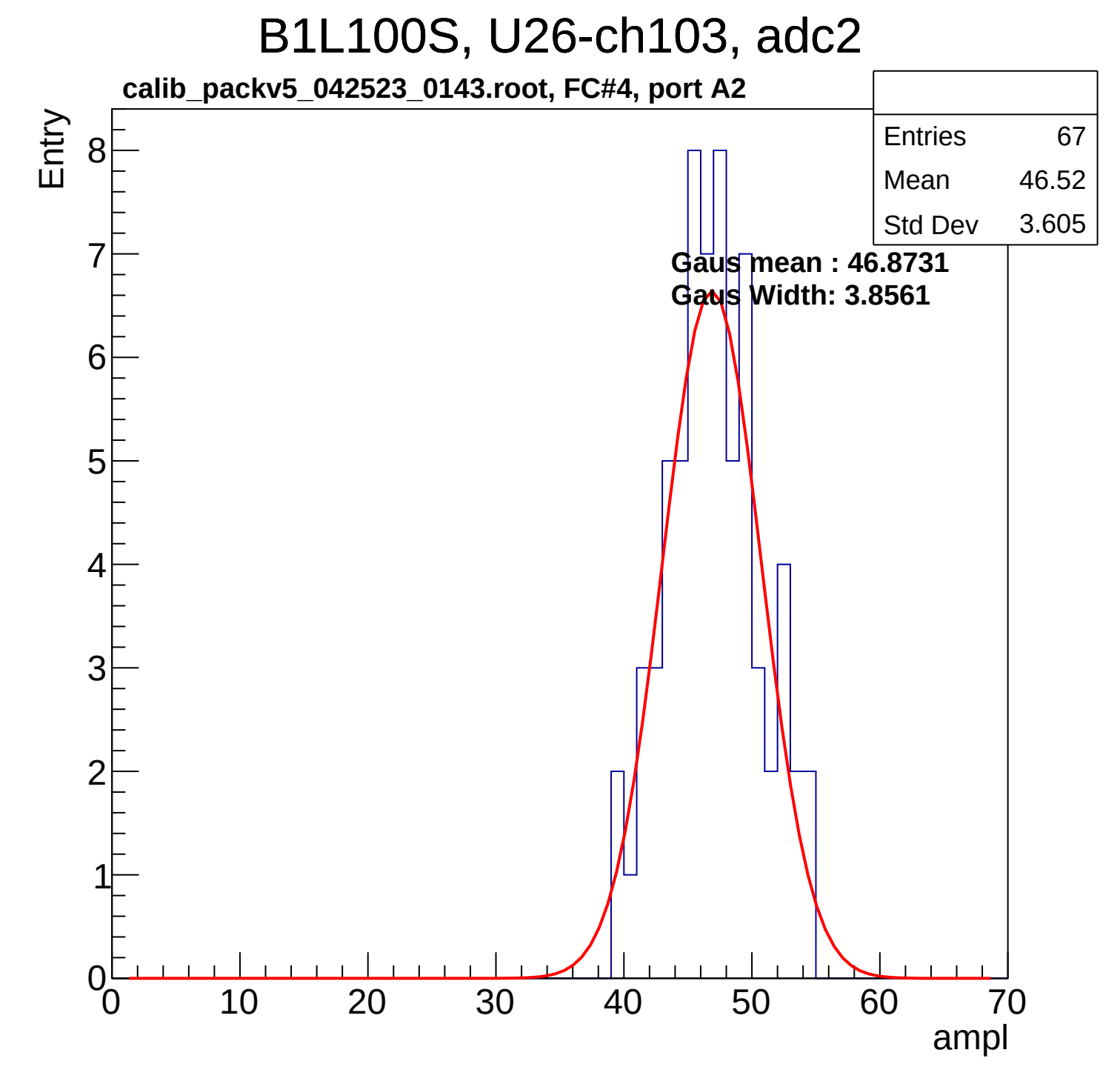

B1L100S, U26-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 38.76
Std Dev 3.652
Gaus mean : 38.9253
Gaus Width: 4.3361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 46.52
Std Dev 3.605
Gaus mean : 46.8731
Gaus Width: 3.8561
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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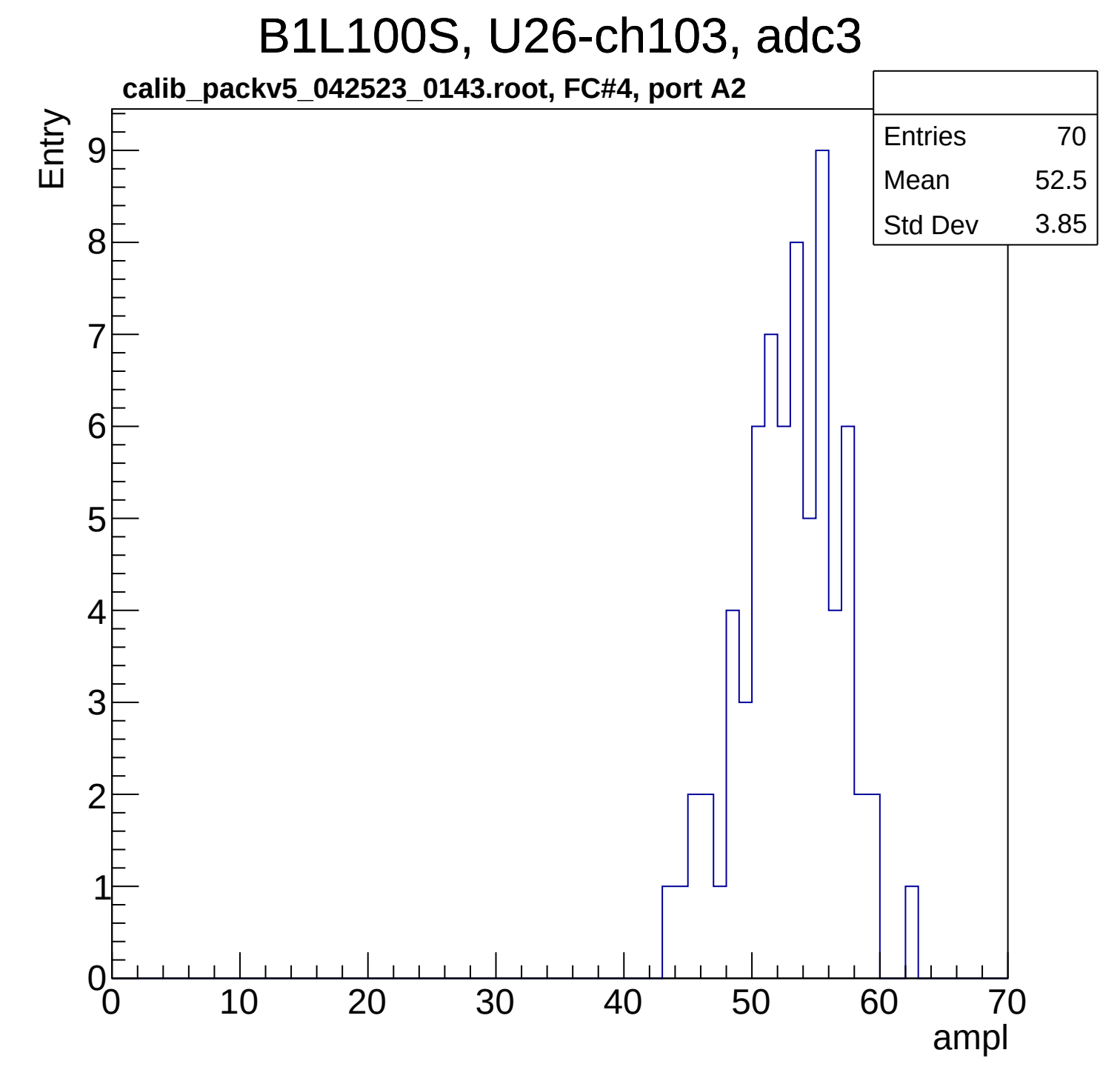
B1L100S, U26-ch103, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 52.5
Std Dev 3.85
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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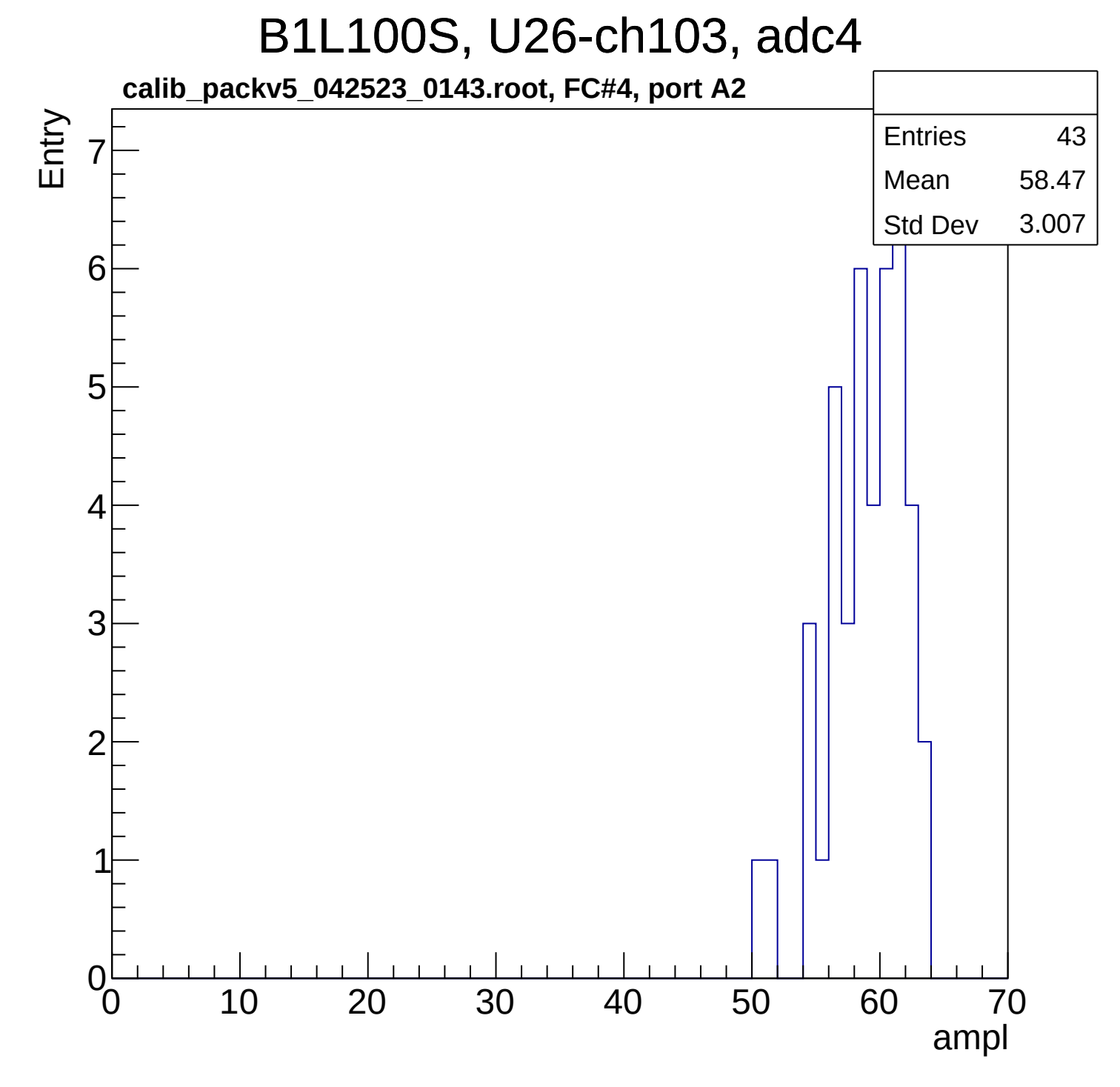
B1L100S, U26-ch103, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.47
Std Dev 3.007
Entry
ampl
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0
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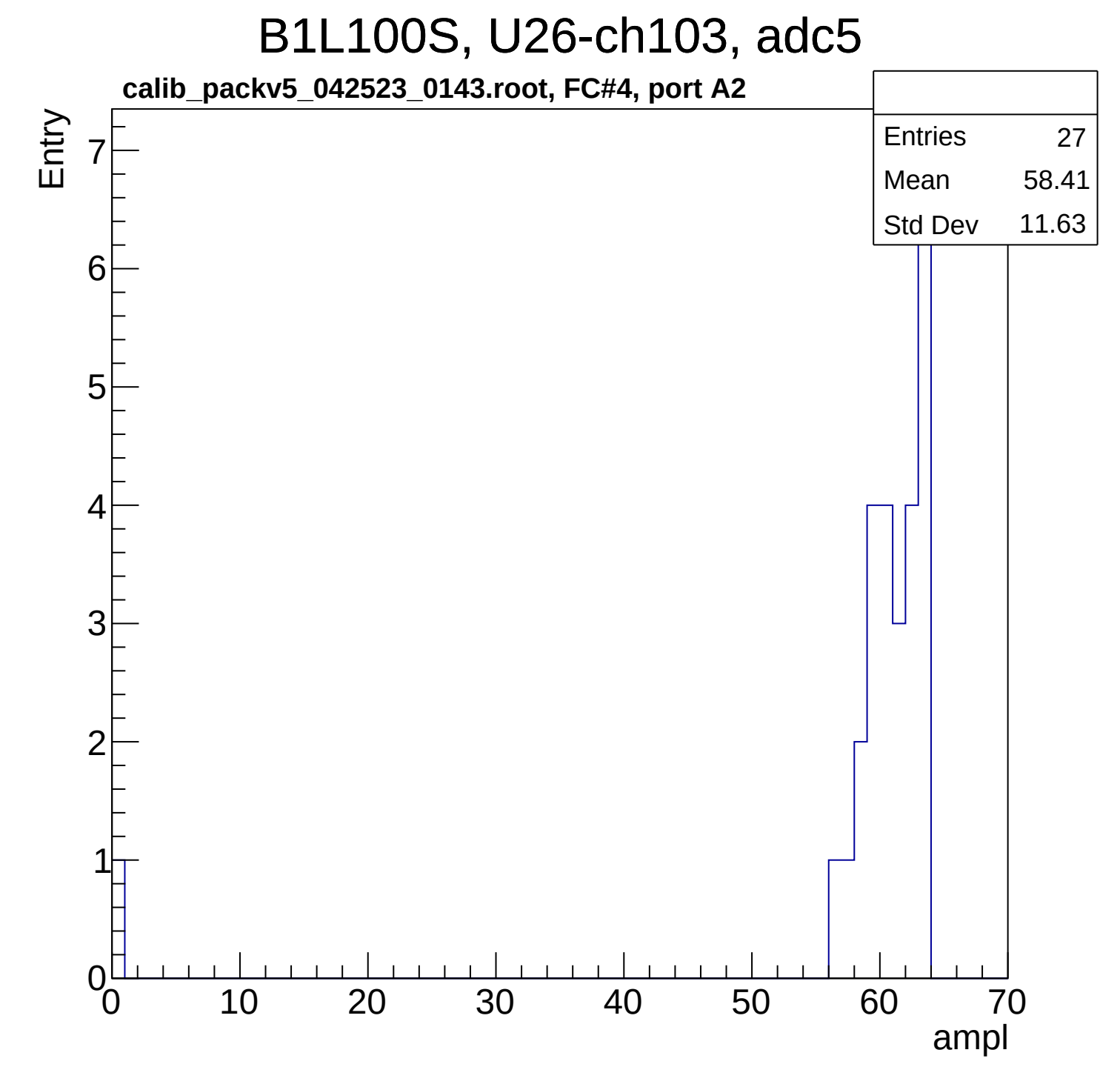
B1L100S, U26-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 27
Mean 58.41
Std Dev 11.63
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
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60
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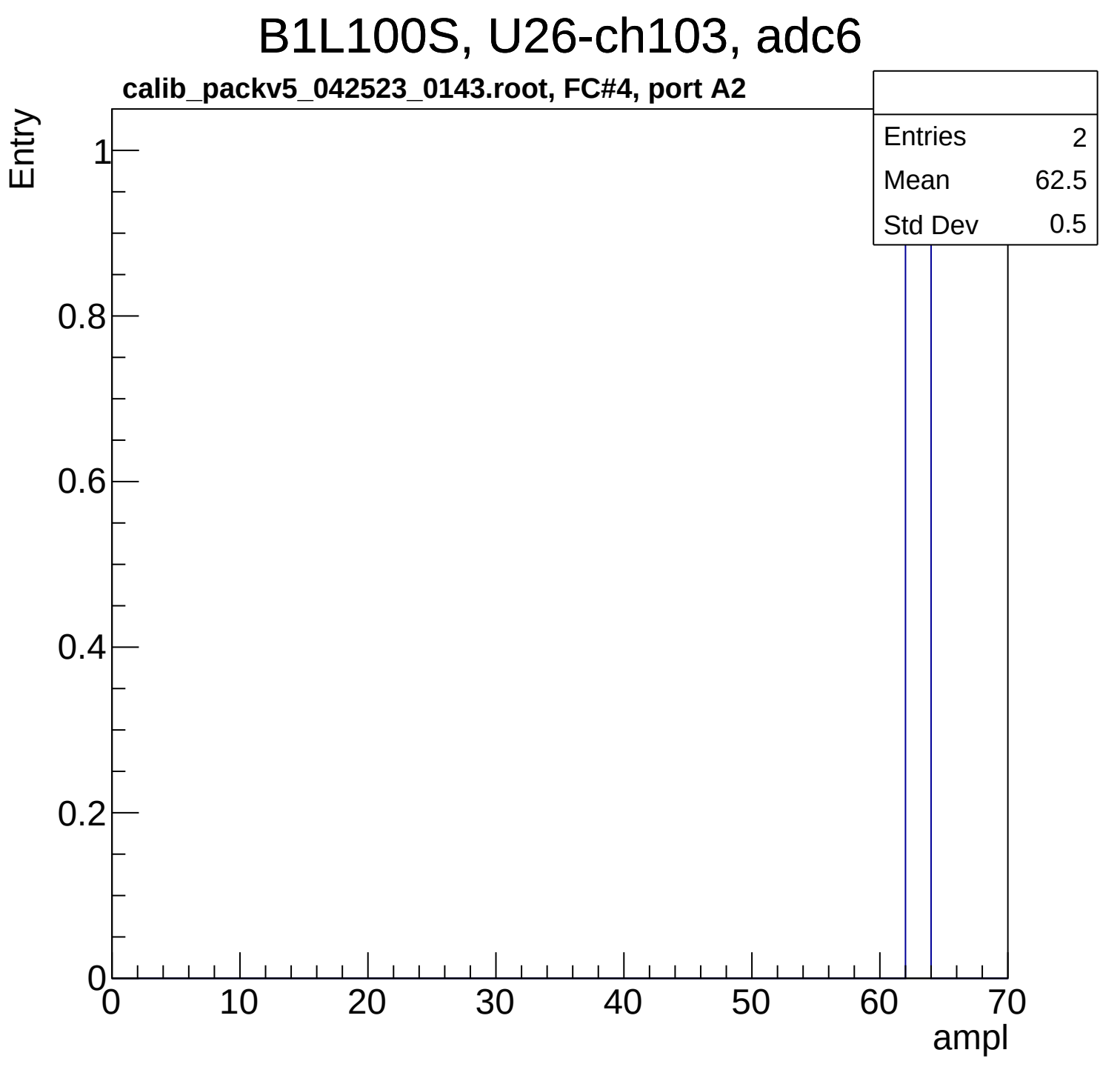
B1L100S, U26-ch103, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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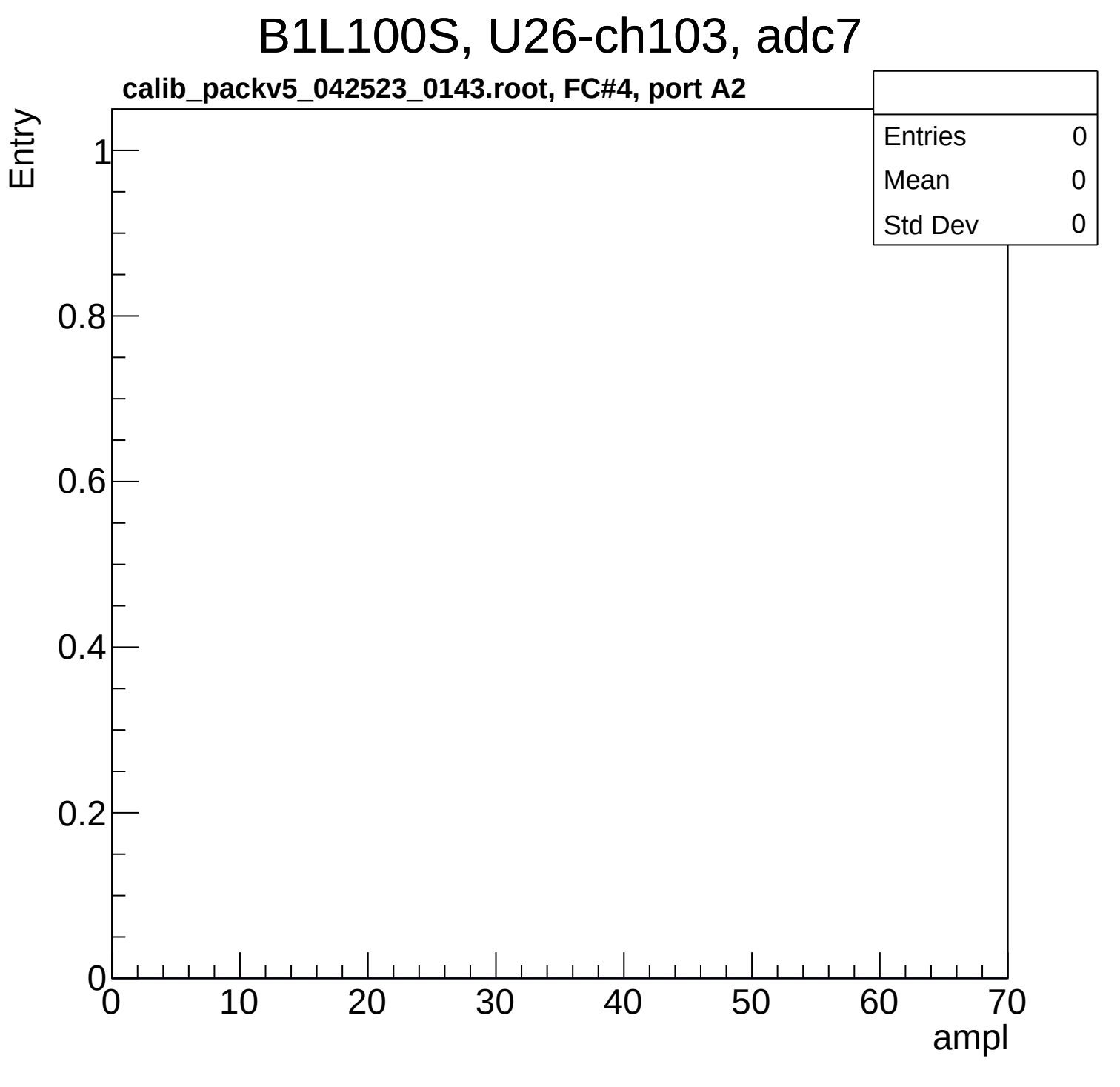
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch103, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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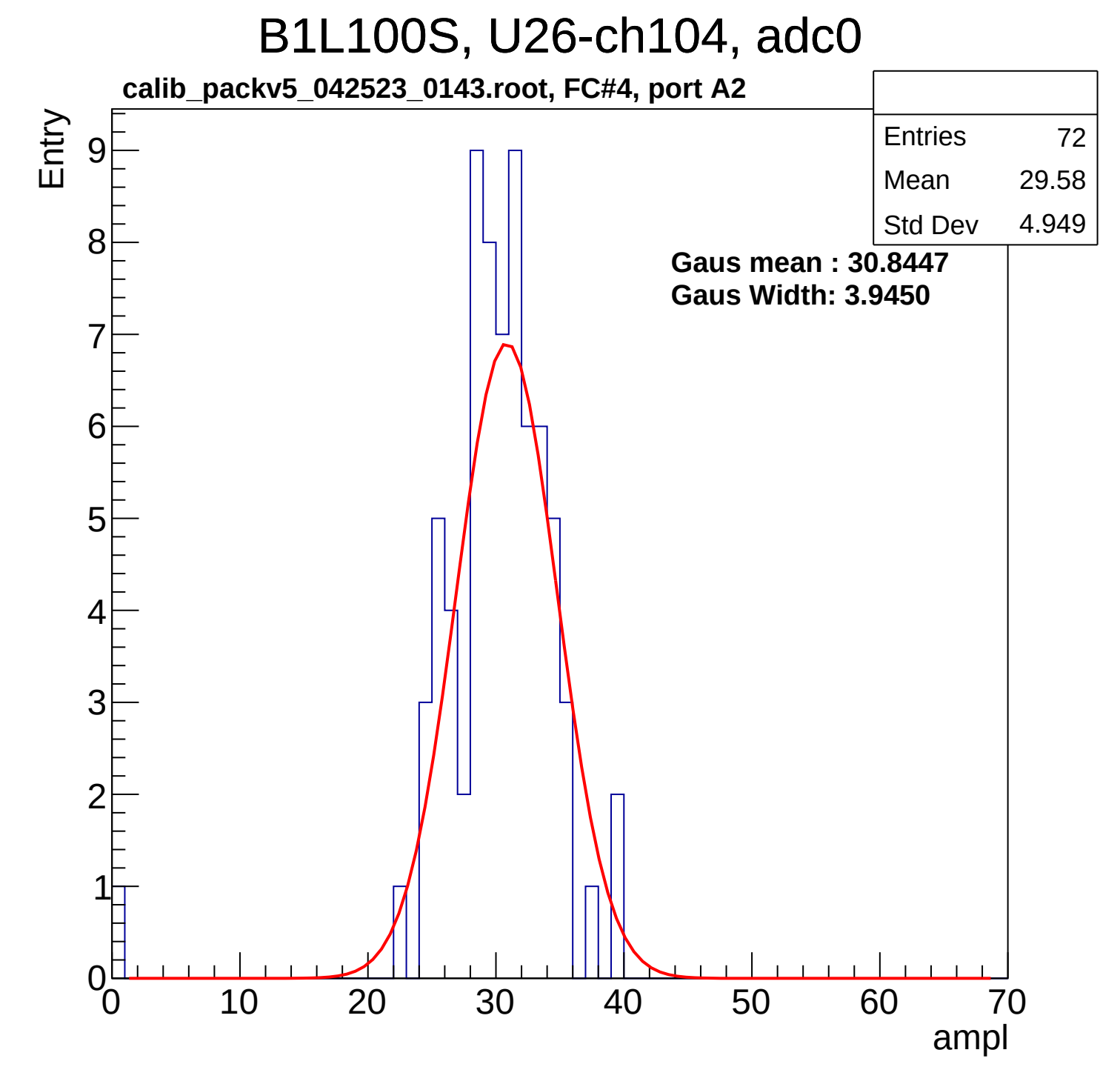
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.58
Std Dev 4.949
Gaus mean : 30.8447
Gaus Width: 3.9450
Entry
ampl
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2
3
4
5
6
7
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0
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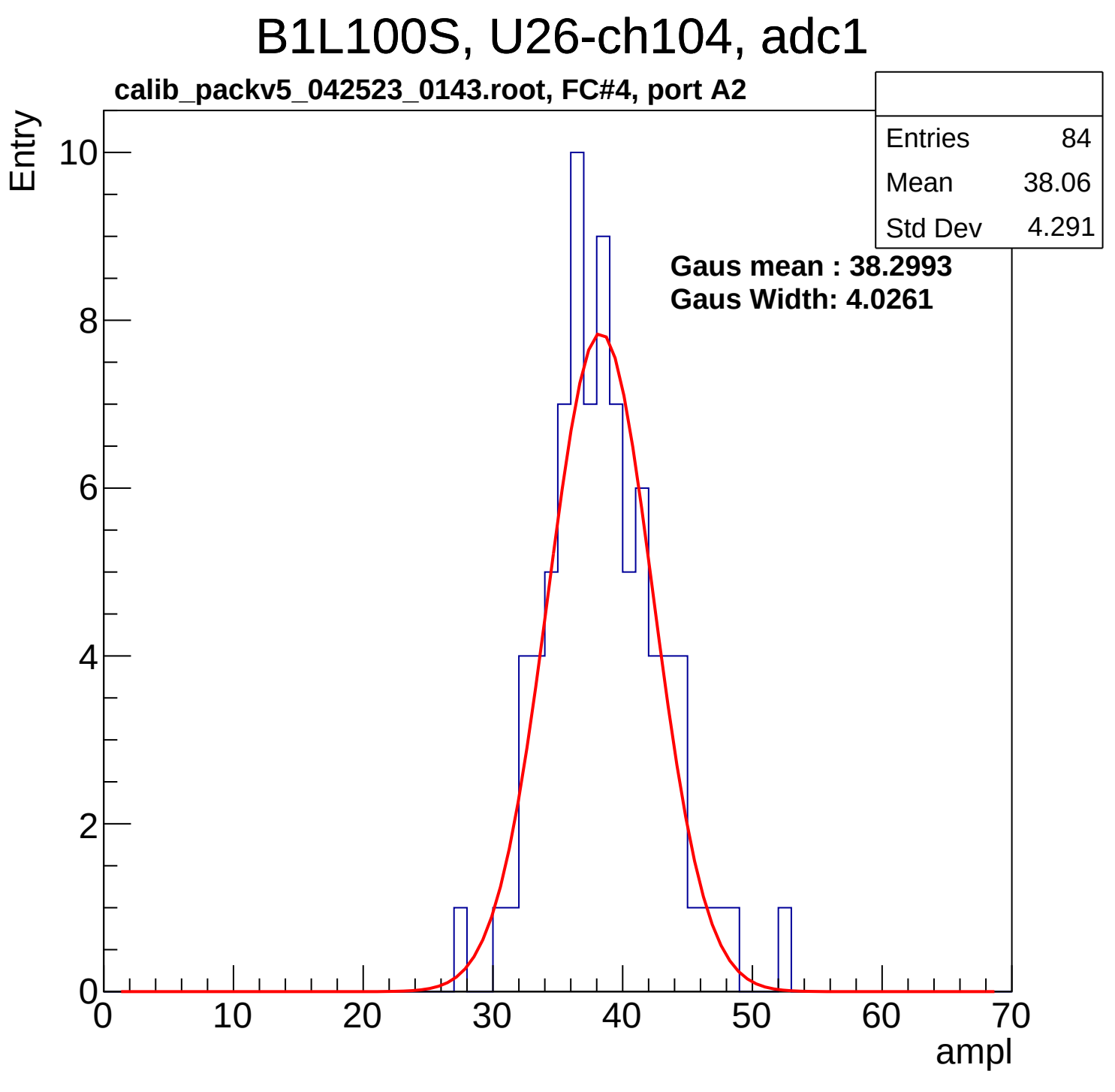
B1L100S, U26-ch104, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 38.06
Std Dev 4.291
Gaus mean : 38.2993
Gaus Width: 4.0261
Entry
ampl
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4
6
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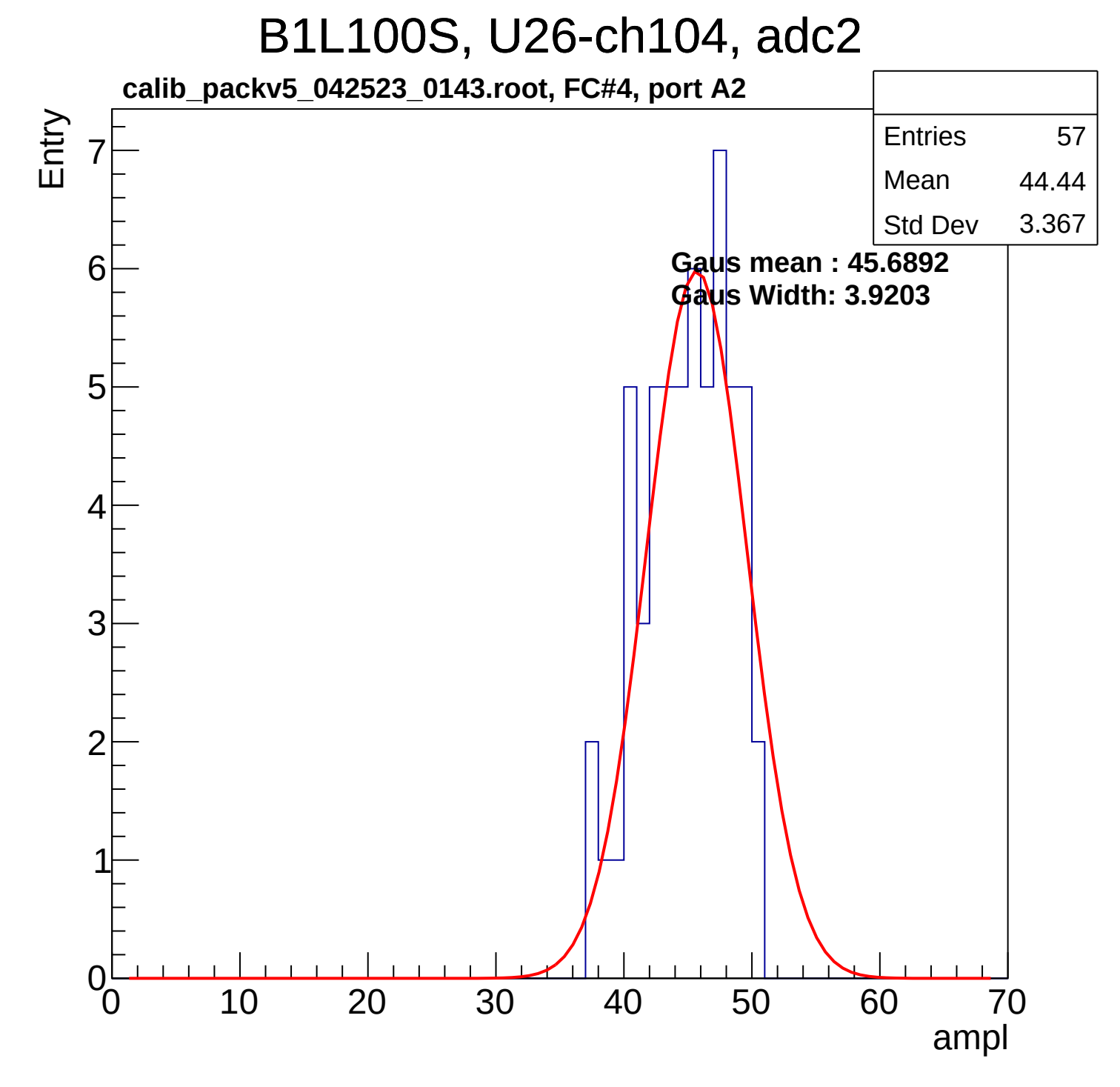
B1L100S, U26-ch104, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.44
Std Dev 3.367
Gaus mean : 45.6892
Gaus Width: 3.9203
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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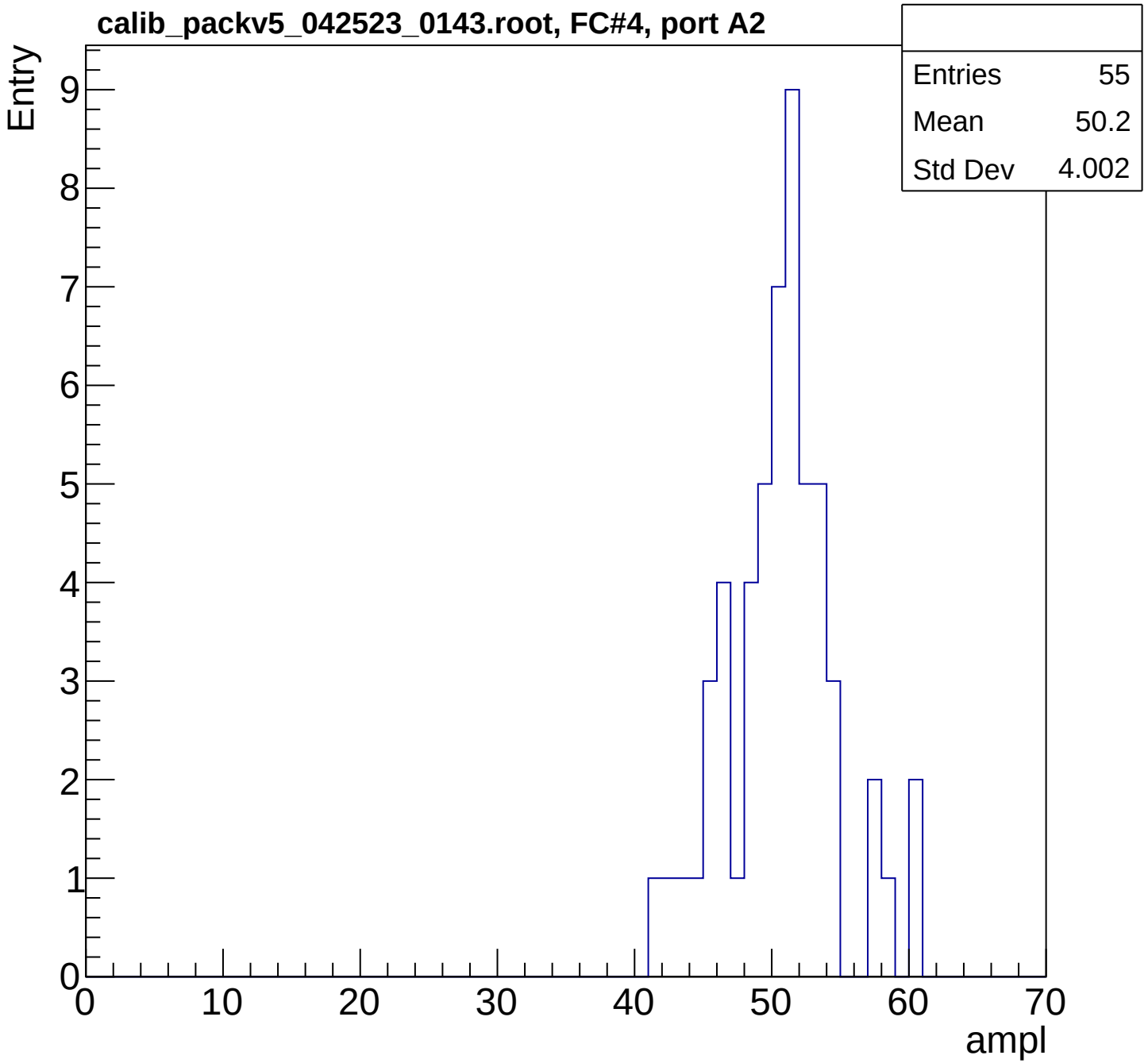
B1L100S, U26-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 50.2
Std Dev 4.002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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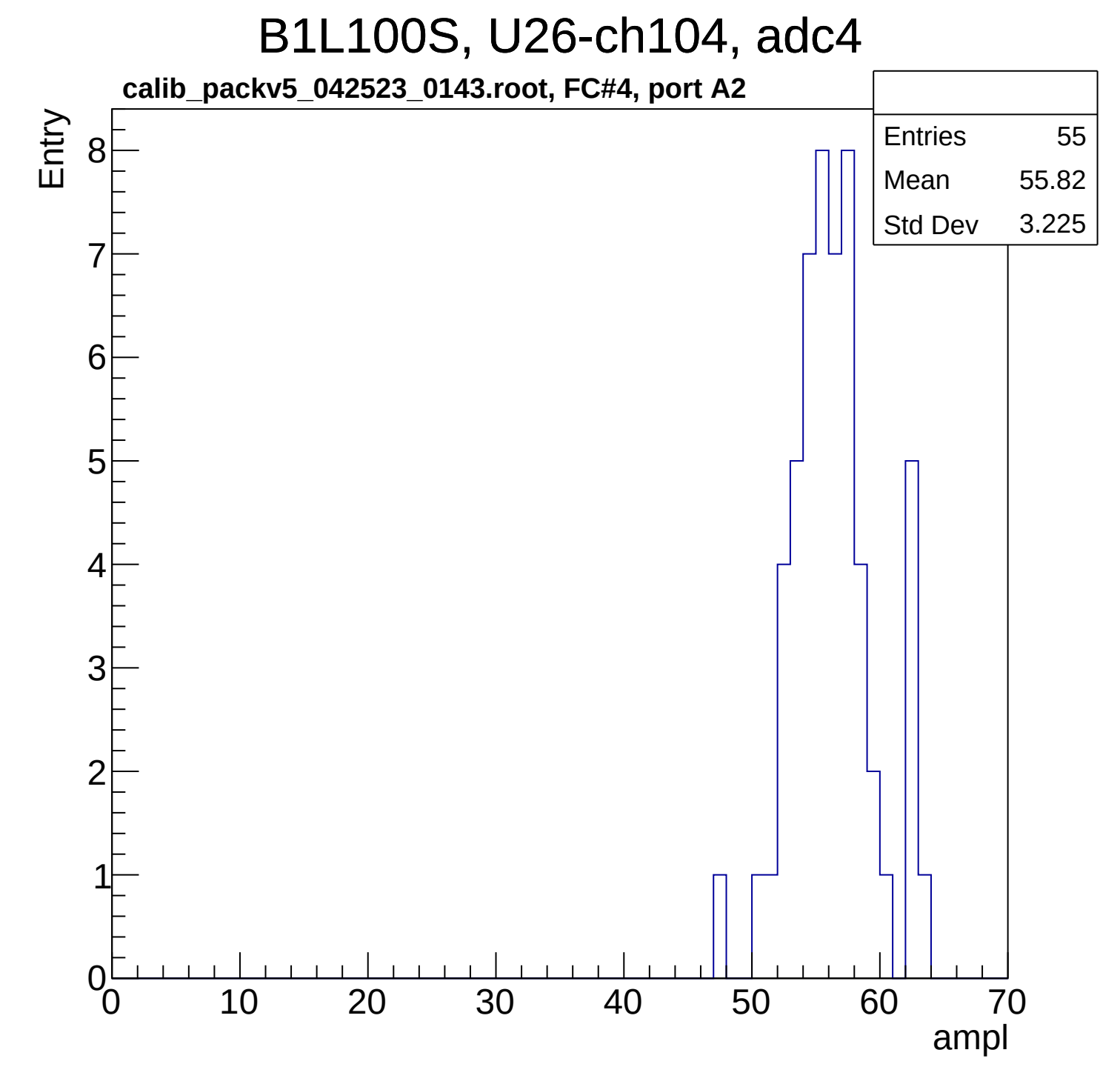
B1L100S, U26-ch104, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.82
Std Dev 3.225
Entry
ampl
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1
2
3
4
5
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7
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0
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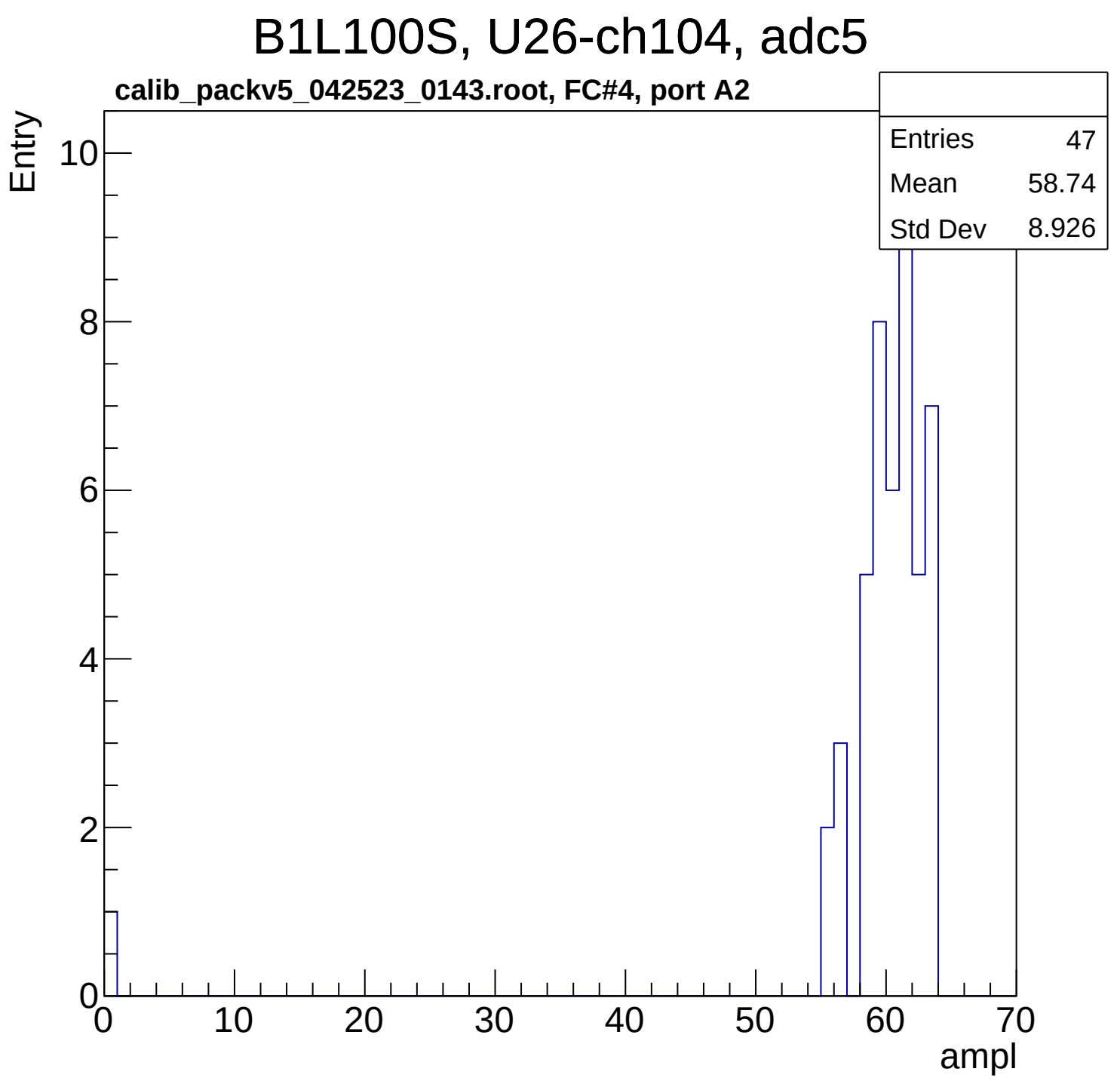
B1L100S, U26-ch104, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.74
Std Dev 8.926
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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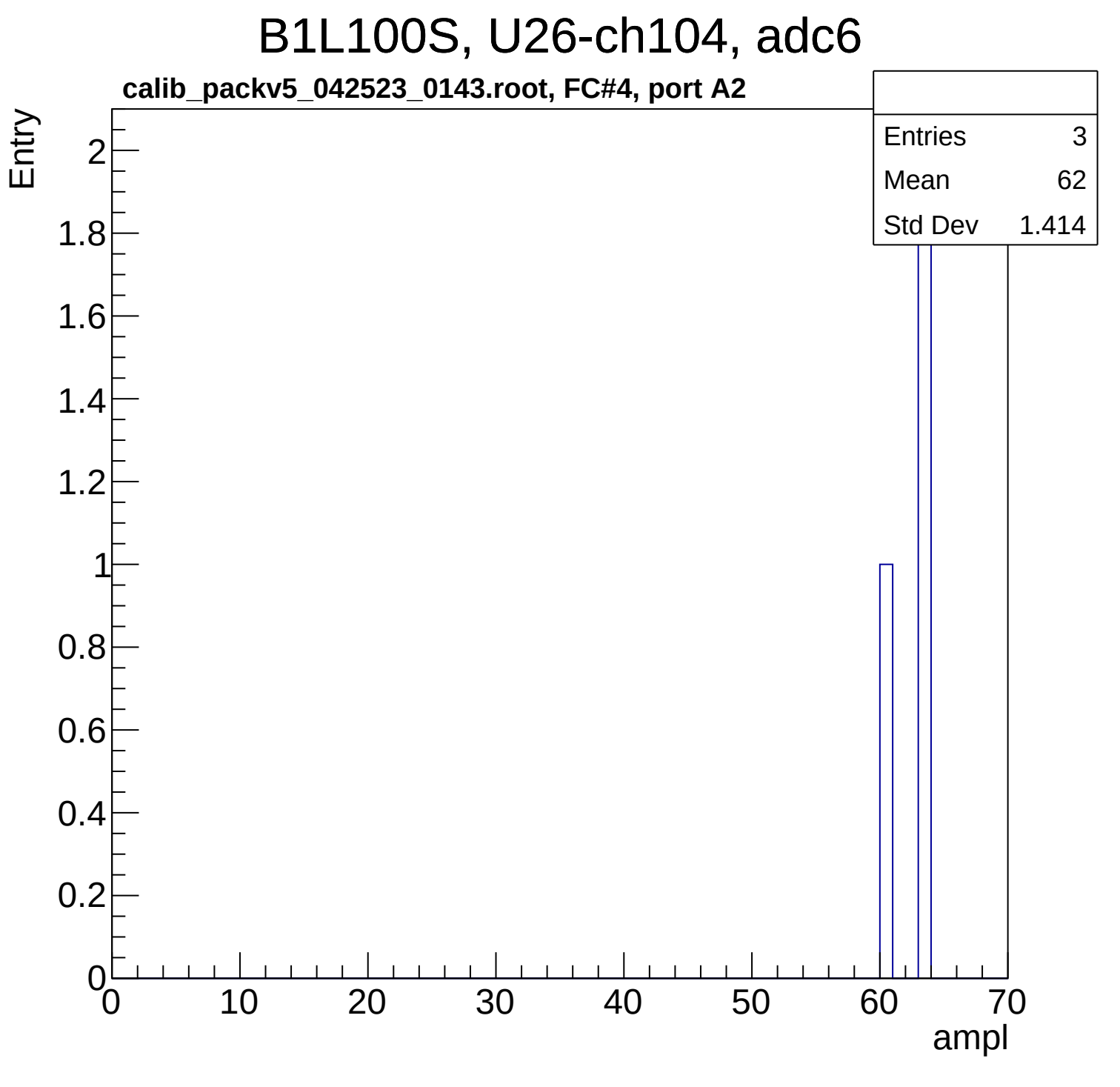
B1L100S, U26-ch104, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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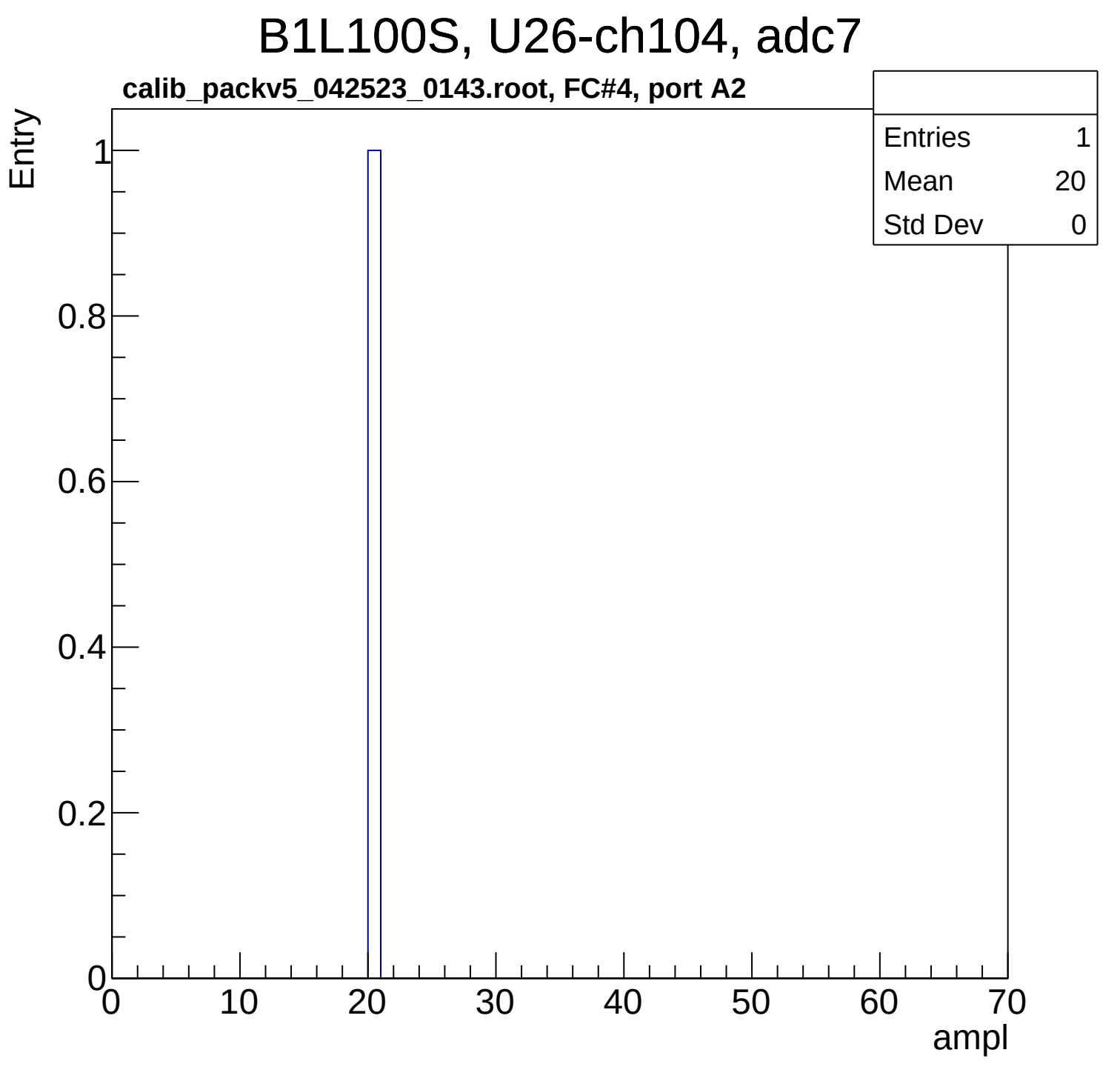
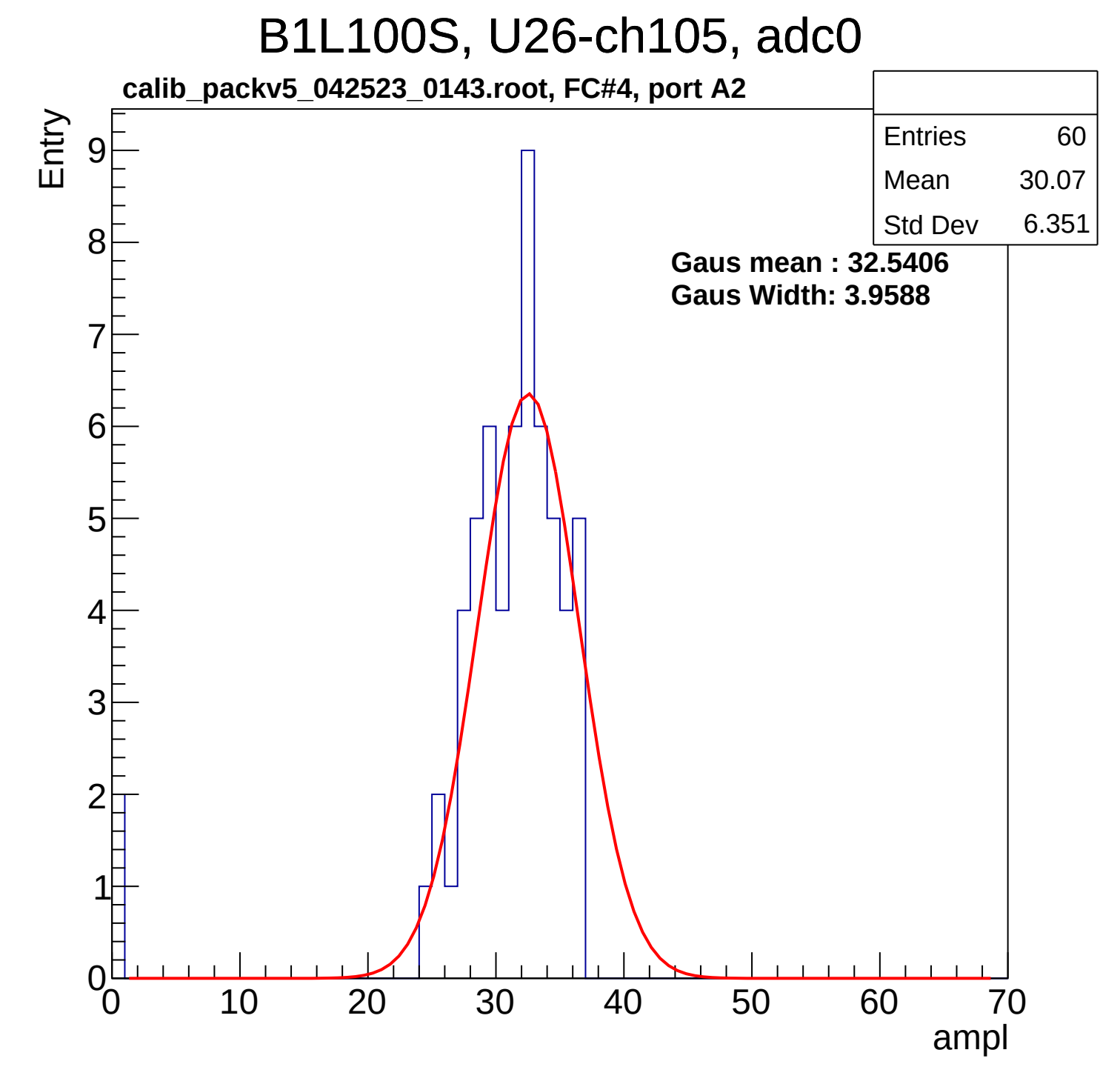

B1L100S, U26-ch104, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 30.07
Std Dev 6.351
Gaus mean : 32.5406
Gaus Width: 3.9588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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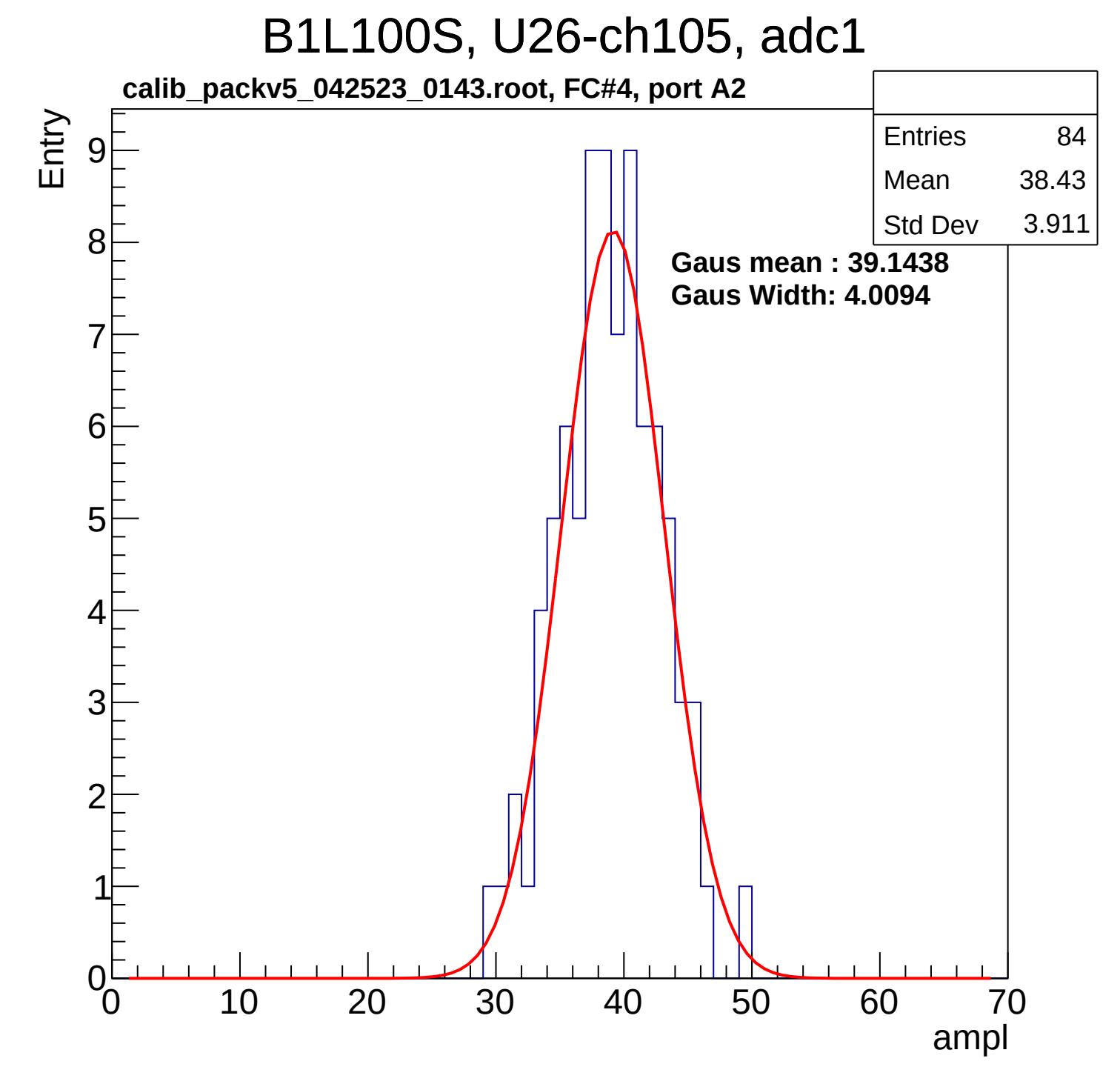
B1L100S, U26-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 38.43
Std Dev 3.911
Gaus mean : 39.1438
Gaus Width: 4.0094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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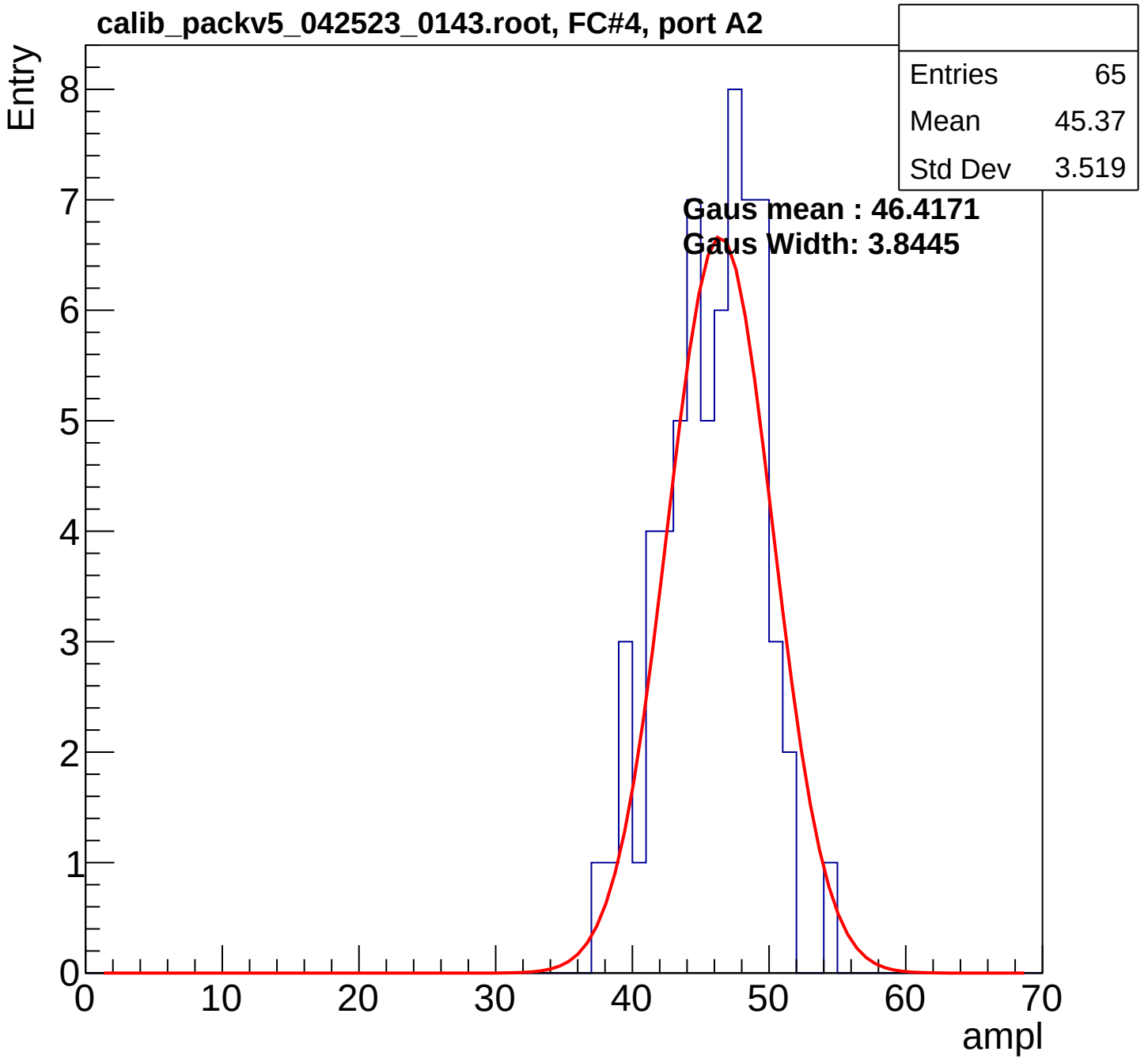
B1L100S, U26-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 45.37
Std Dev 3.519
Gaus mean : 46.4171
Gaus Width: 3.8445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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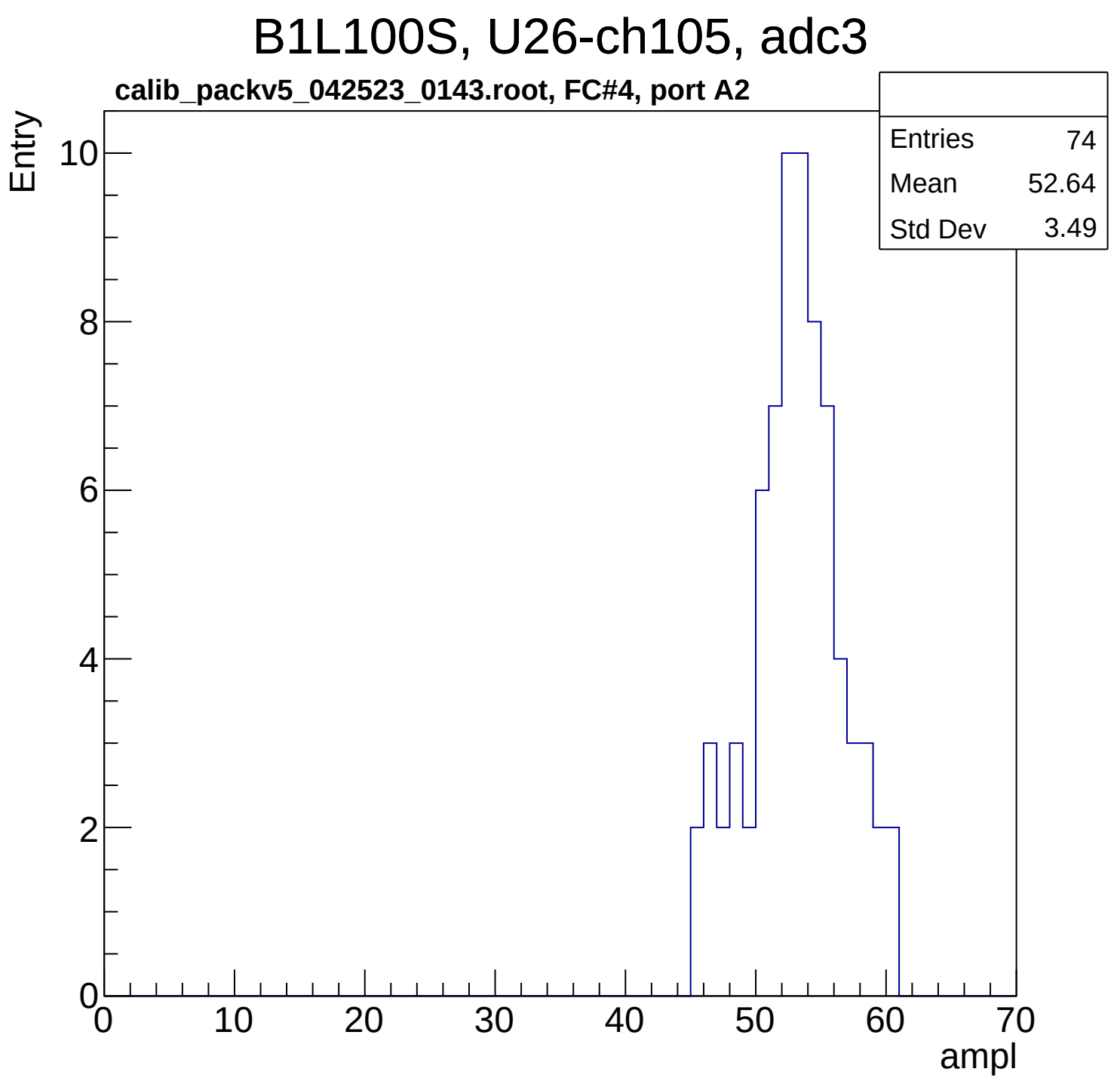
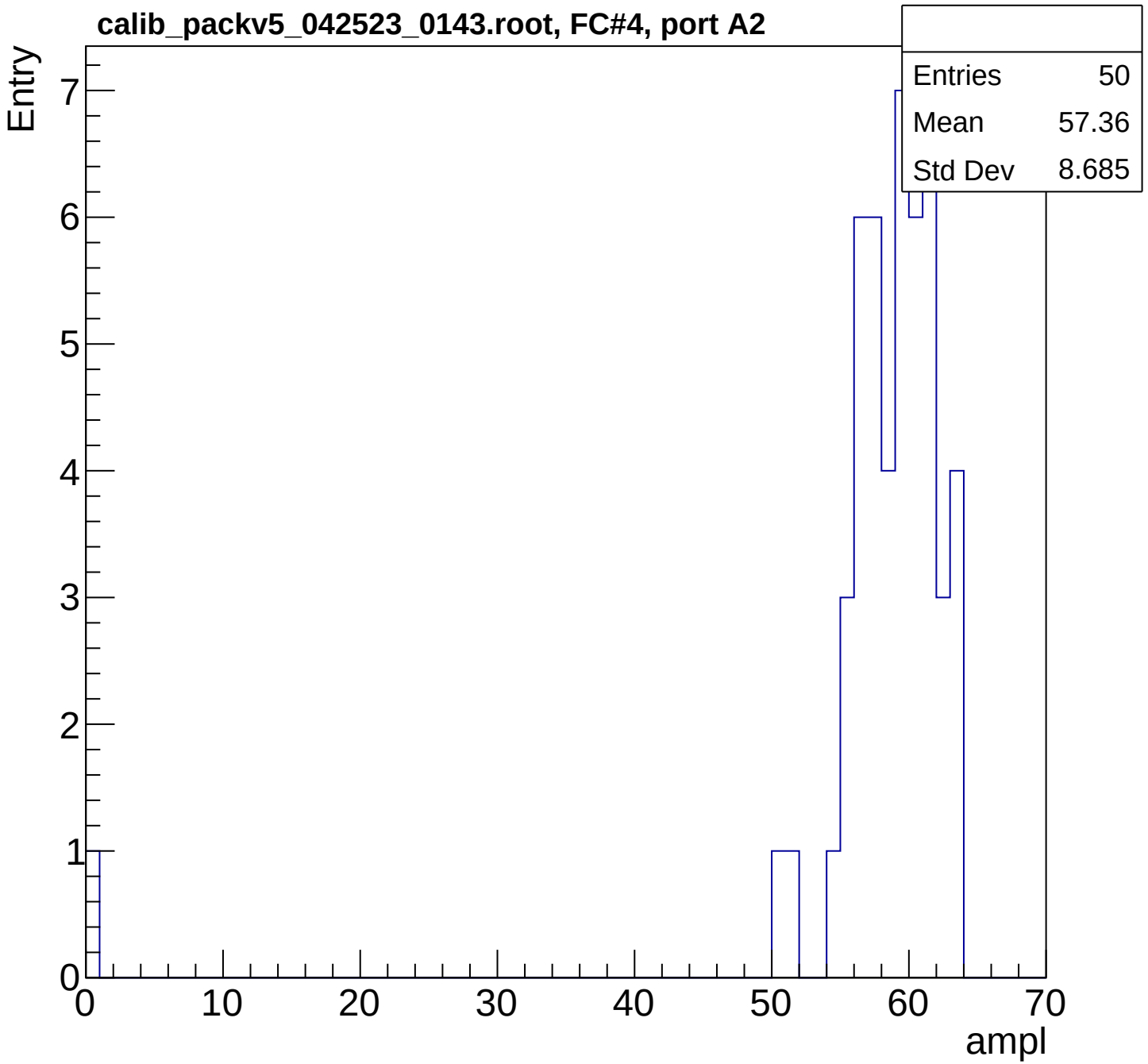
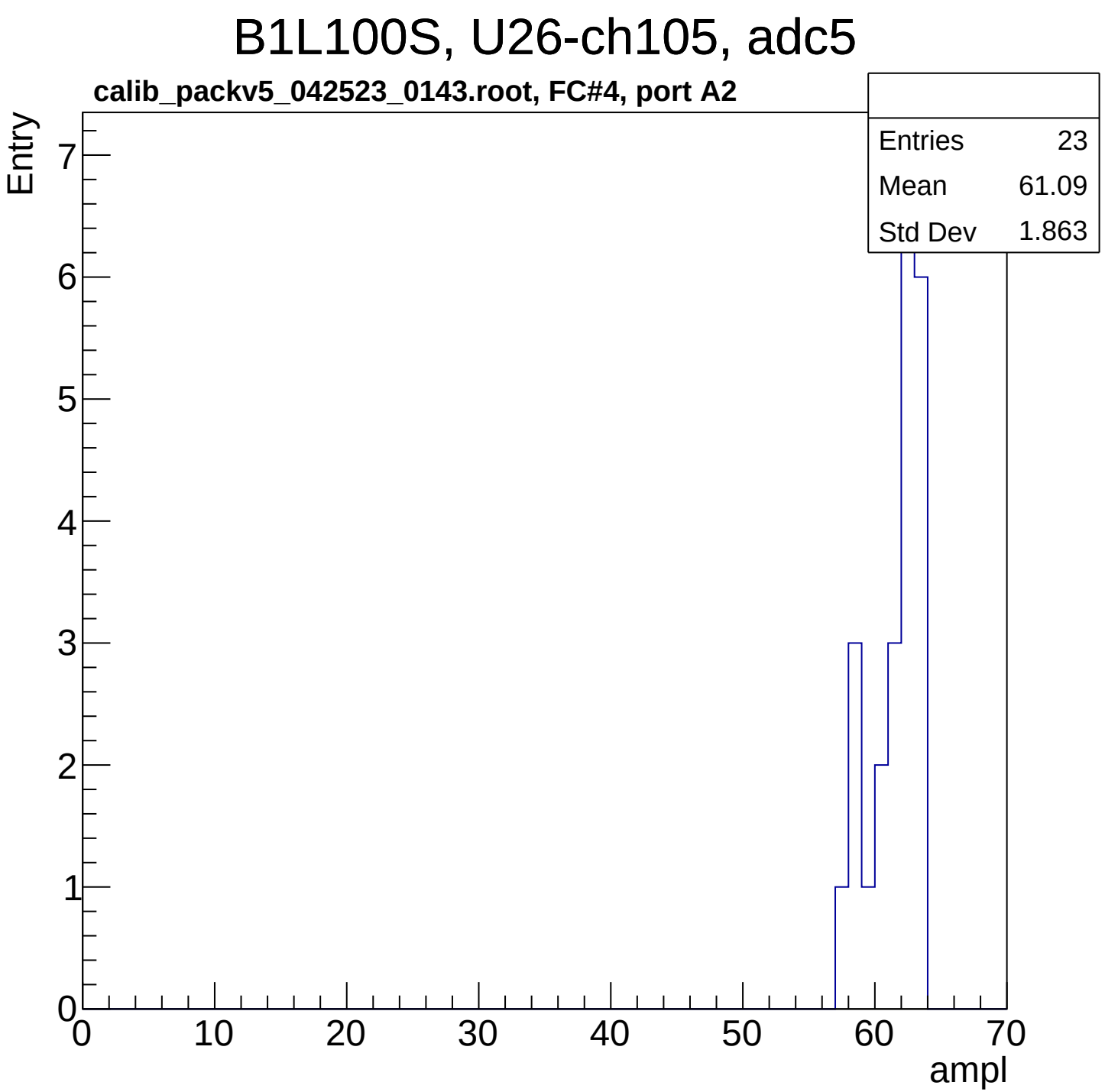

B1L100S, U26-ch105, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 52.64
Std Dev 3.49
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.36
Std Dev 8.685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch105, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 61.09
Std Dev 1.863
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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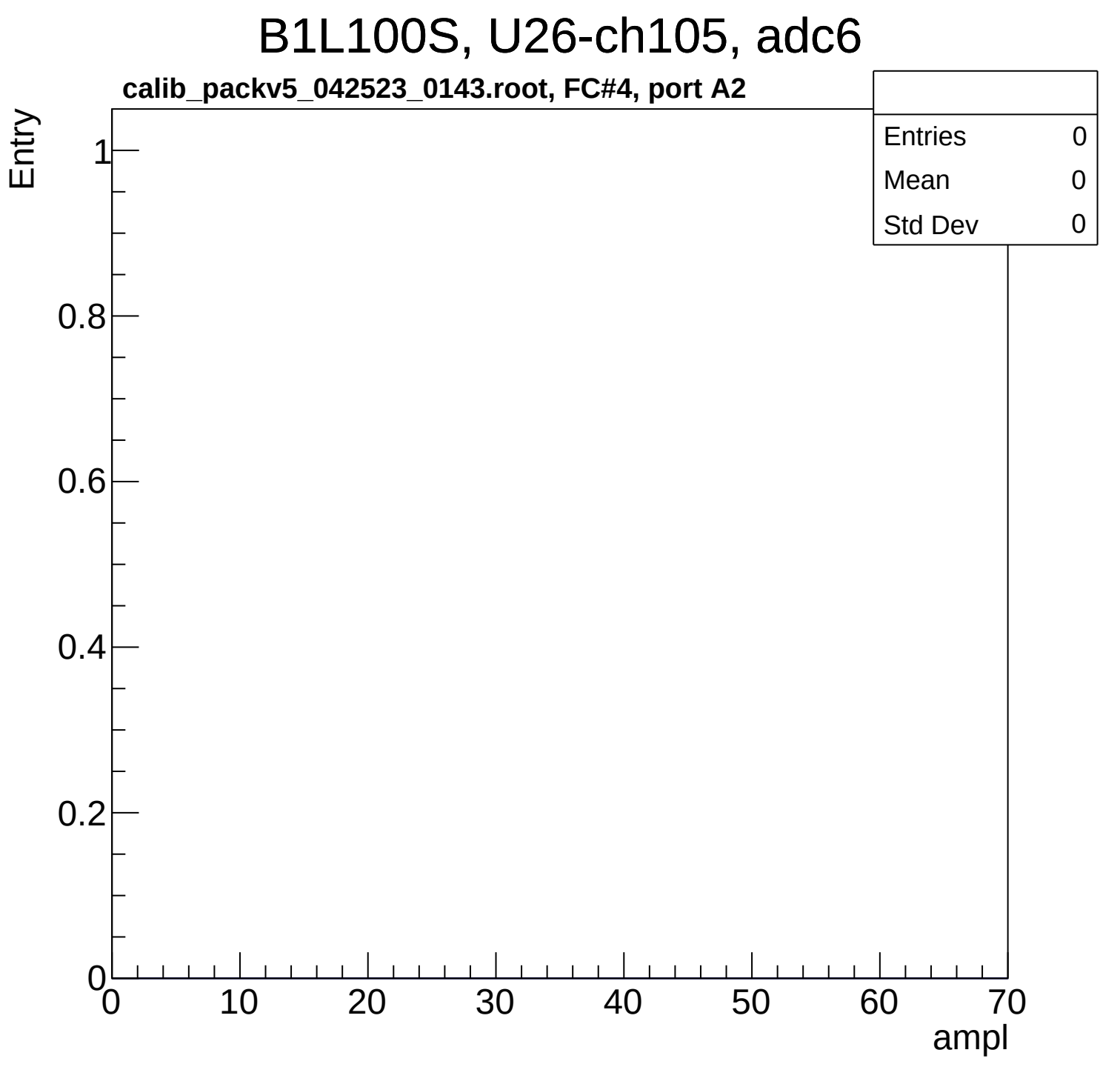
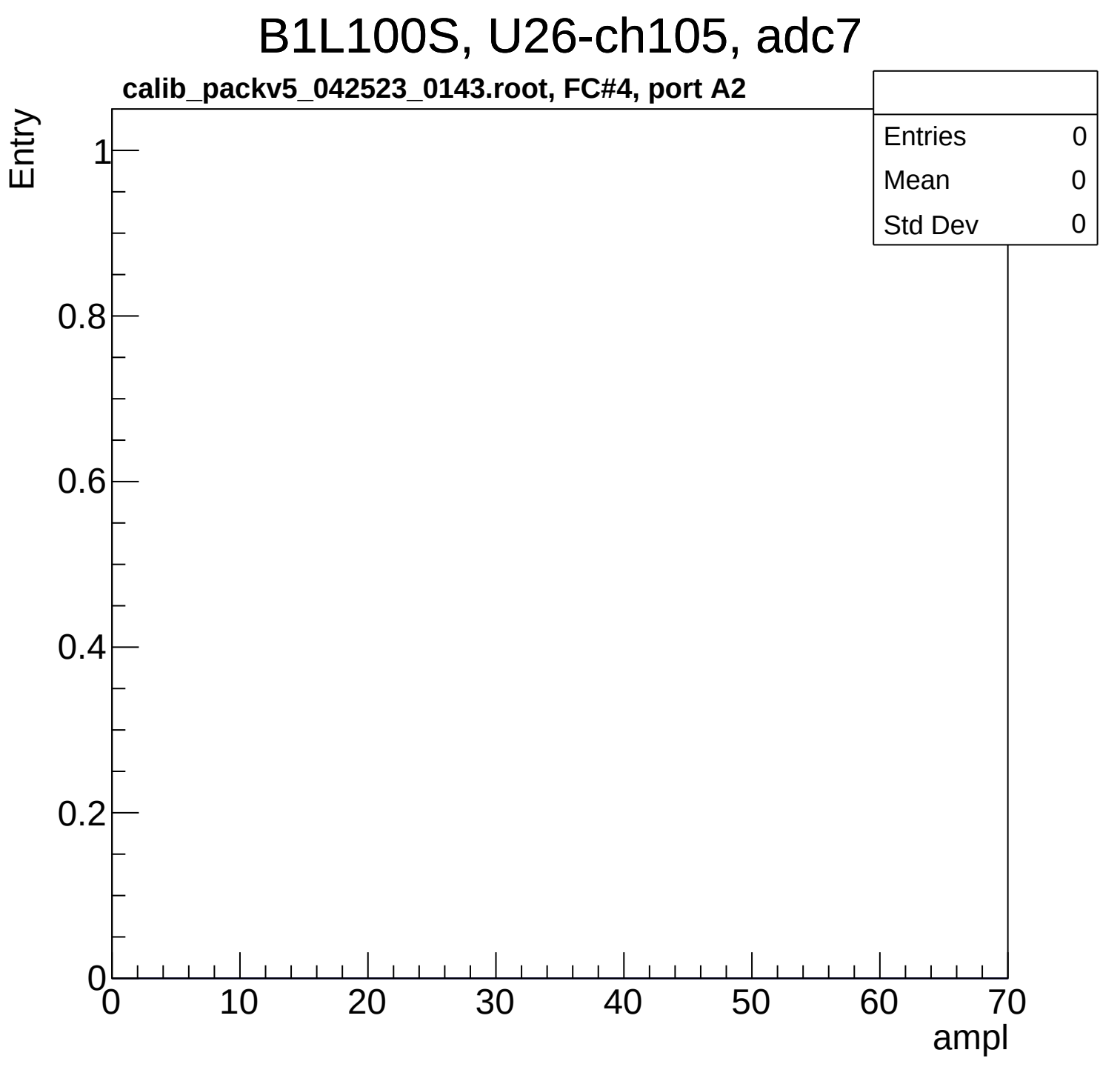
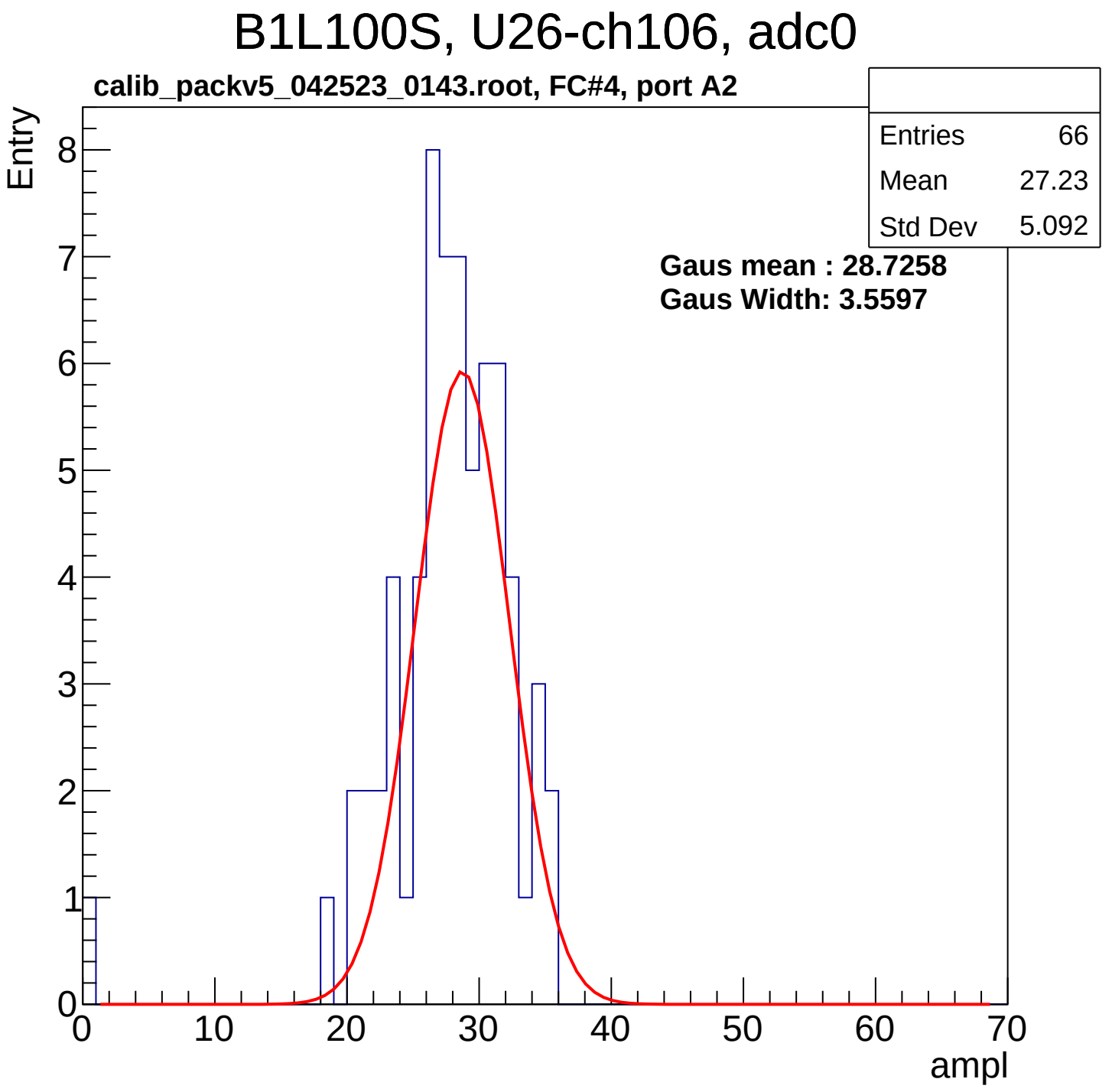
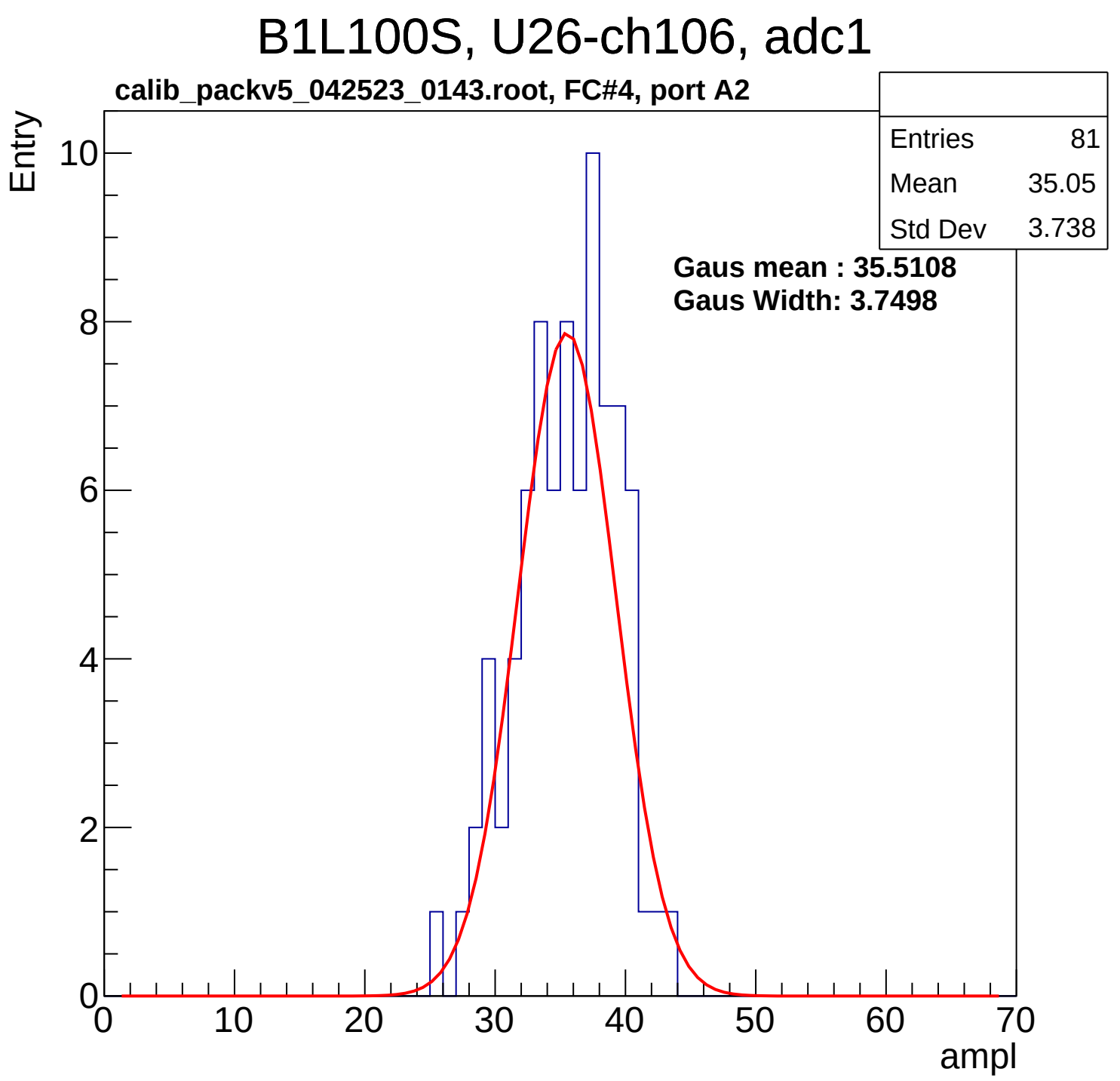
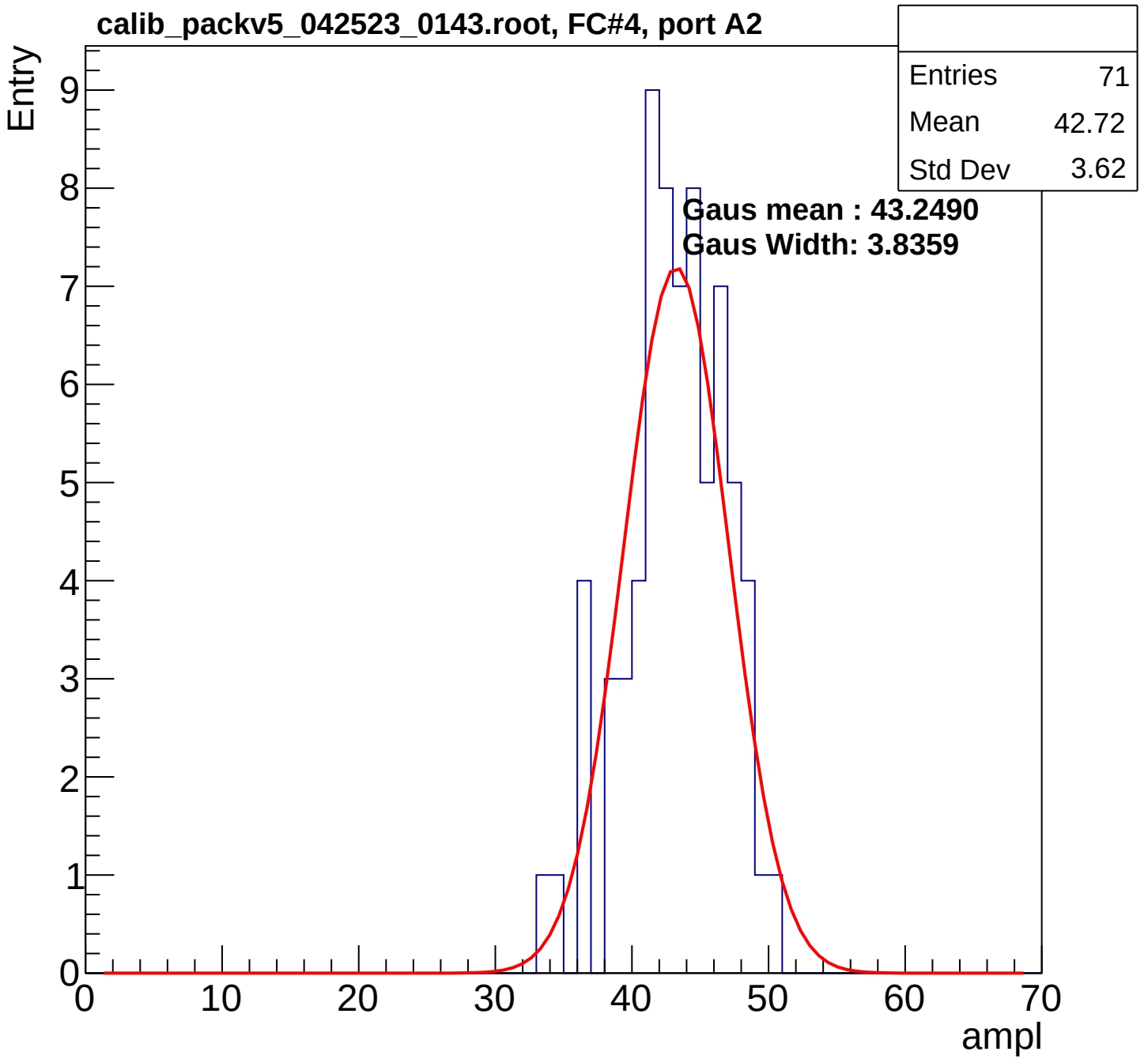
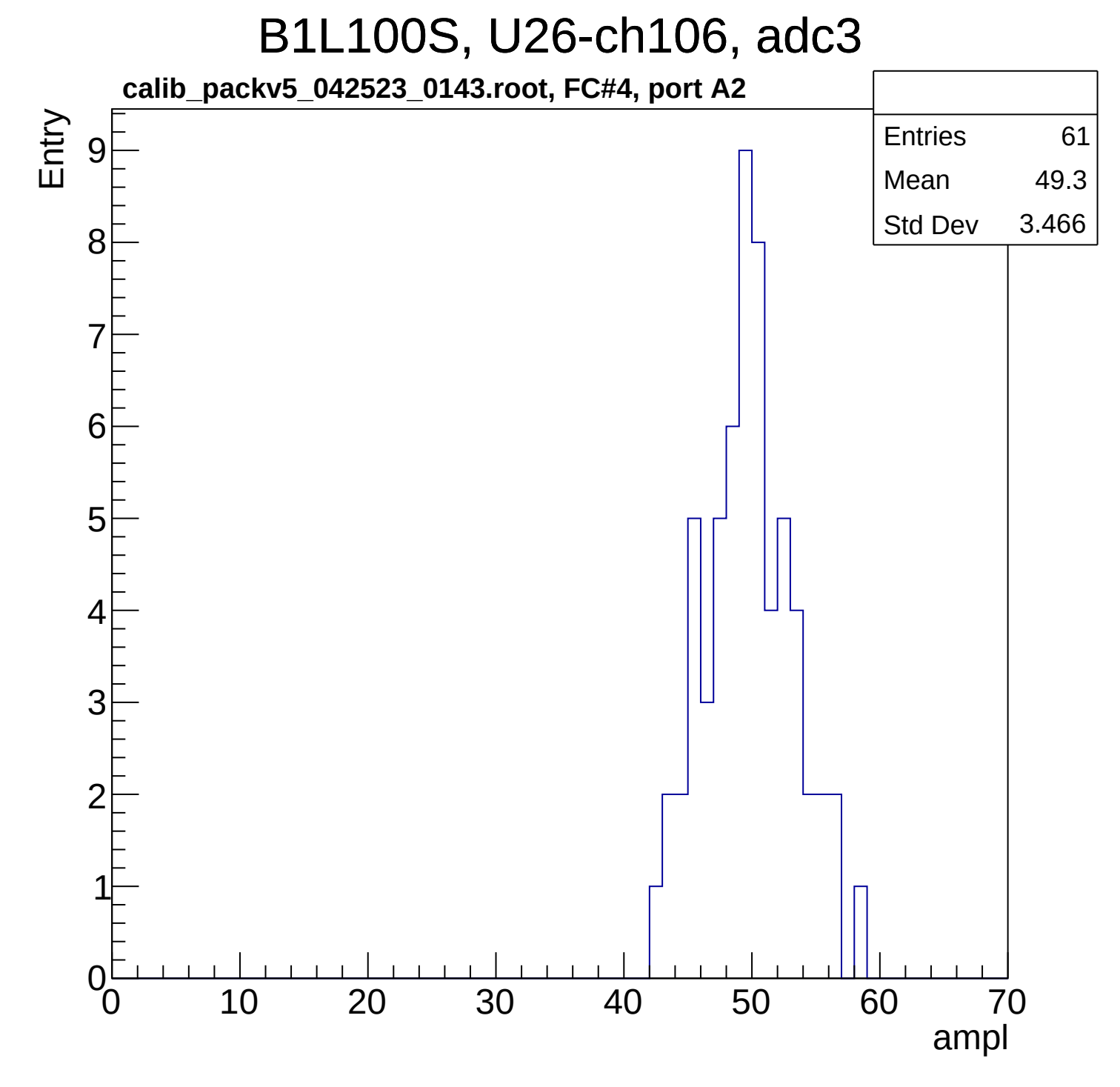

B1L100S, U26-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

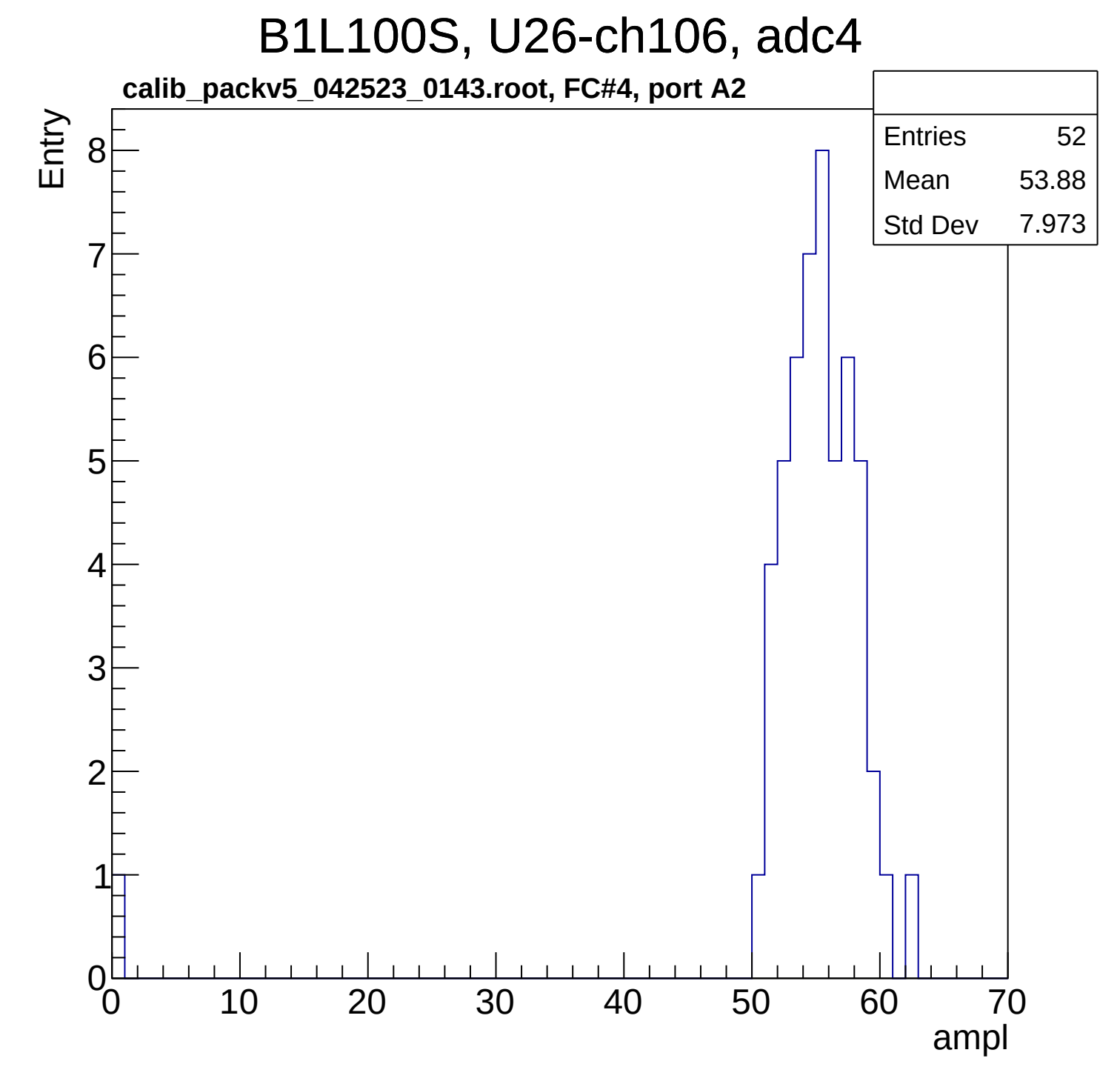

B1L100S, U26-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 27.23
Std Dev 5.092
Gaus mean : 28.7258
Gaus Width: 3.5597
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch106, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 35.05
Std Dev 3.738
Gaus mean : 35.5108
Gaus Width: 3.7498
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.72
Std Dev 3.62
Gaus mean : 43.2490
Gaus Width: 3.8359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch106, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.3
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch106, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 53.88
Std Dev 7.973
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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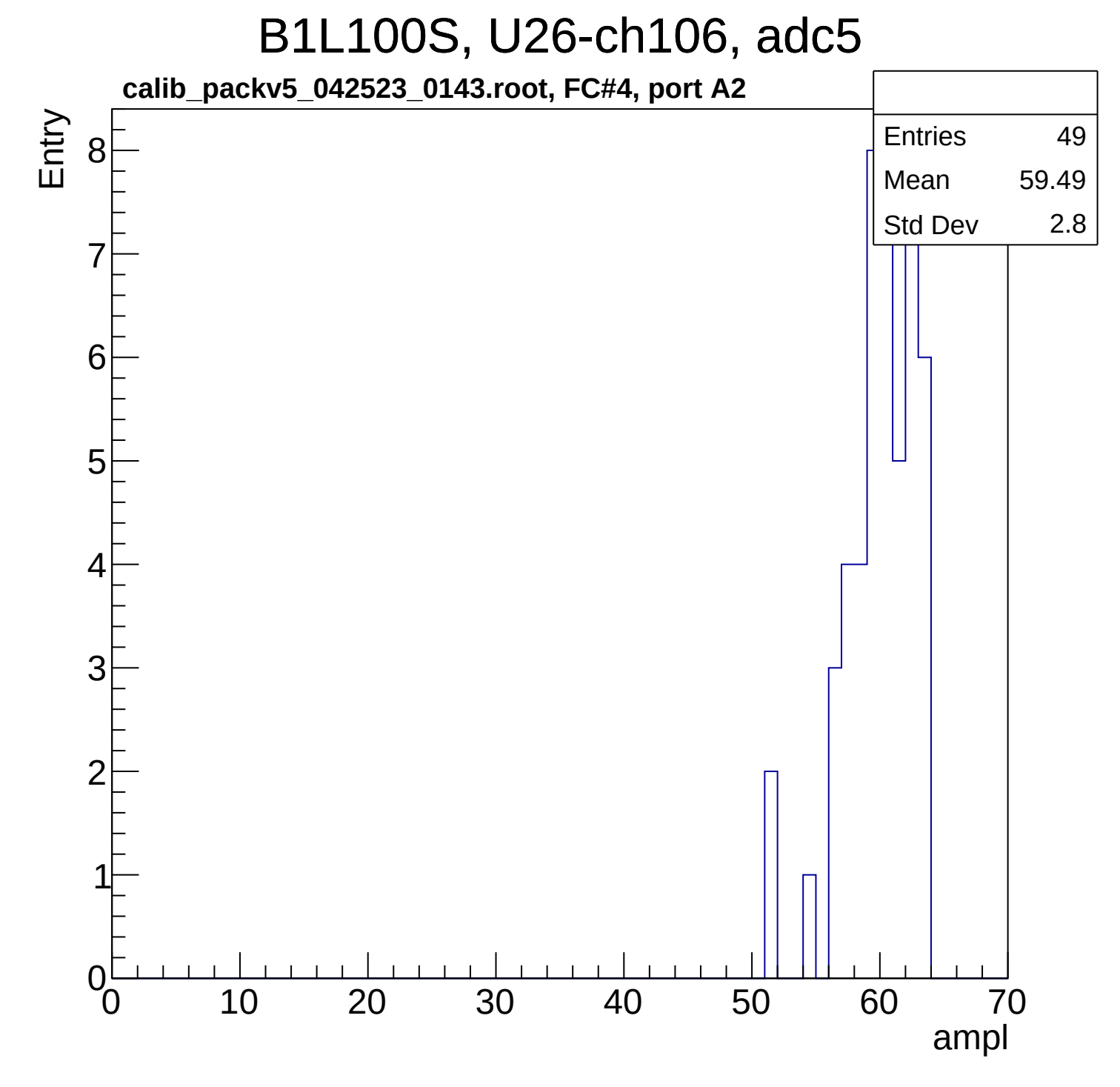
B1L100S, U26-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 59.49
Std Dev 2.8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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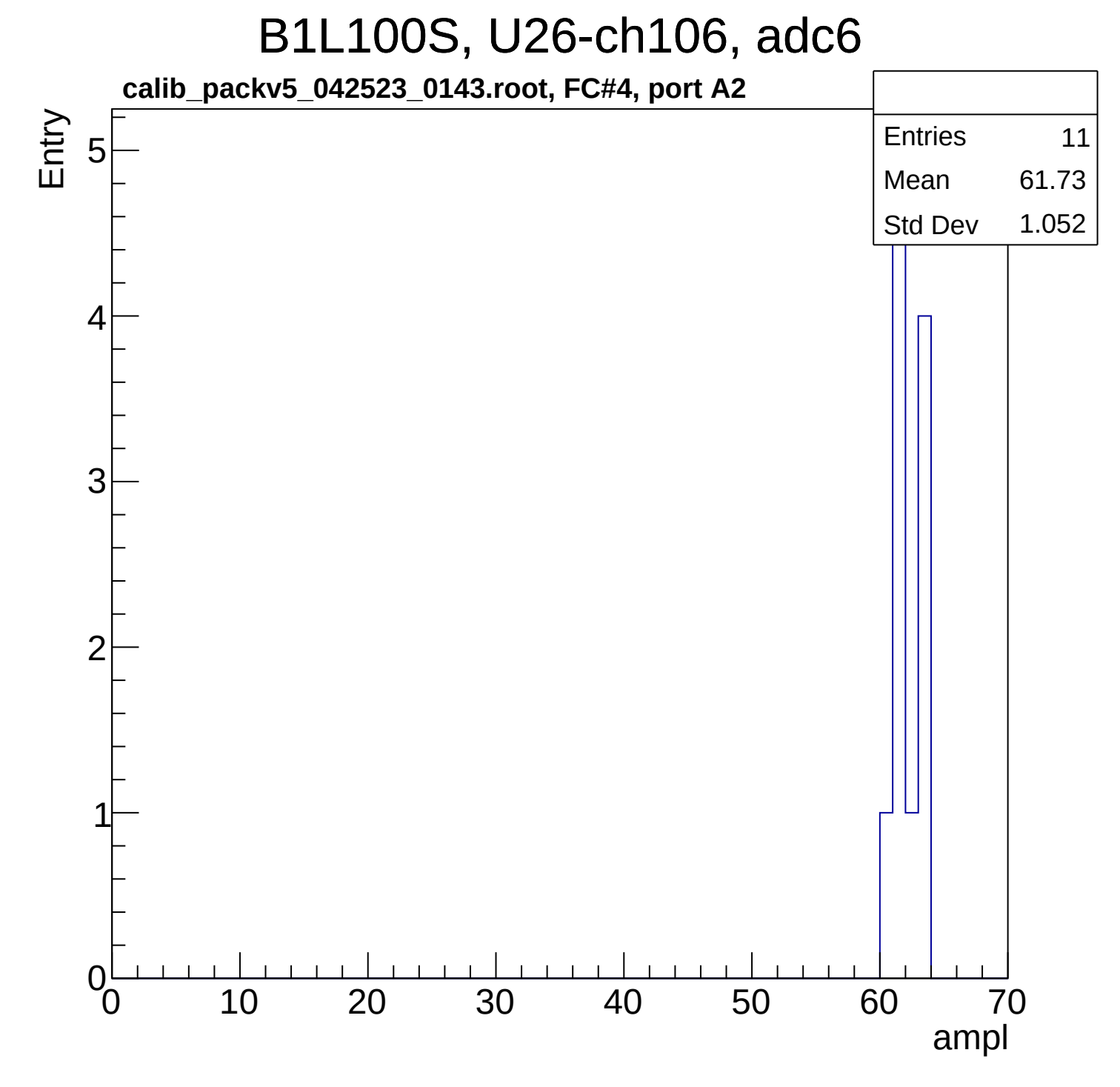
B1L100S, U26-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.73
Std Dev 1.052
Entry
ampl
0
1
2
3
4
5
0
10
20
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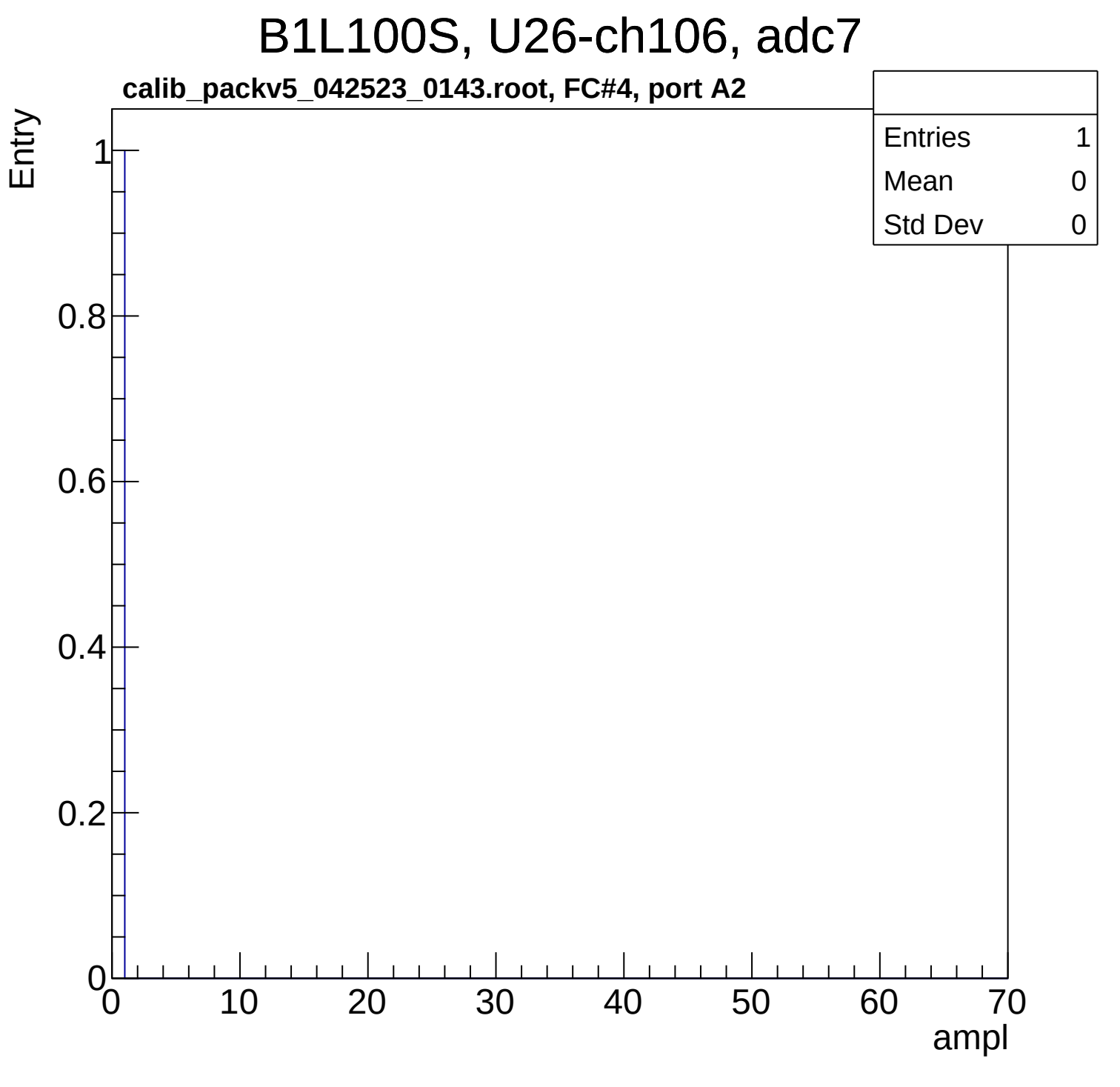
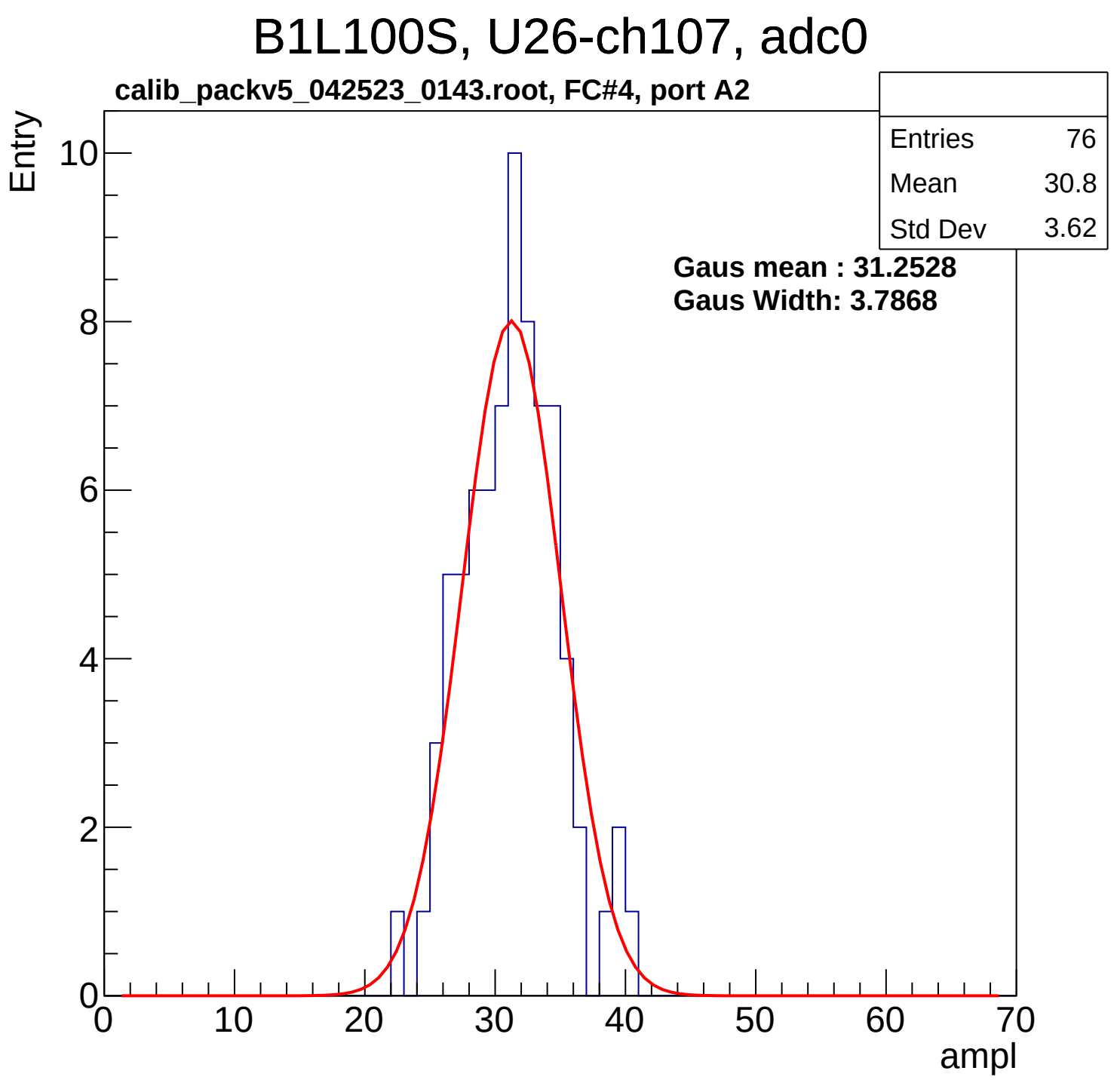
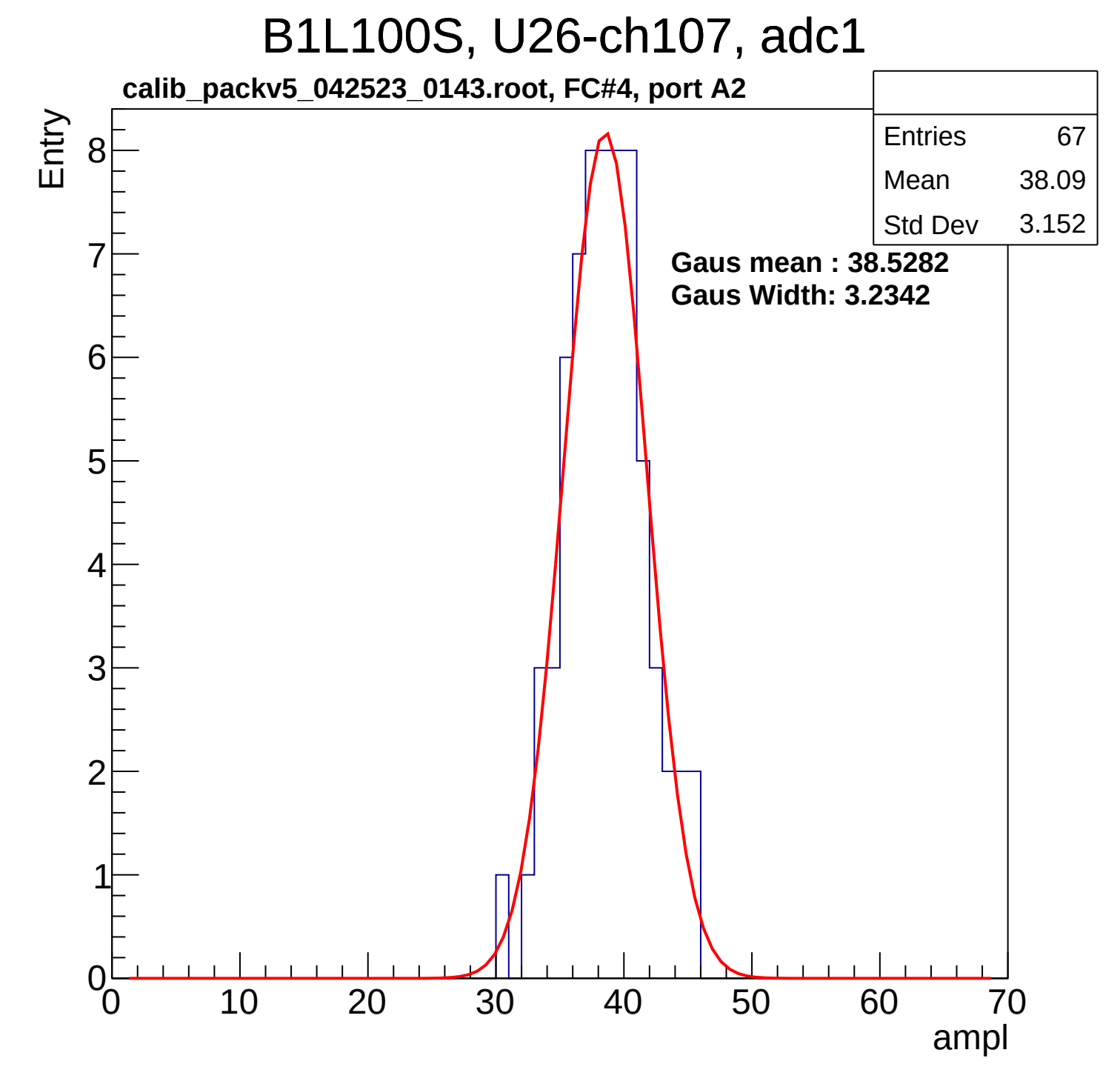
40
50
60
70


B1L100S, U26-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch107, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 30.8
Std Dev 3.62
Gaus mean : 31.2528
Gaus Width: 3.7868
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 38.09
Std Dev 3.152
Gaus mean : 38.5282
Gaus Width: 3.2342
Entry
ampl
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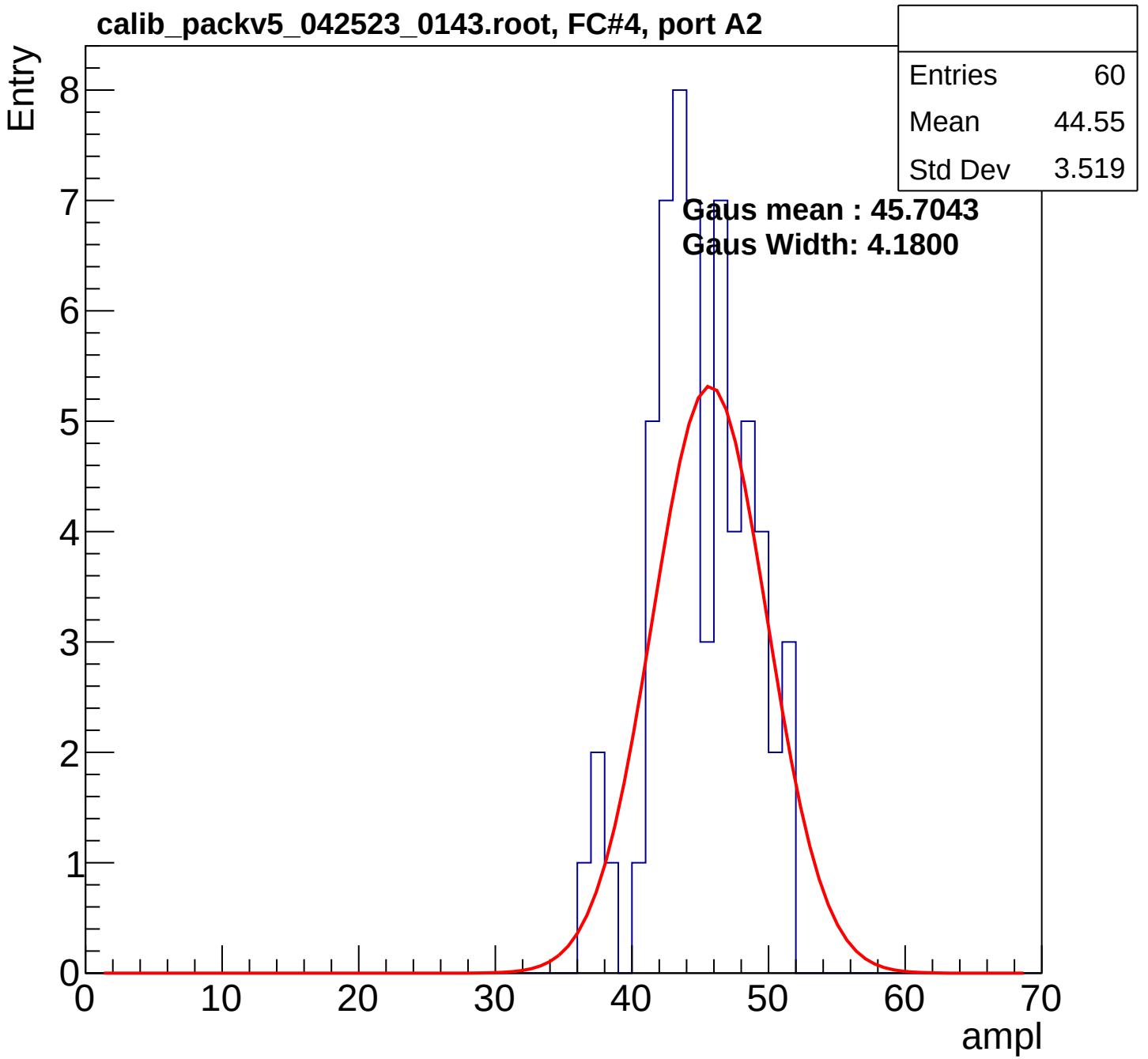
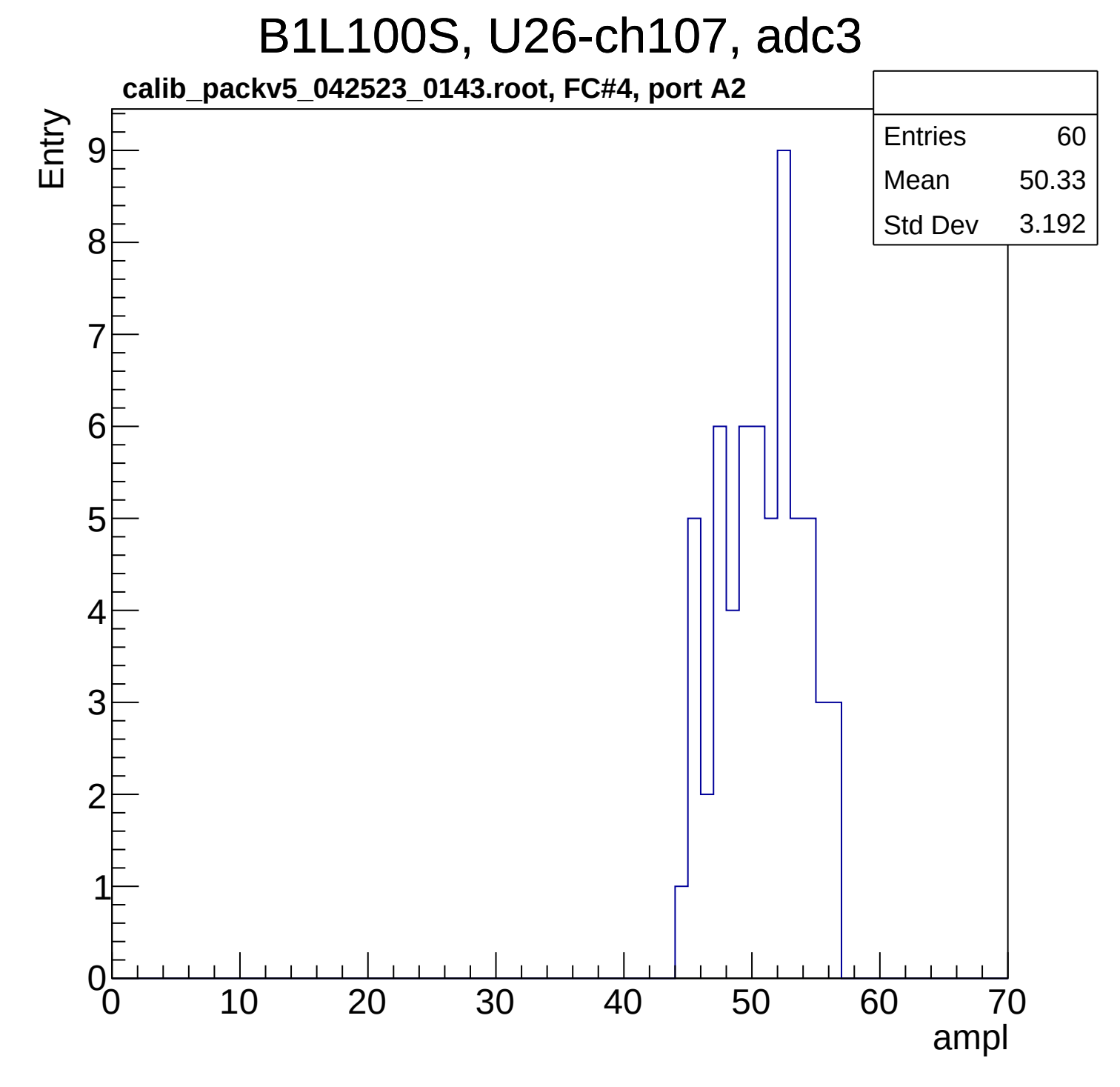
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.55
Std Dev 3.519
Gaus mean : 45.7043
Gaus Width: 4.1800
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 50.33
Std Dev 3.192
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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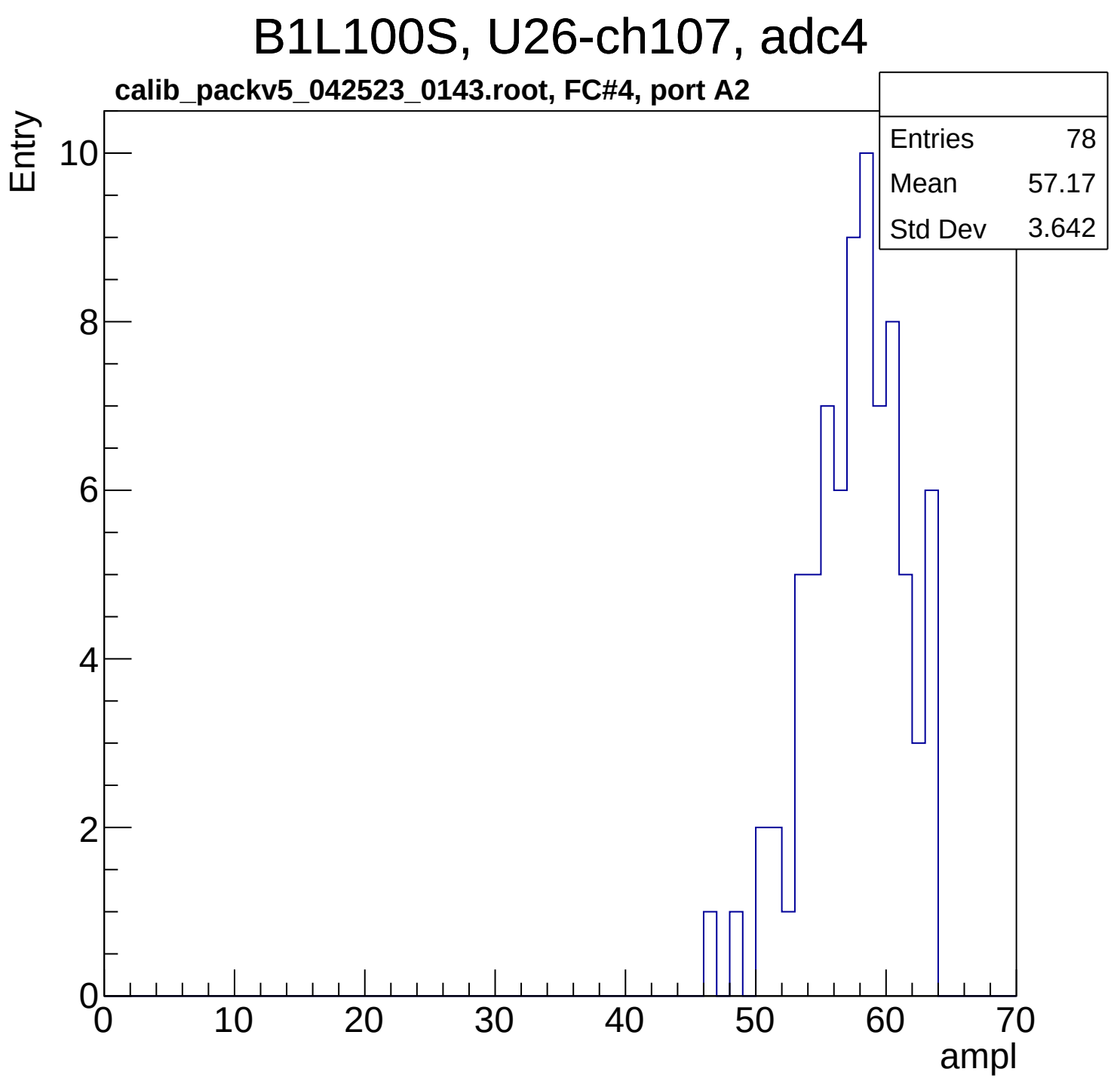
B1L100S, U26-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 57.17
Std Dev 3.642
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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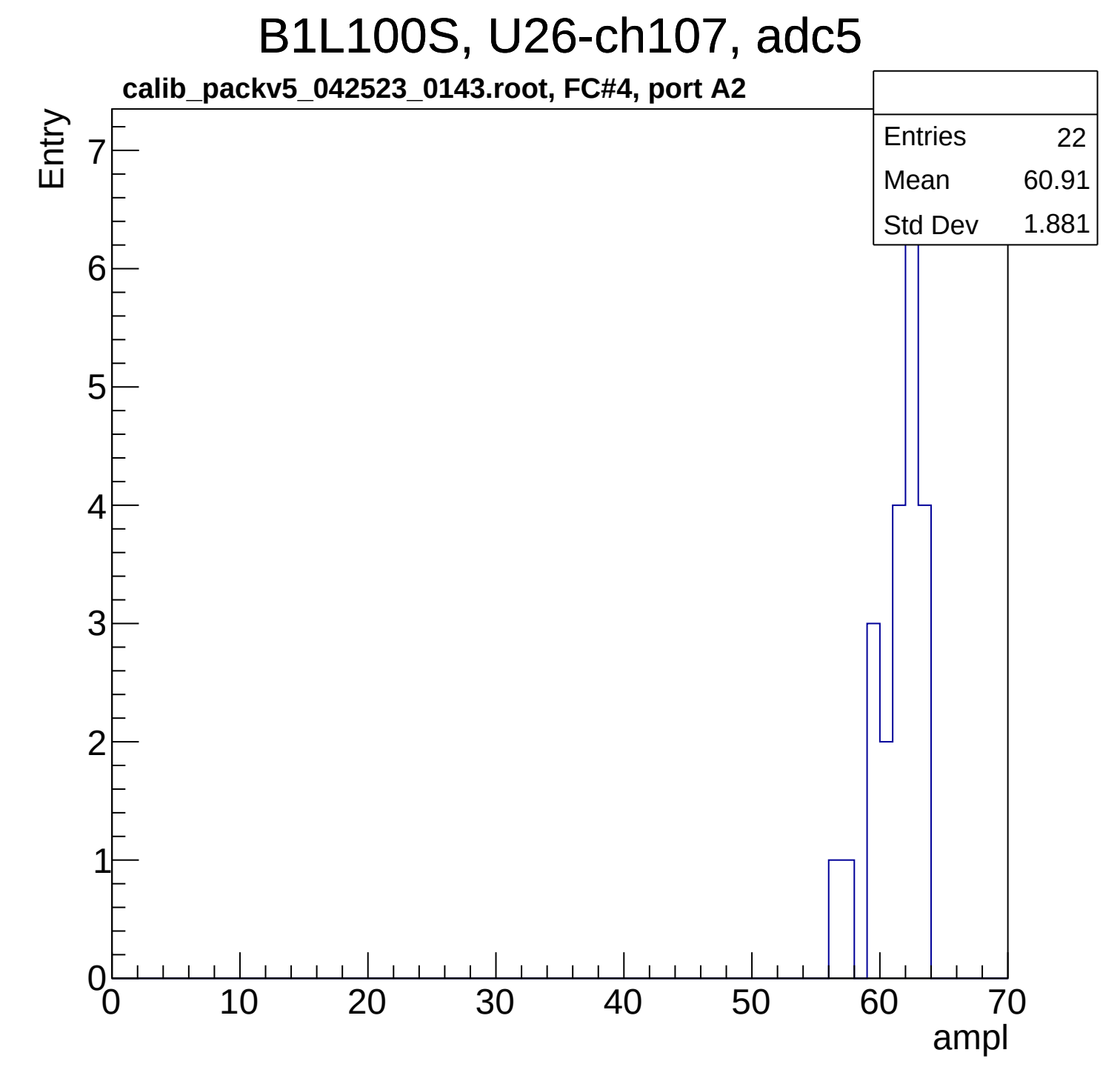
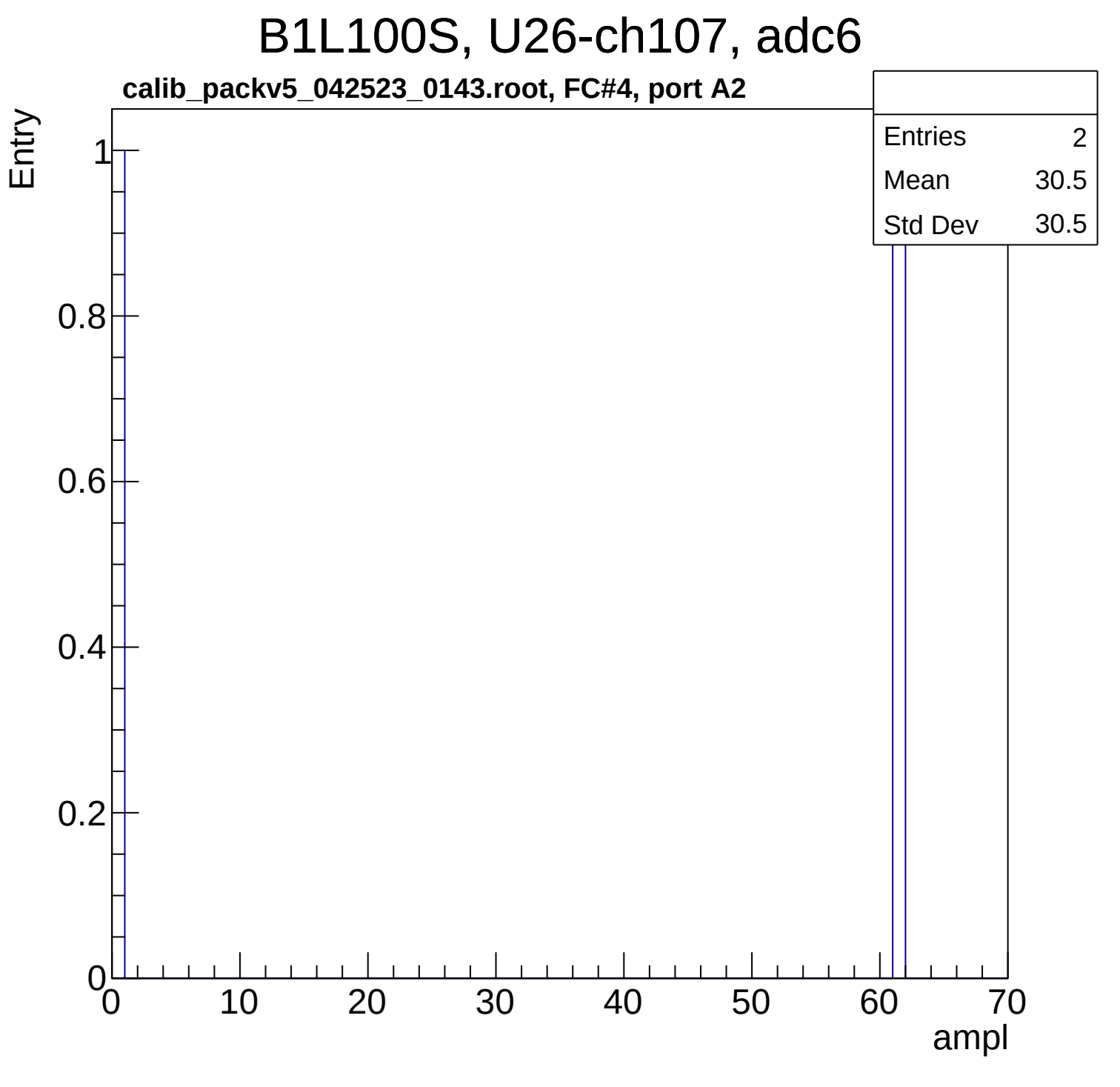
60
70


B1L100S, U26-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 60.91
Std Dev 1.881
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch107, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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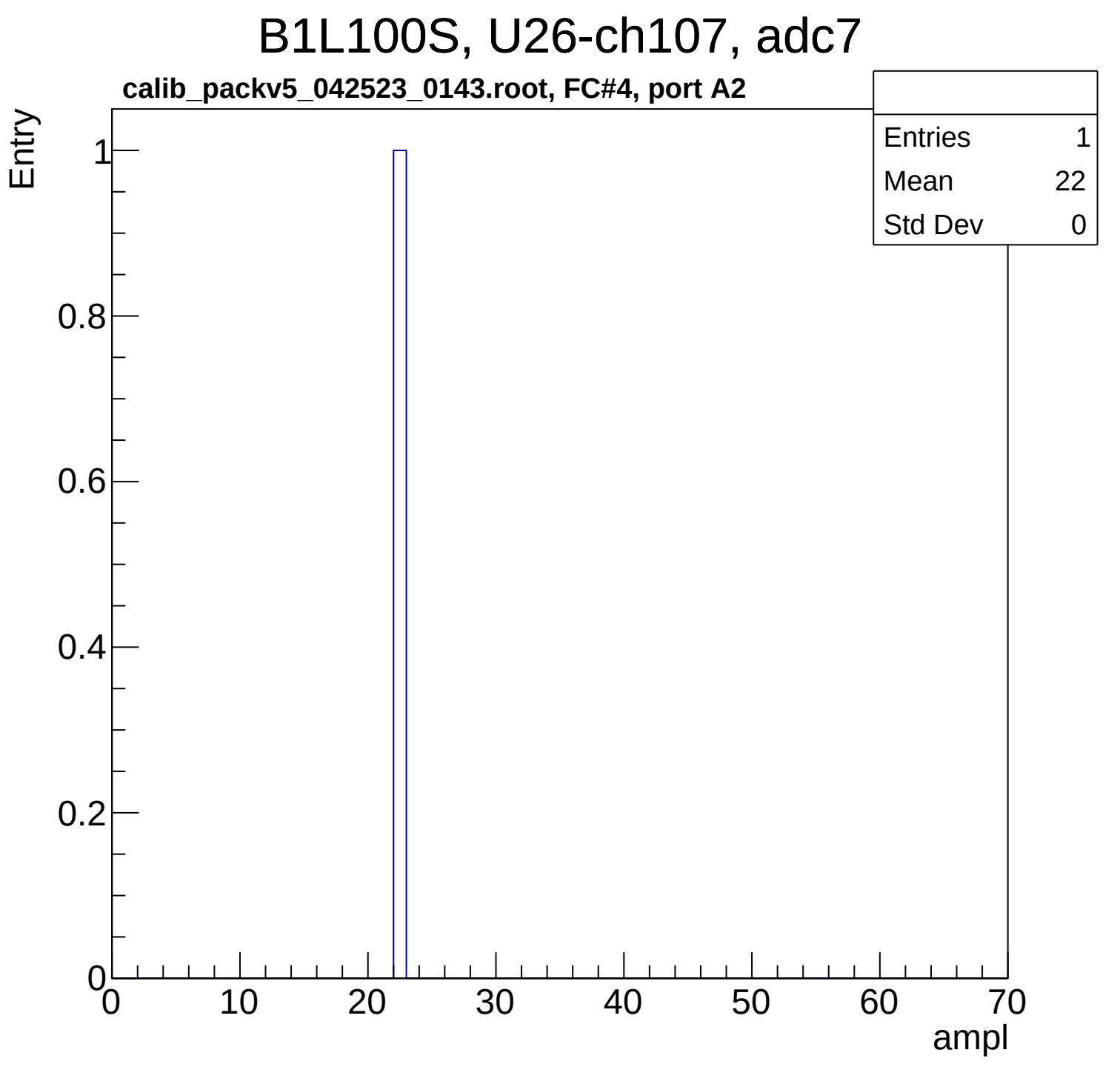
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch107, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
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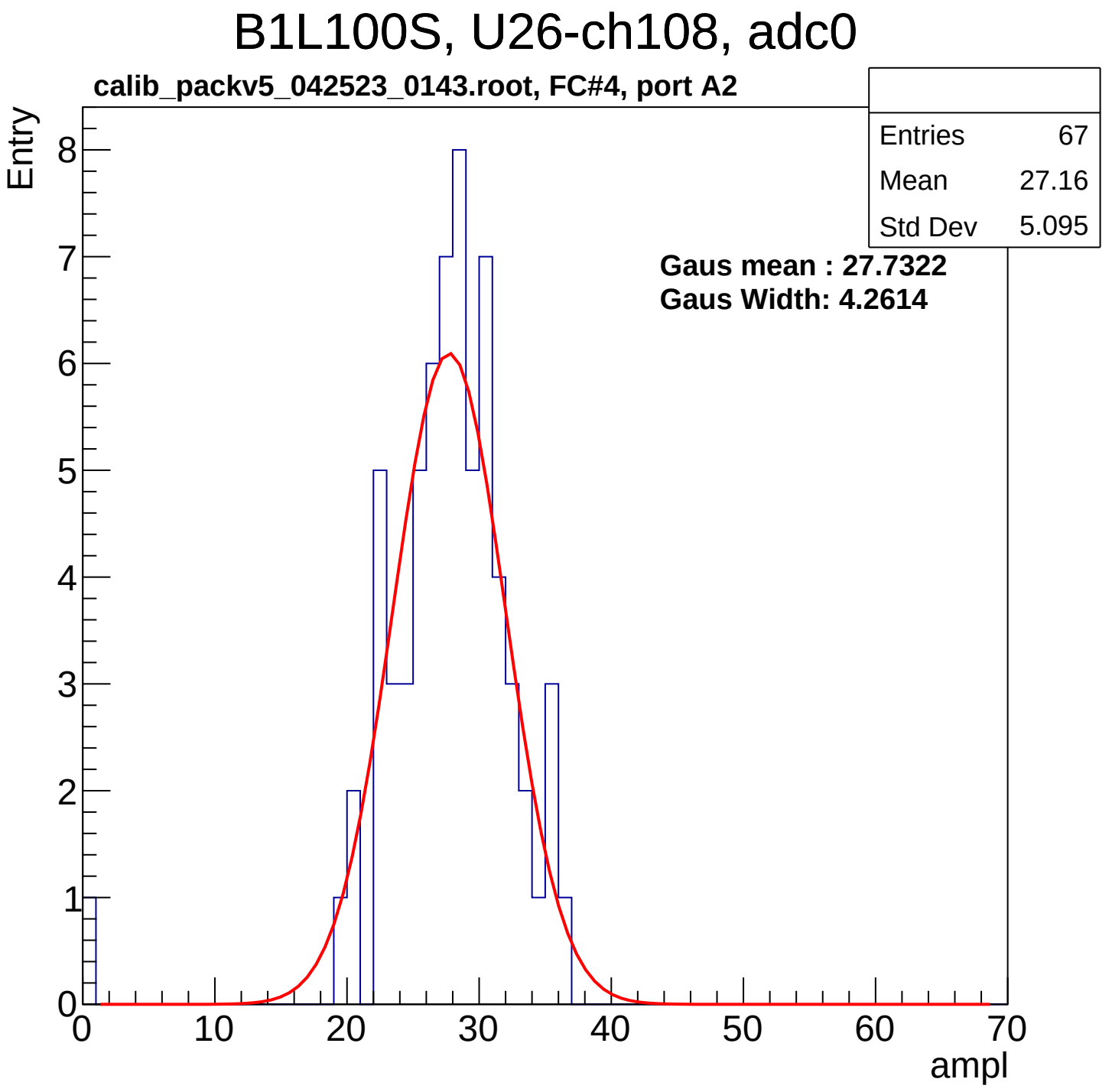
50
60
70
ampl


B1L100S, U26-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.16
Std Dev 5.095
Gaus mean : 27.7322
Gaus Width: 4.2614
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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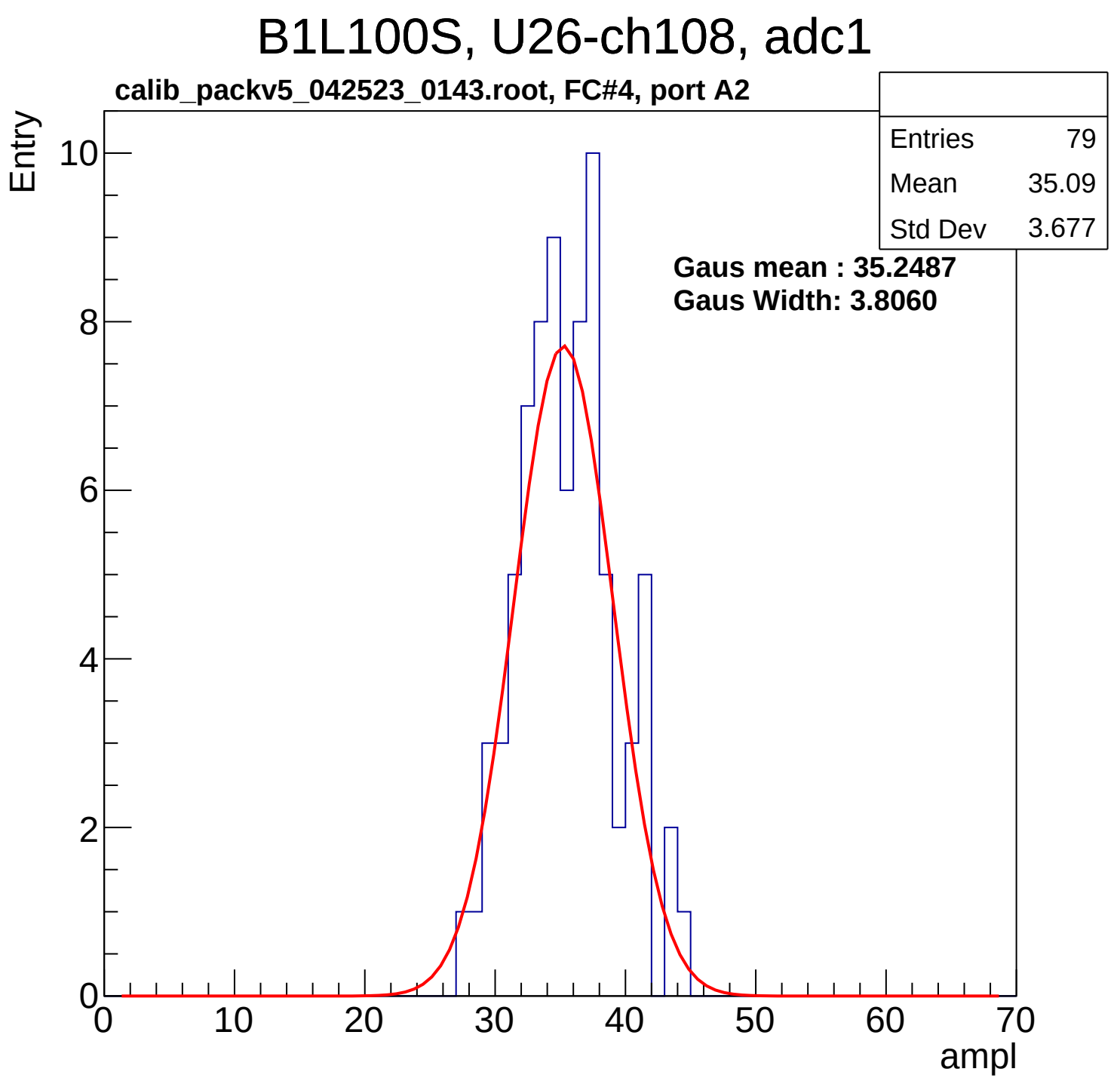
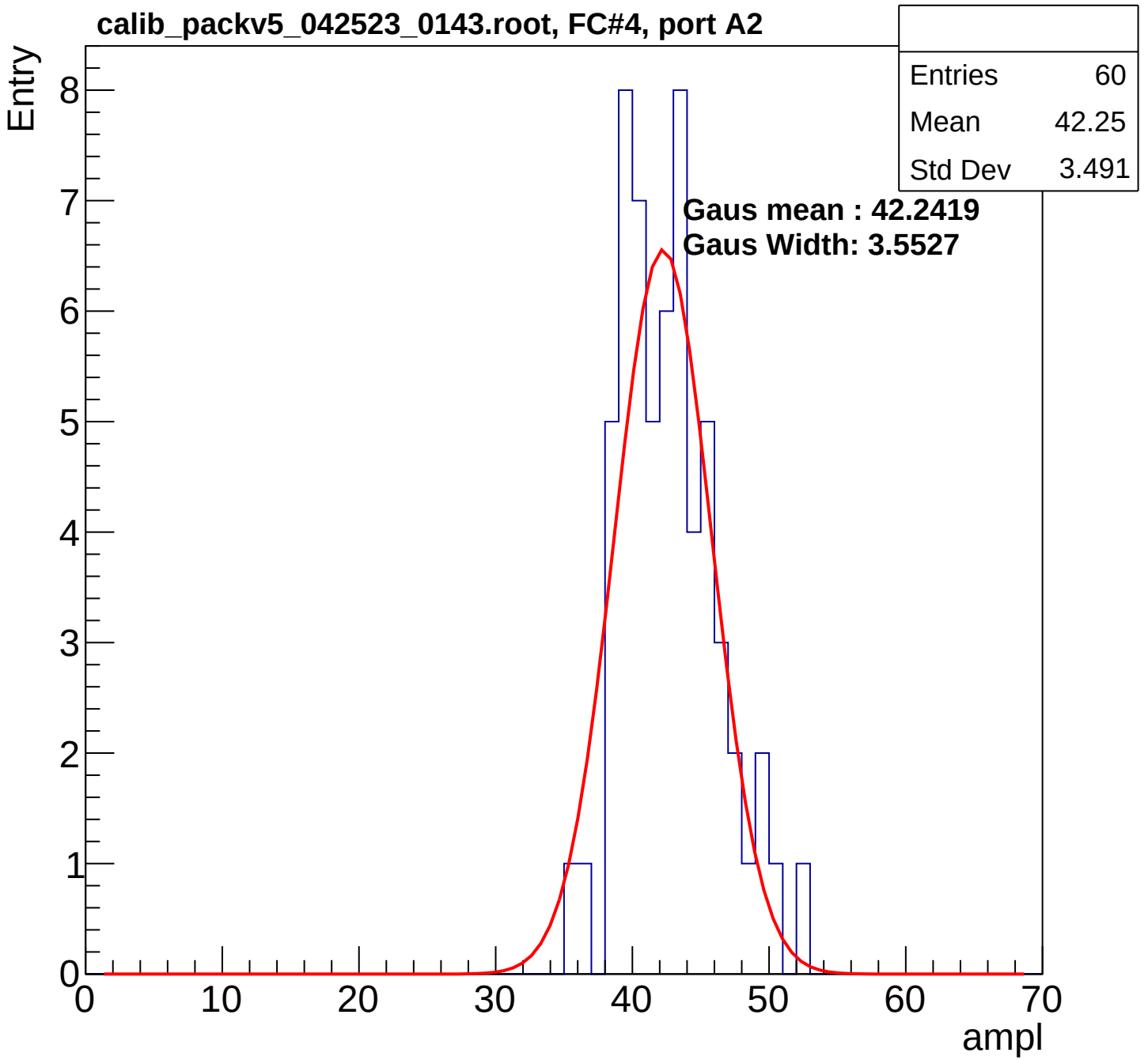

B1L100S, U26-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.09
Std Dev 3.677
Gaus mean : 35.2487
Gaus Width: 3.8060
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.25
Std Dev 3.491
Gaus mean : 42.2419
Gaus Width: 3.5527
Entry
ampl
0
1
2
3
4
5
6
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8
0
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40
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60
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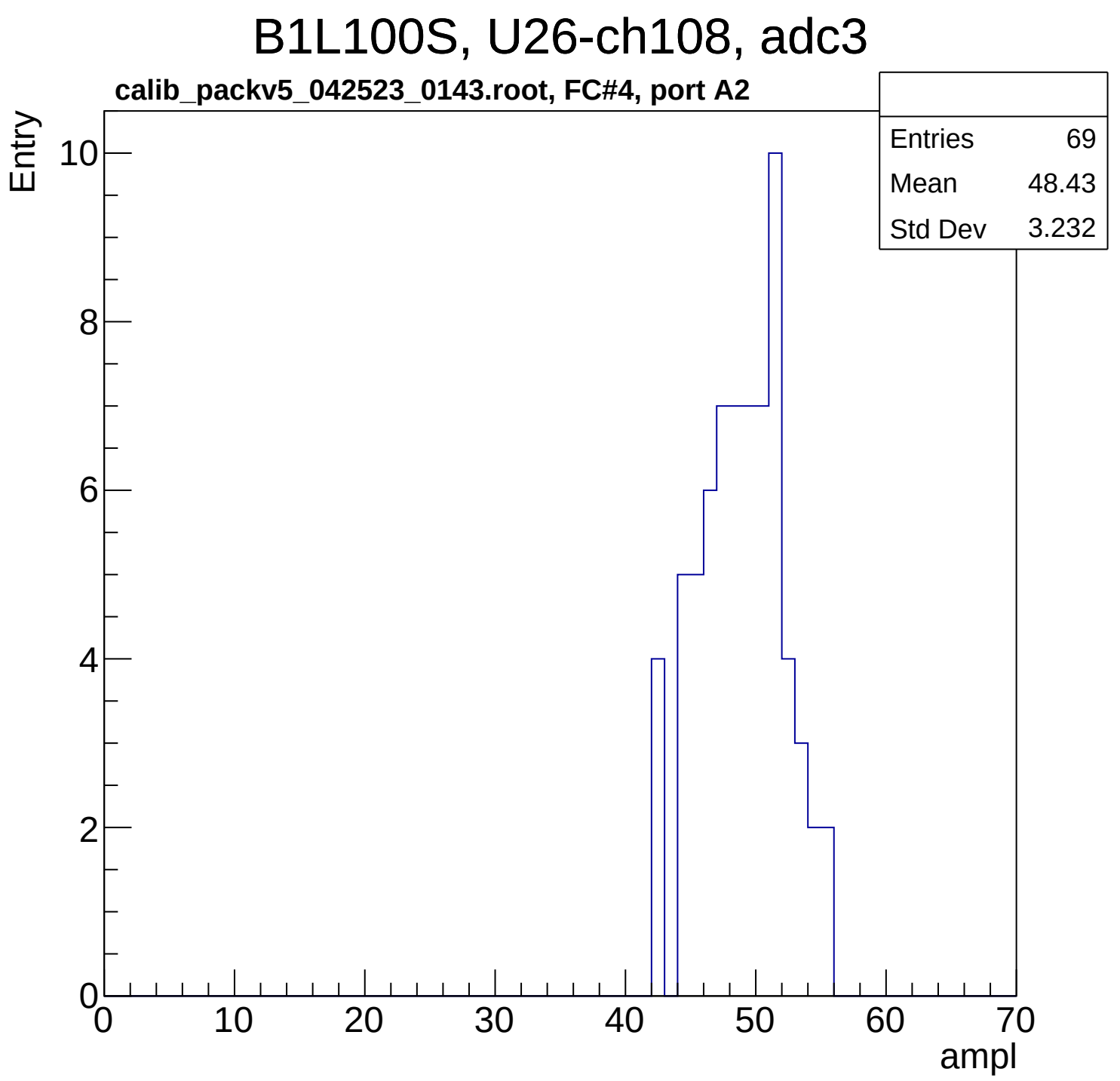
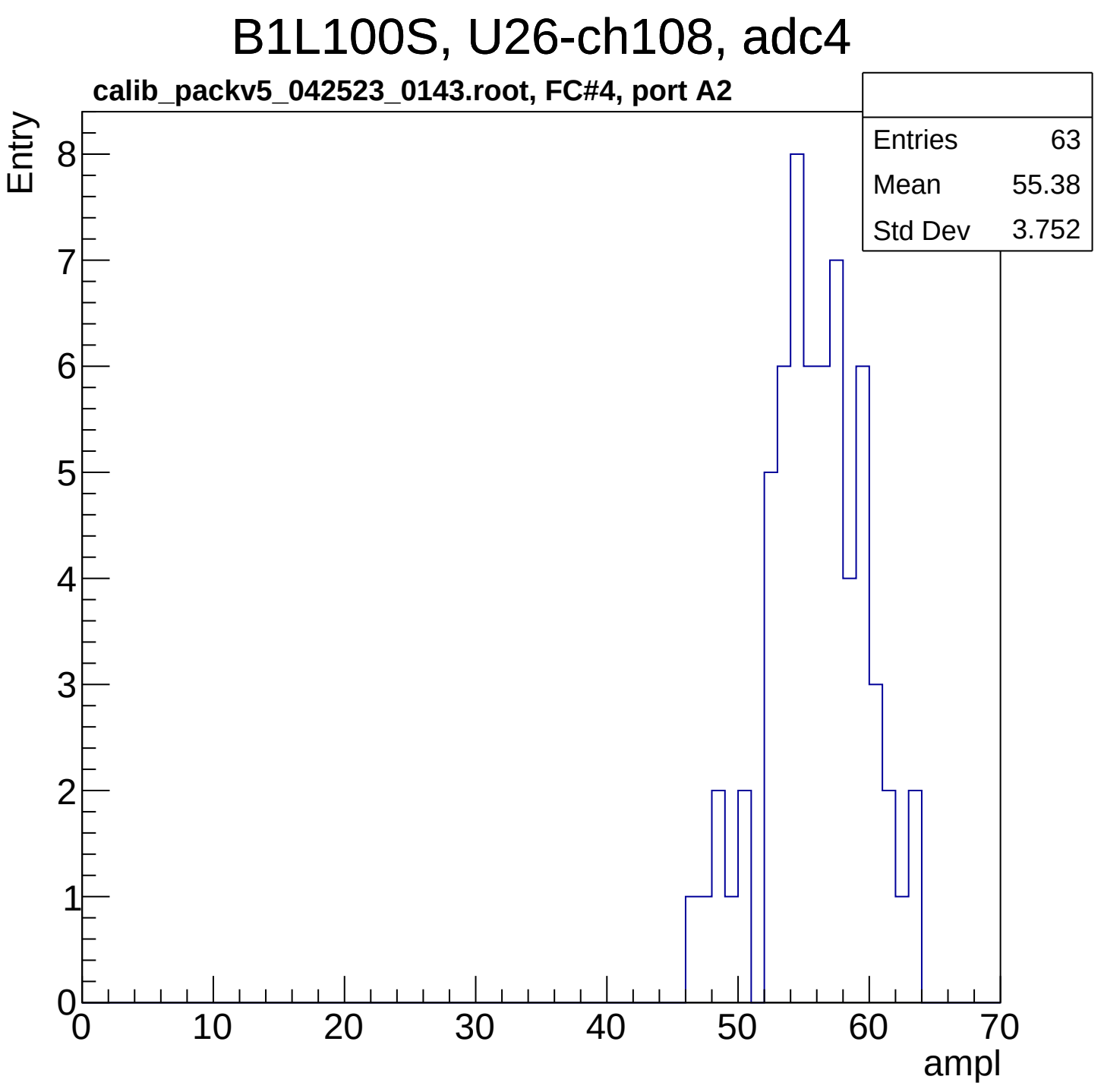

B1L100S, U26-ch108, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.43
Std Dev 3.232
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.38
Std Dev 3.752
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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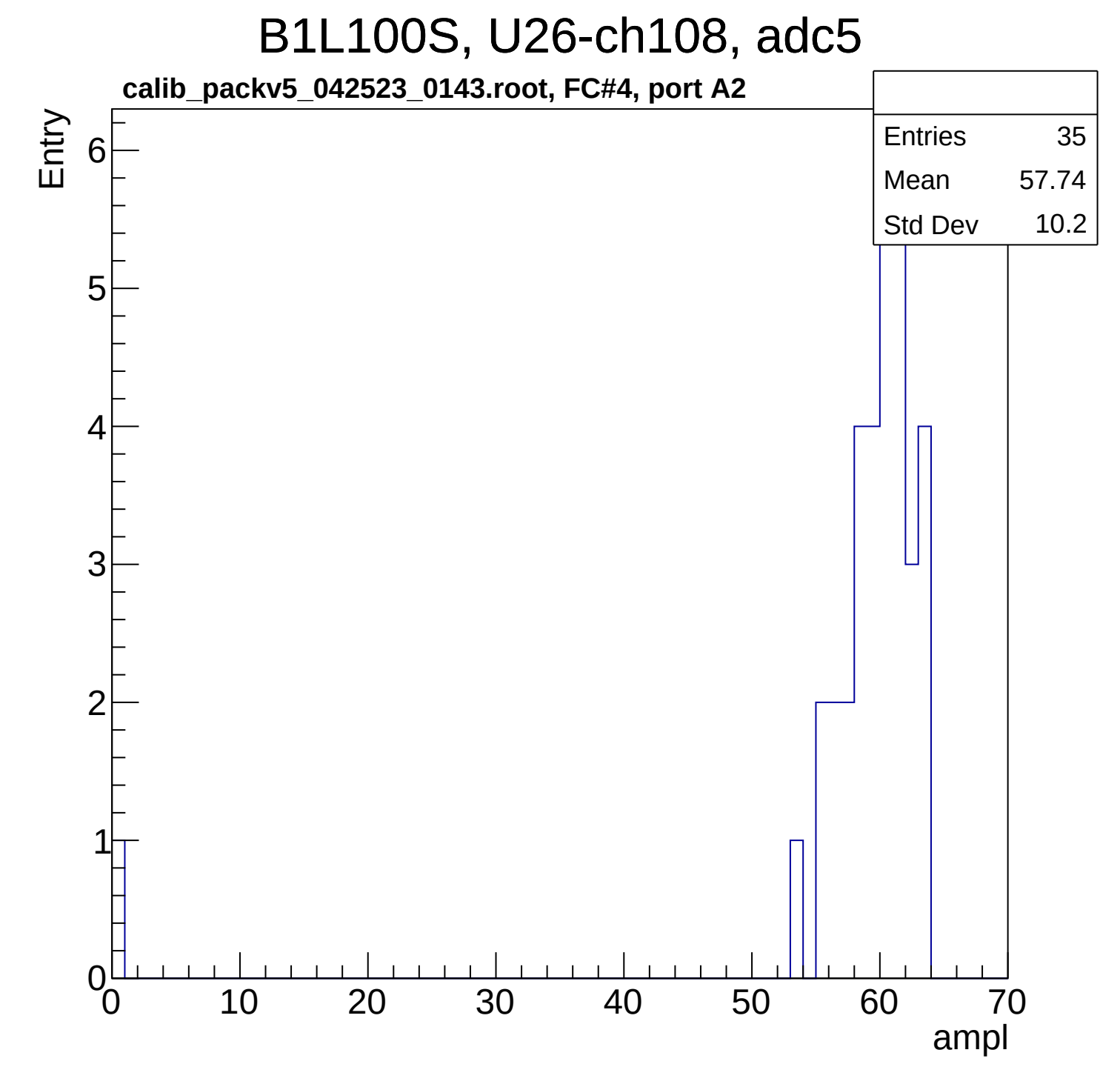
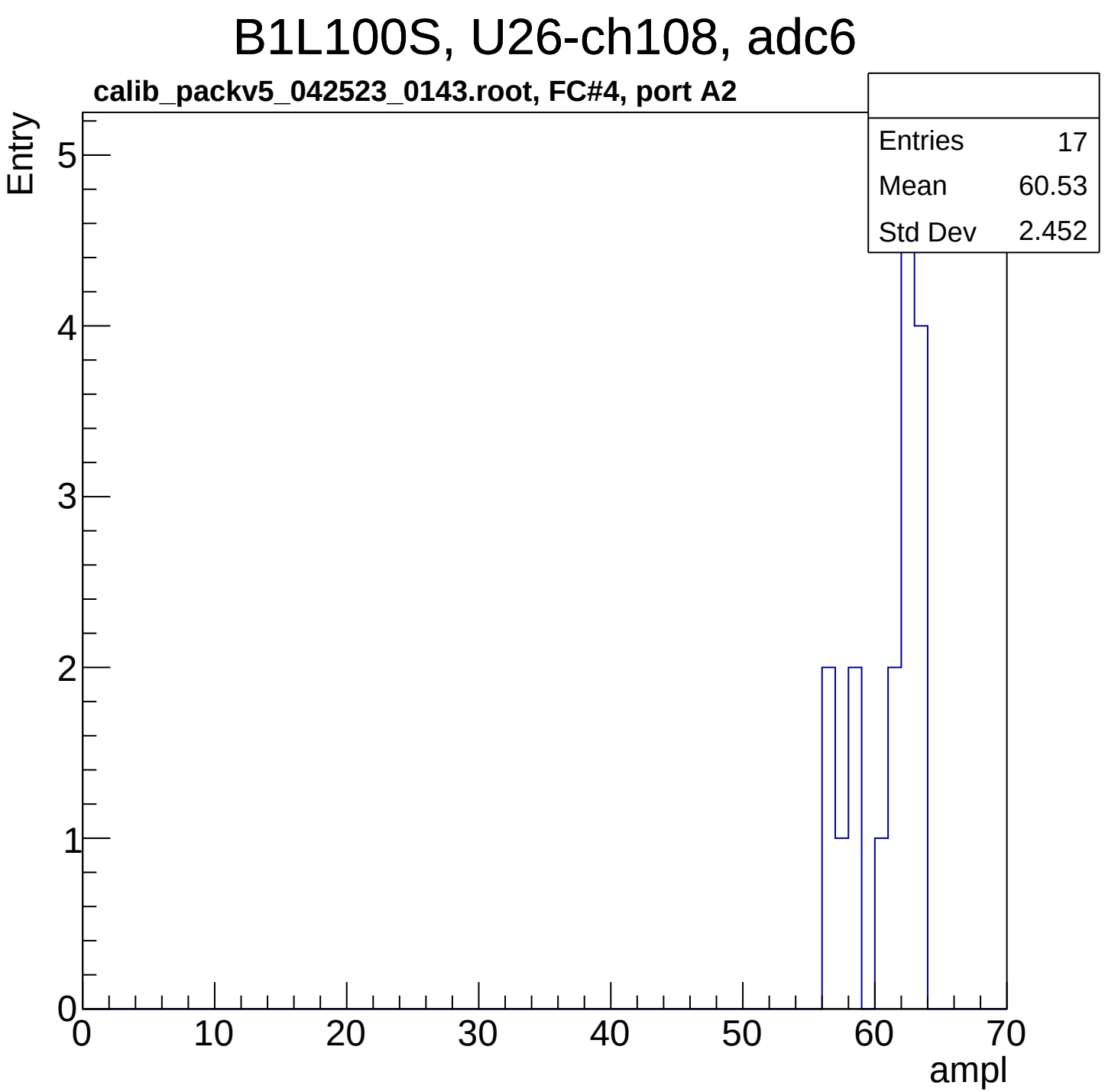
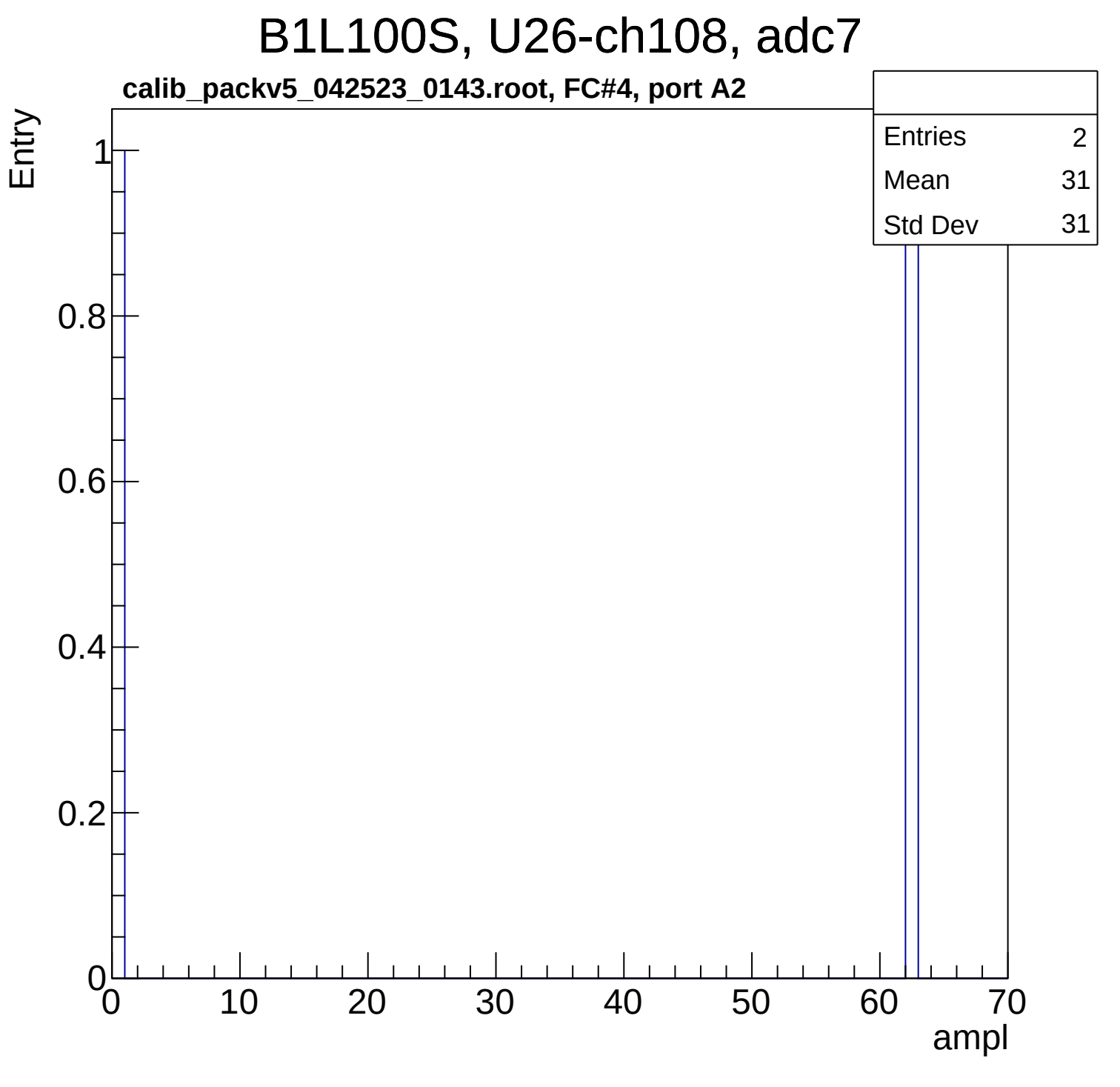

B1L100S, U26-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 57.74
Std Dev 10.2
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 17
Mean 60.53
Std Dev 2.452
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U26-ch108, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
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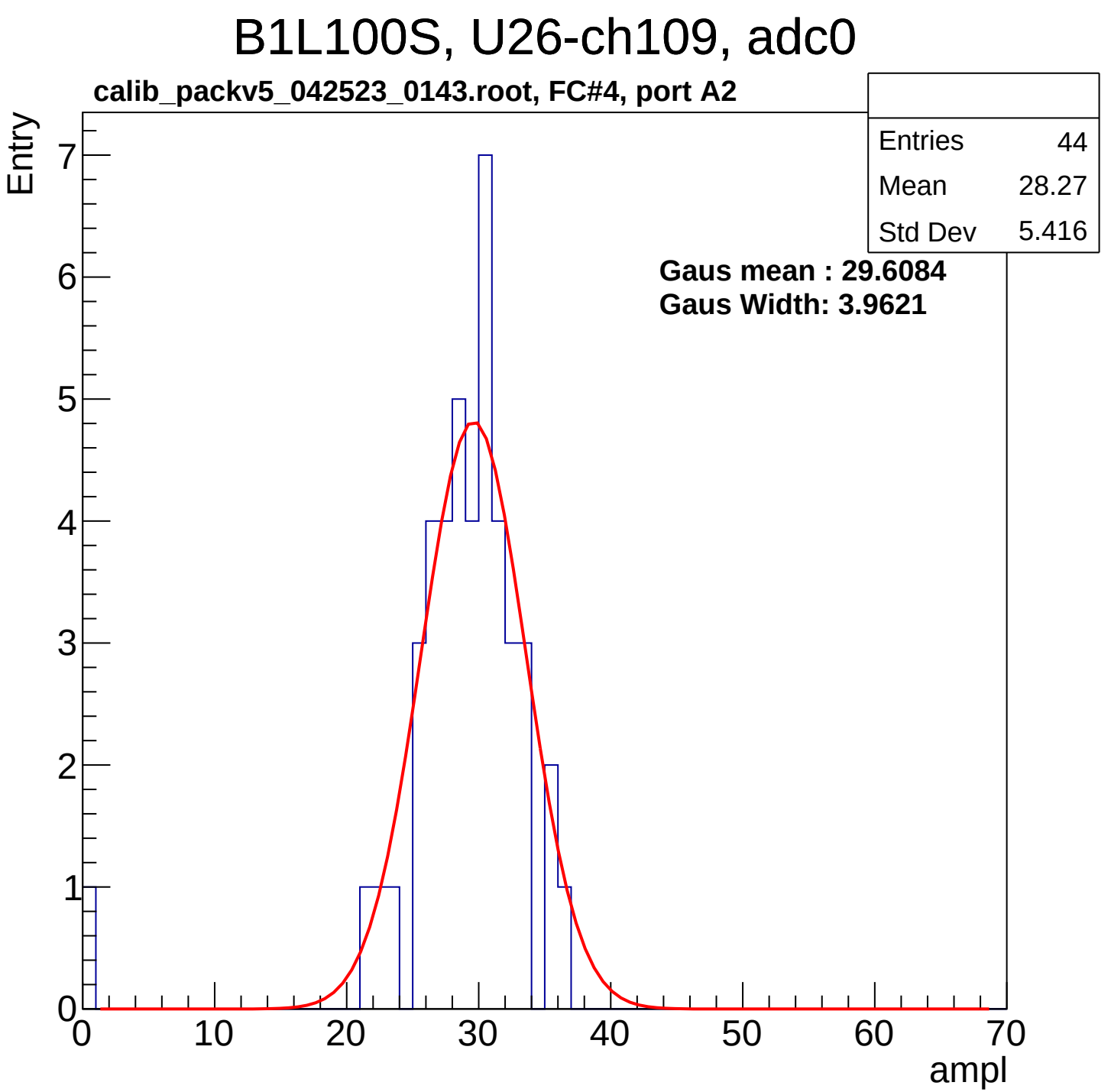
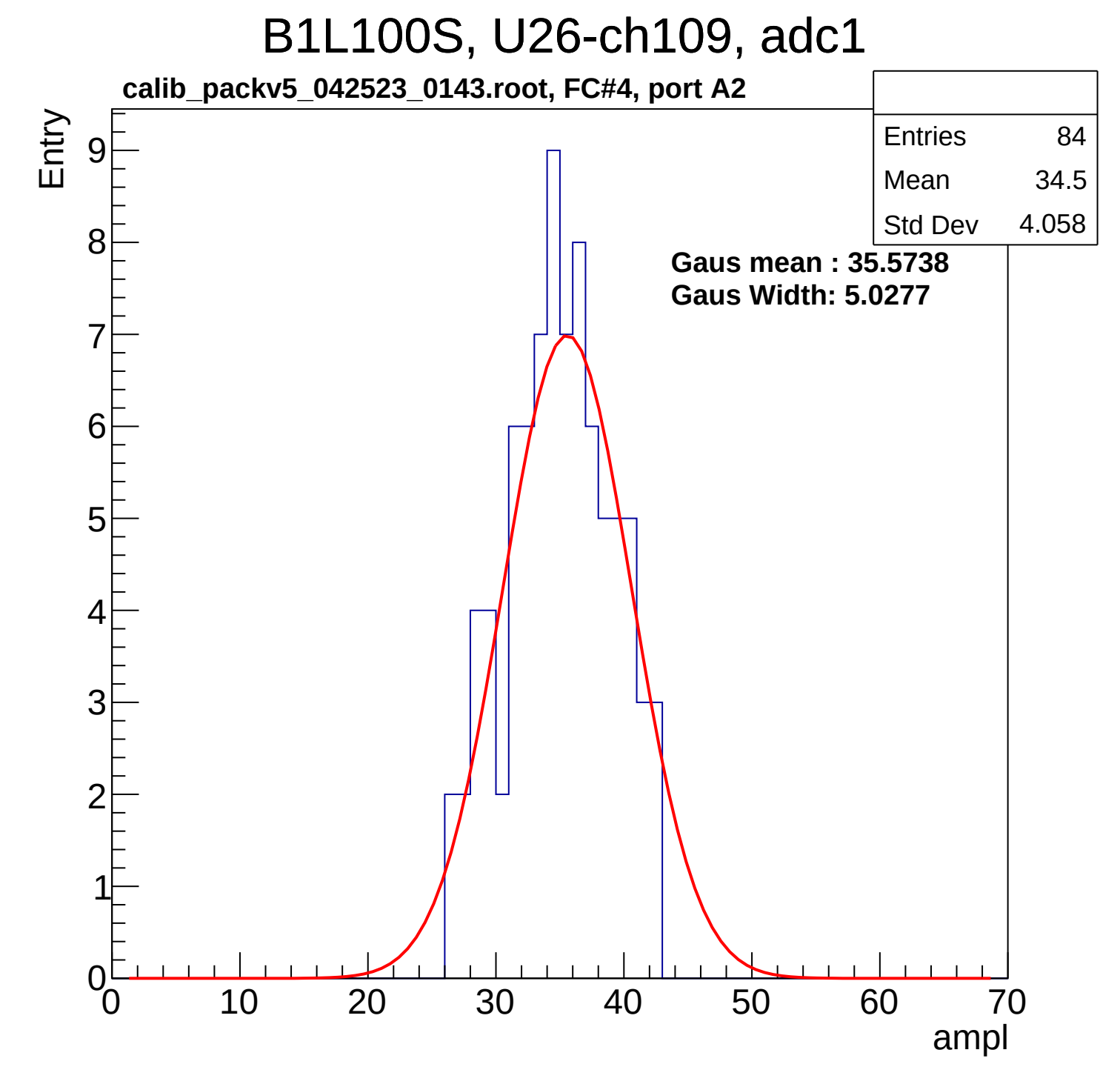
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 28.27
Std Dev 5.416
Gaus mean : 29.6084
Gaus Width: 3.9621
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 34.5
Std Dev 4.058
Gaus mean : 35.5738
Gaus Width: 5.0277
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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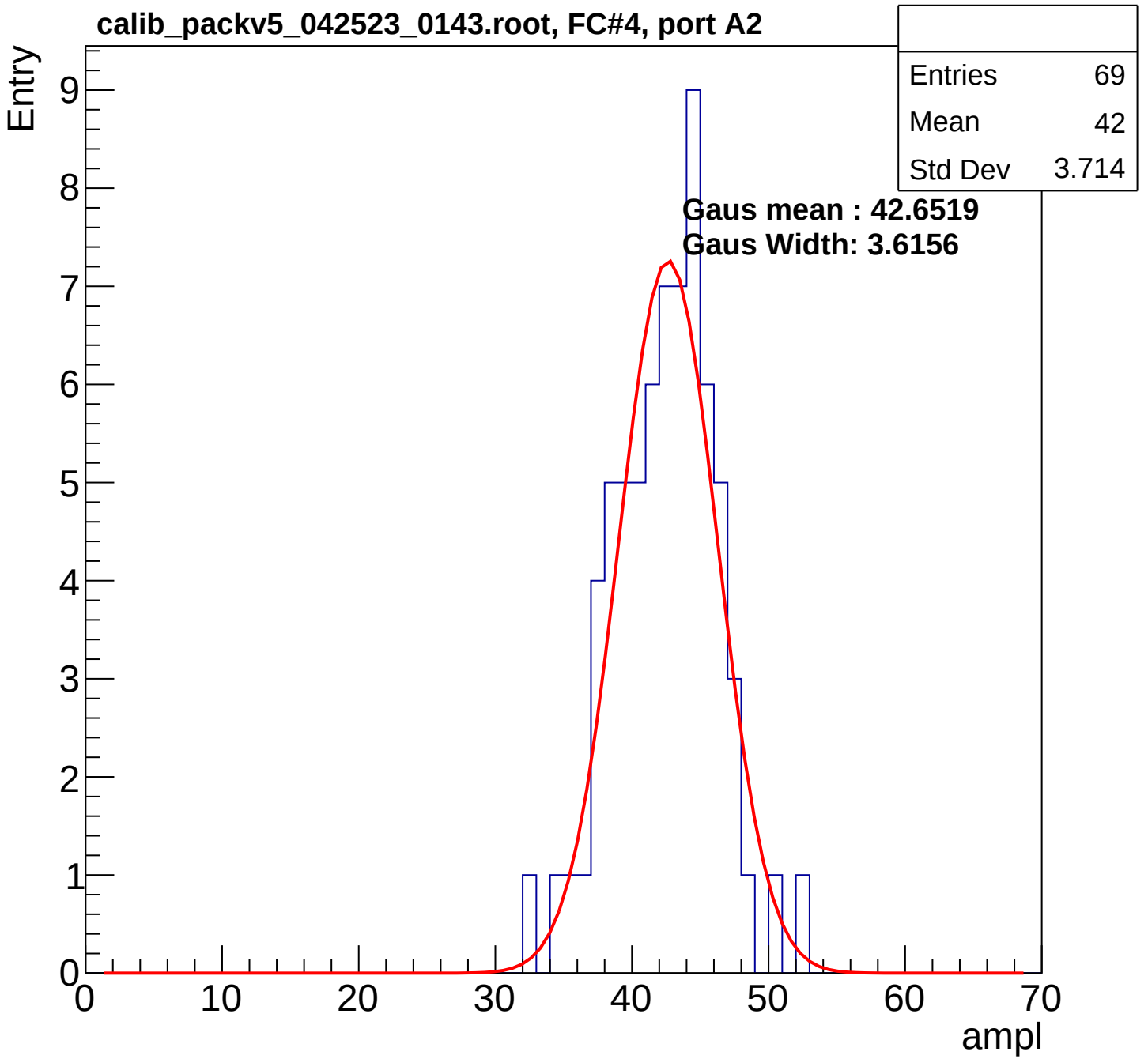
B1L100S, U26-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42
Std Dev 3.714
Gaus mean : 42.6519
Gaus Width: 3.6156
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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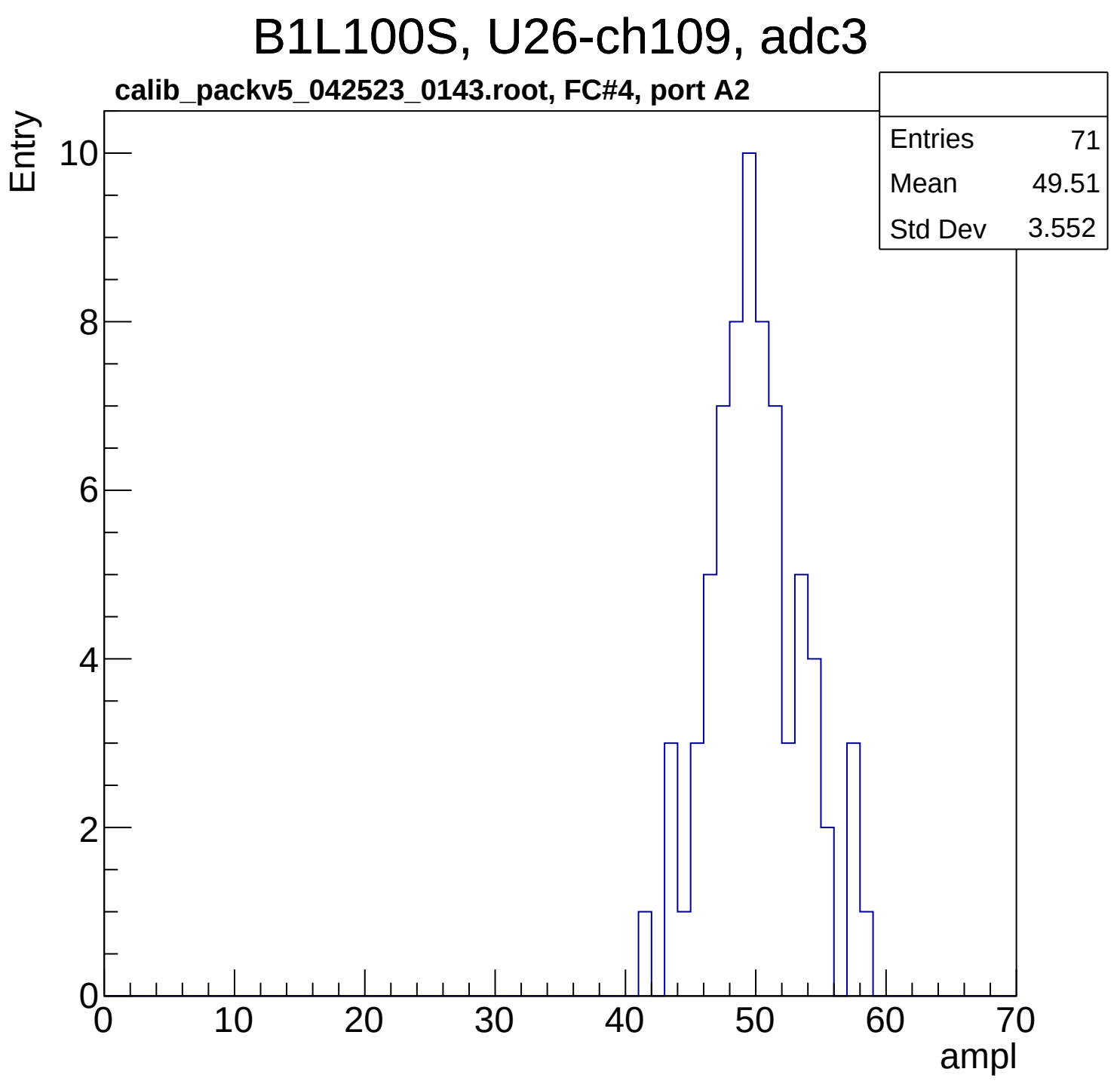
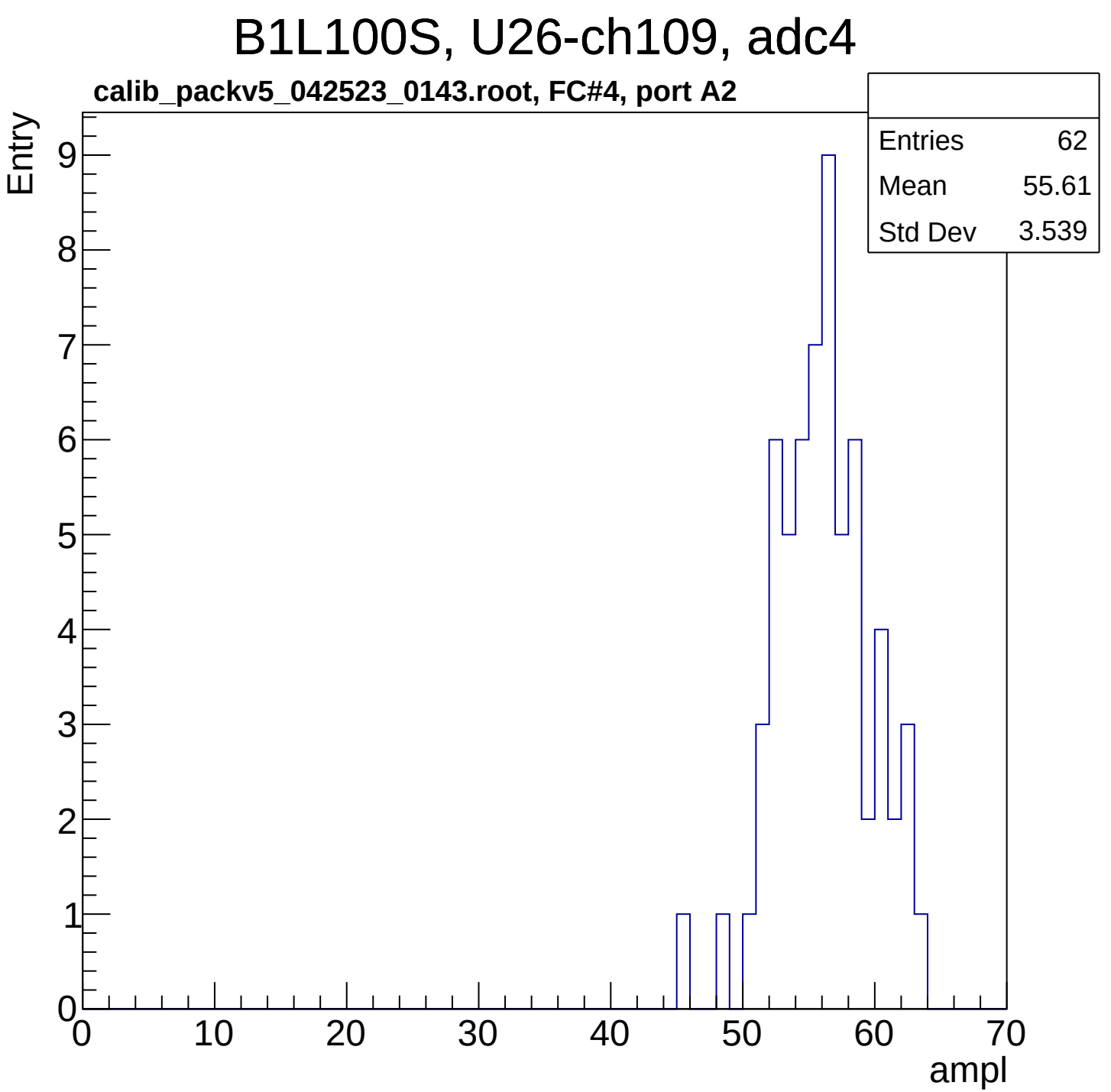

B1L100S, U26-ch109, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.51
Std Dev 3.552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch109, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.61
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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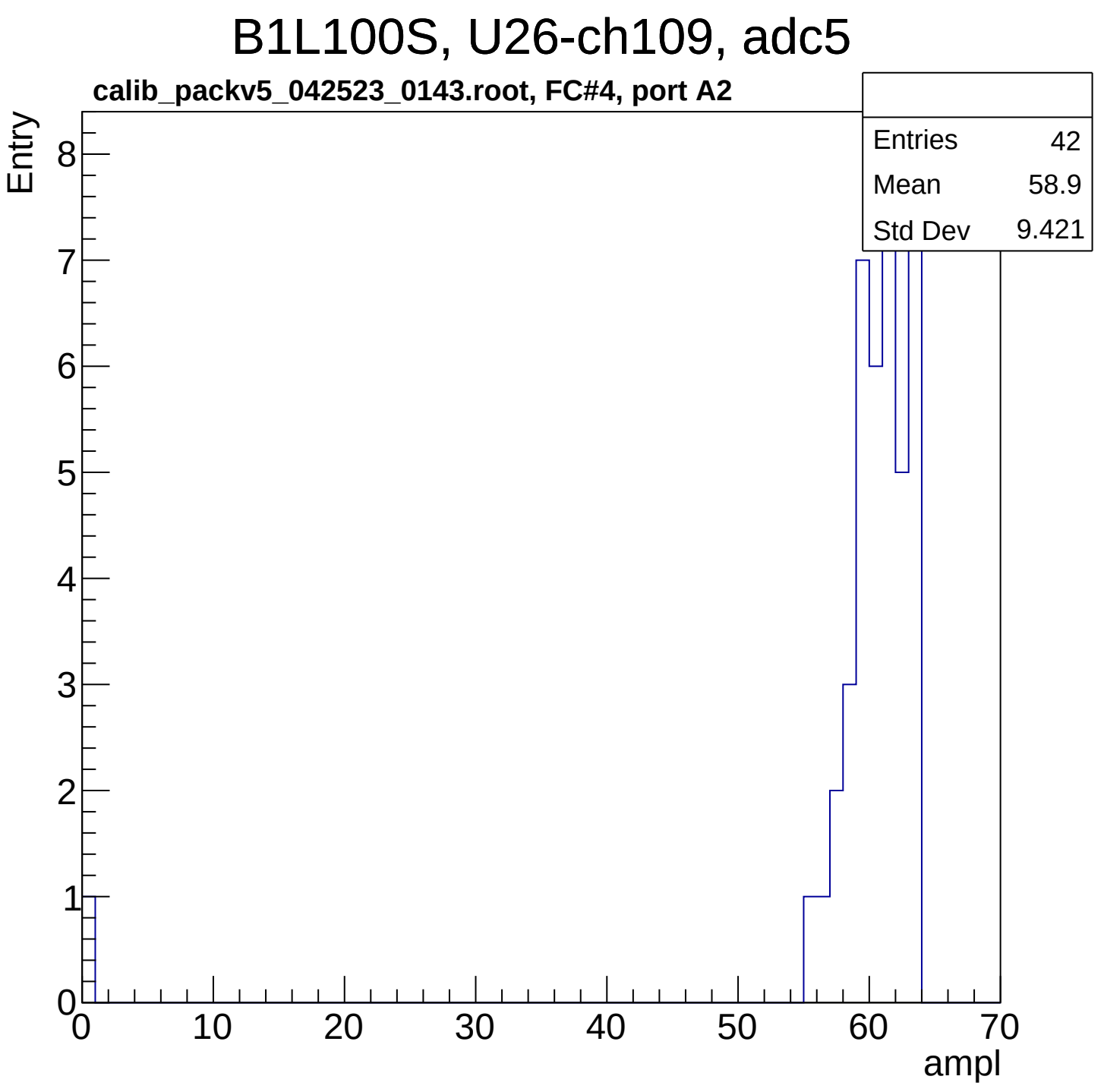
B1L100S, U26-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.9
Std Dev 9.421
Entry
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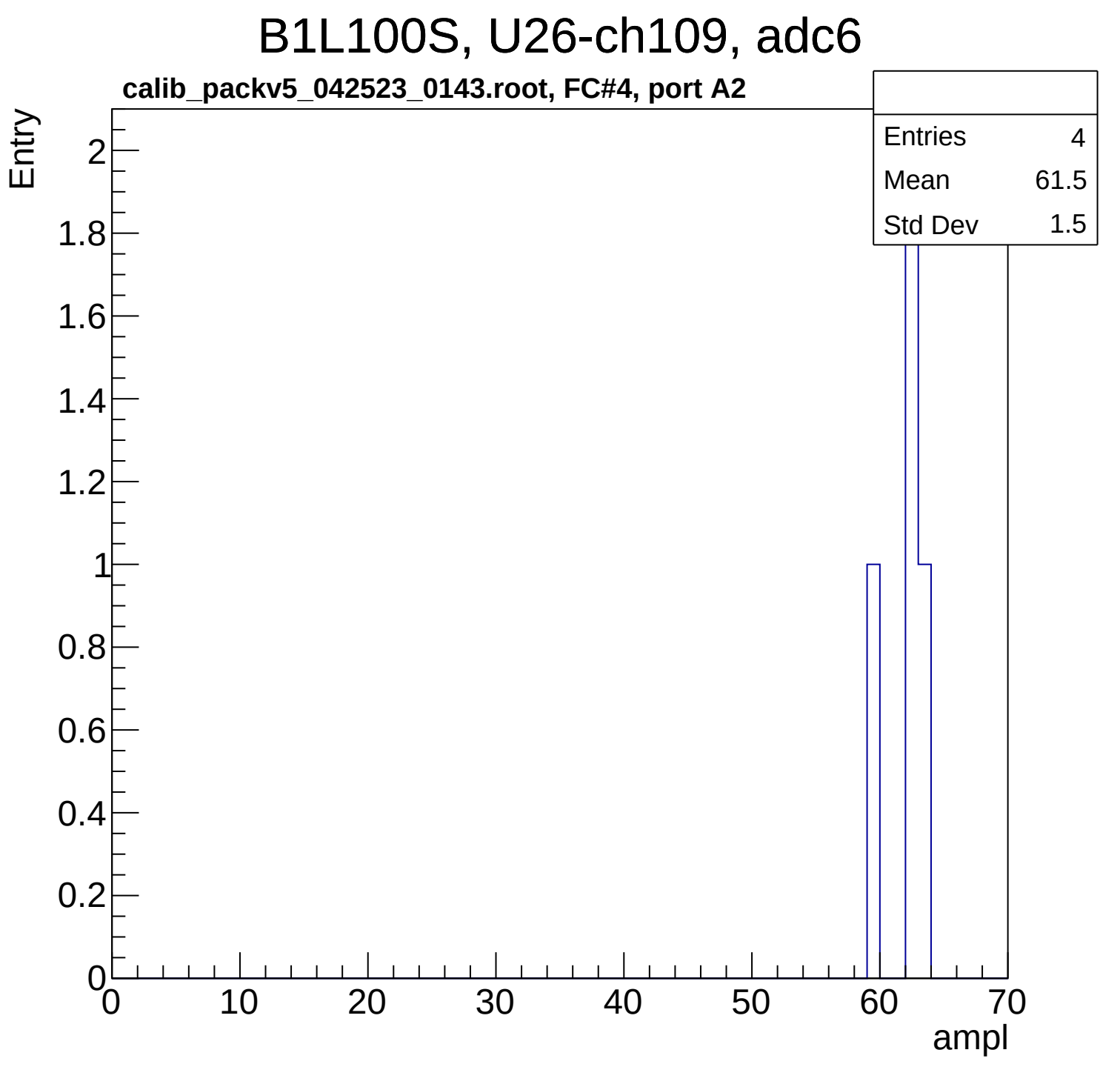
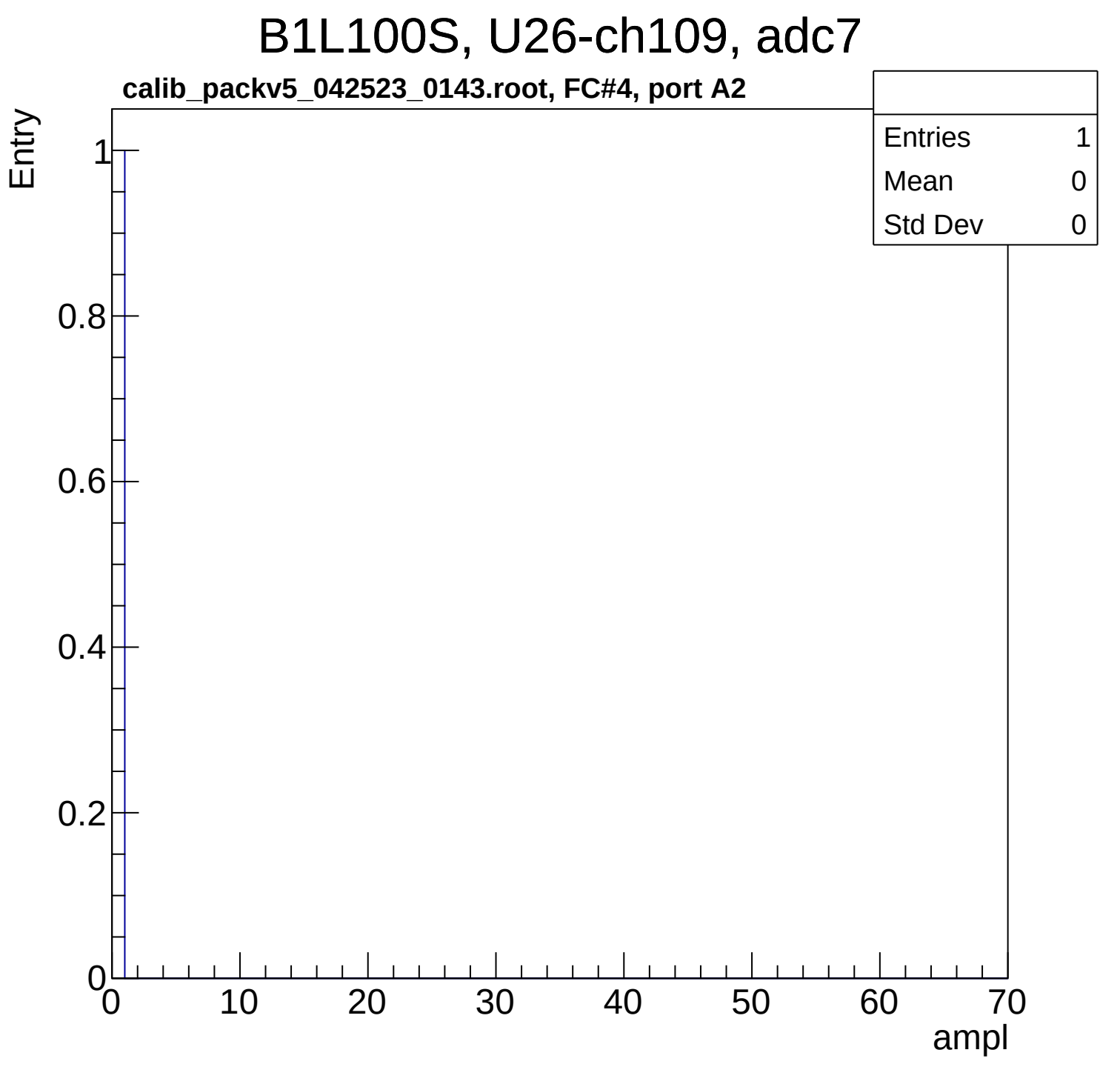
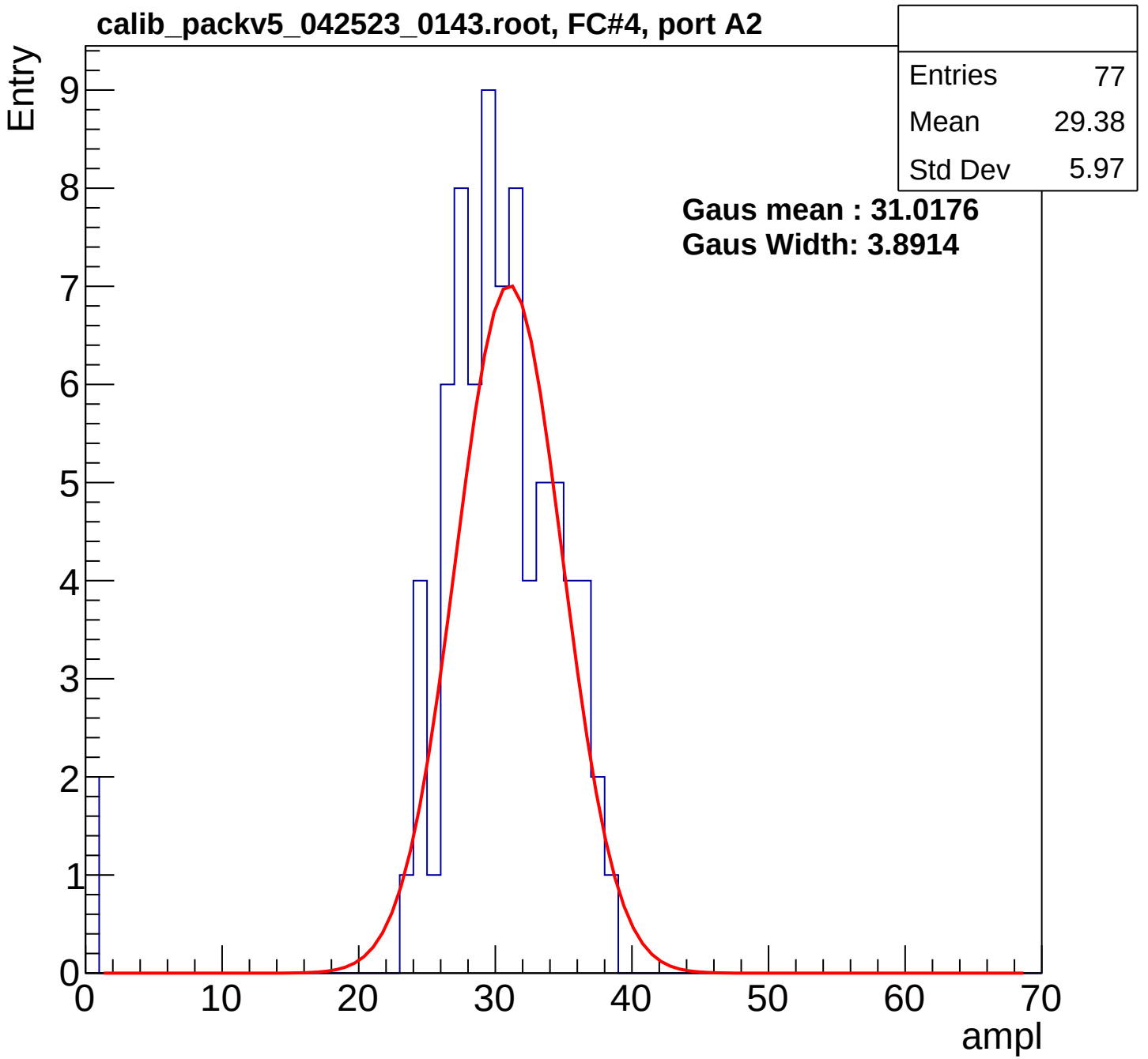
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch109, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch109, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.38
Std Dev 5.97
Gaus mean : 31.0176
Gaus Width: 3.8914
Entry
ampl
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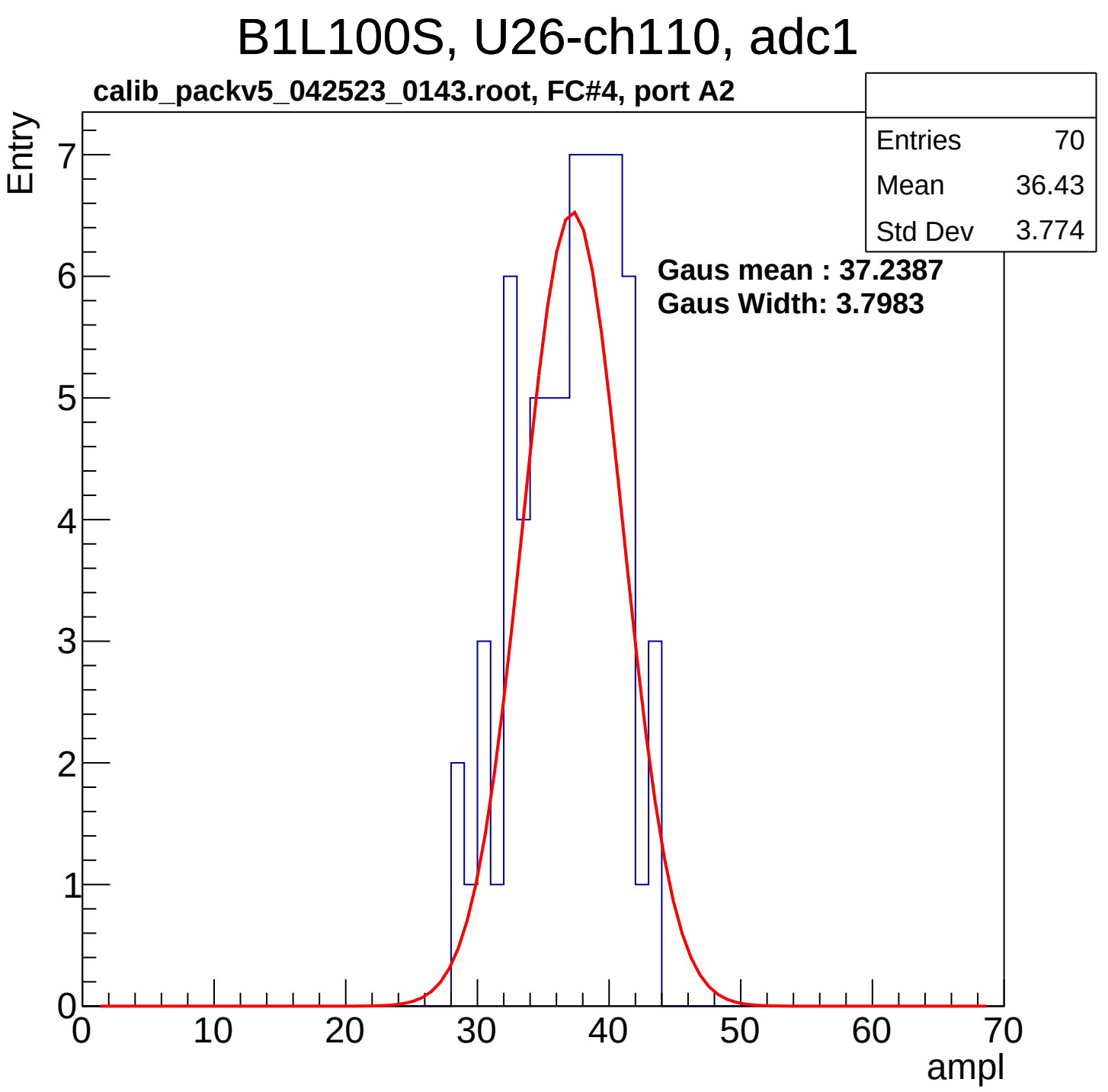
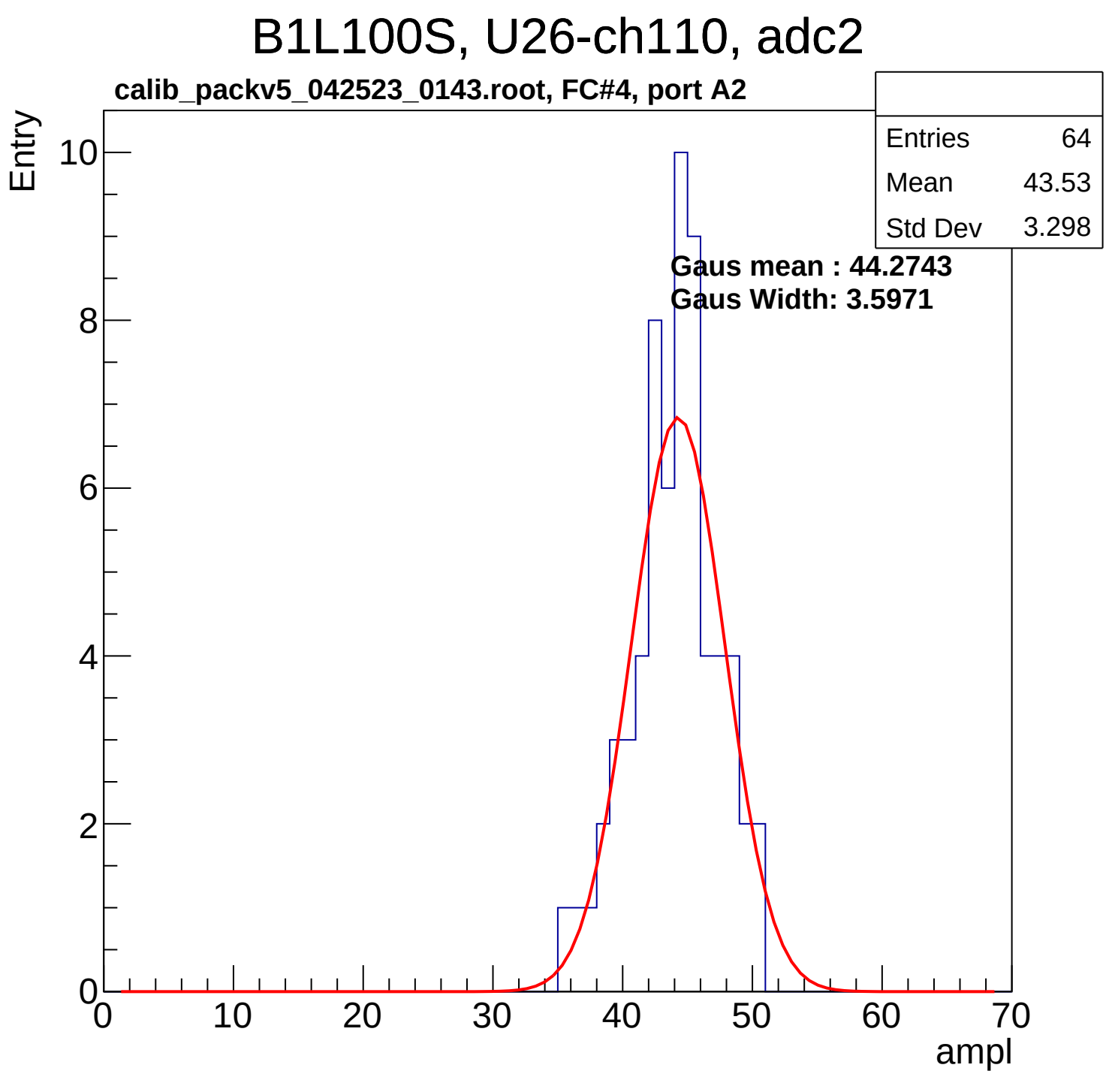
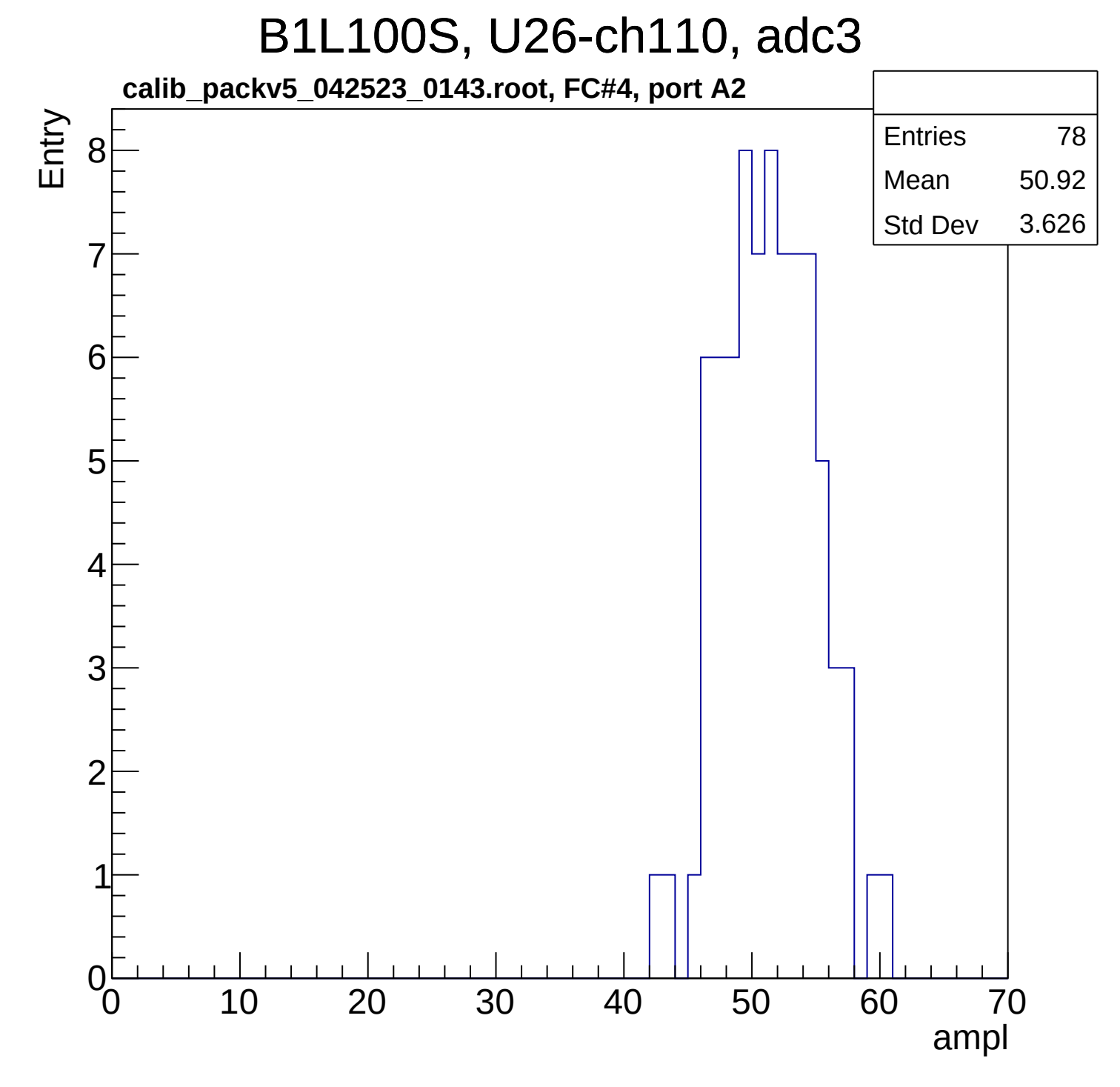
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.43
Std Dev 3.774
Gaus mean : 37.2387
Gaus Width: 3.7983
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.53
Std Dev 3.298
Gaus mean : 44.2743
Gaus Width: 3.5971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch110, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 50.92
Std Dev 3.626
Entry
ampl
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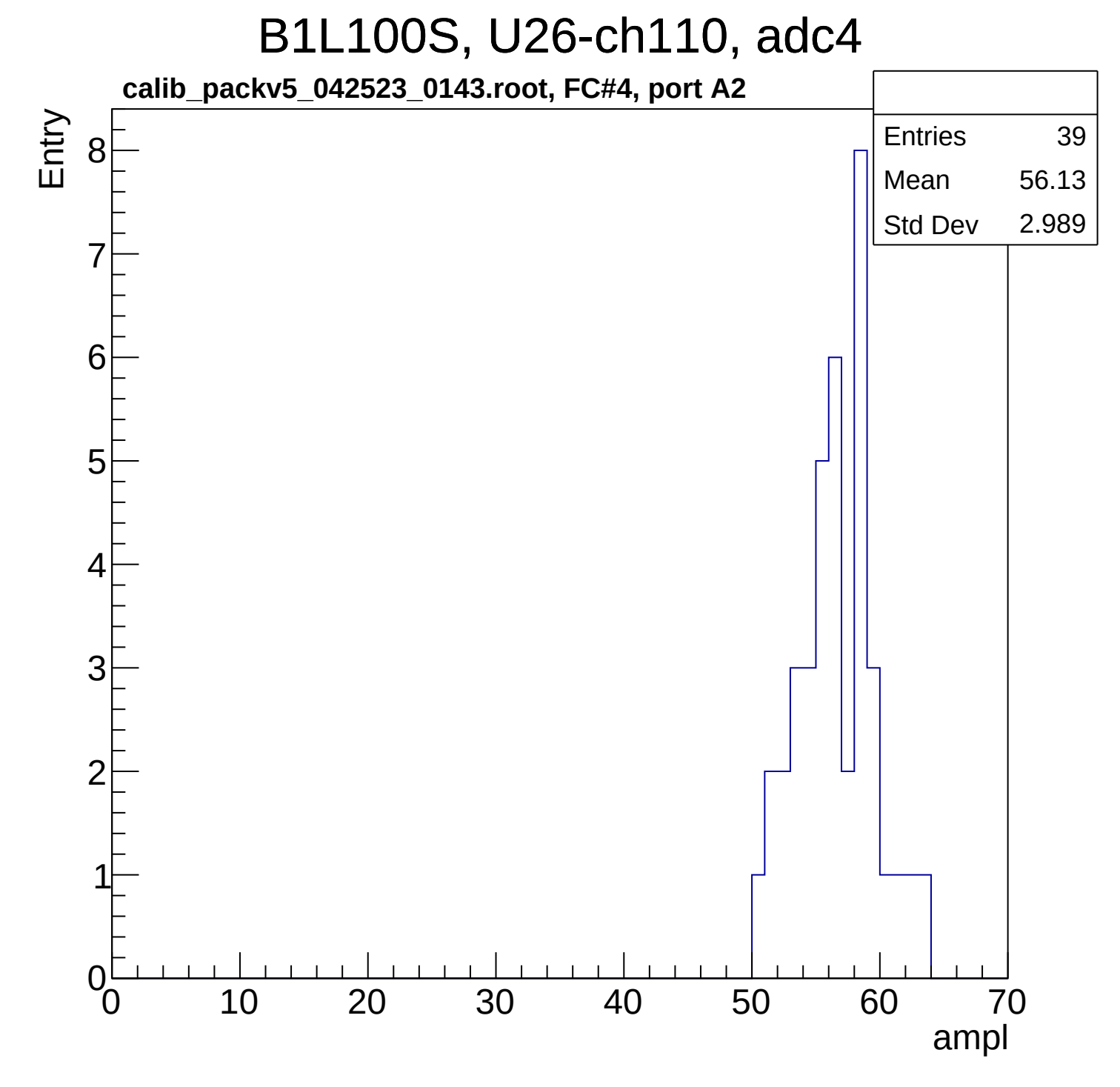
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 56.13
Std Dev 2.989
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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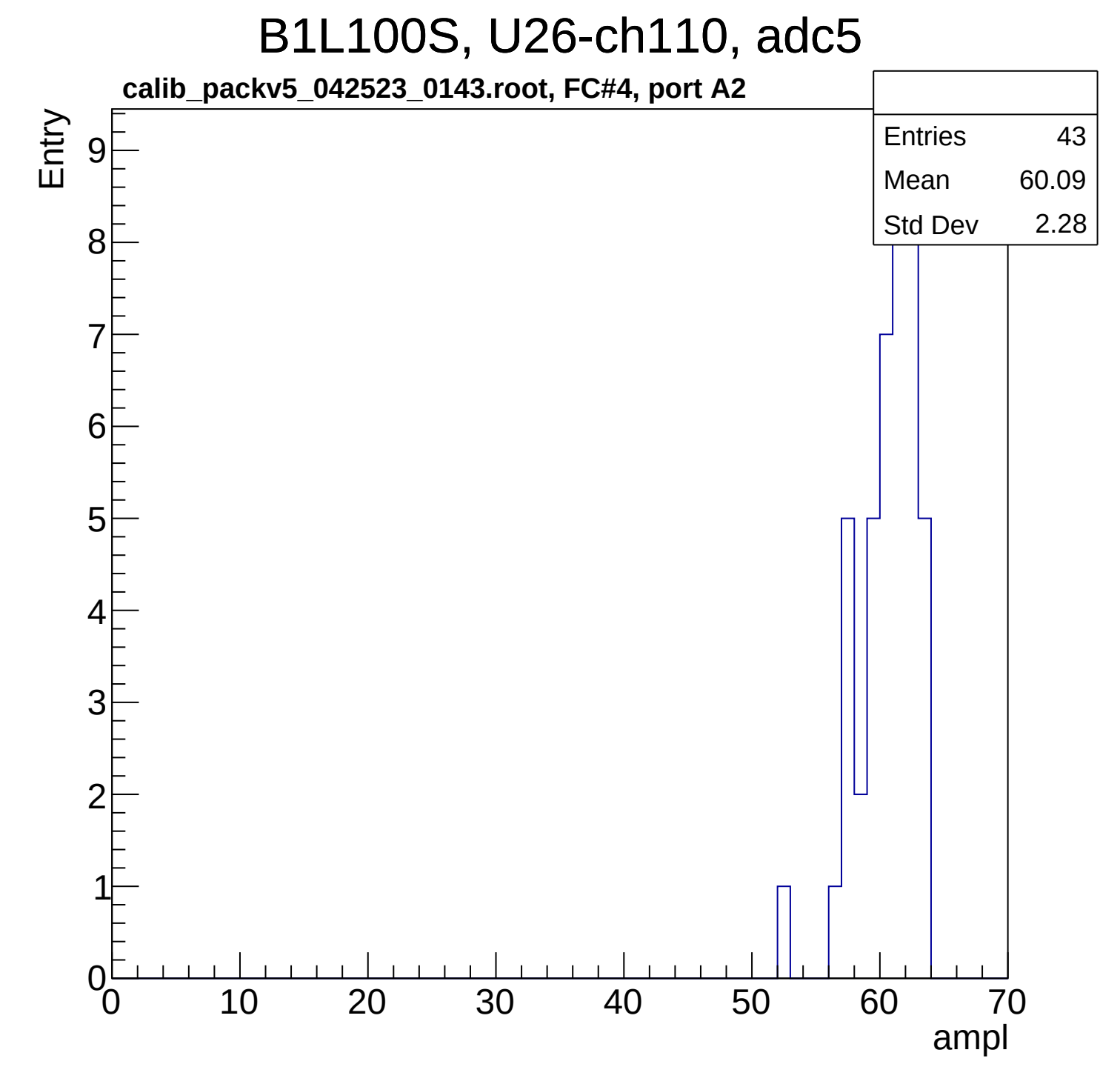
B1L100S, U26-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 60.09
Std Dev 2.28
Entry
ampl
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2
3
4
5
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7
8
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0
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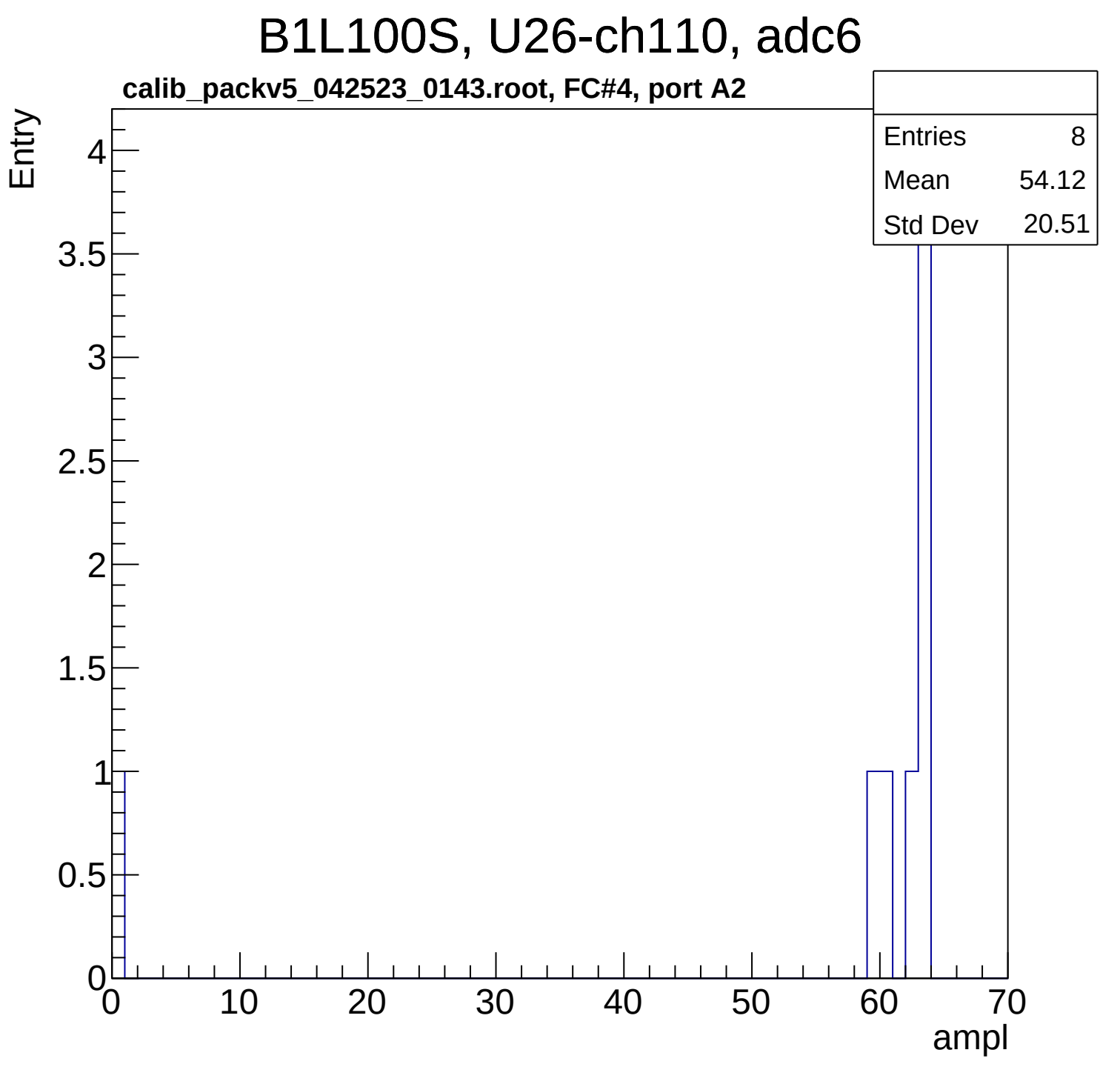
B1L100S, U26-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54.12
Std Dev 20.51
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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40
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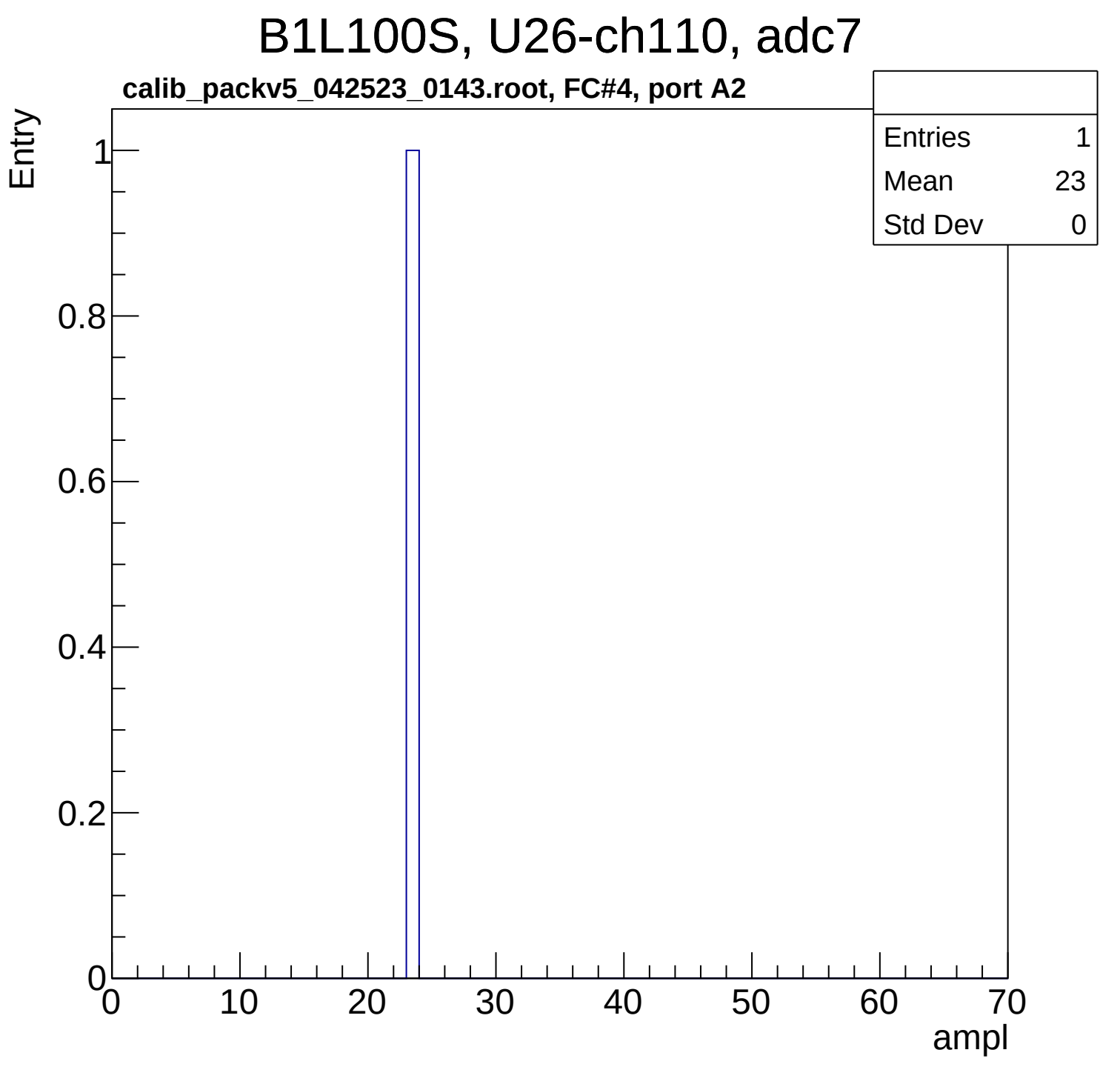
60
70


B1L100S, U26-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
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10
20
30
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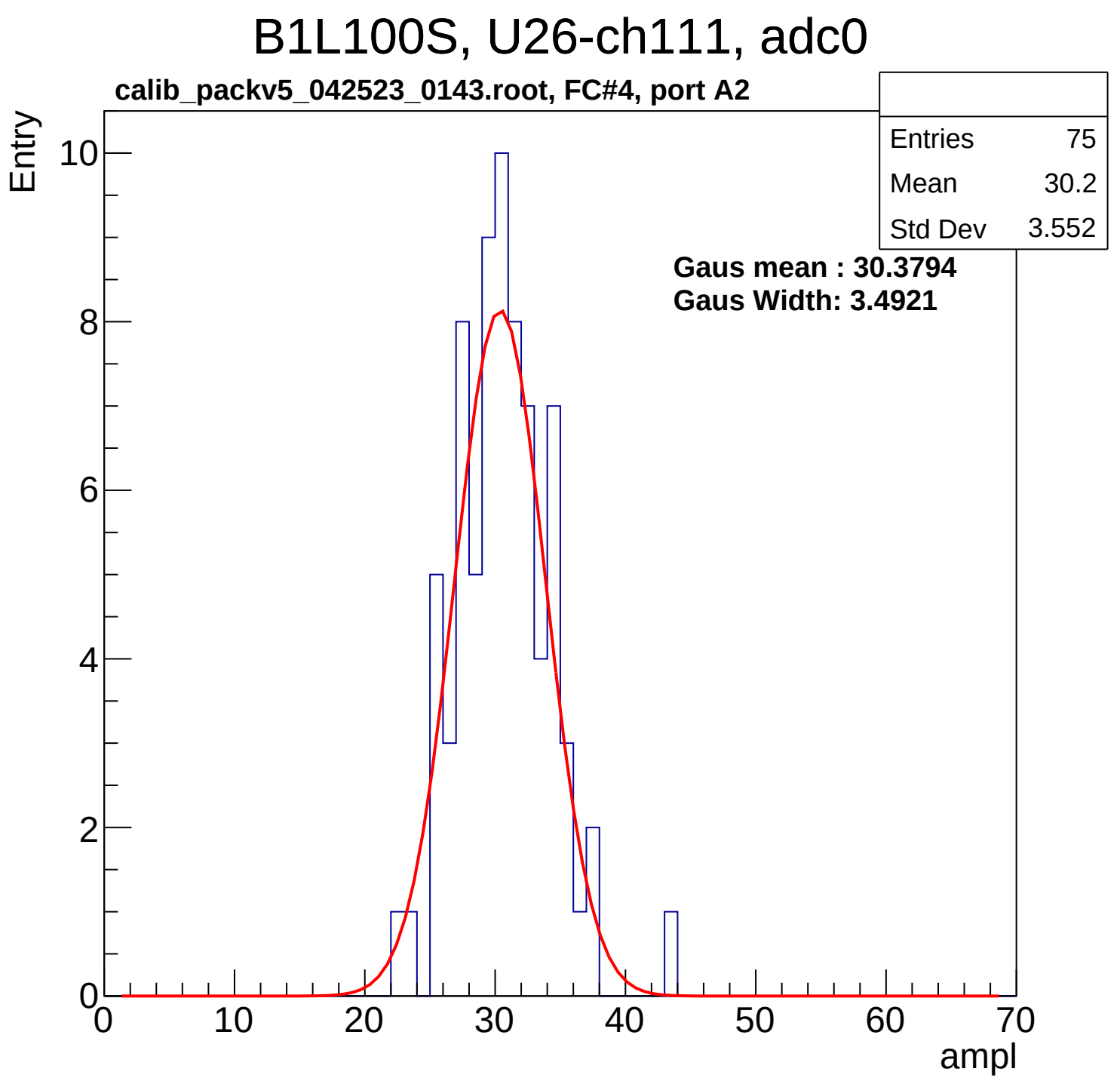
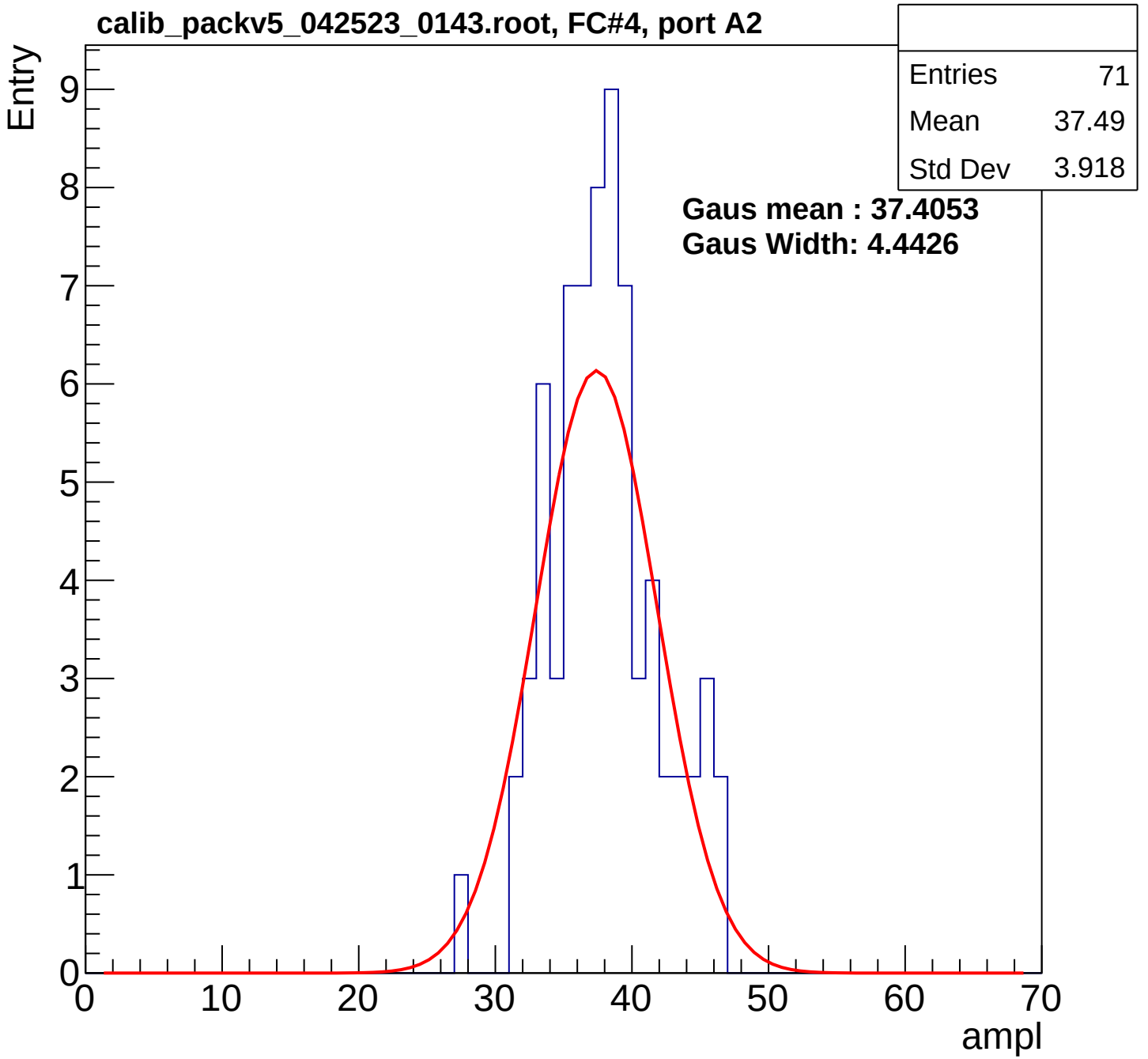
50
60
70
ampl


B1L100S, U26-ch111, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.2
Std Dev 3.552
Gaus mean : 30.3794
Gaus Width: 3.4921
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.49
Std Dev 3.918
Gaus mean : 37.4053
Gaus Width: 4.4426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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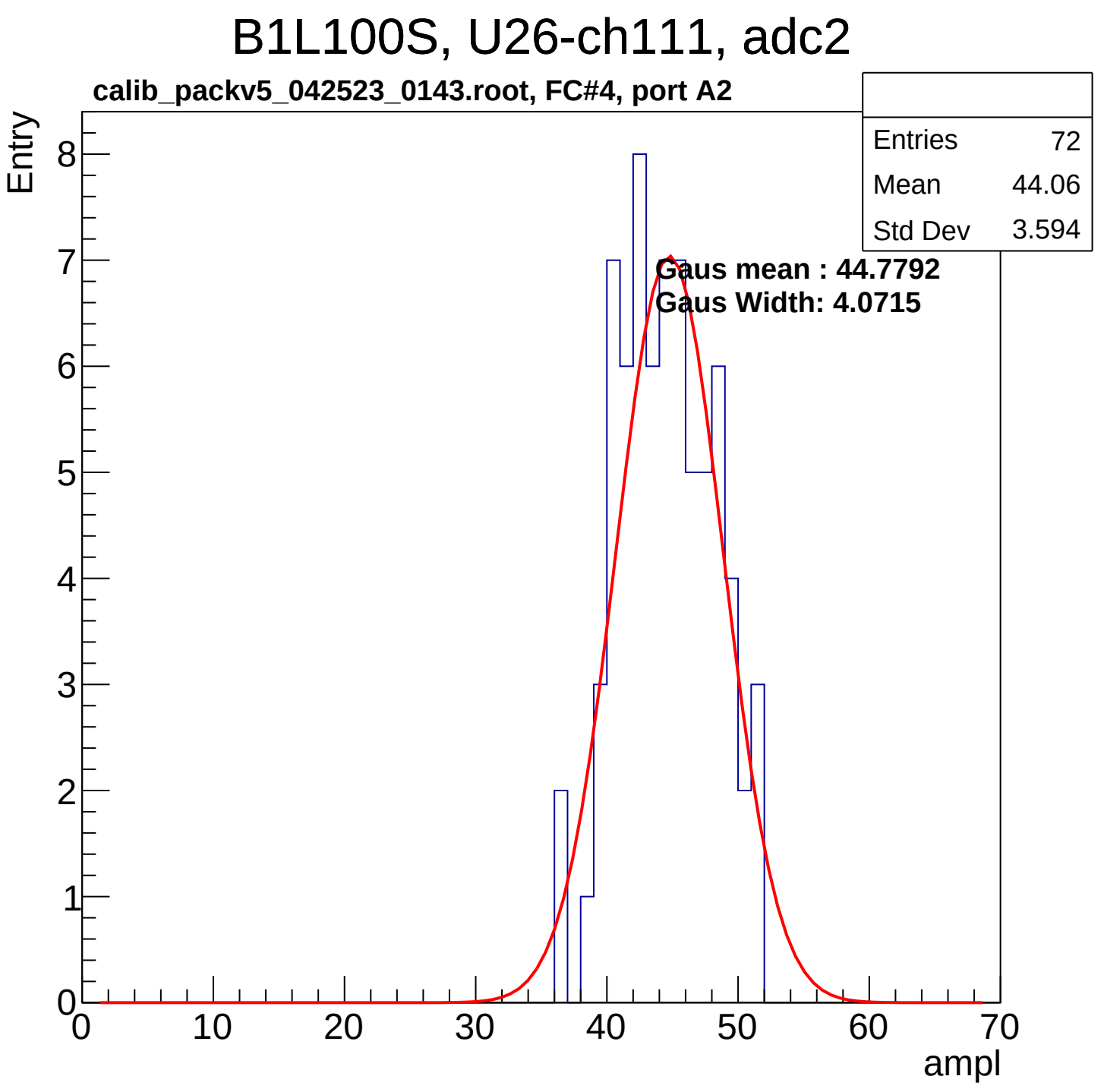
B1L100S, U26-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.06
Std Dev 3.594
Gaus mean : 44.7792
Gaus Width: 4.0715
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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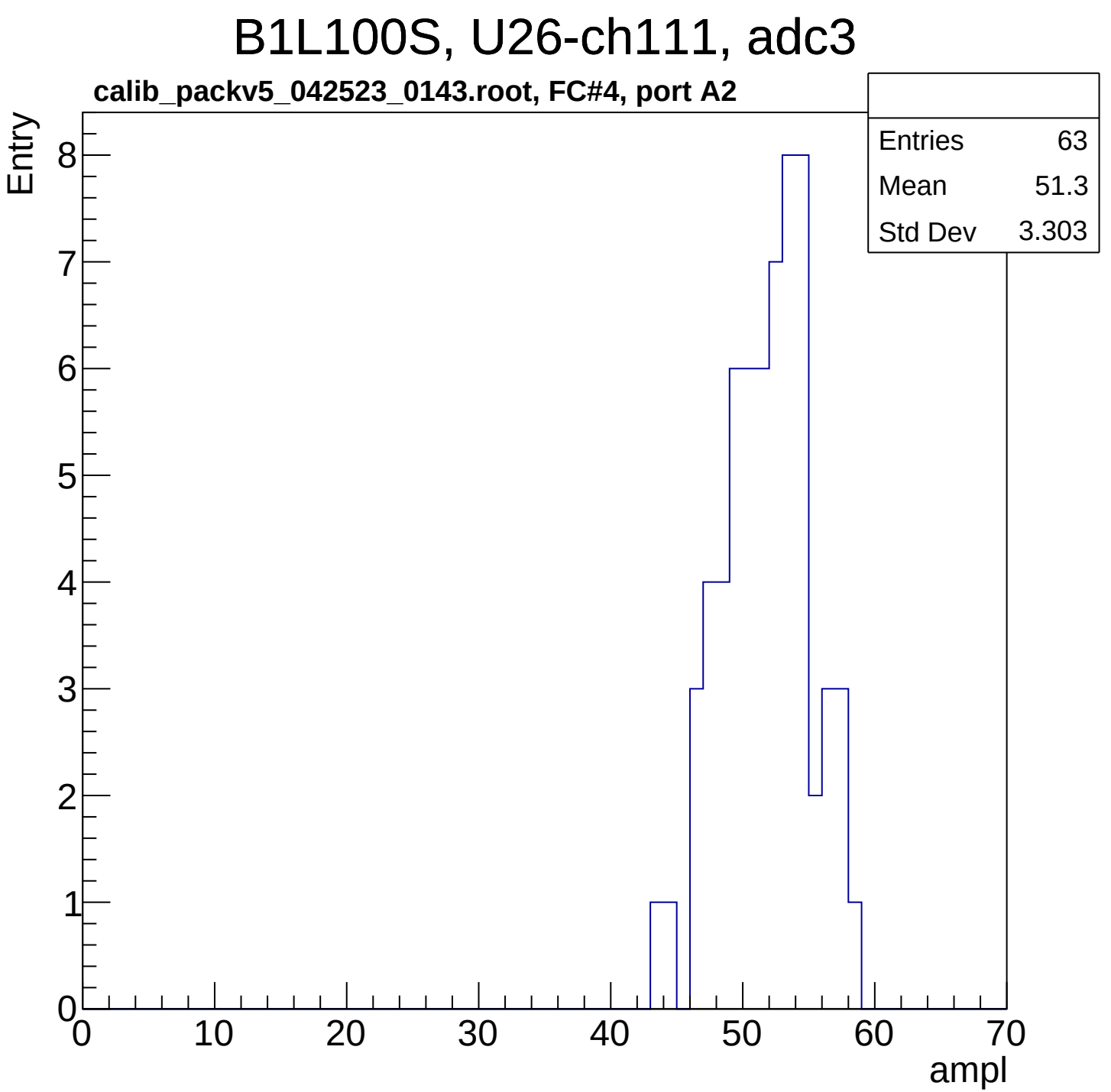
B1L100S, U26-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 51.3
Std Dev 3.303
Entry
ampl
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2
3
4
5
6
7
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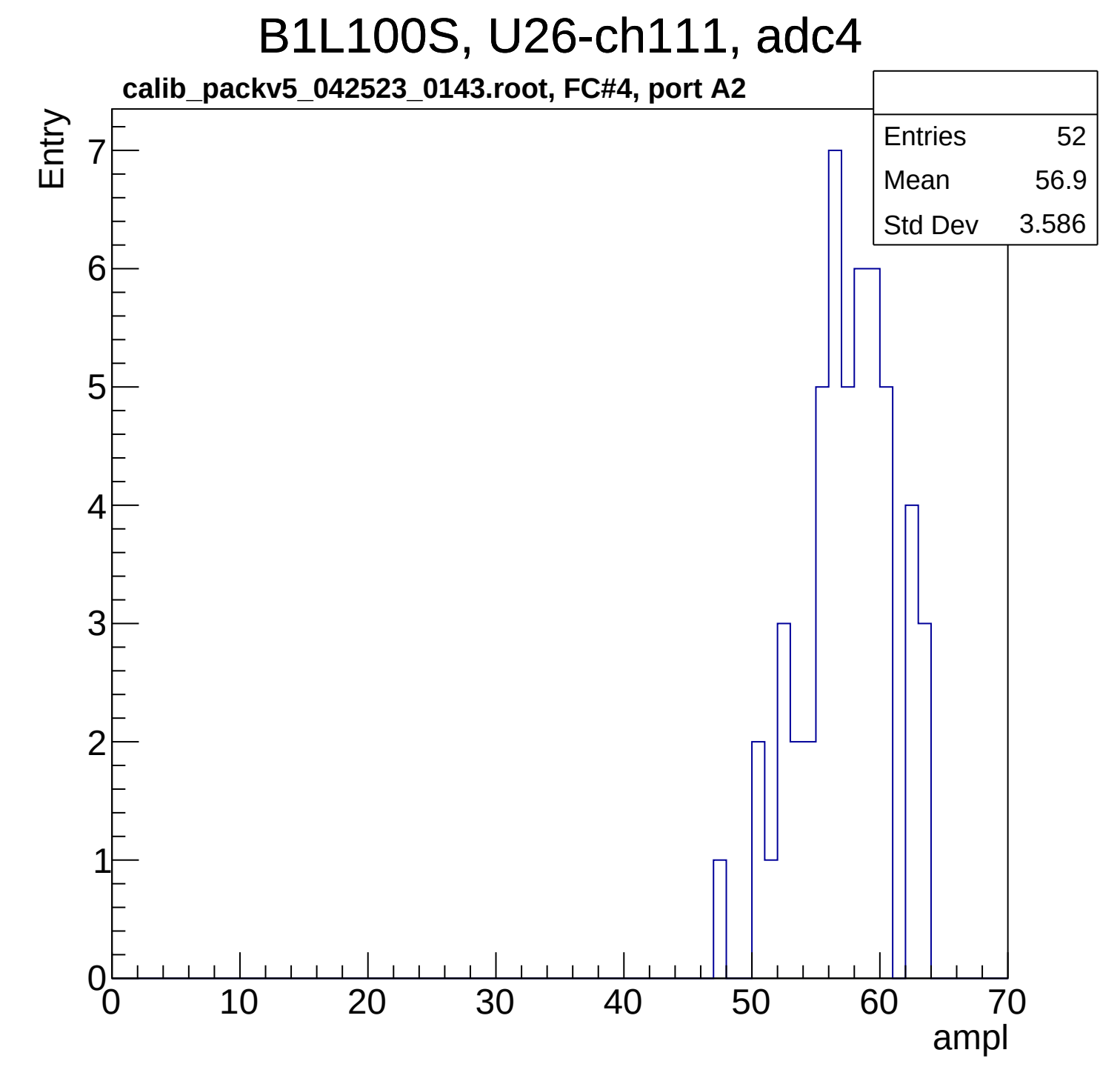
B1L100S, U26-ch111, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.9
Std Dev 3.586
Entry
ampl
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1
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3
4
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60
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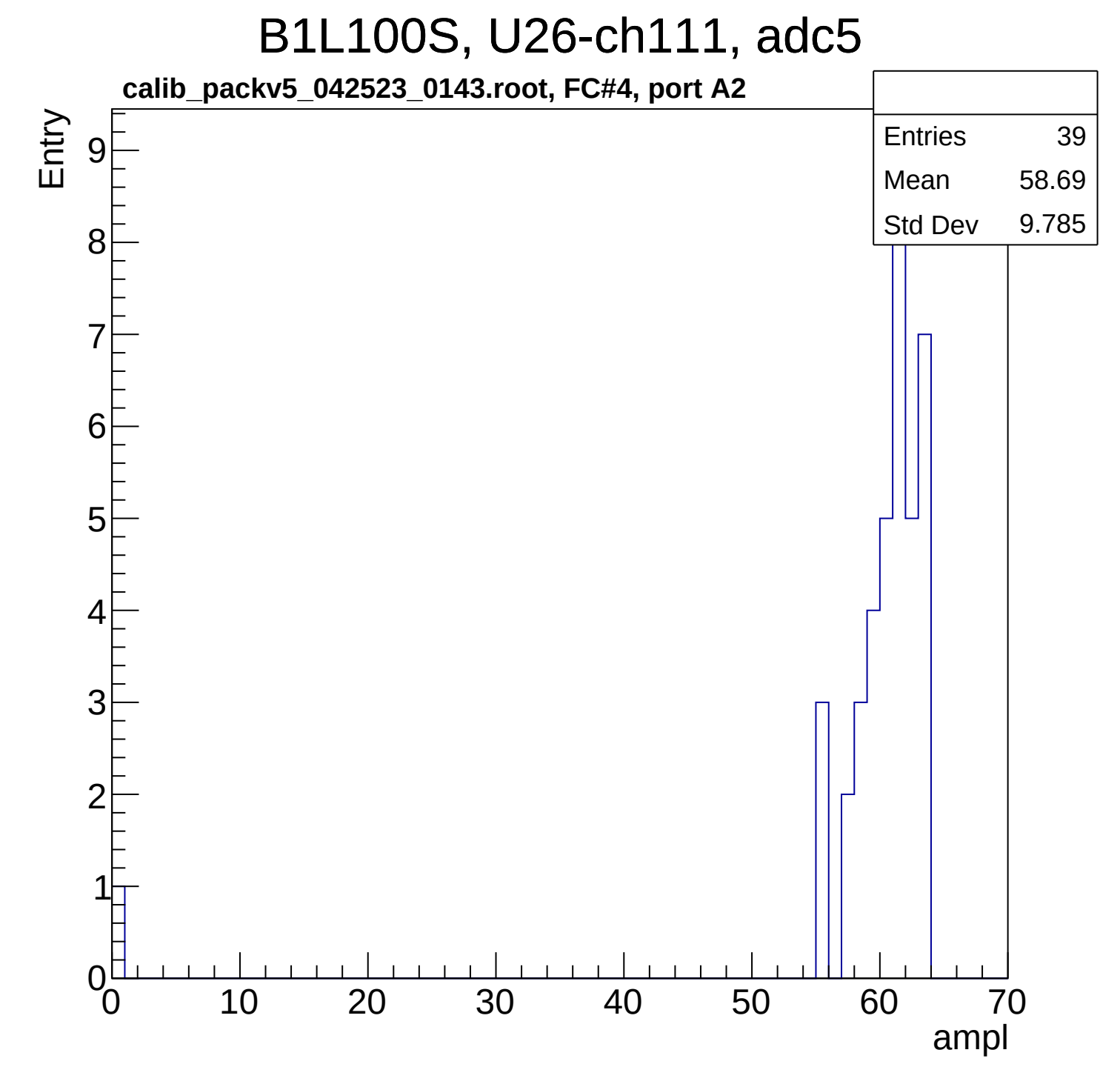
B1L100S, U26-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.69
Std Dev 9.785
Entry
ampl
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2
3
4
5
6
7
8
9
0
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30
40
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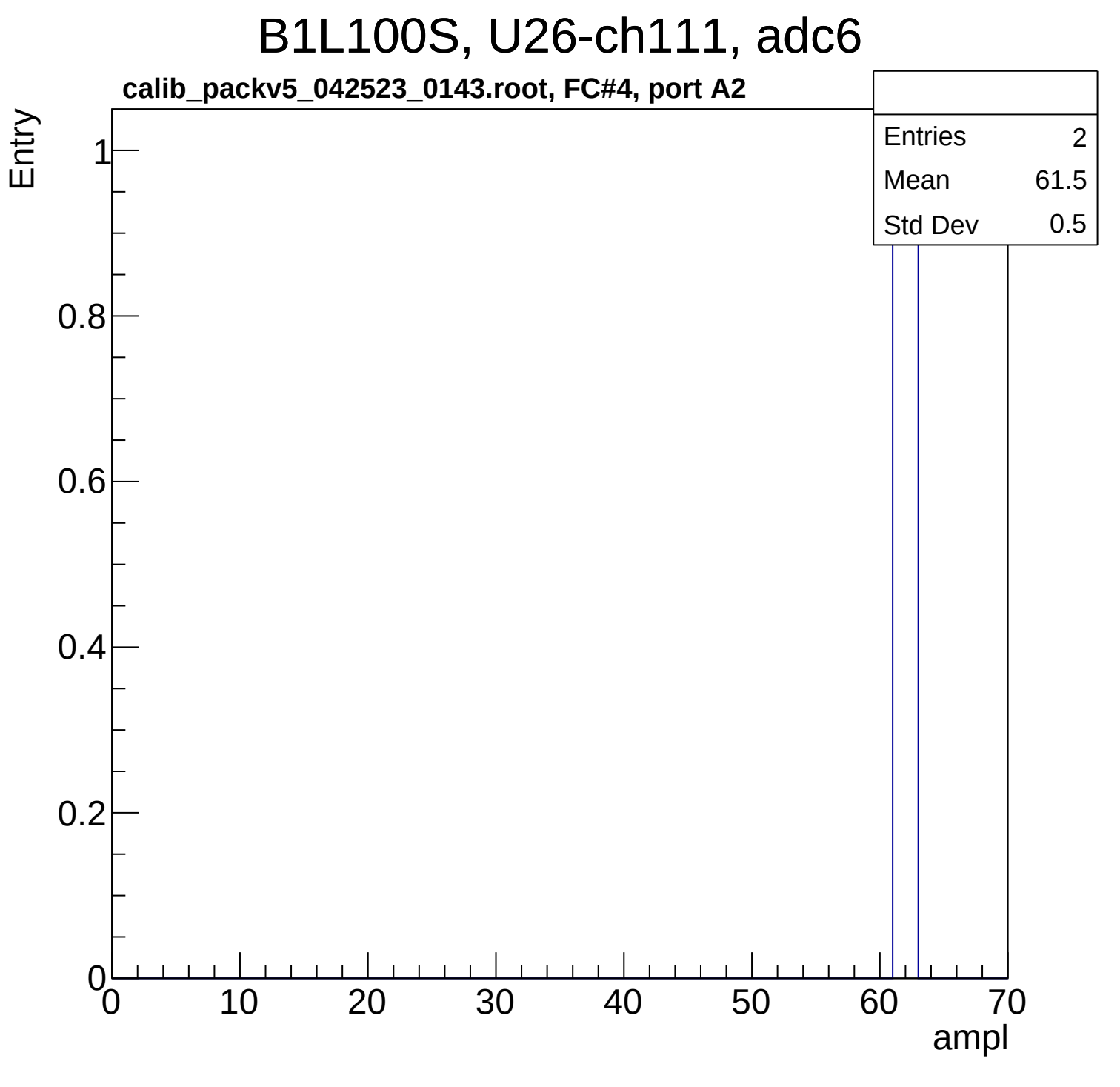
B1L100S, U26-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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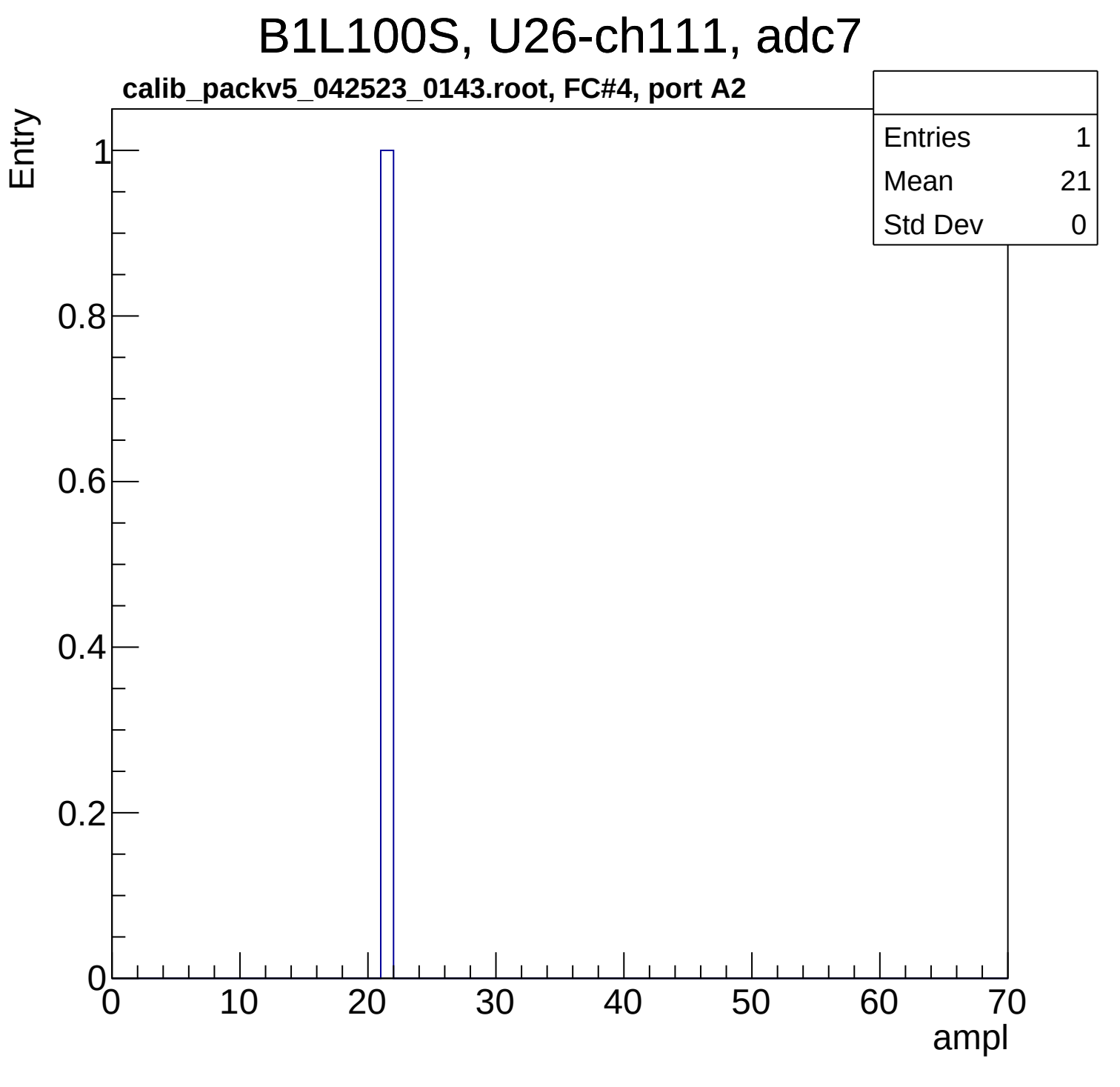
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch111, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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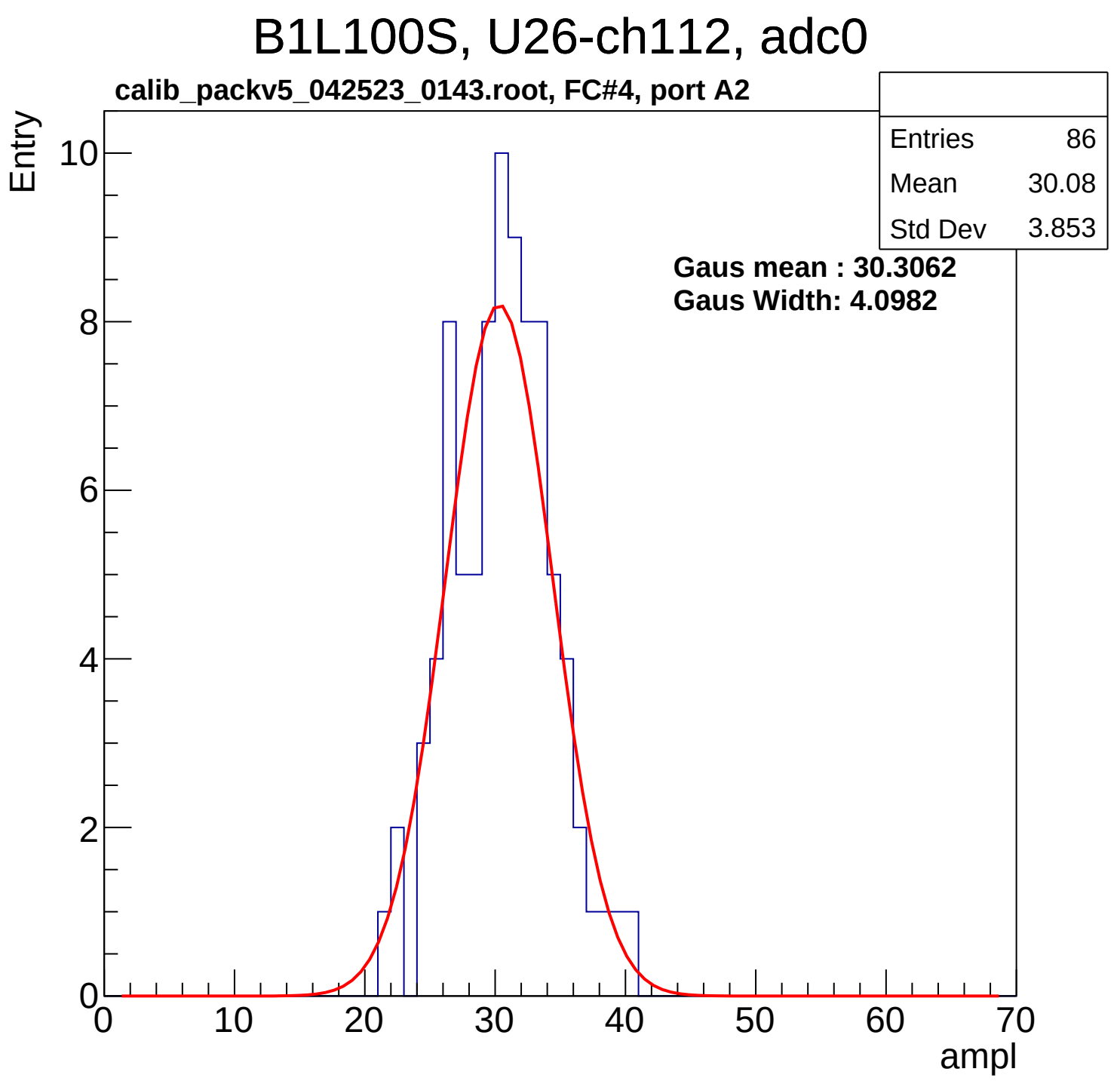
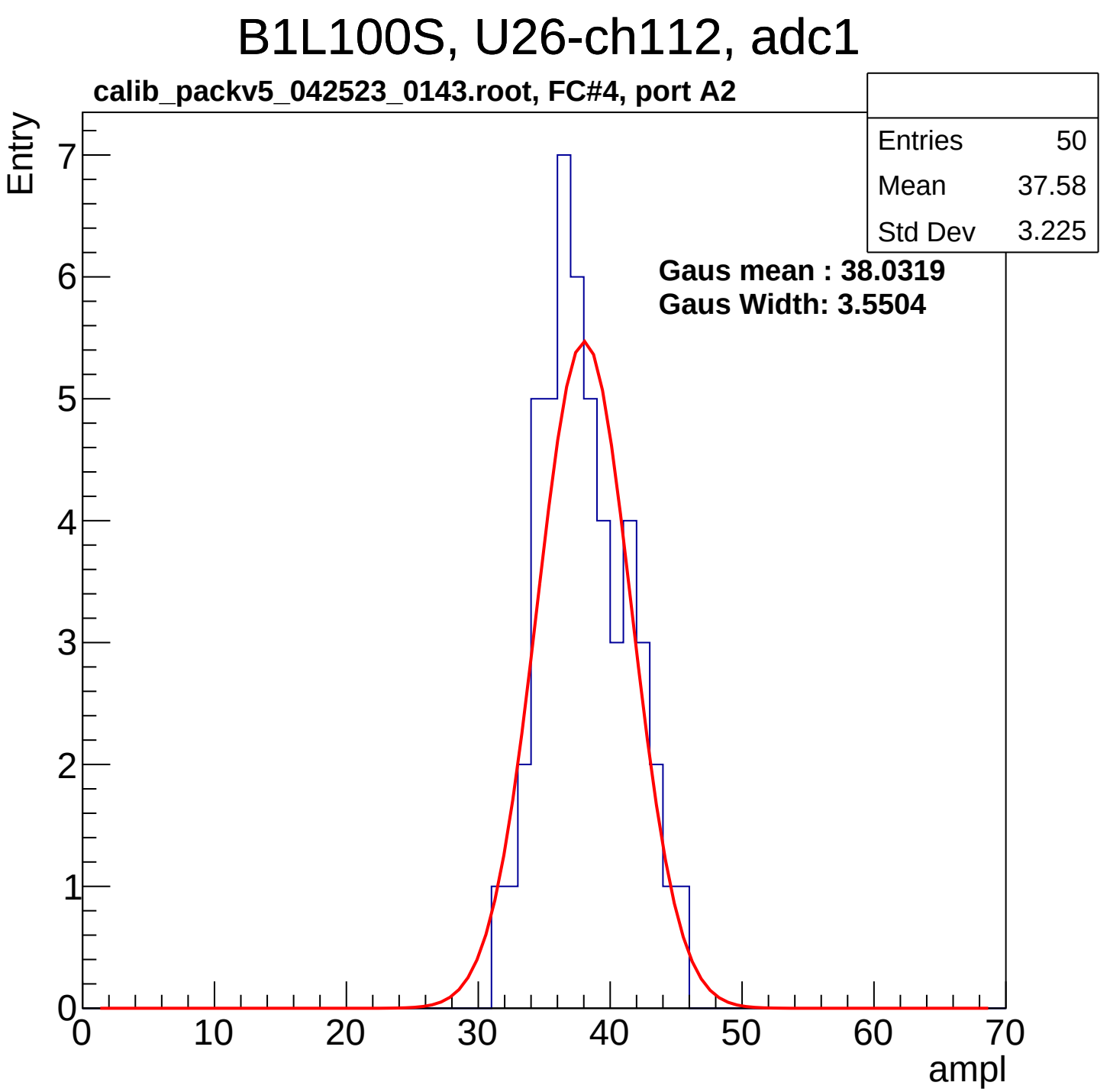
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 30.08
Std Dev 3.853
Gaus mean : 30.3062
Gaus Width: 4.0982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 37.58
Std Dev 3.225
Gaus mean : 38.0319
Gaus Width: 3.5504
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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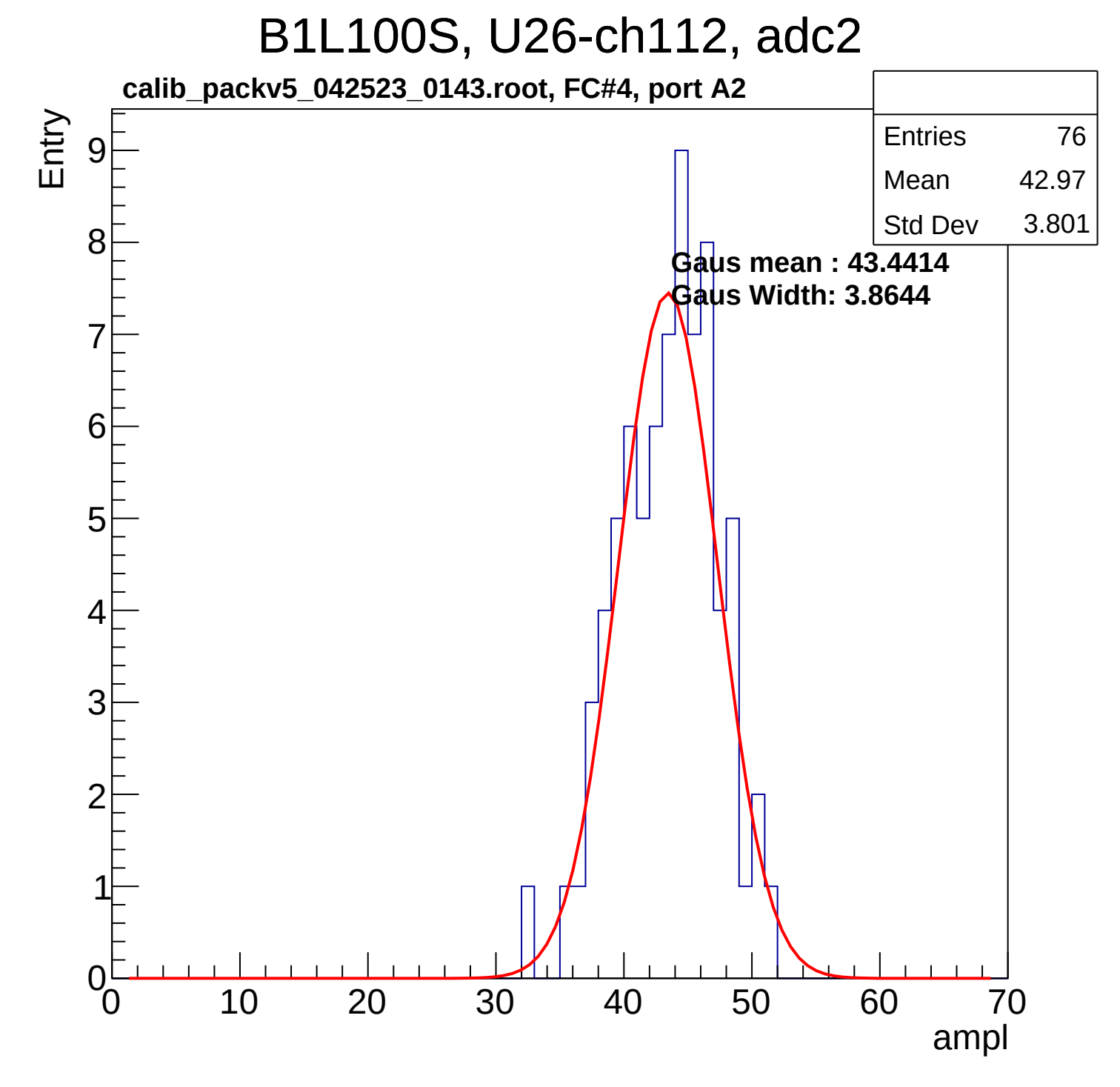
B1L100S, U26-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 42.97
Std Dev 3.801
Gaus mean : 43.4414
Gaus Width: 3.8644
Entry
ampl
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2
3
4
5
6
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8
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0
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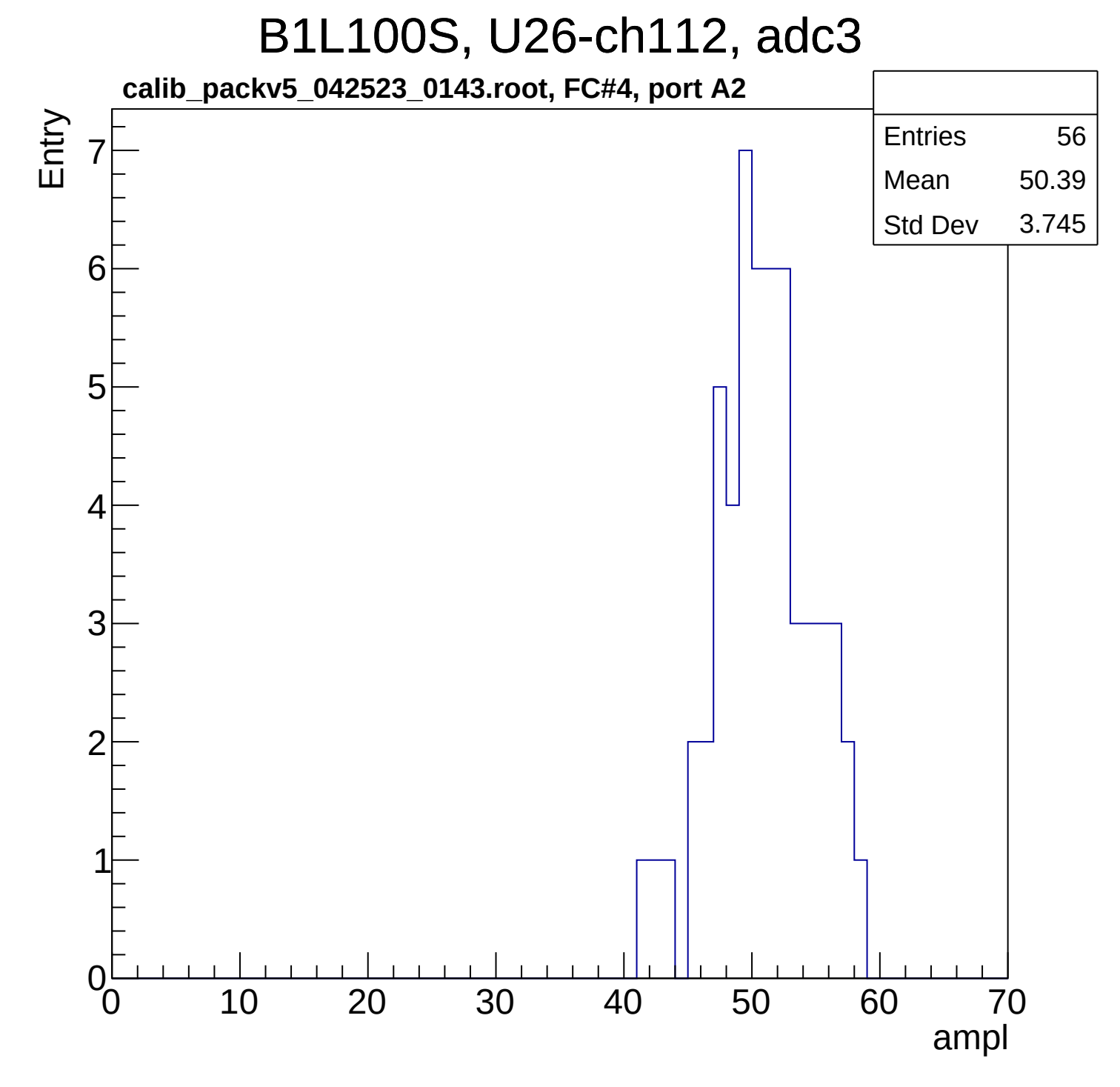
B1L100S, U26-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.39
Std Dev 3.745
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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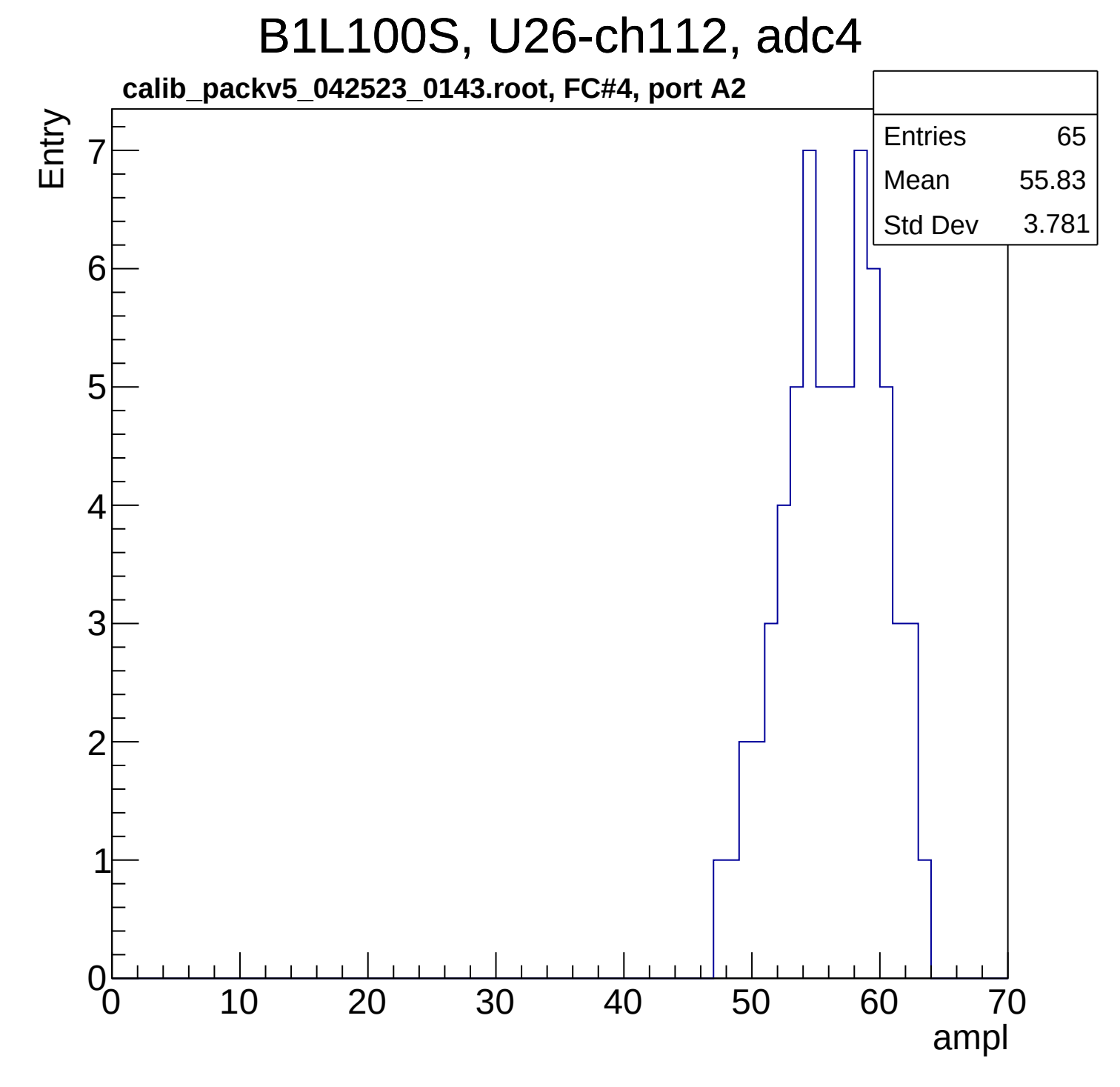
B1L100S, U26-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.83
Std Dev 3.781
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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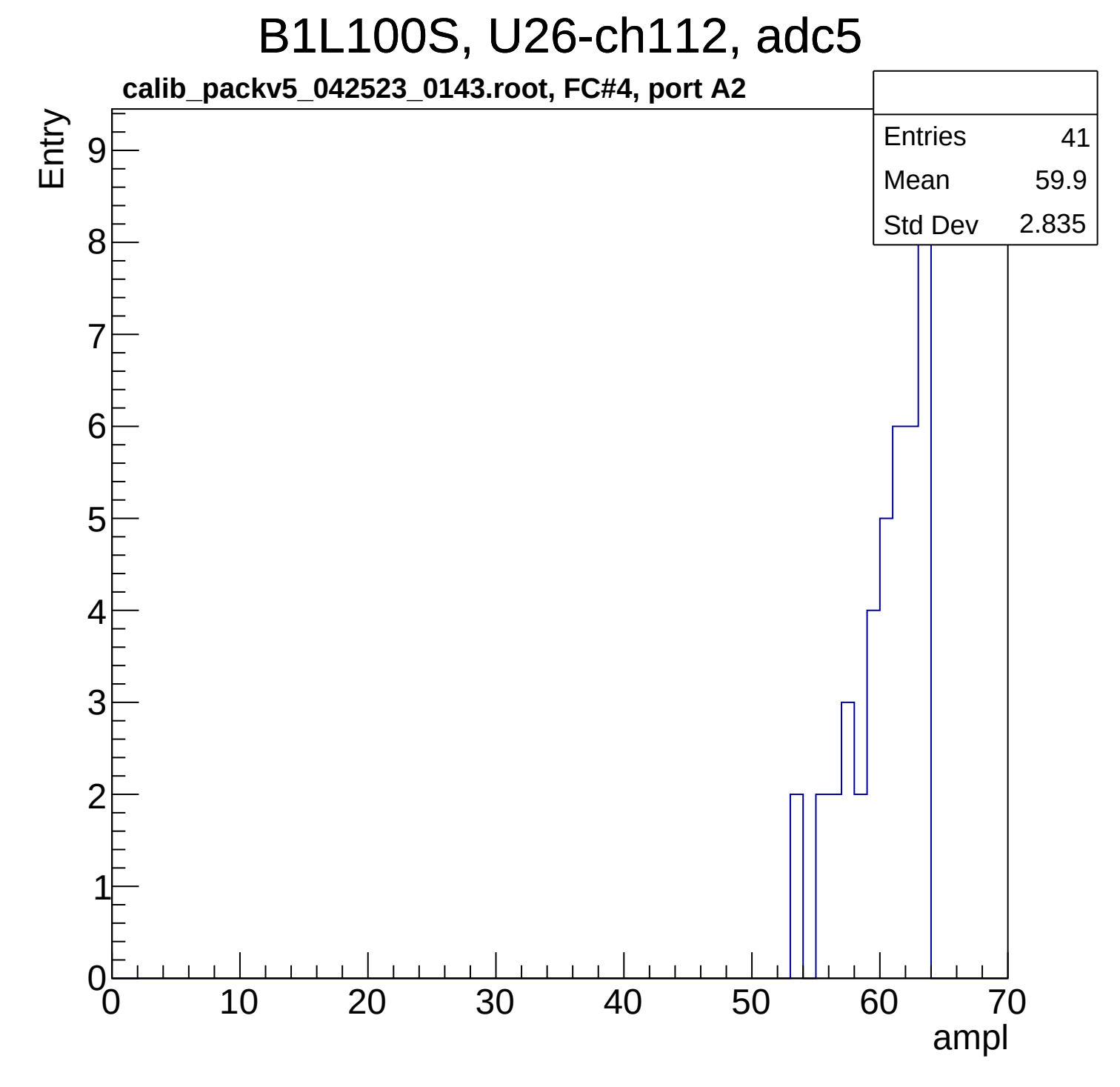
B1L100S, U26-ch112, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.9
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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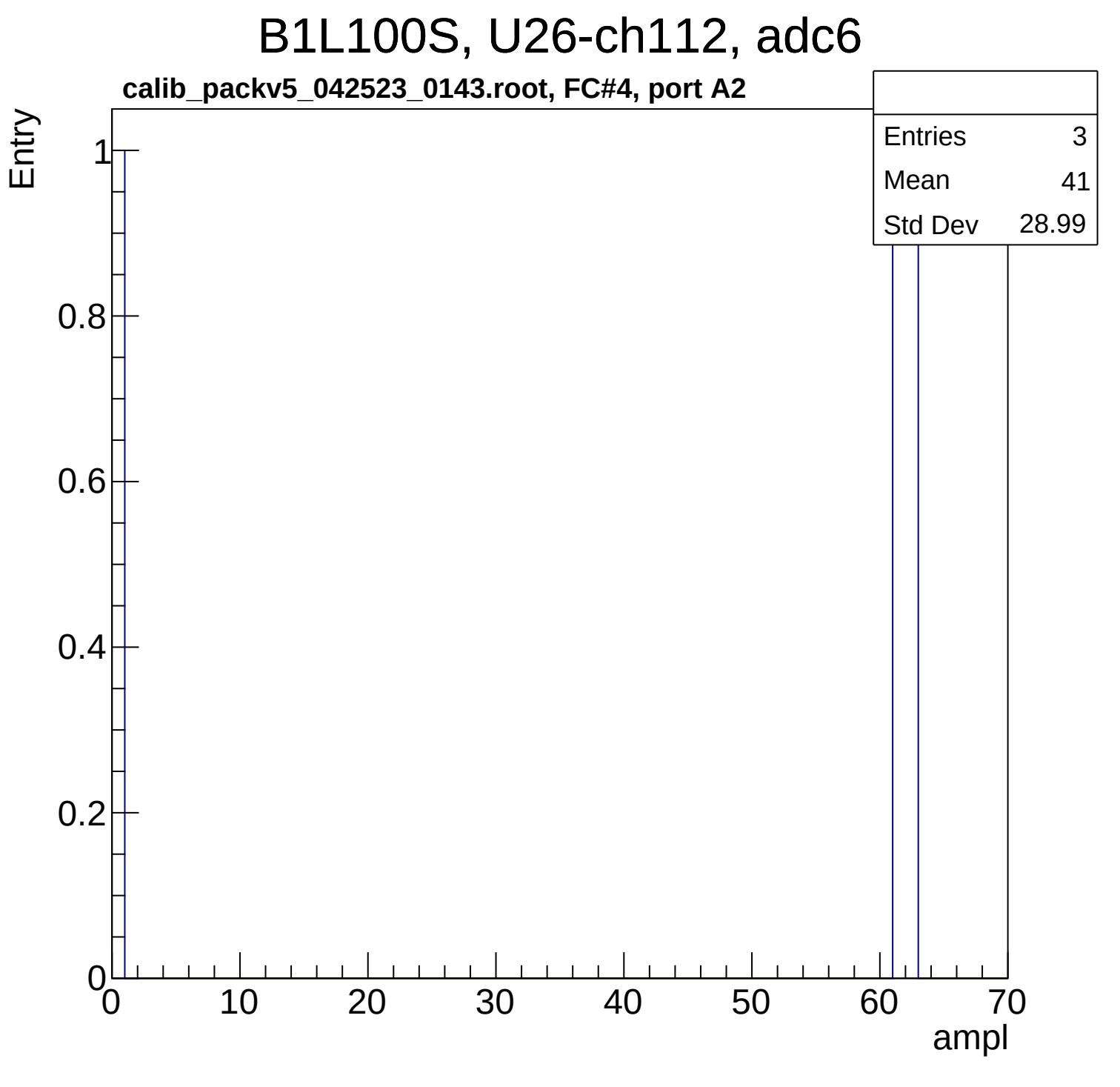
B1L100S, U26-ch112, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41
Std Dev 28.99
Entry
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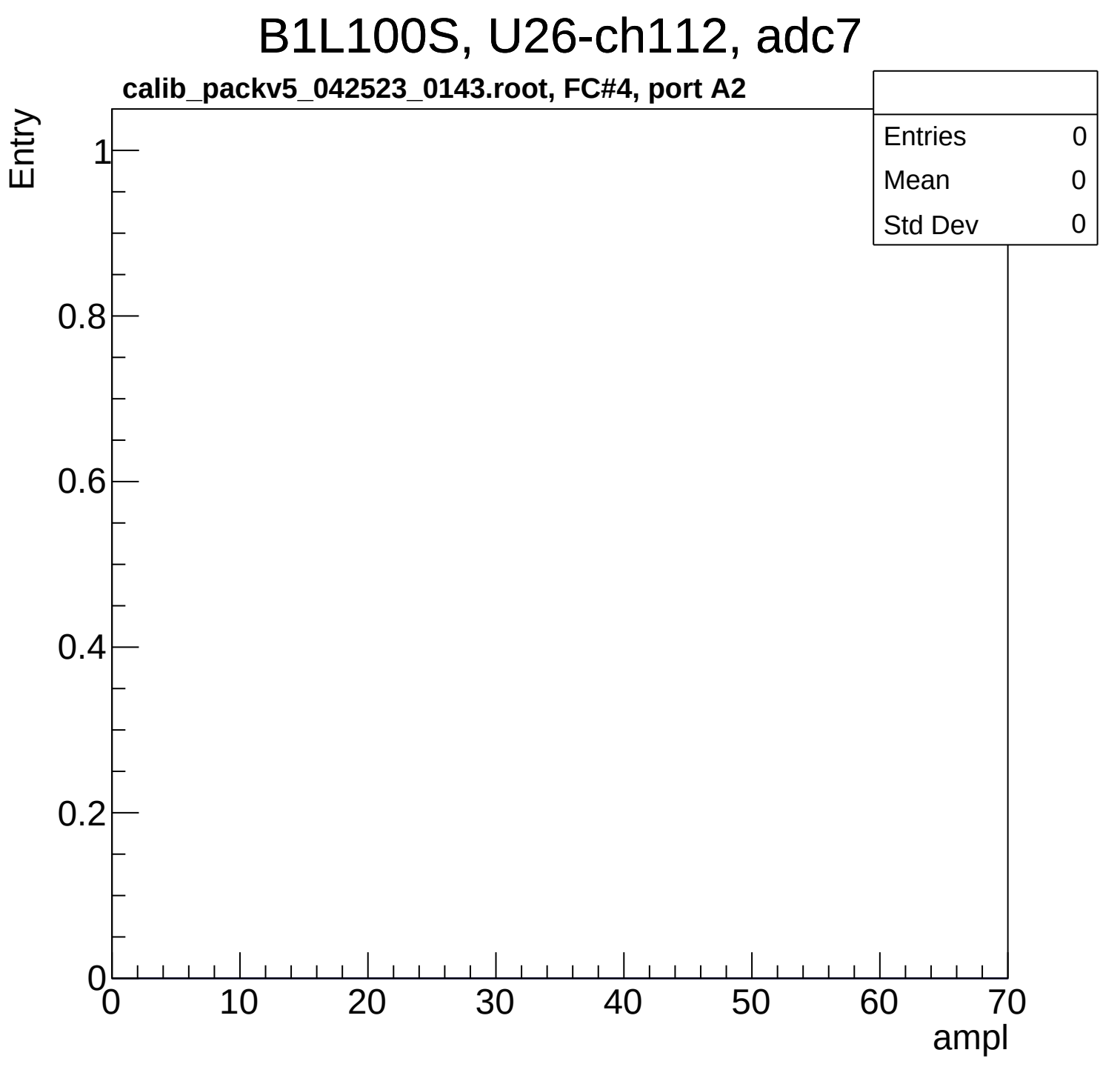
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch112, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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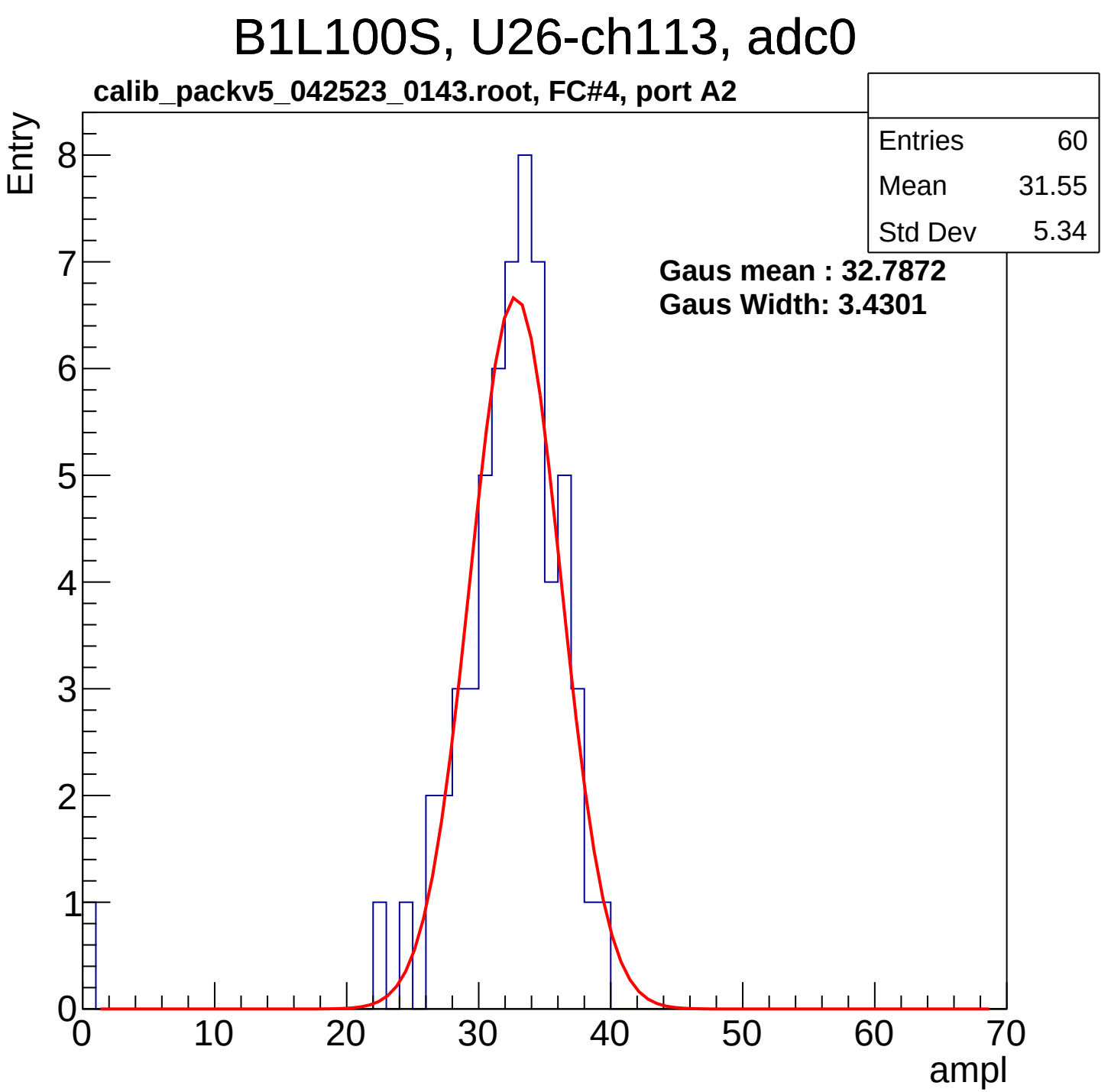
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 31.55
Std Dev 5.34
Gaus mean : 32.7872
Gaus Width: 3.4301
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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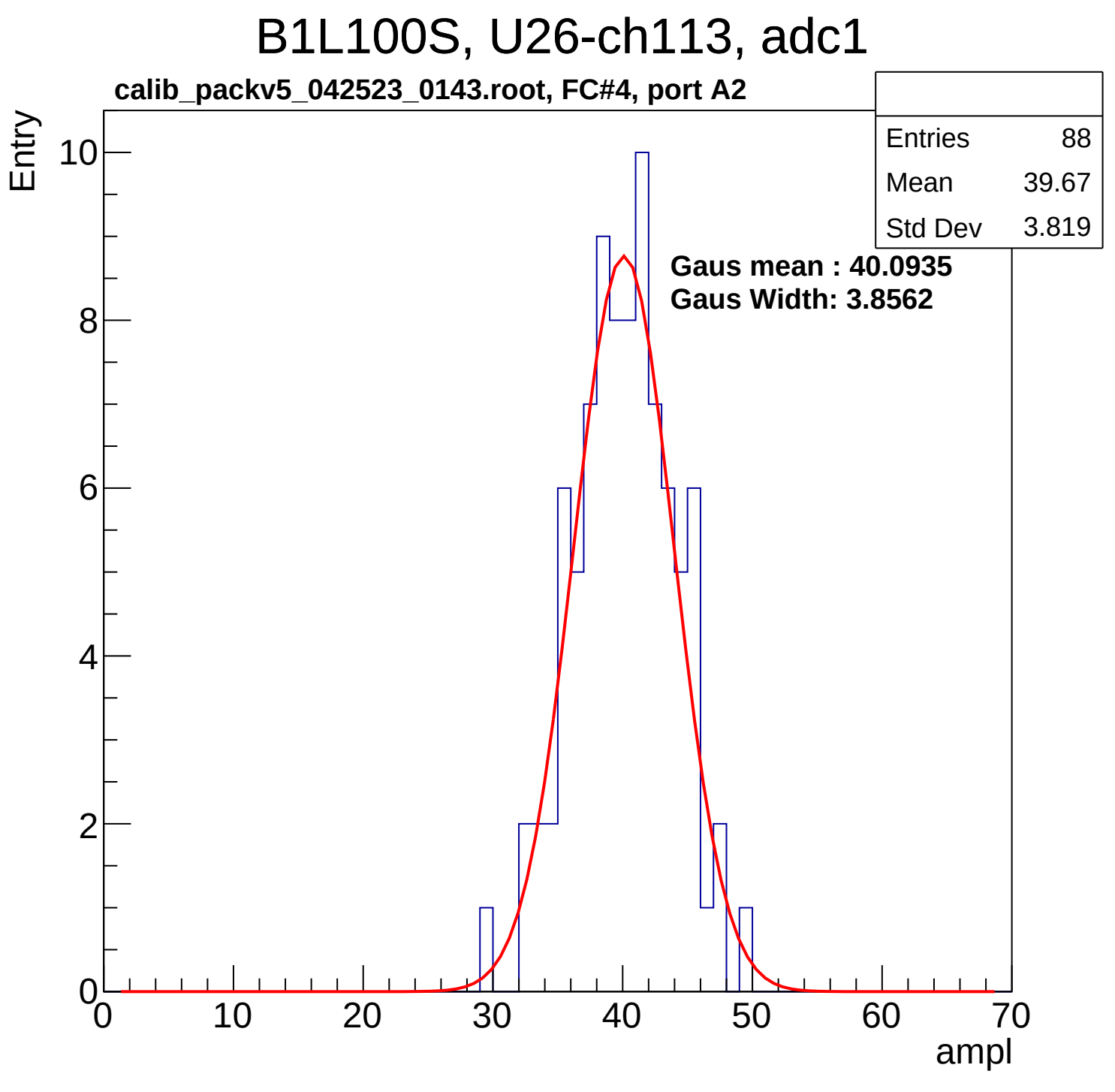
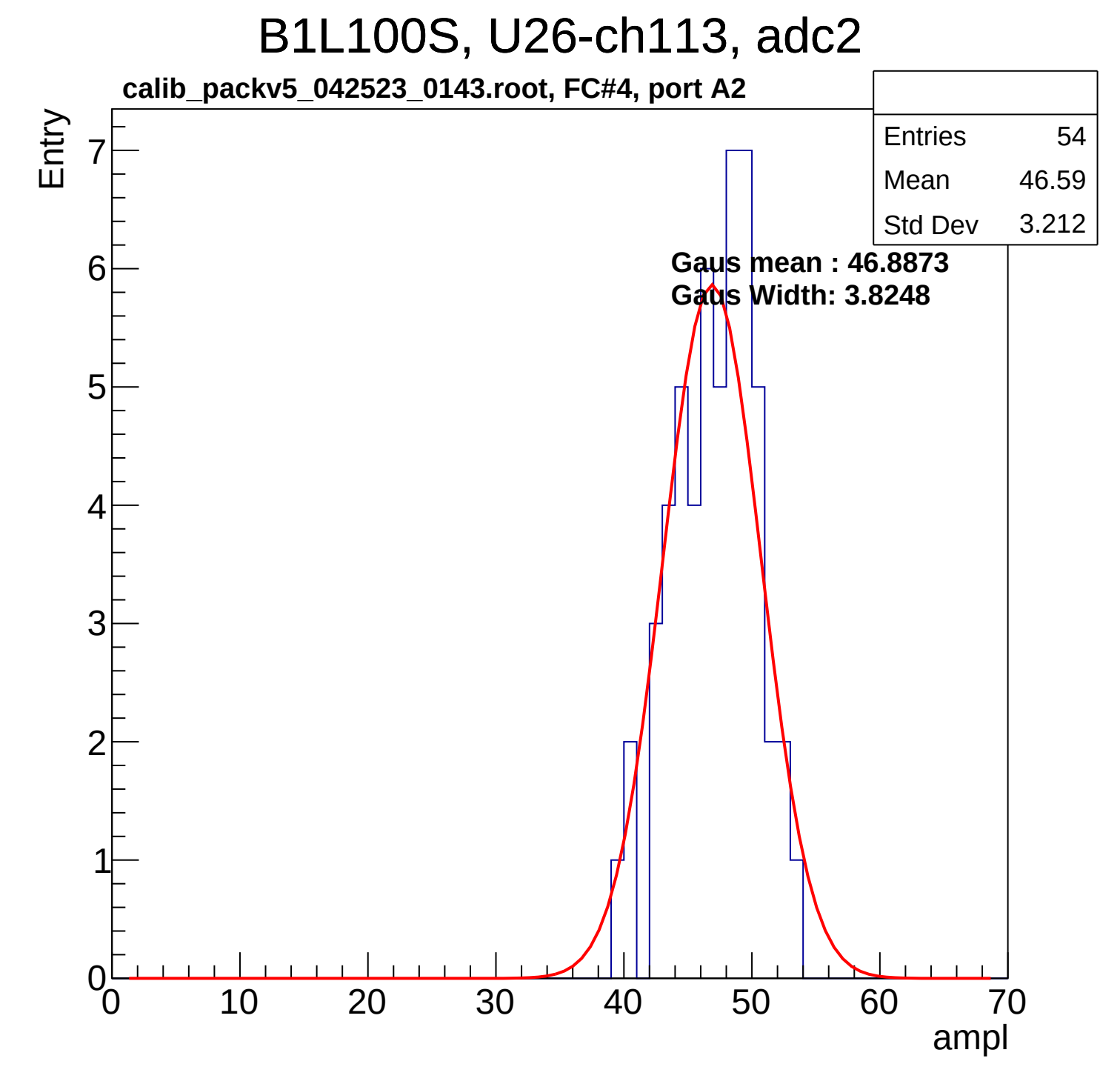
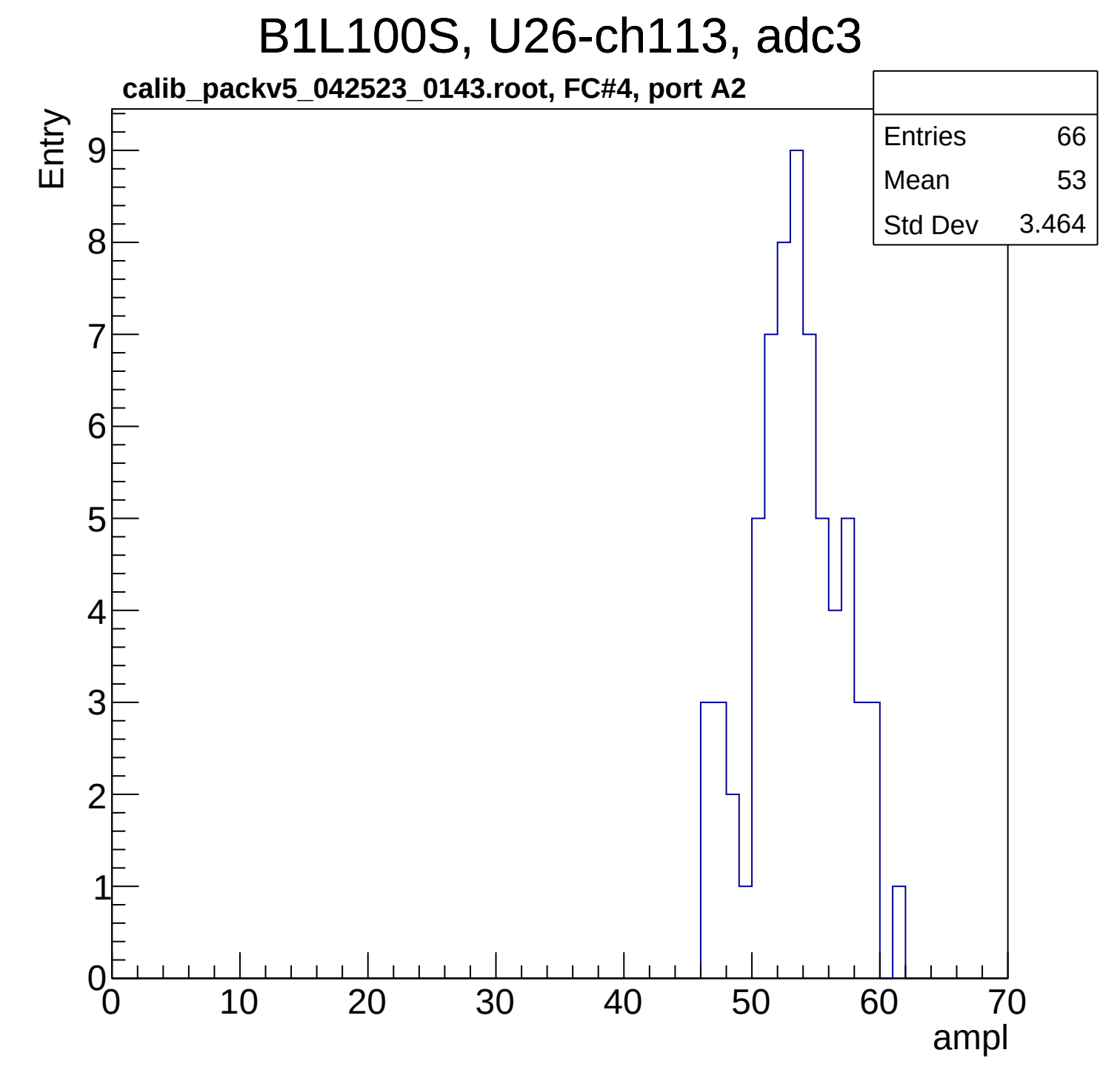

B1L100S, U26-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 39.67
Std Dev 3.819
Gaus mean : 40.0935
Gaus Width: 3.8562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 46.59
Std Dev 3.212
Gaus mean : 46.8873
Gaus Width: 3.8248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch113, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 53
Std Dev 3.464
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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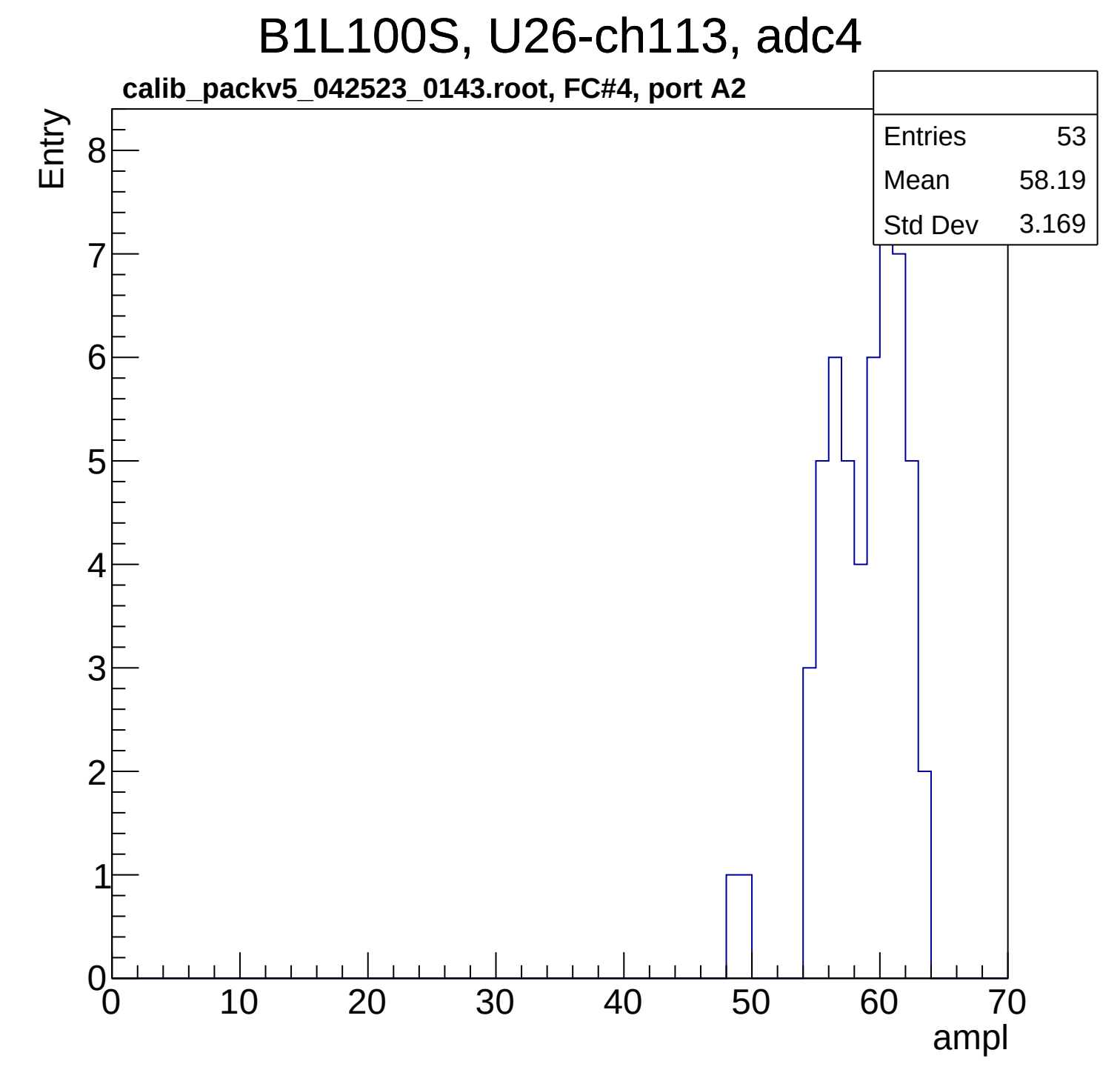
B1L100S, U26-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.19
Std Dev 3.169
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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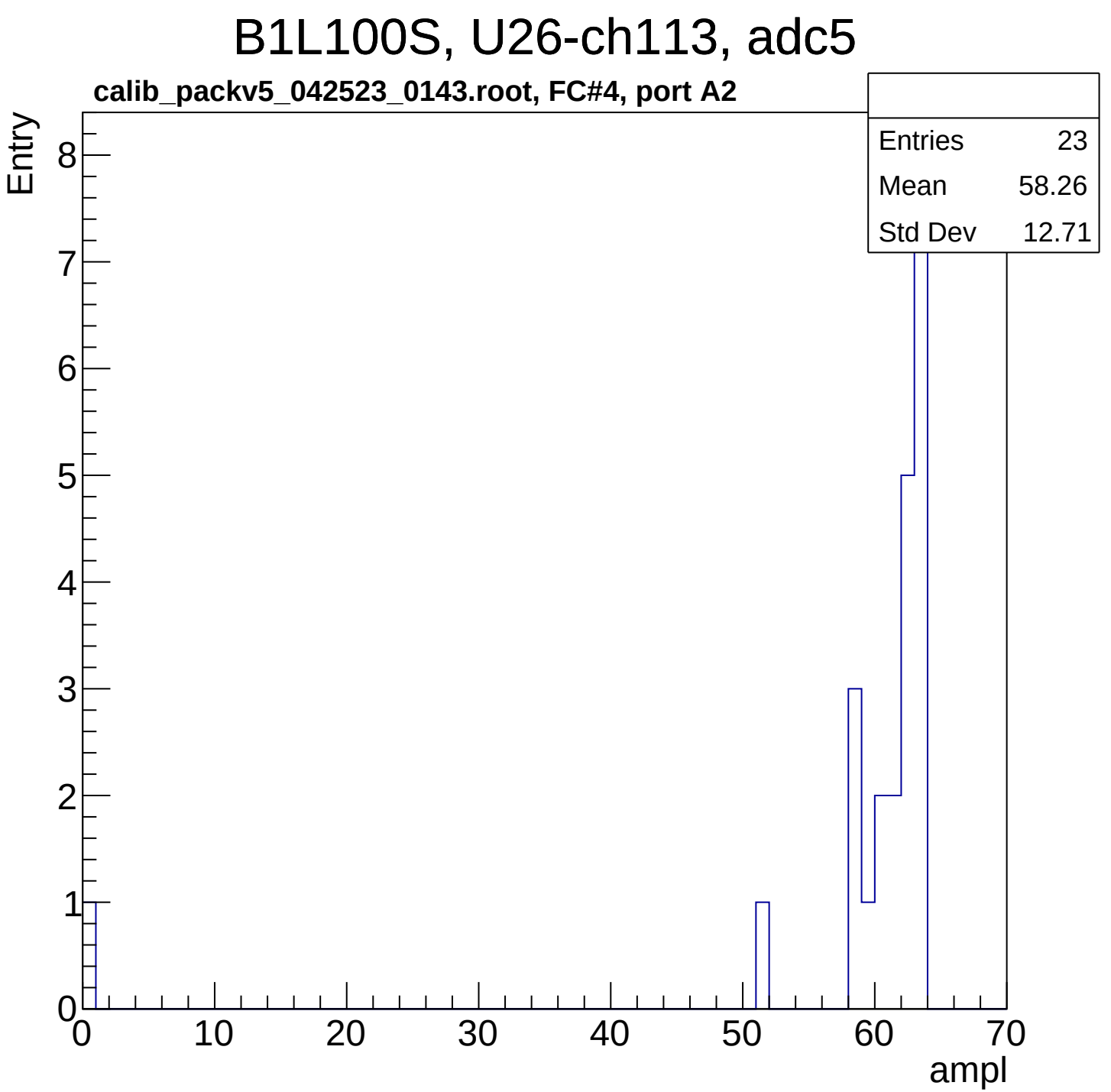
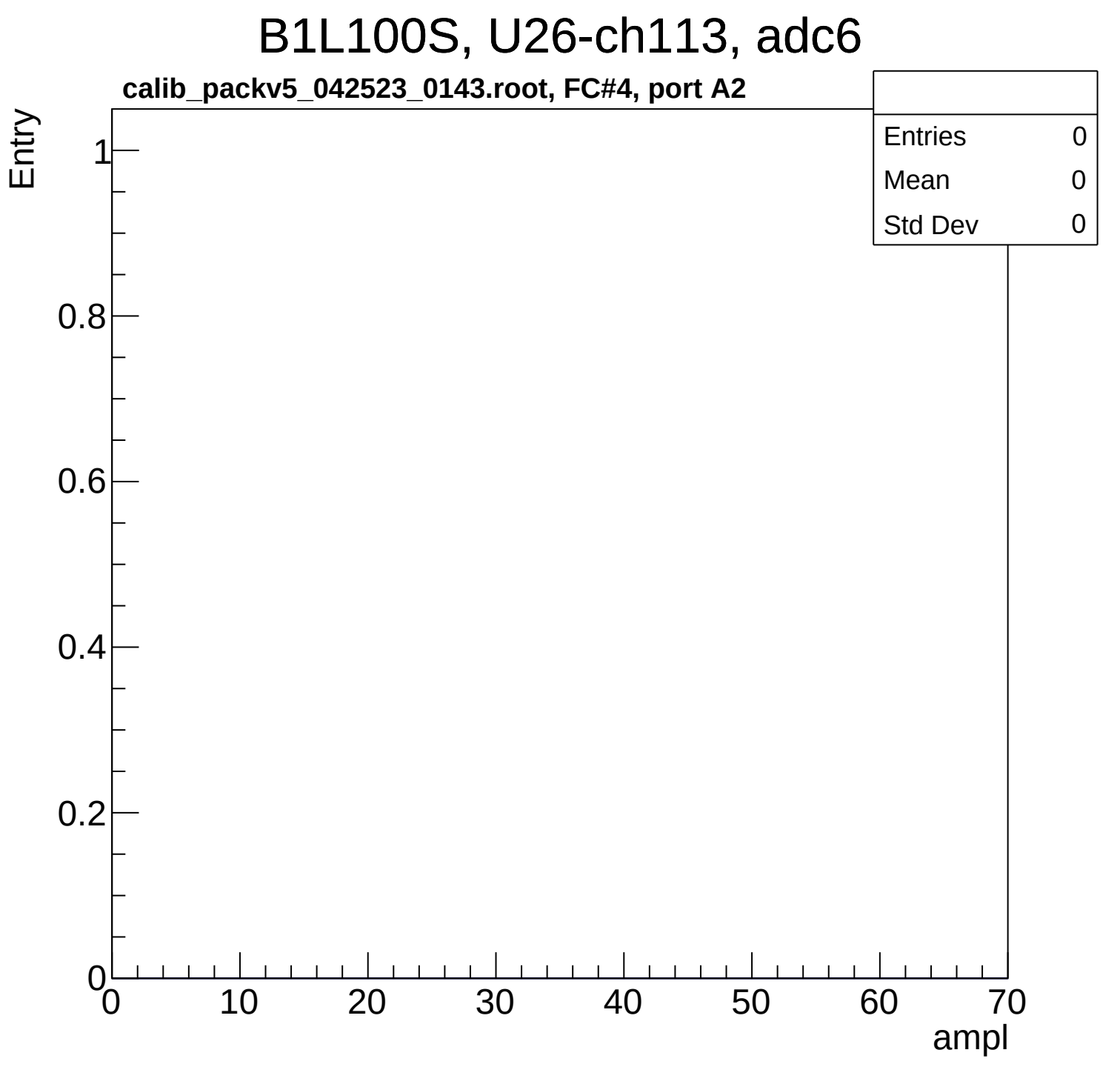
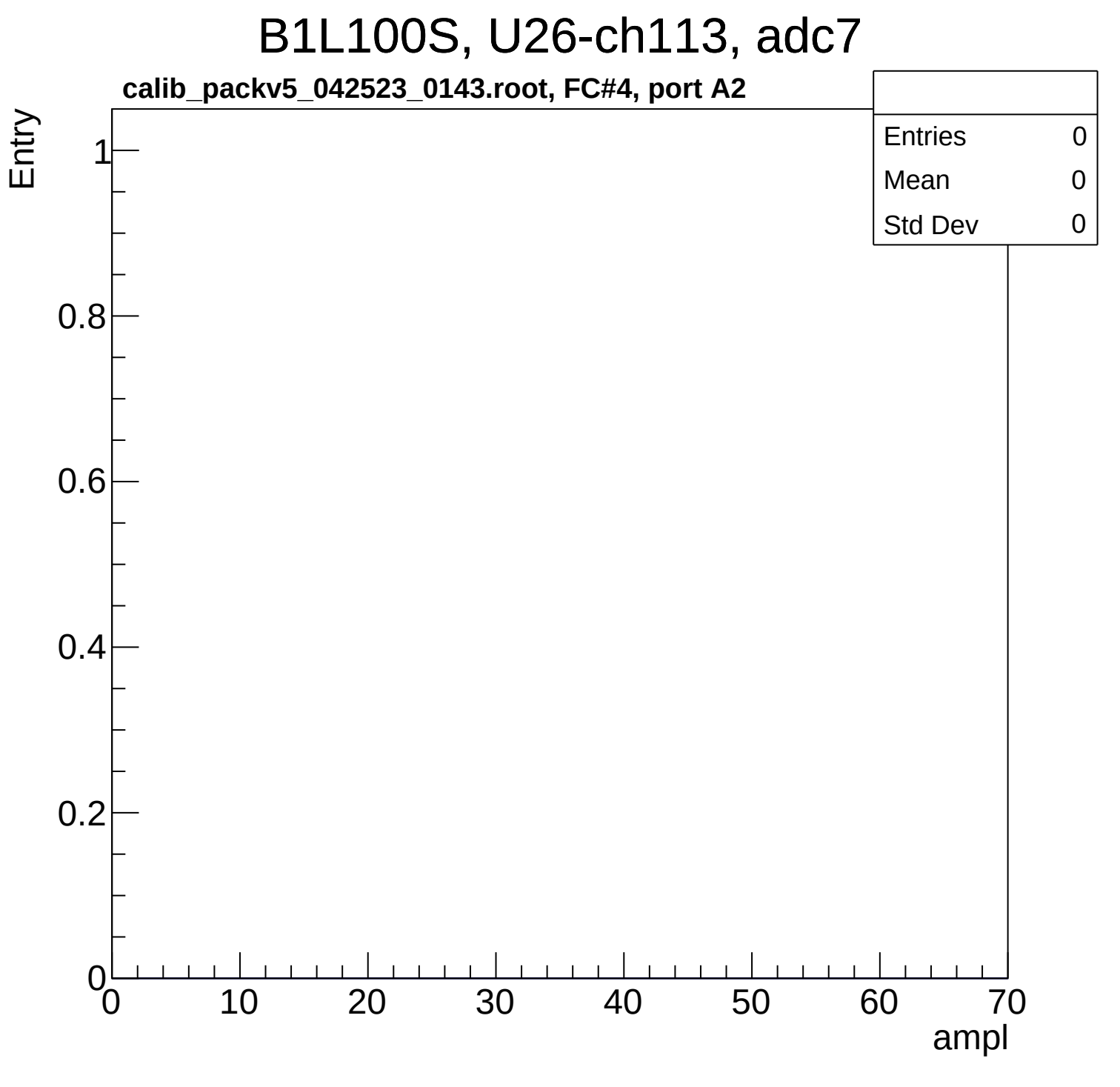

B1L100S, U26-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 58.26
Std Dev 12.71
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch113, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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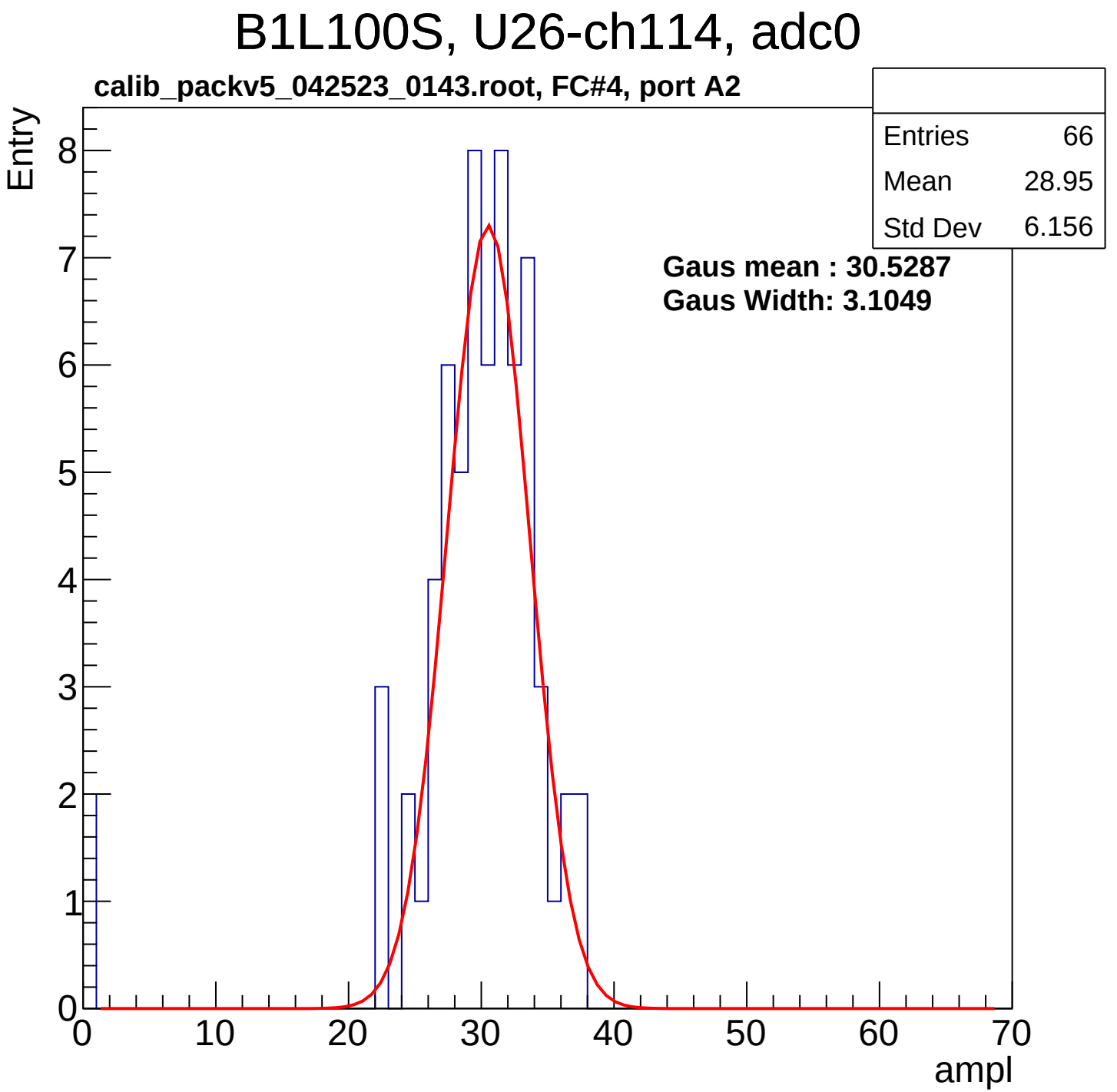
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch114, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.95
Std Dev 6.156
Gaus mean : 30.5287
Gaus Width: 3.1049
Entry
ampl
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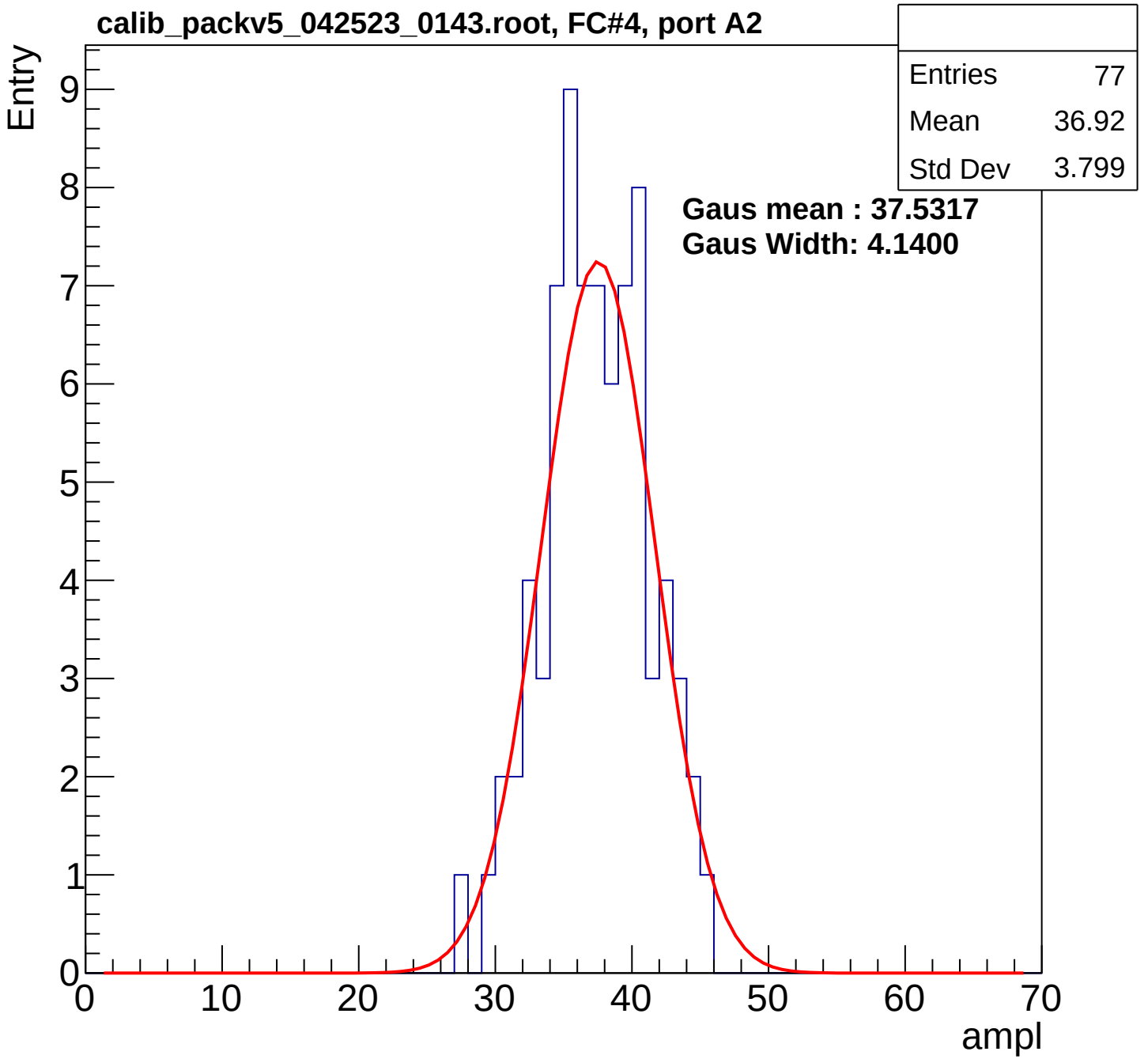
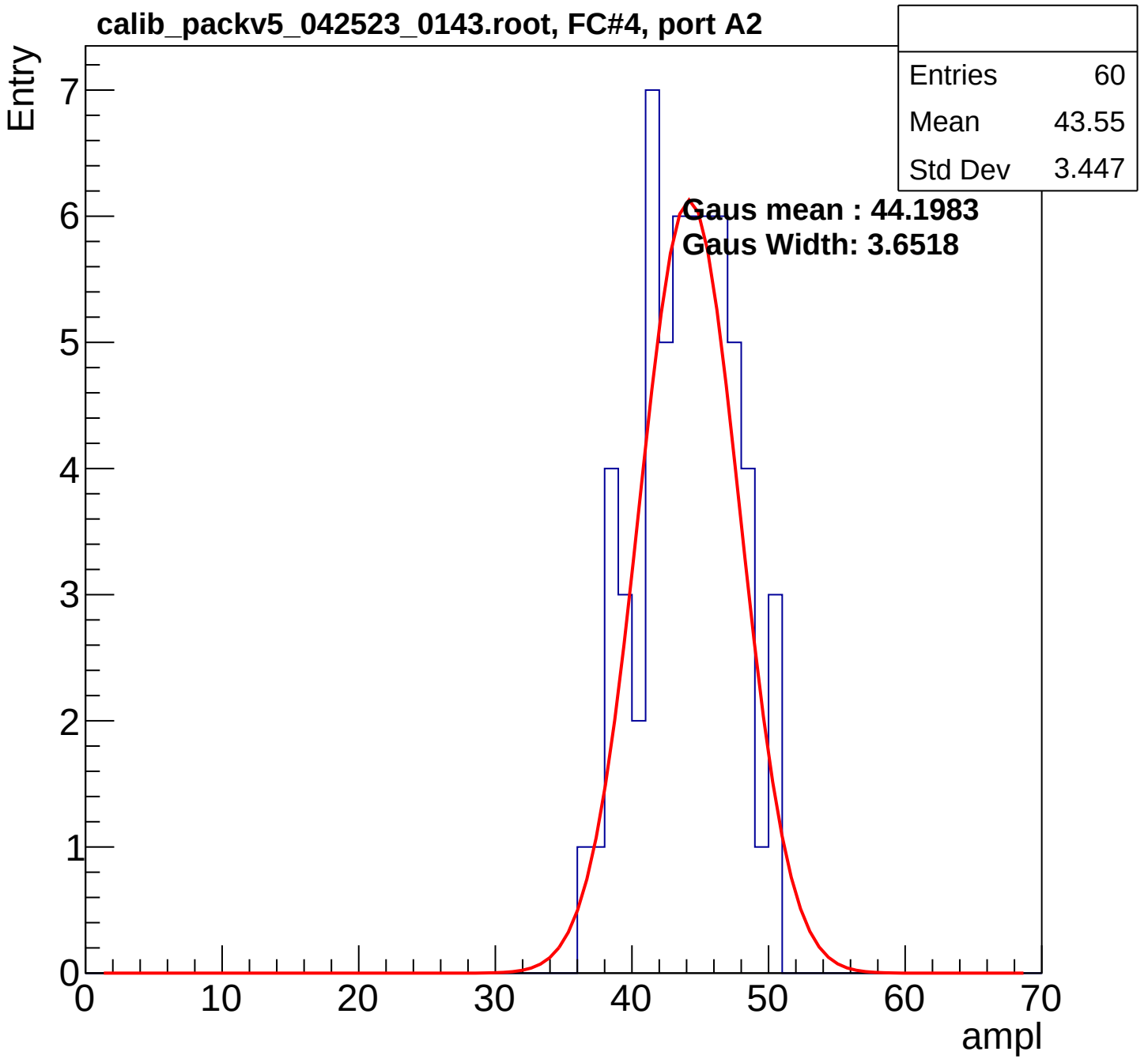
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.92
Std Dev 3.799
Gaus mean : 37.5317
Gaus Width: 4.1400
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.55
Std Dev 3.447
Gaus mean : 44.1983
Gaus Width: 3.6518
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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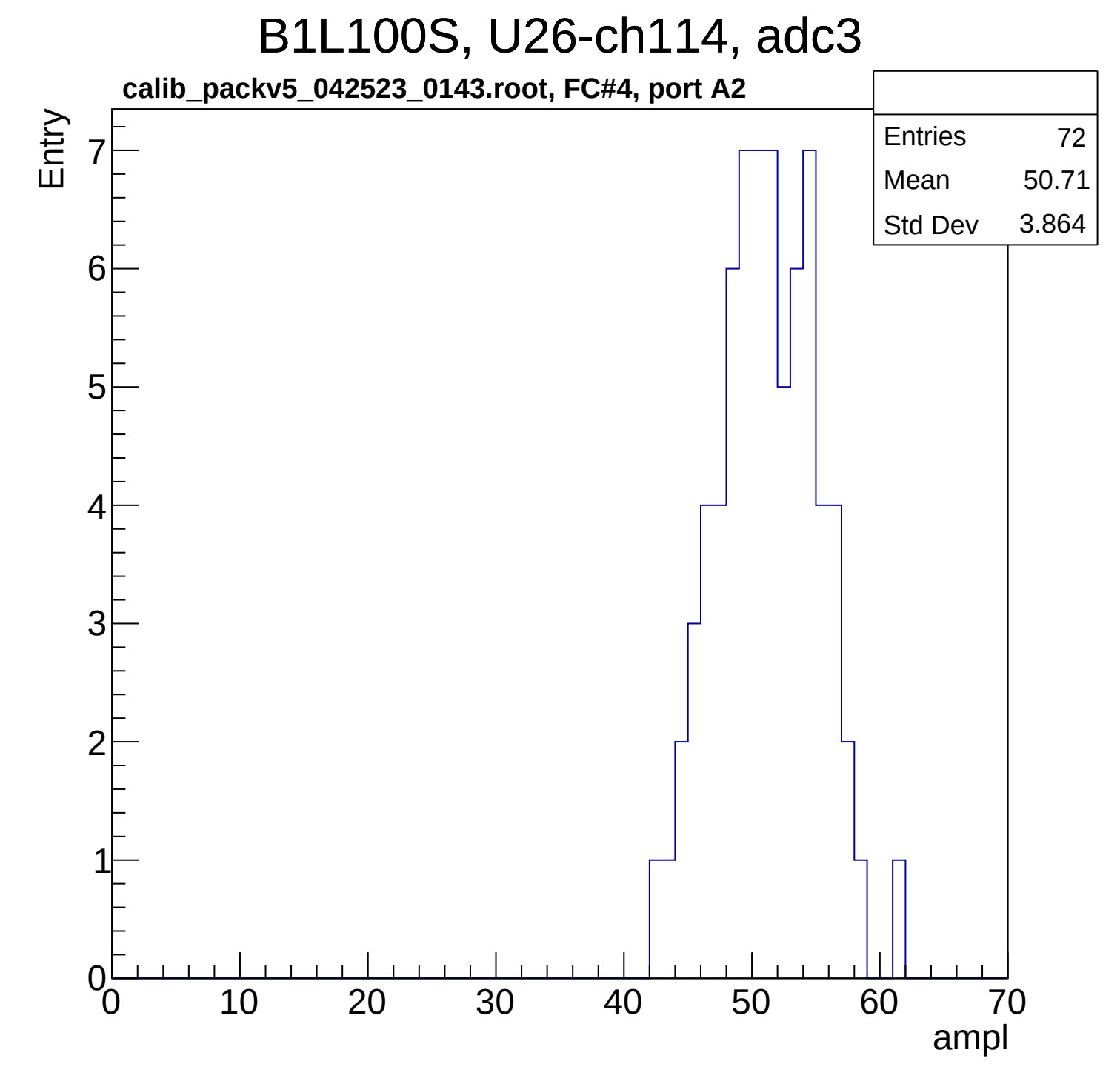
40
50
60
70


B1L100S, U26-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.71
Std Dev 3.864
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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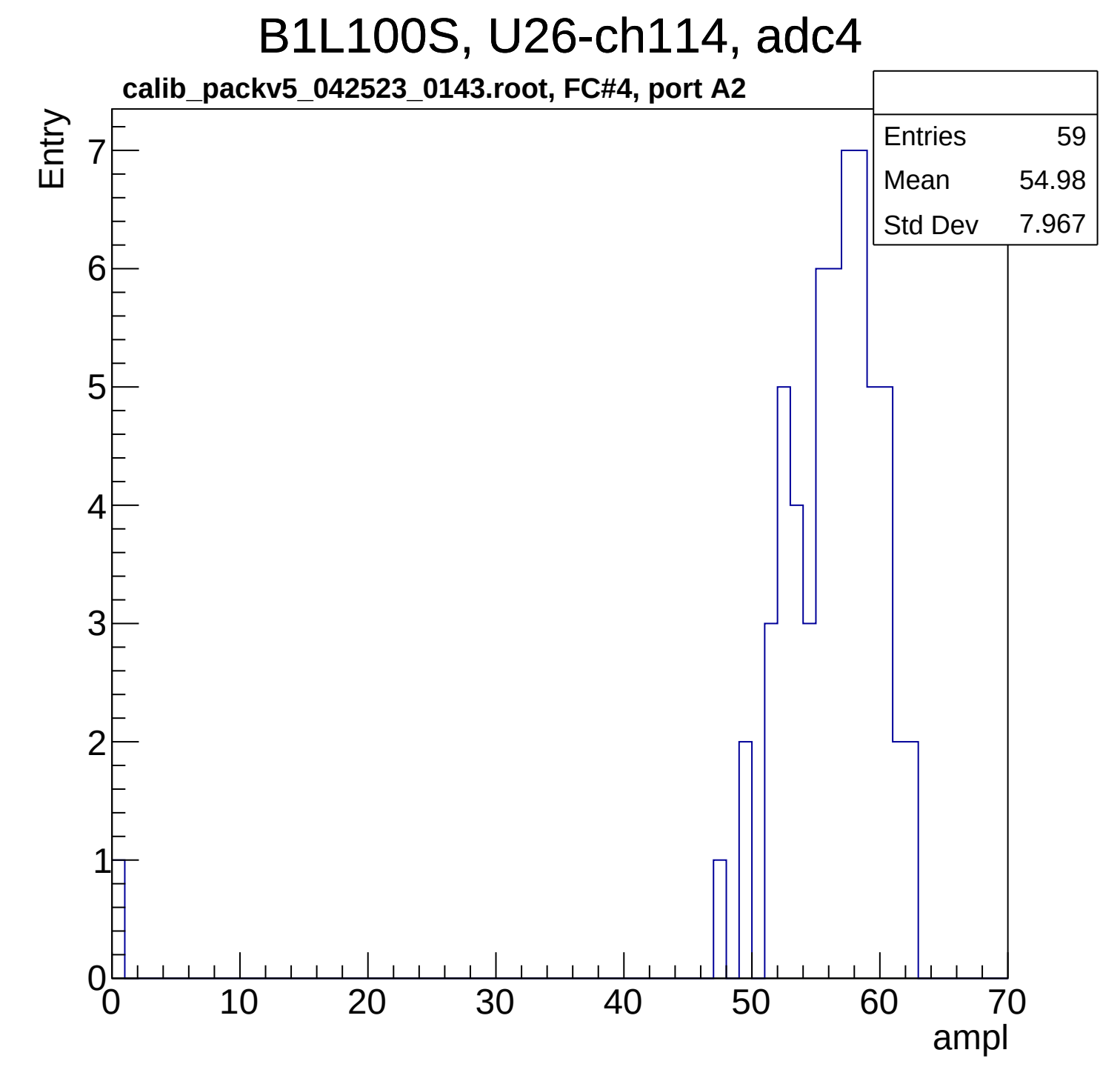
B1L100S, U26-ch114, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.98
Std Dev 7.967
Entry
ampl
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4
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6
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0
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40
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60
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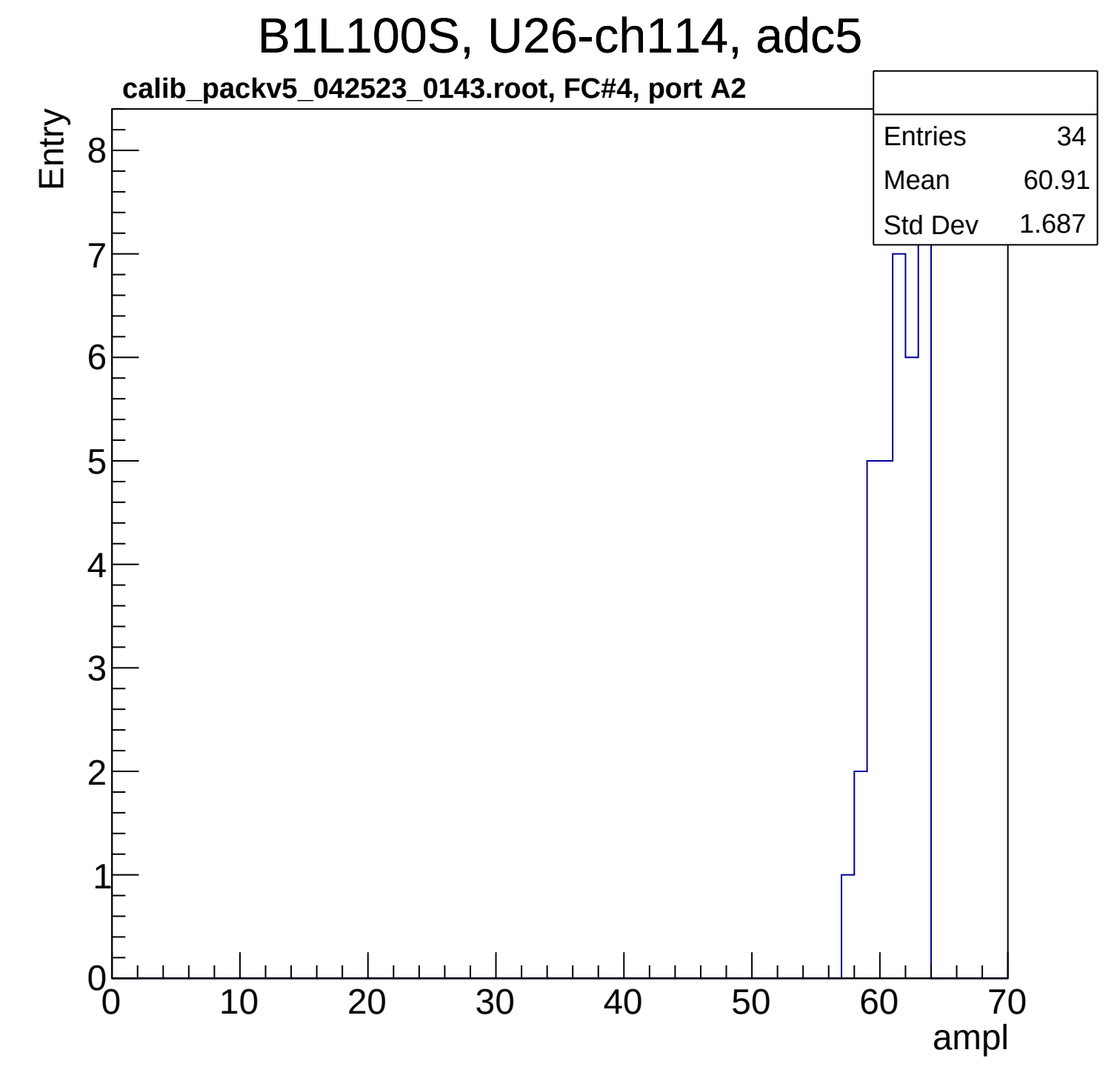
B1L100S, U26-ch114, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 60.91
Std Dev 1.687
Entry
ampl
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3
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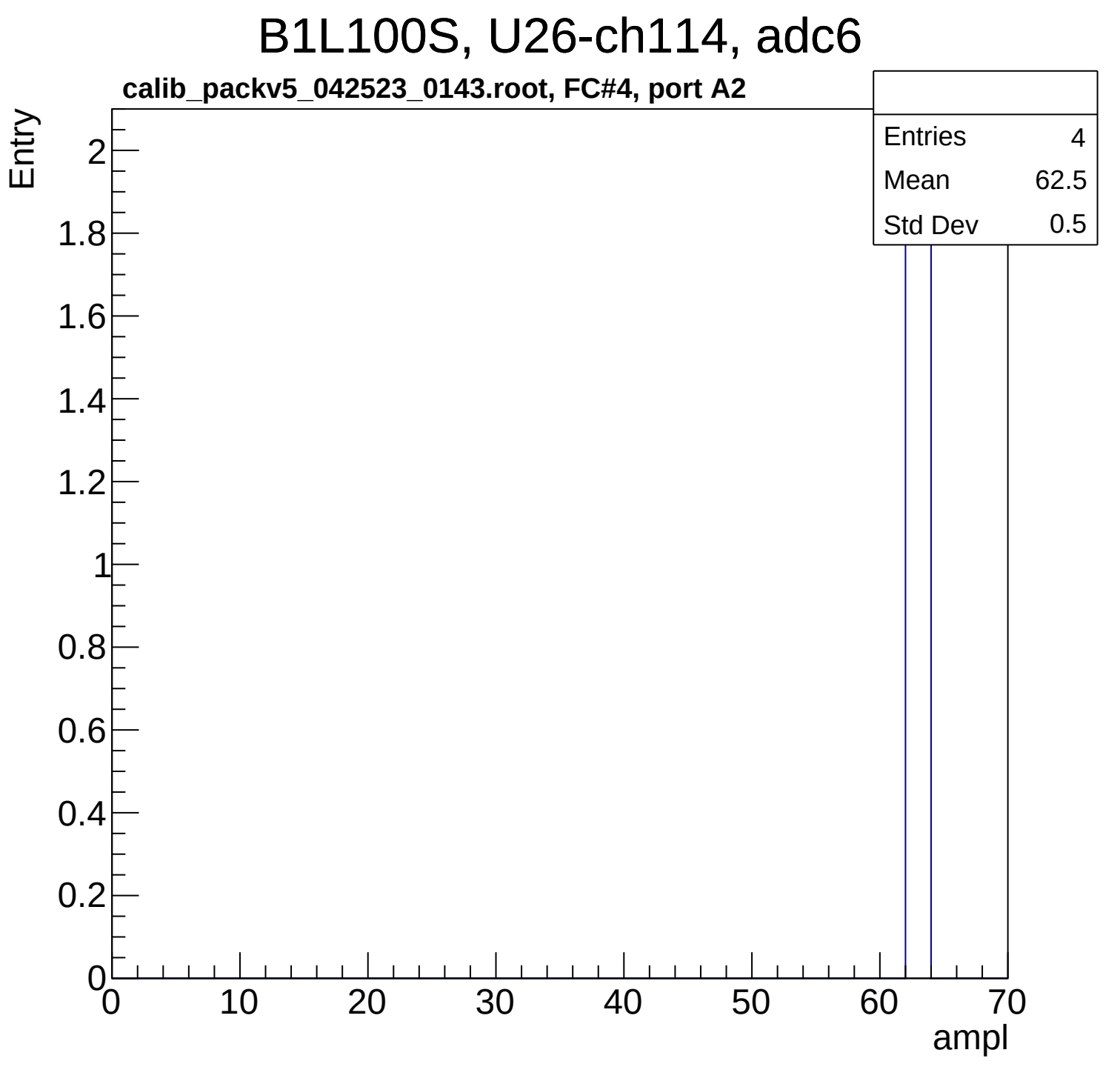
B1L100S, U26-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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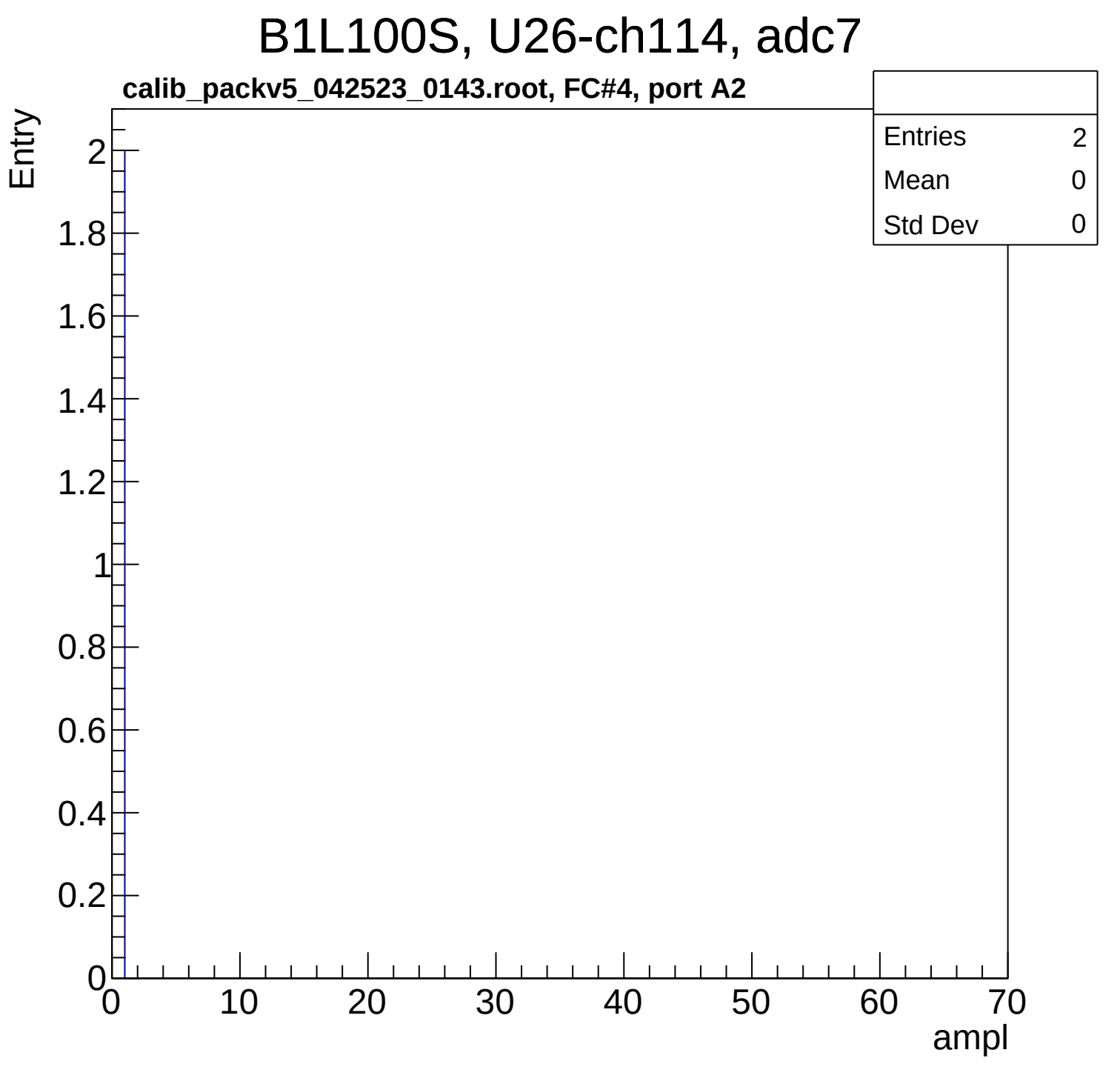
B1L100S, U26-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
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0
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60
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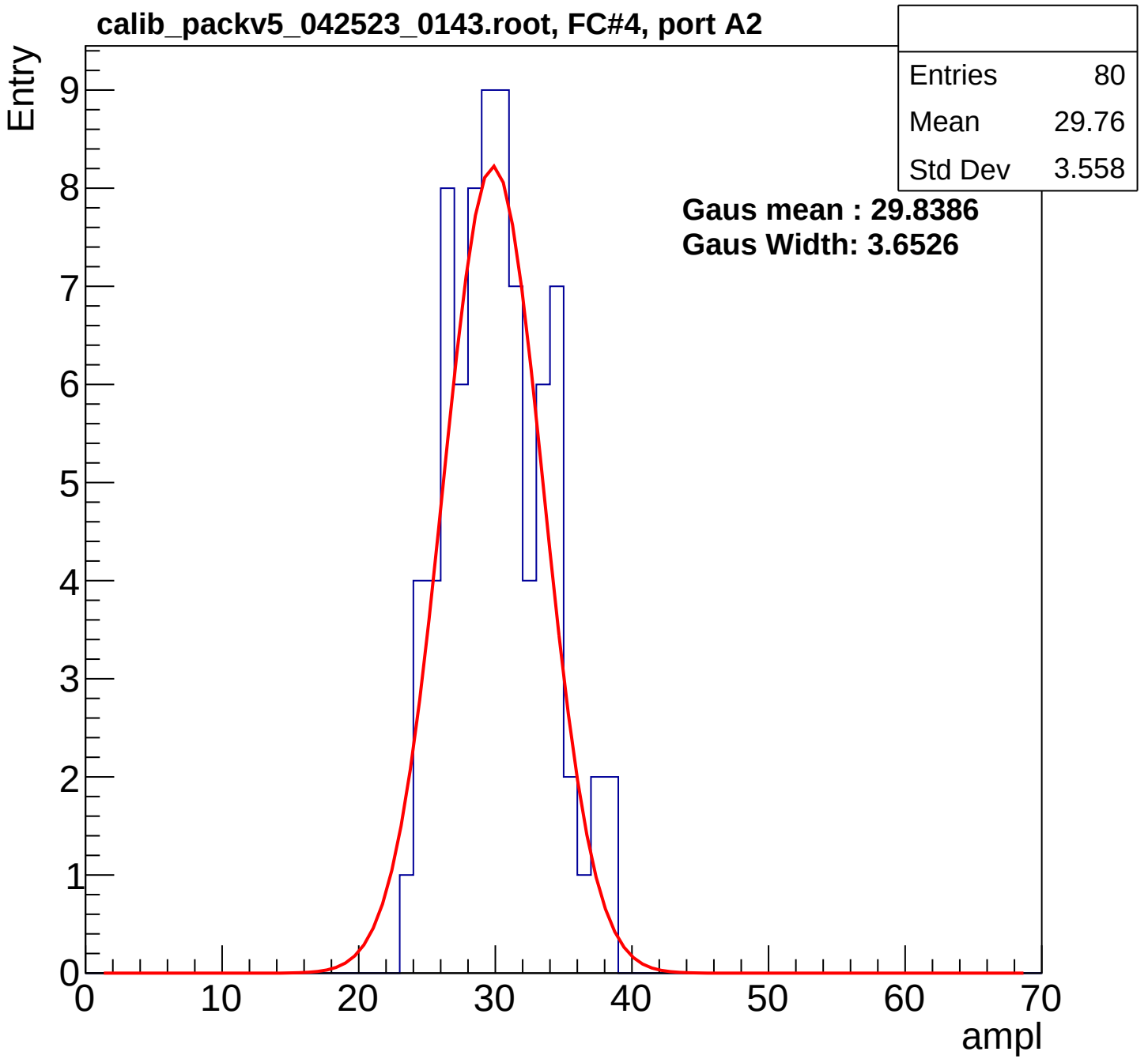
B1L100S, U26-ch115, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.76
Std Dev 3.558
Gaus mean : 29.8386
Gaus Width: 3.6526
Entry
ampl
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10
20
30
40
50
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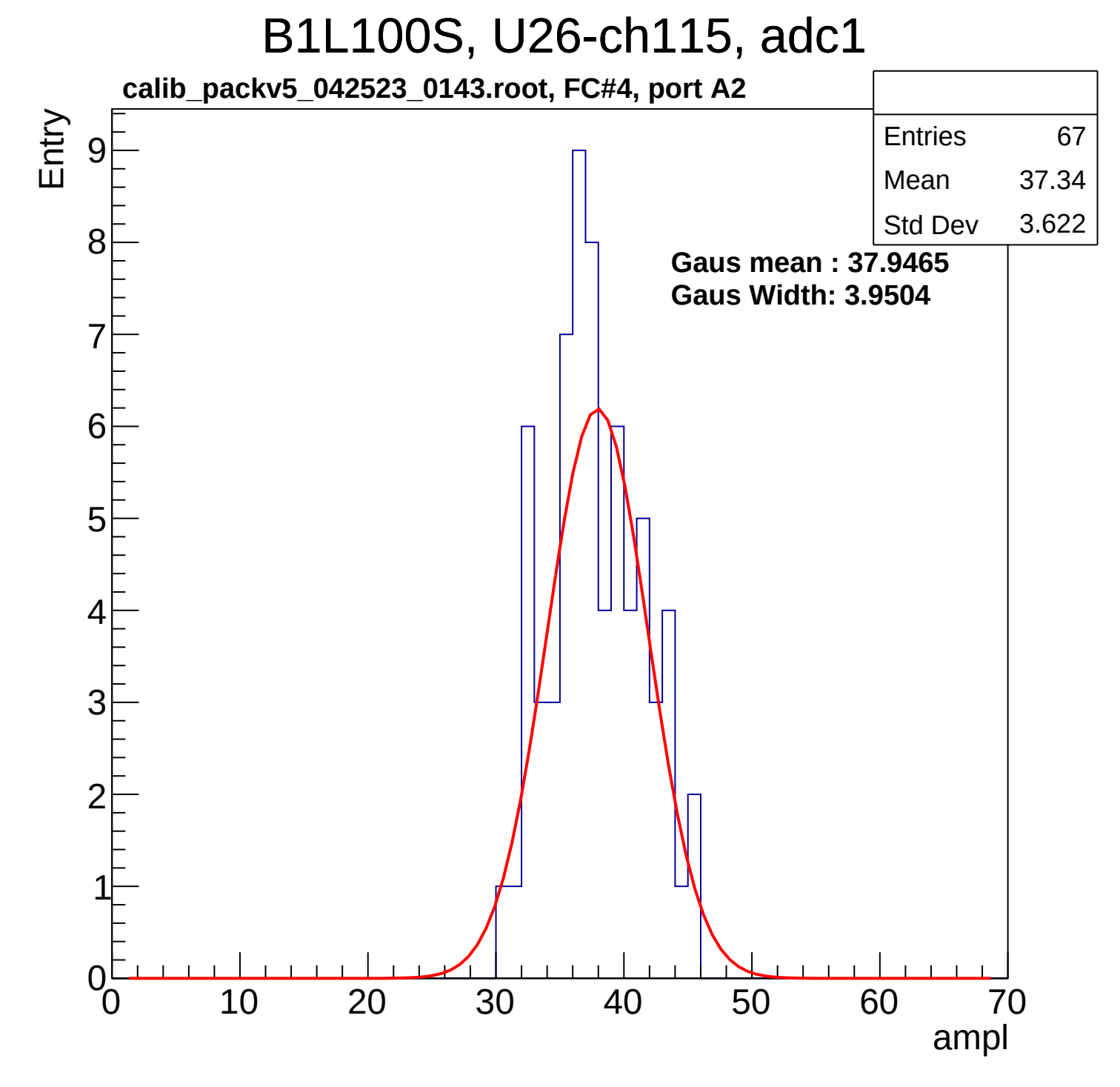
1
2
3
4
5
6
7
8
9


B1L100S, U26-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.34
Std Dev 3.622
Gaus mean : 37.9465
Gaus Width: 3.9504
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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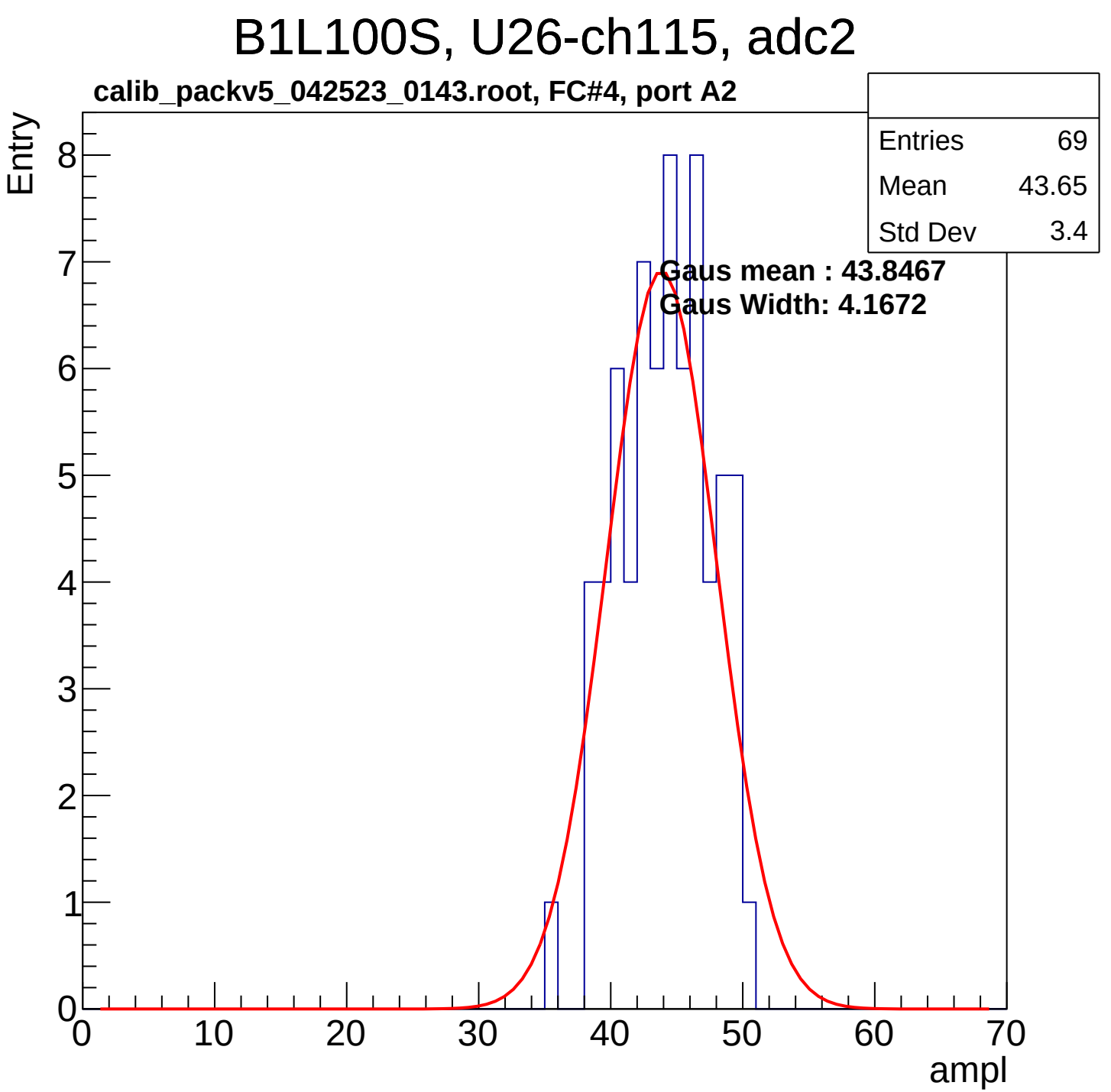
B1L100S, U26-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.65
Std Dev 3.4
Gaus mean : 43.8467
Gaus Width: 4.1672
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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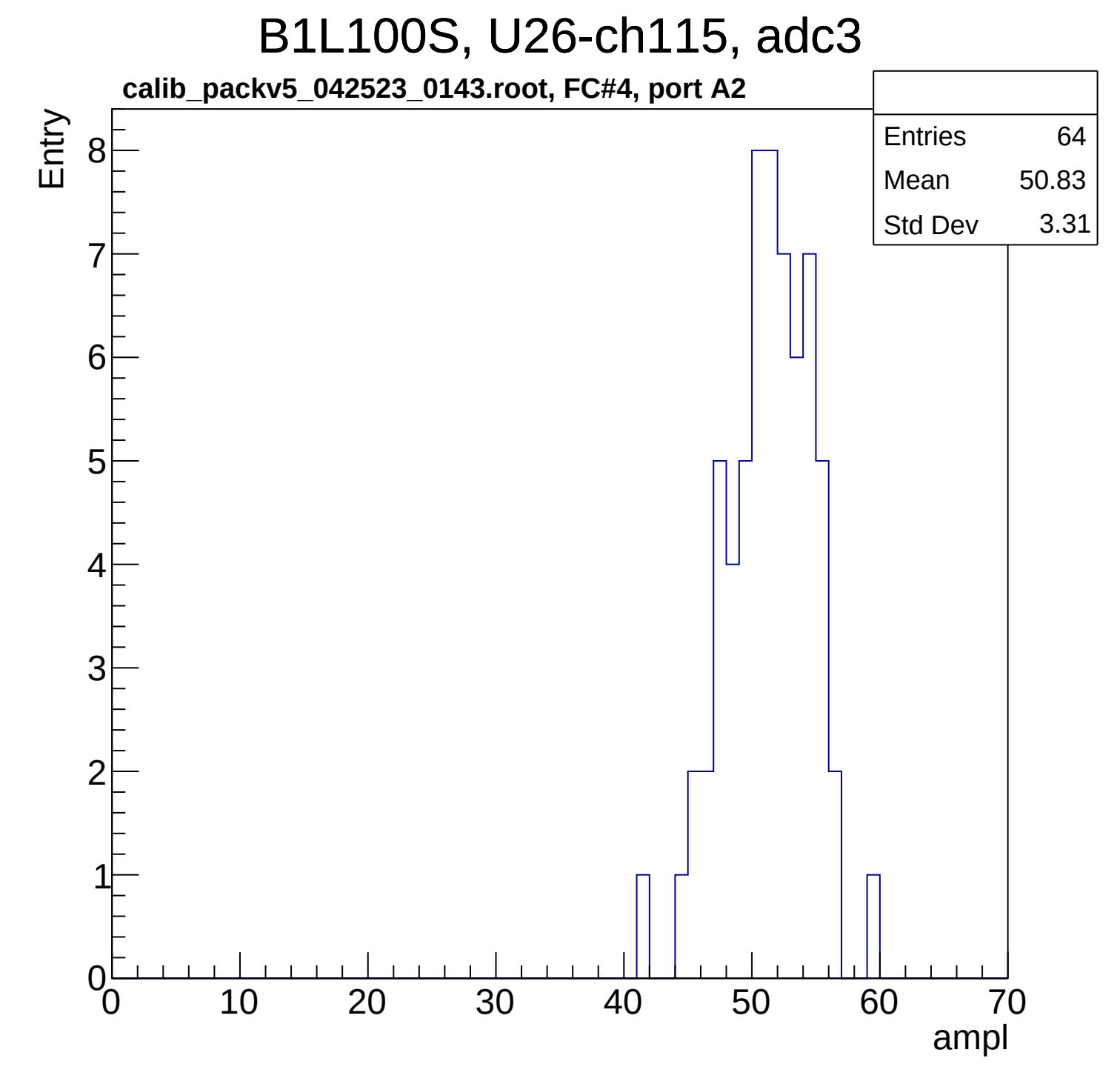
B1L100S, U26-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.83
Std Dev 3.31
Entry
ampl
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60
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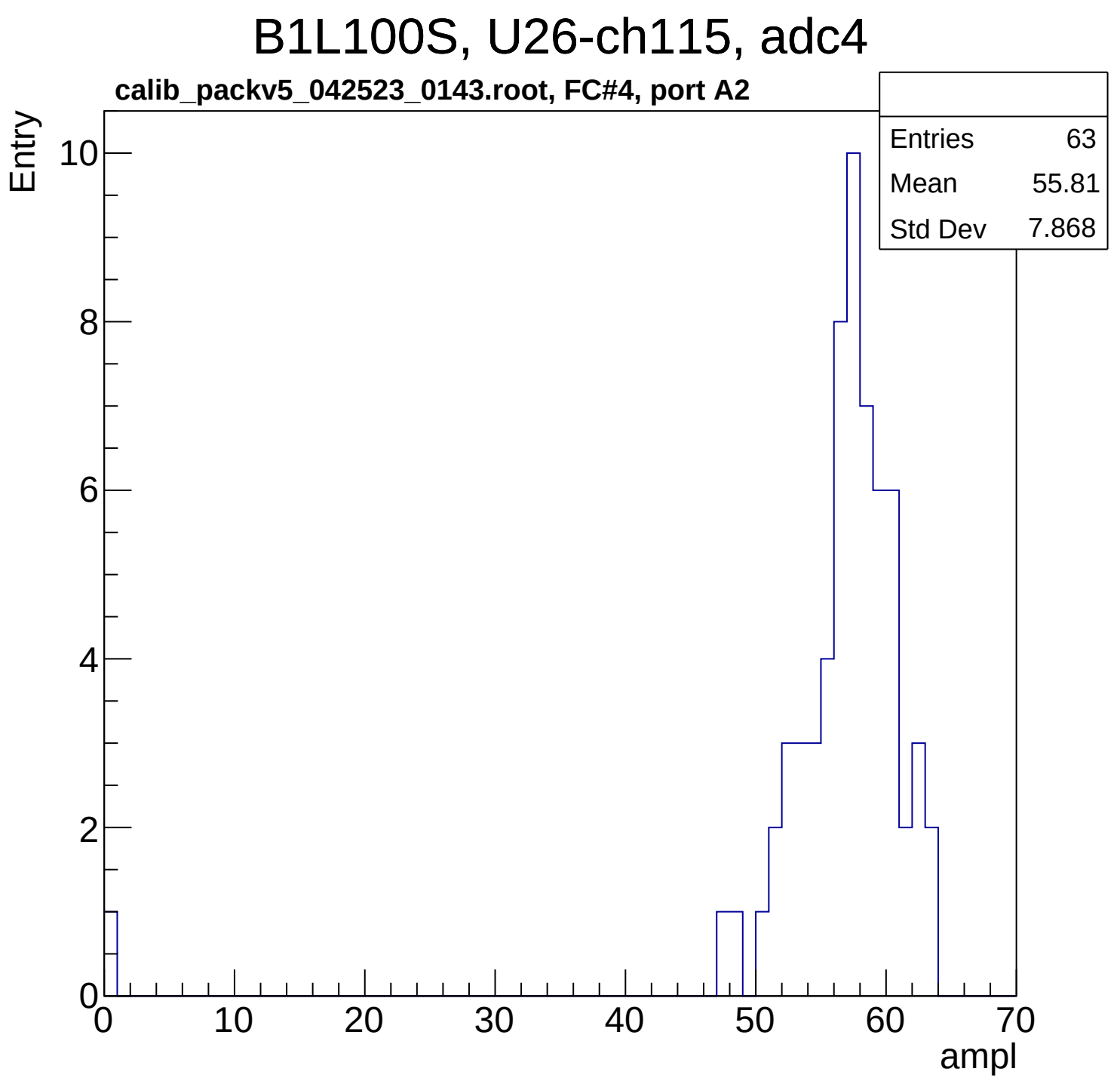
B1L100S, U26-ch115, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.81
Std Dev 7.868
Entry
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4
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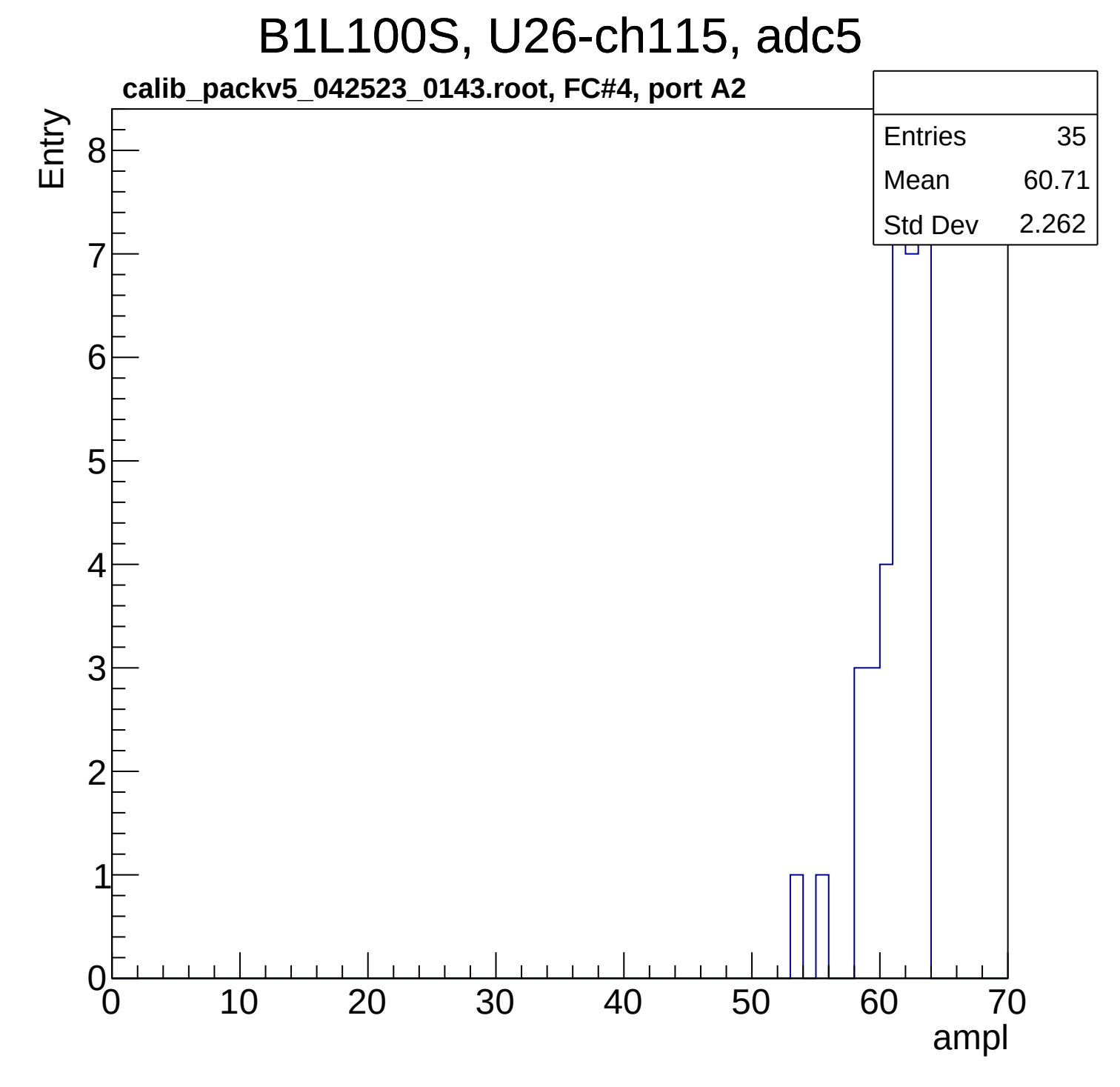
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 60.71
Std Dev 2.262
Entry
ampl
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3
4
5
6
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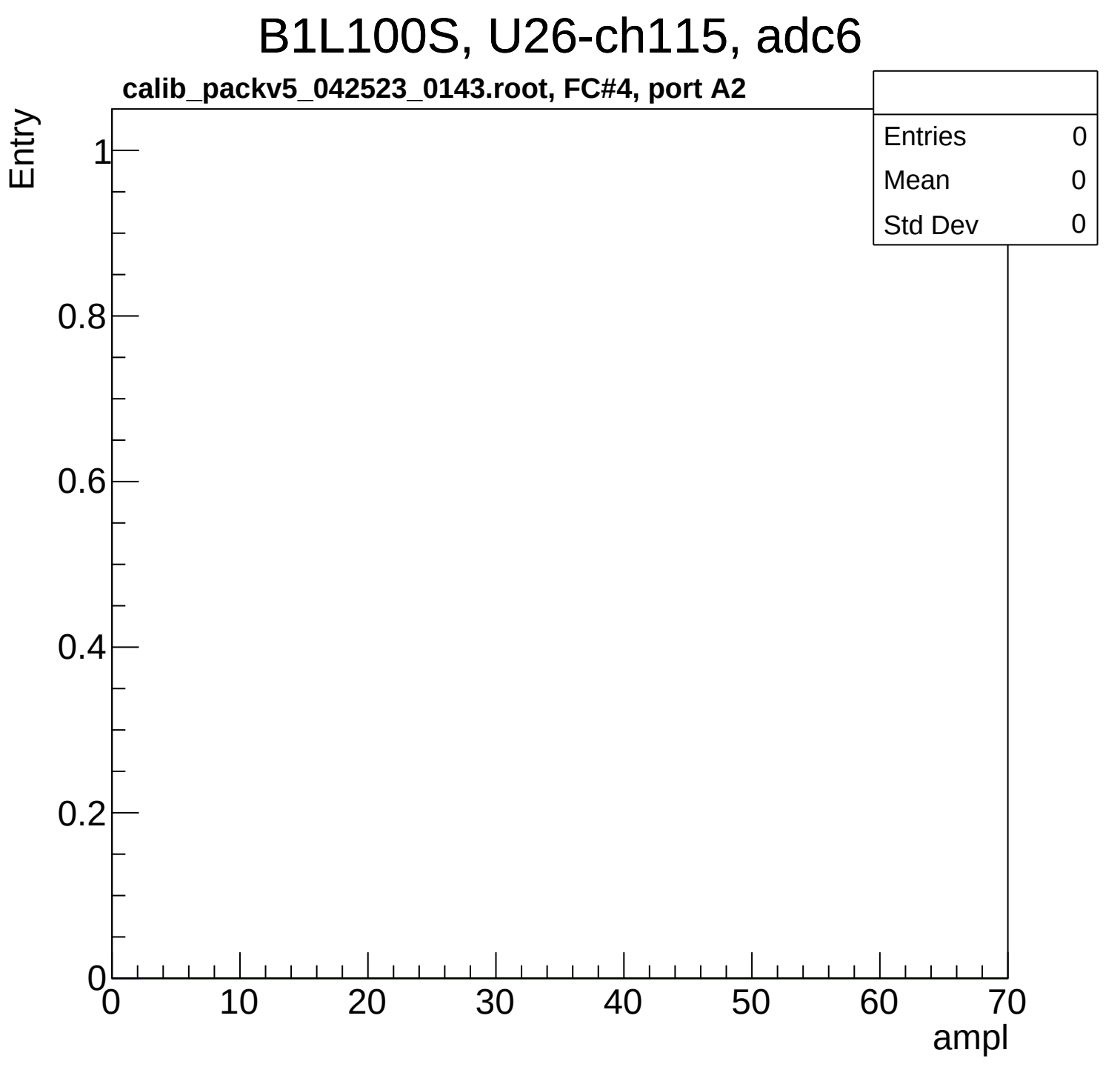
B1L100S, U26-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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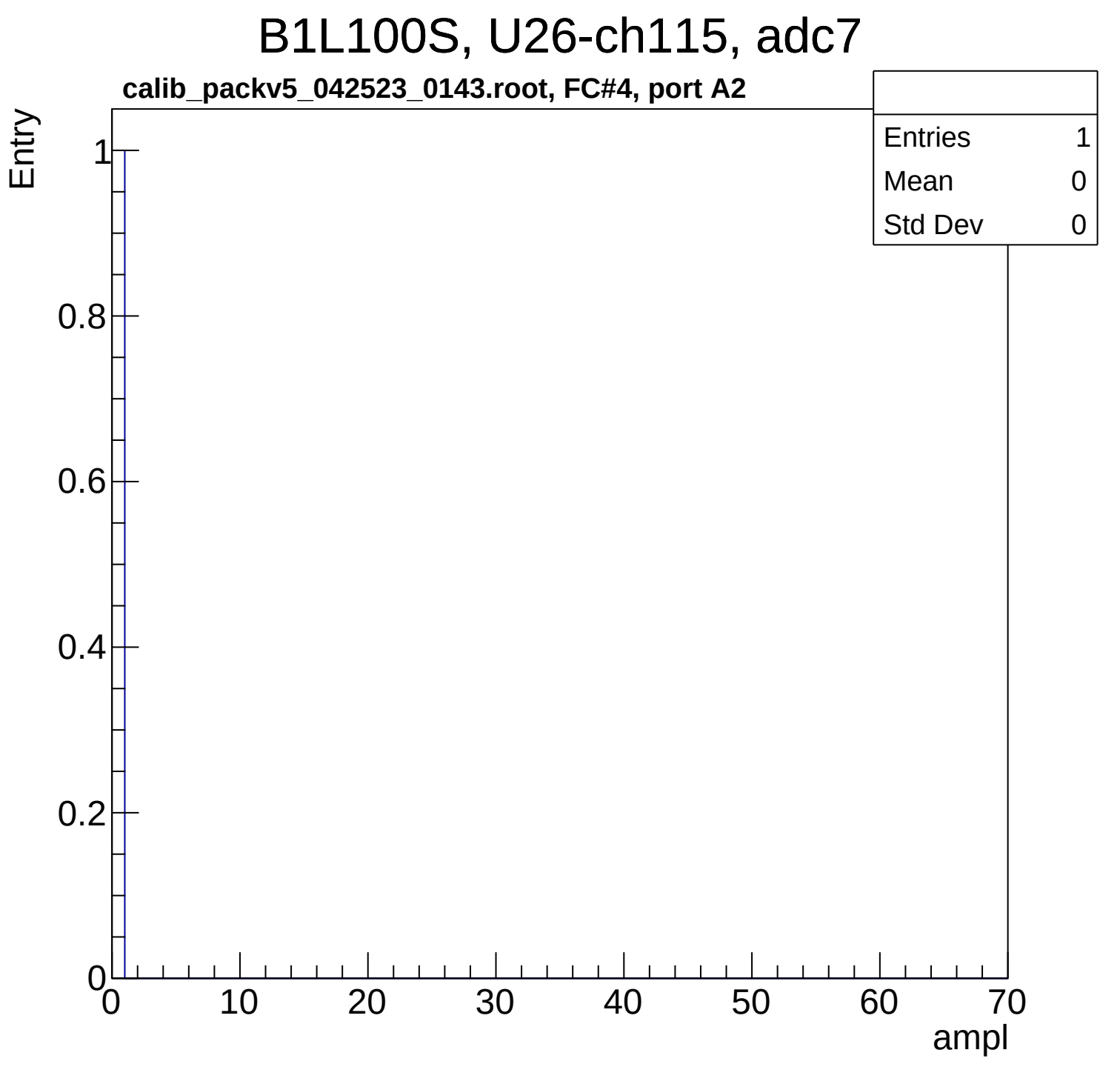
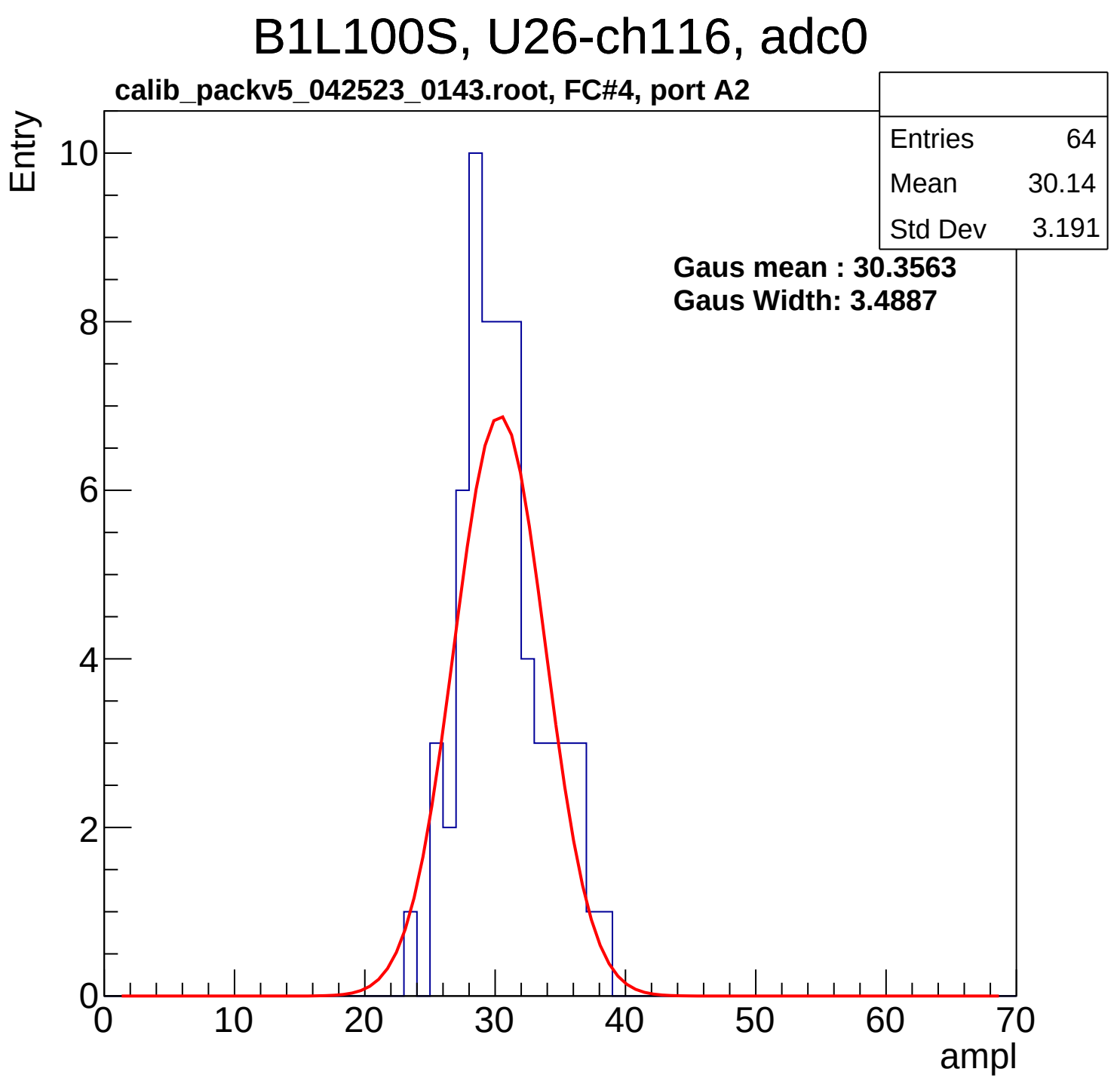
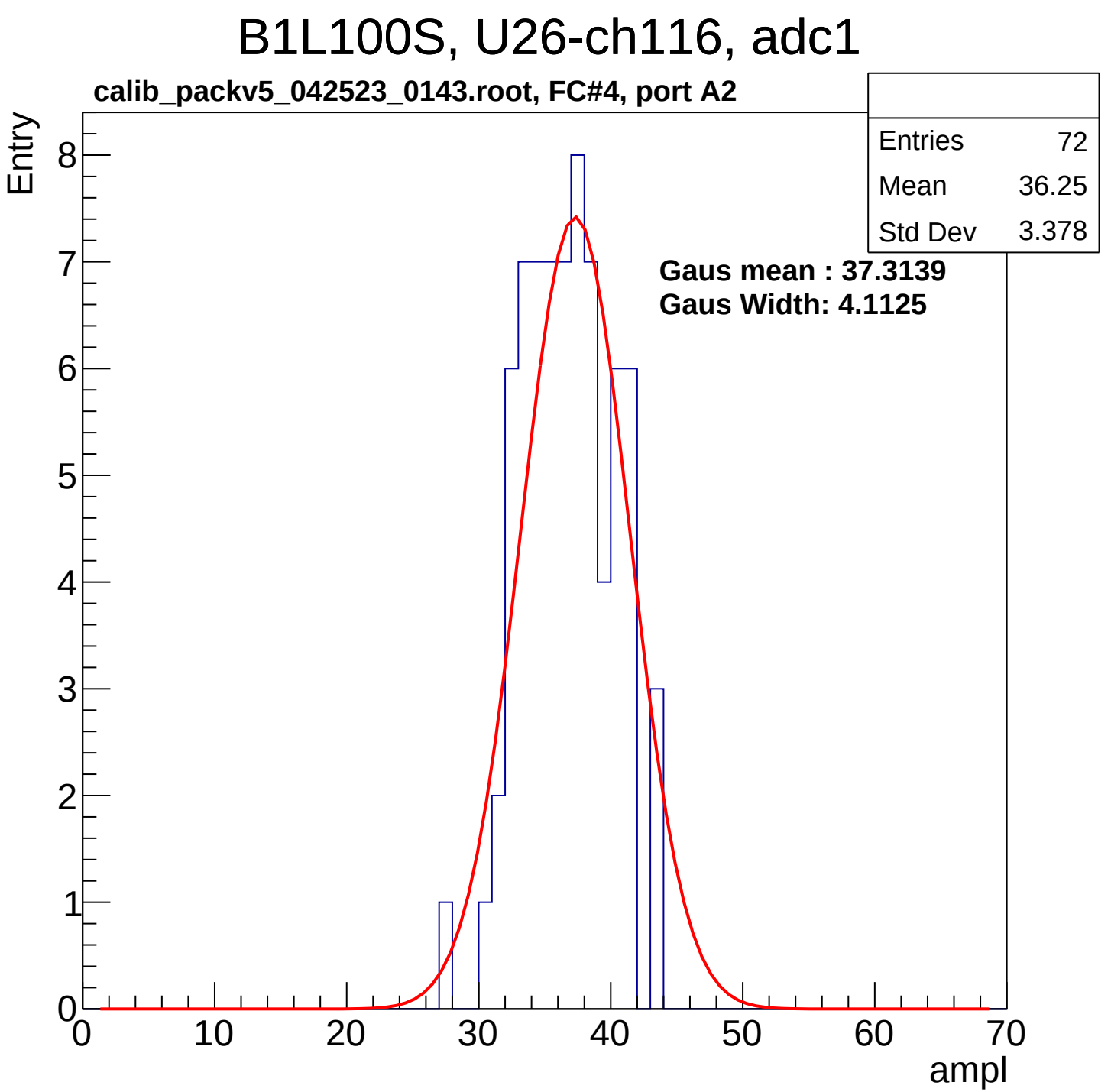
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 30.14
Std Dev 3.191
Gaus mean : 30.3563
Gaus Width: 3.4887
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.25
Std Dev 3.378
Gaus mean : 37.3139
Gaus Width: 4.1125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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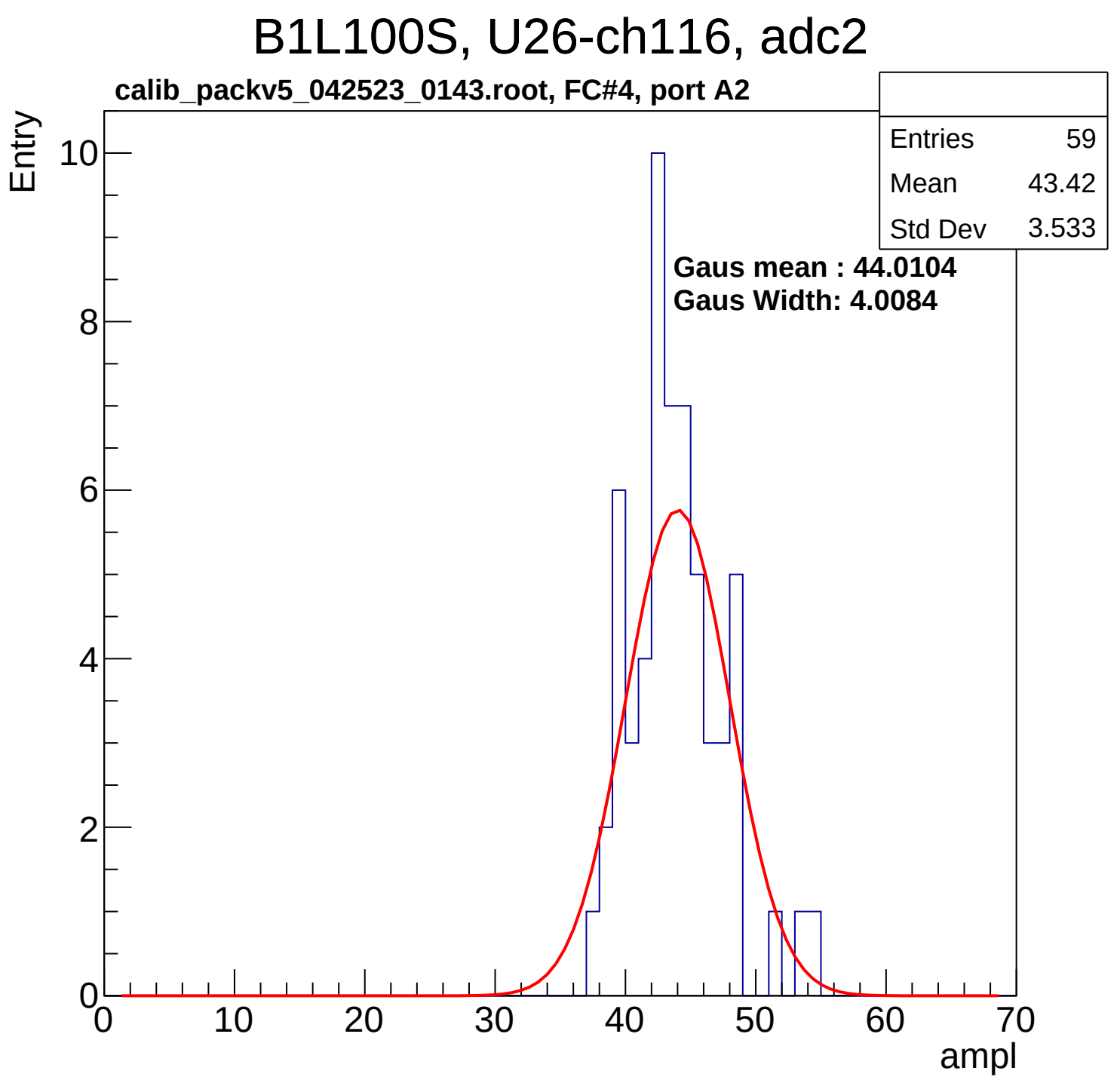
B1L100S, U26-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.42
Std Dev 3.533
Gaus mean : 44.0104
Gaus Width: 4.0084
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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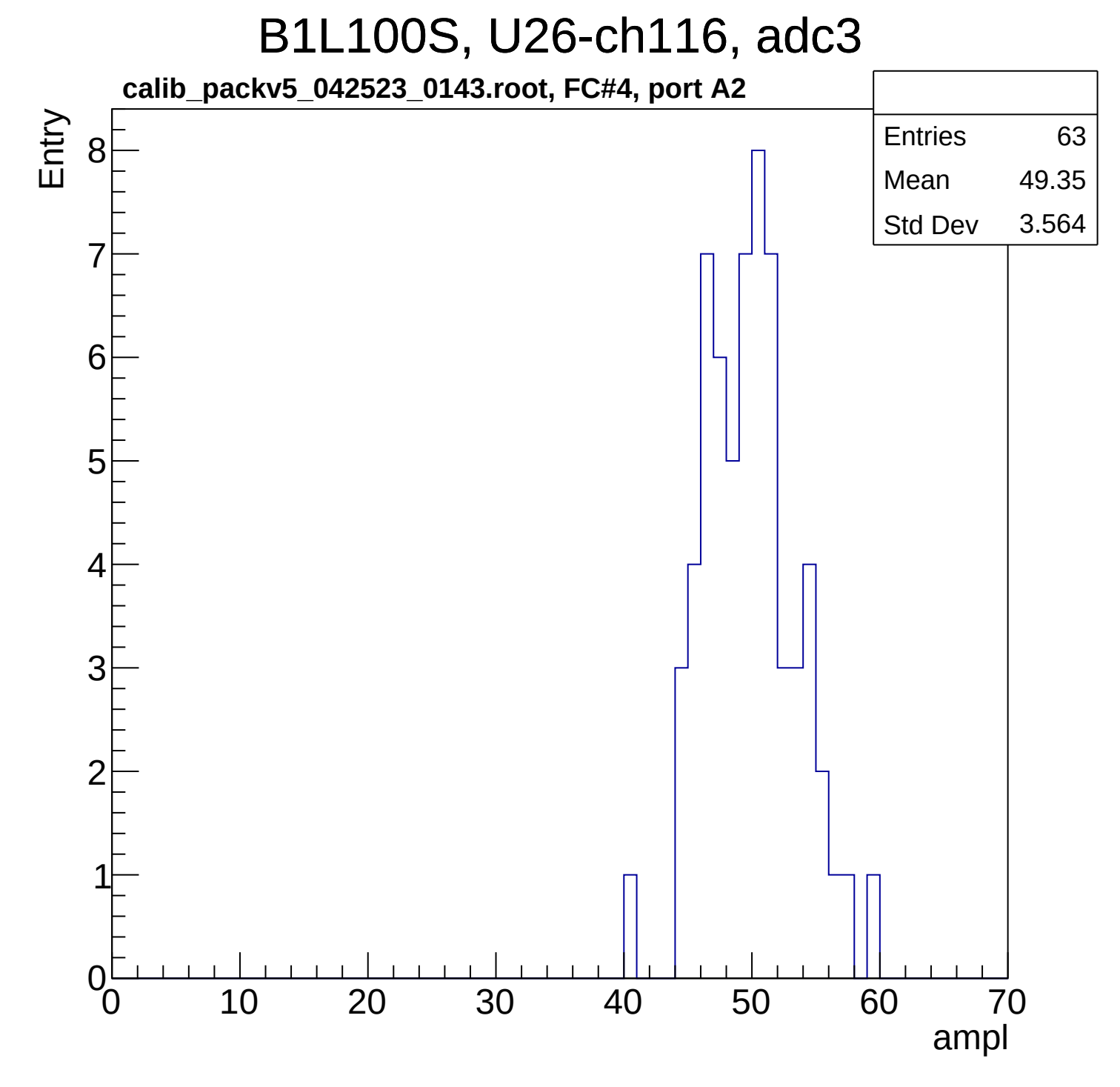
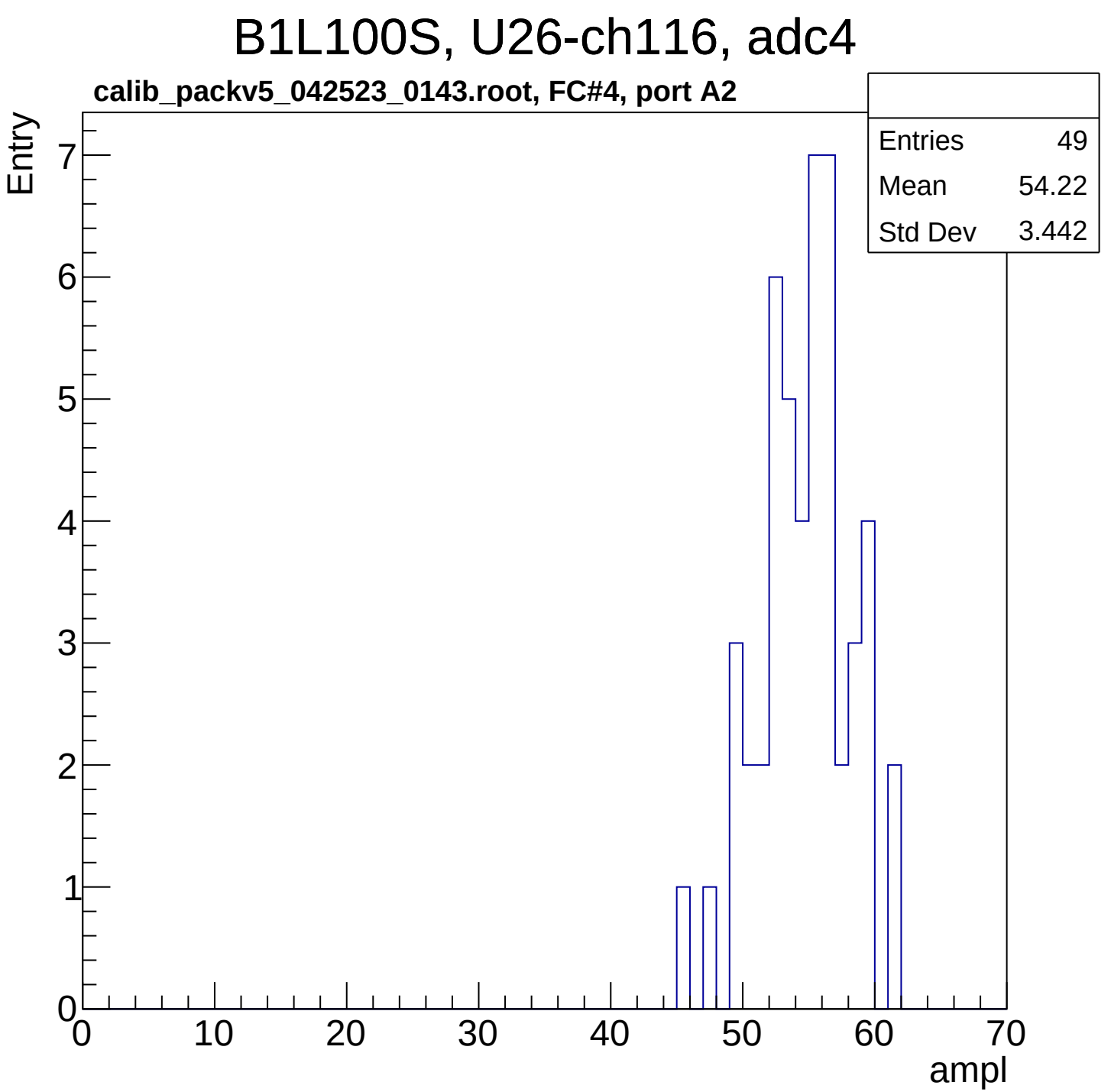
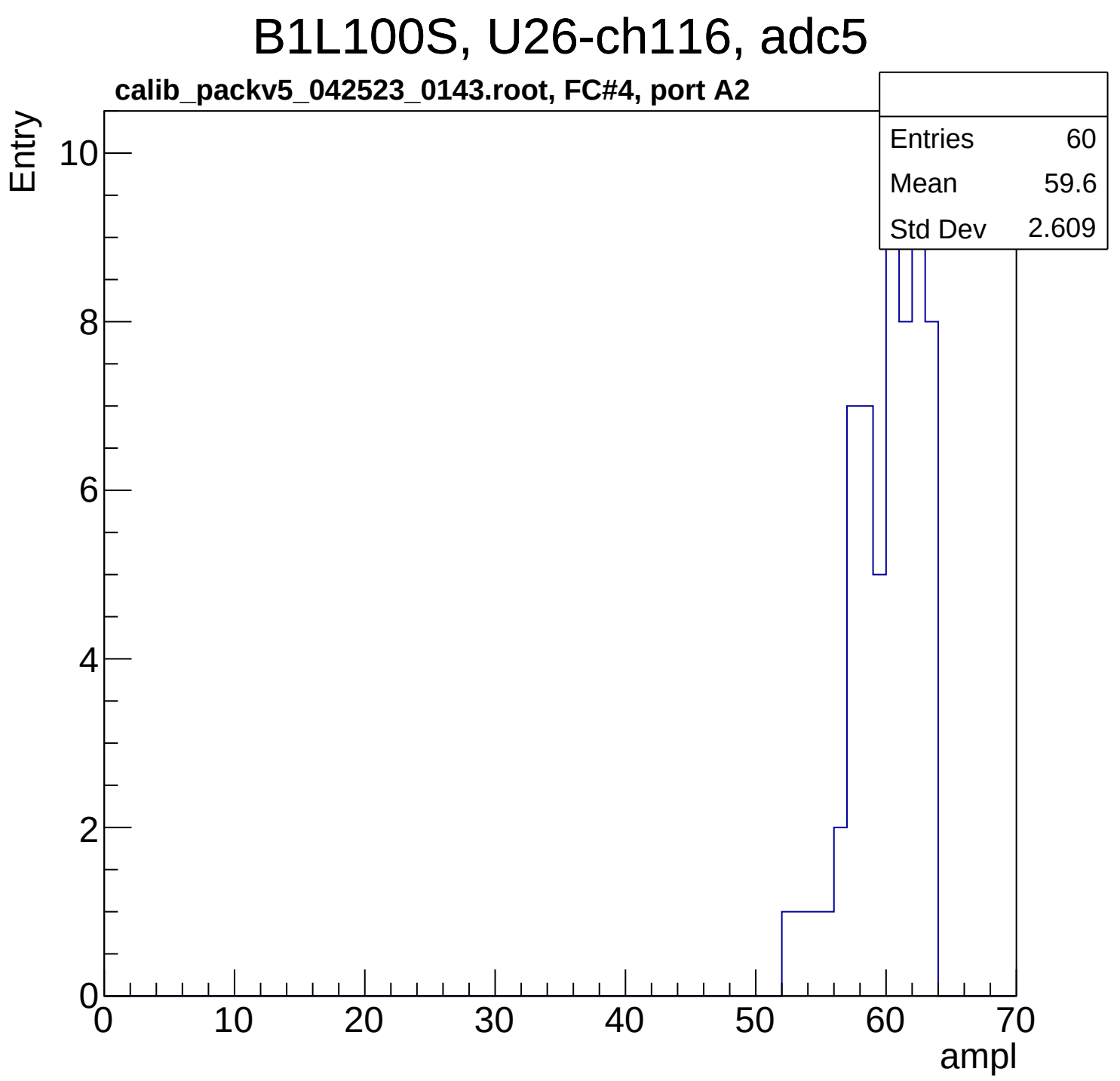
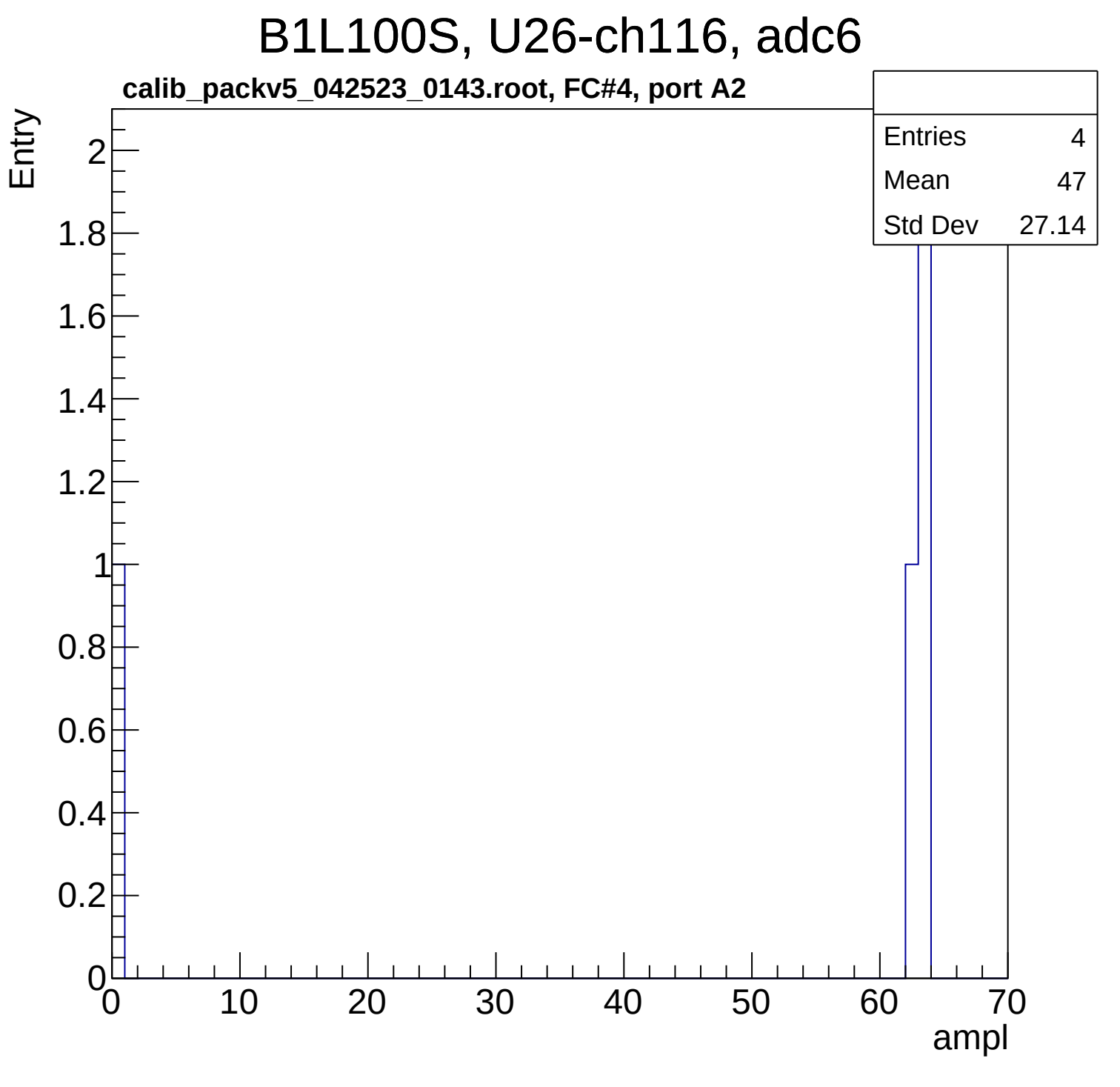
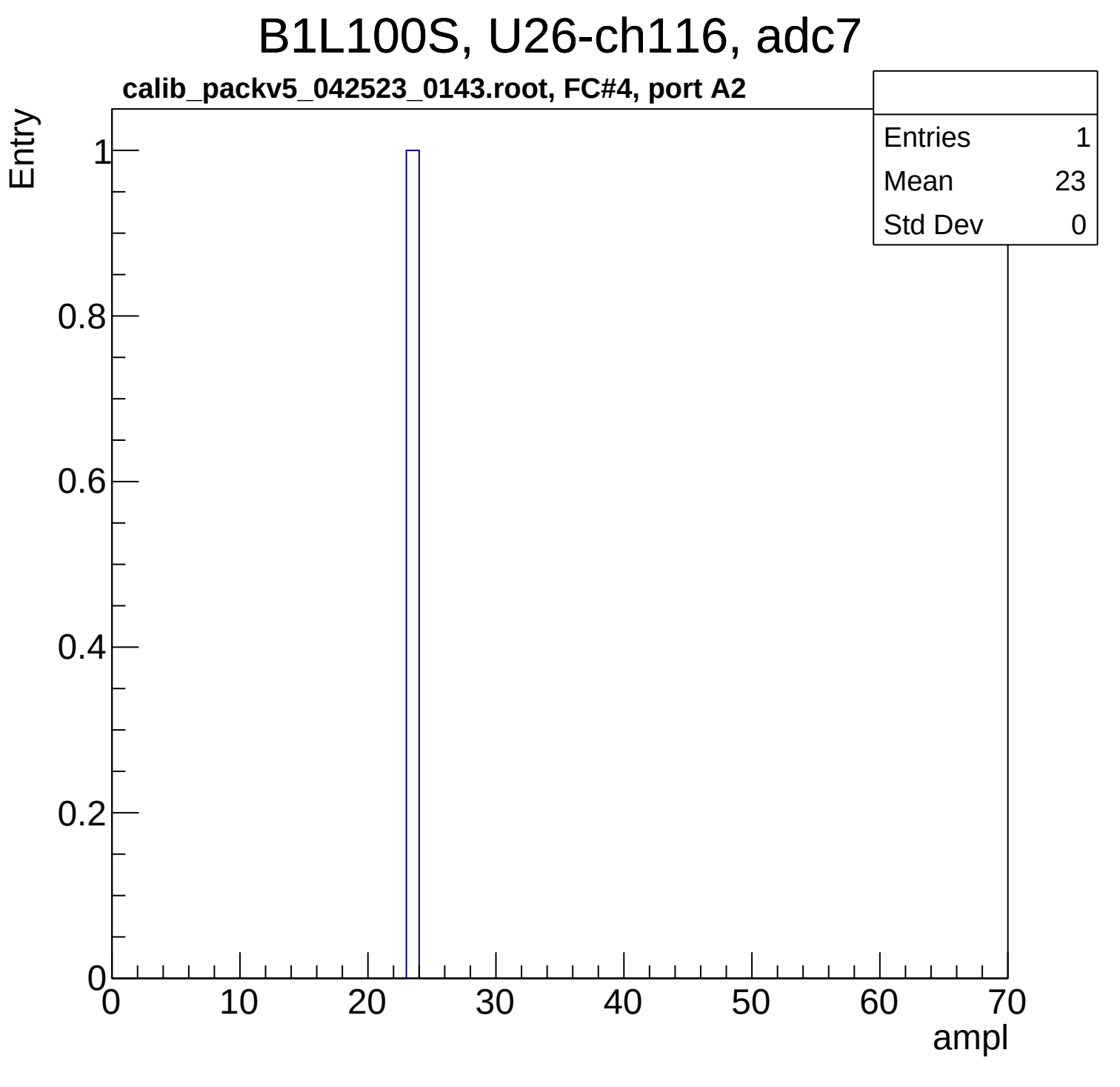
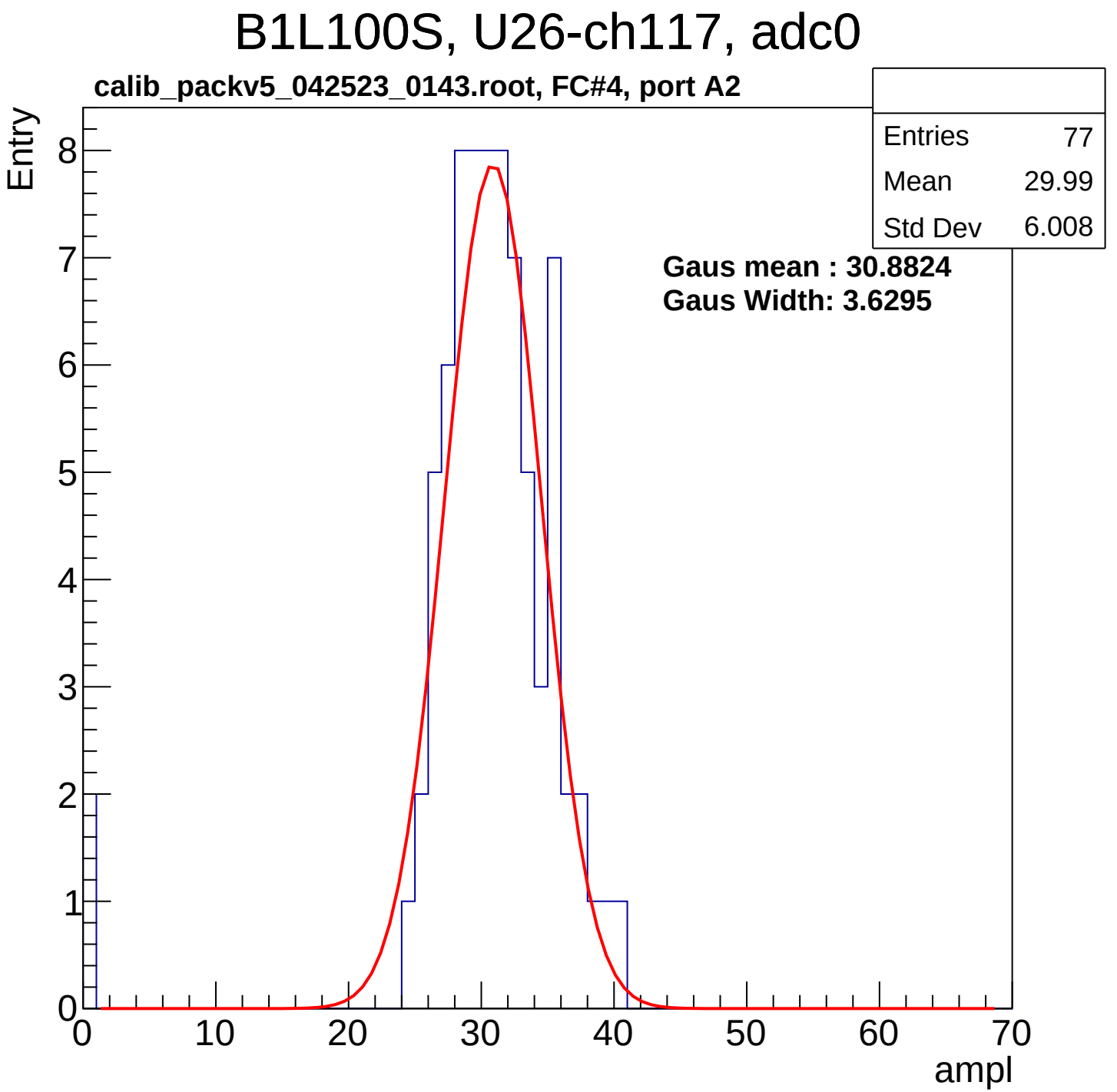

B1L100S, U26-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.35
Std Dev 3.564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

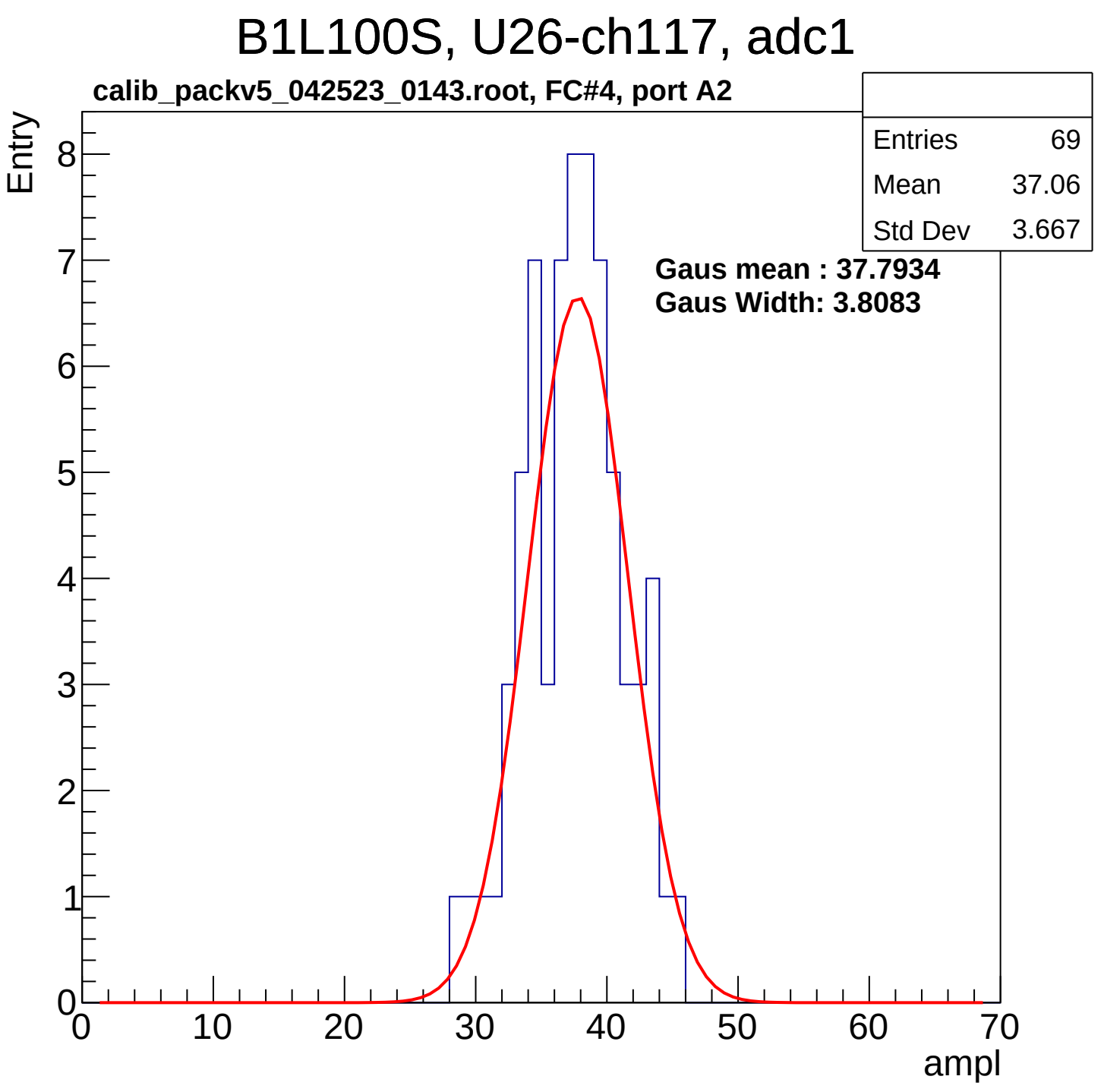

B1L100S, U26-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 54.22
Std Dev 3.442
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

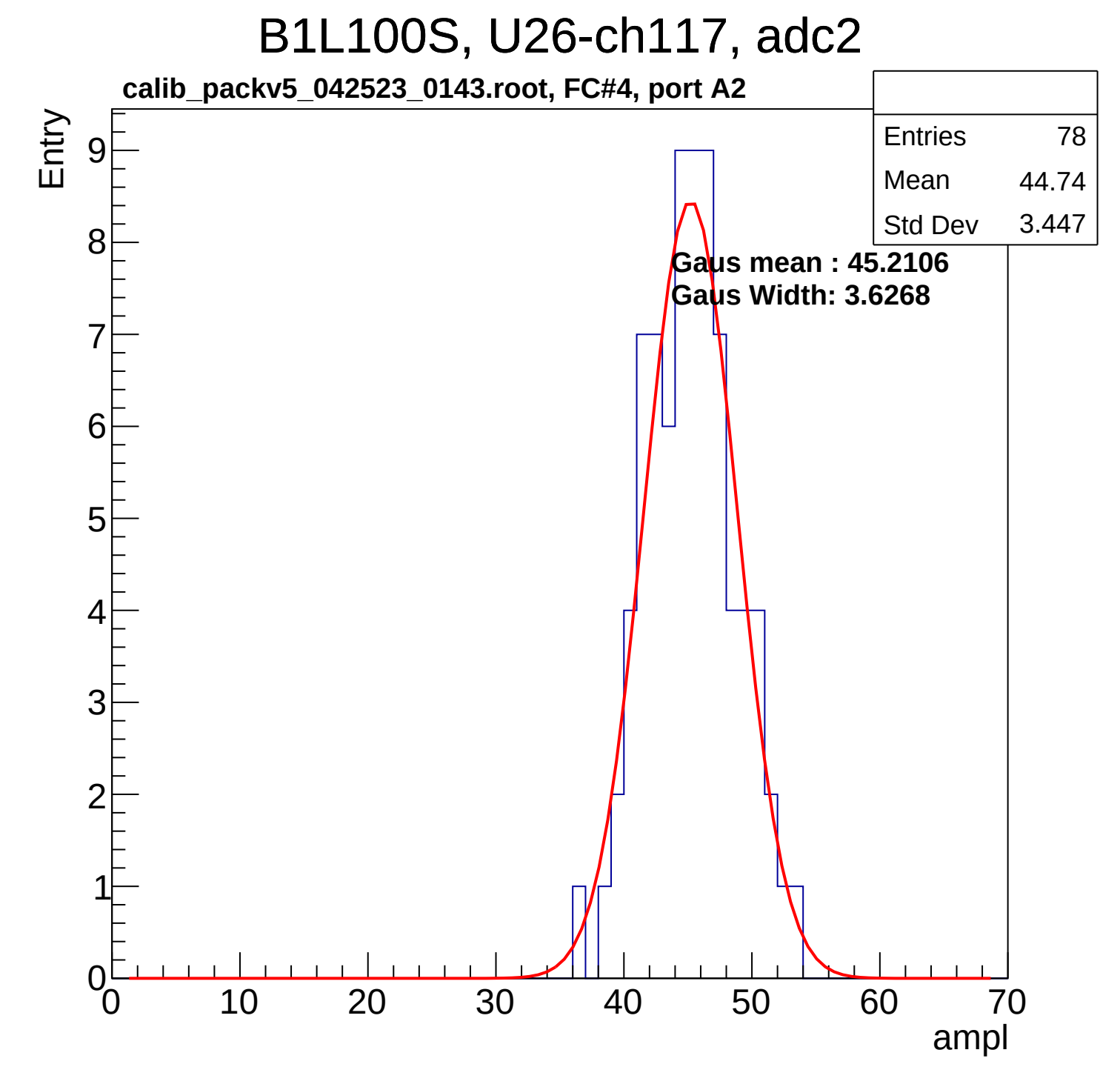

B1L100S, U26-ch116, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 59.6
Std Dev 2.609
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch116, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.99
Std Dev 6.008
Gaus mean : 30.8824
Gaus Width: 3.6295
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.06
Std Dev 3.667
Gaus mean : 37.7934
Gaus Width: 3.8083
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 44.74
Std Dev 3.447
Gaus mean : 45.2106
Gaus Width: 3.6268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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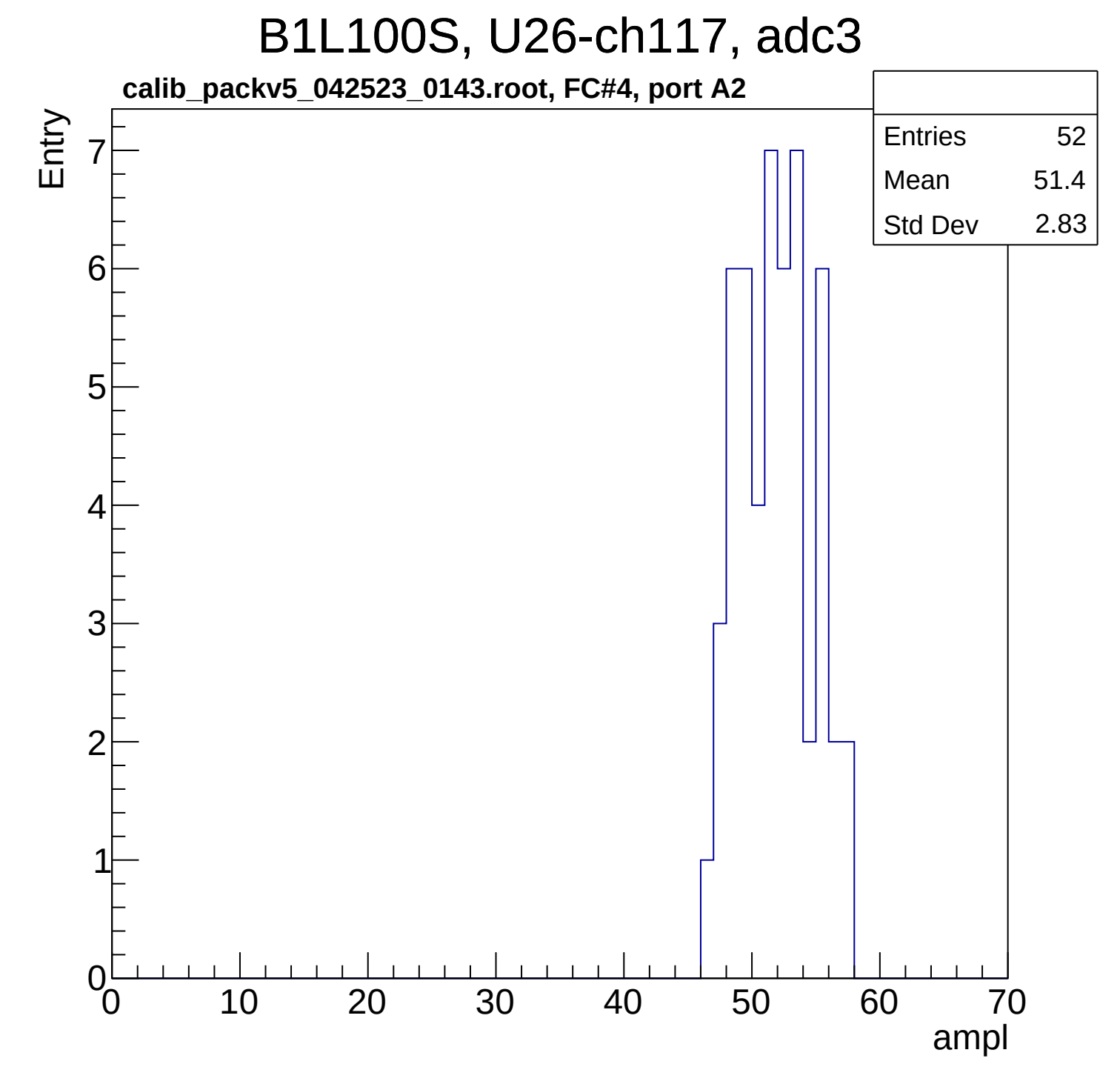
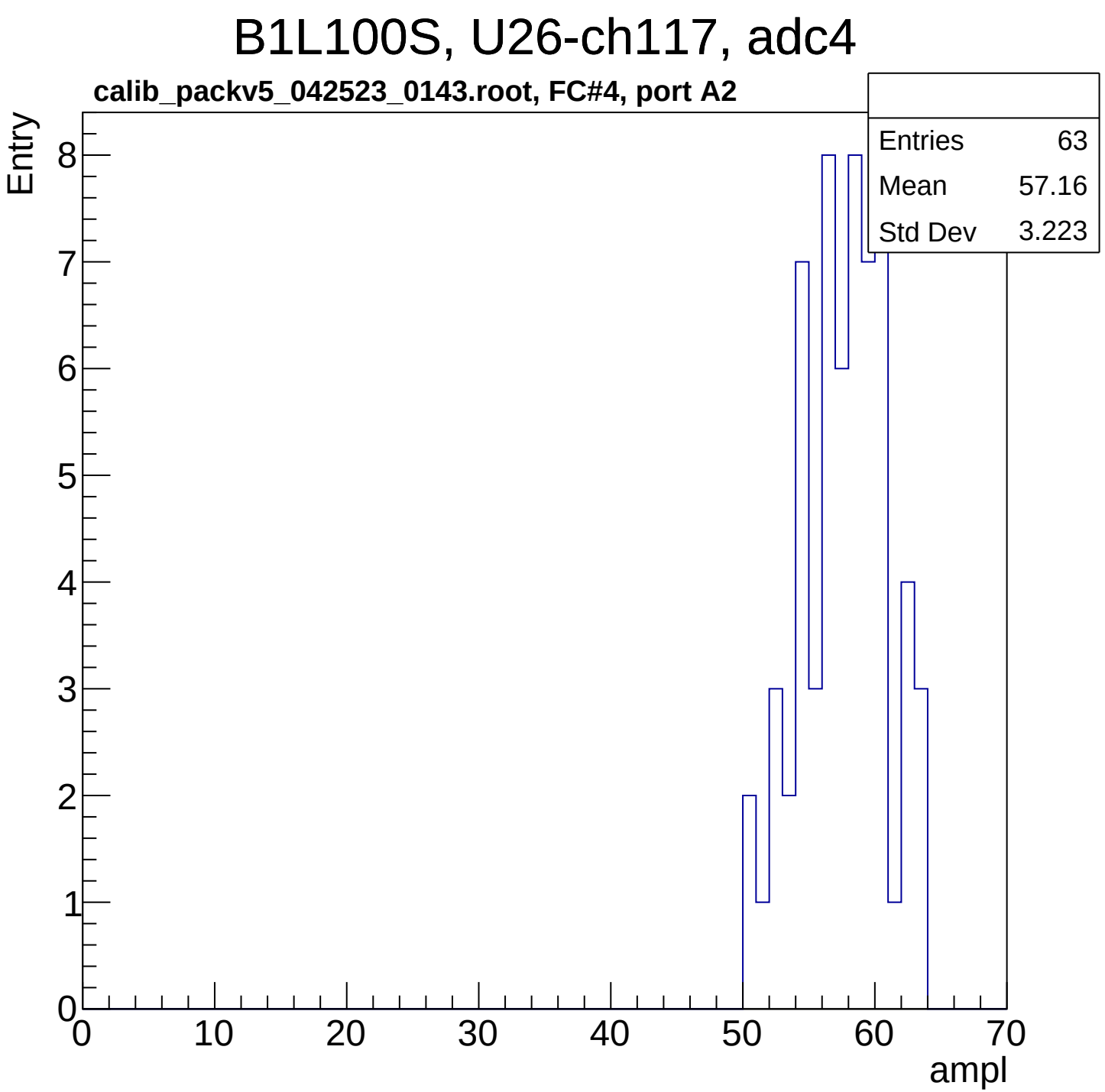
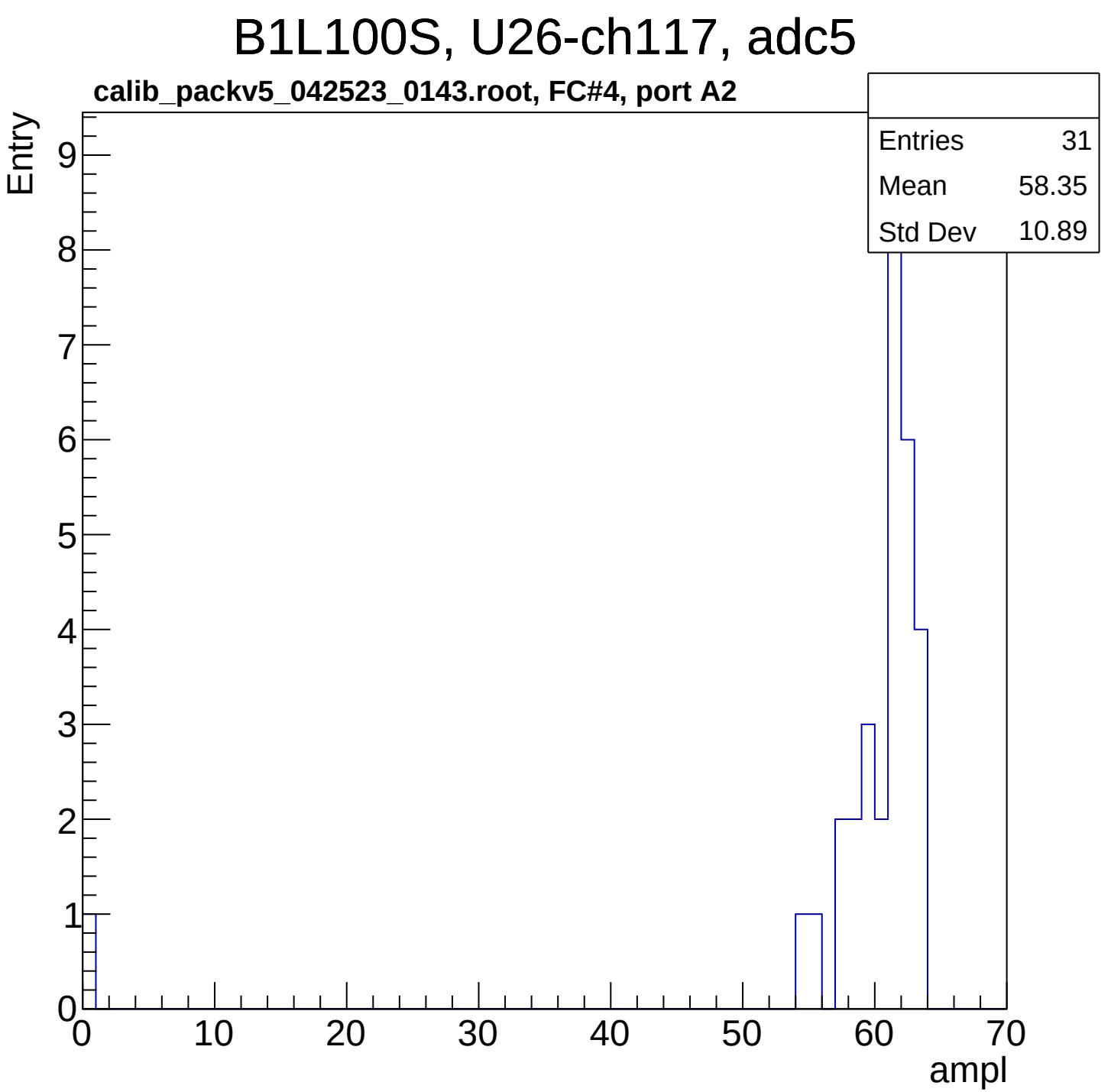
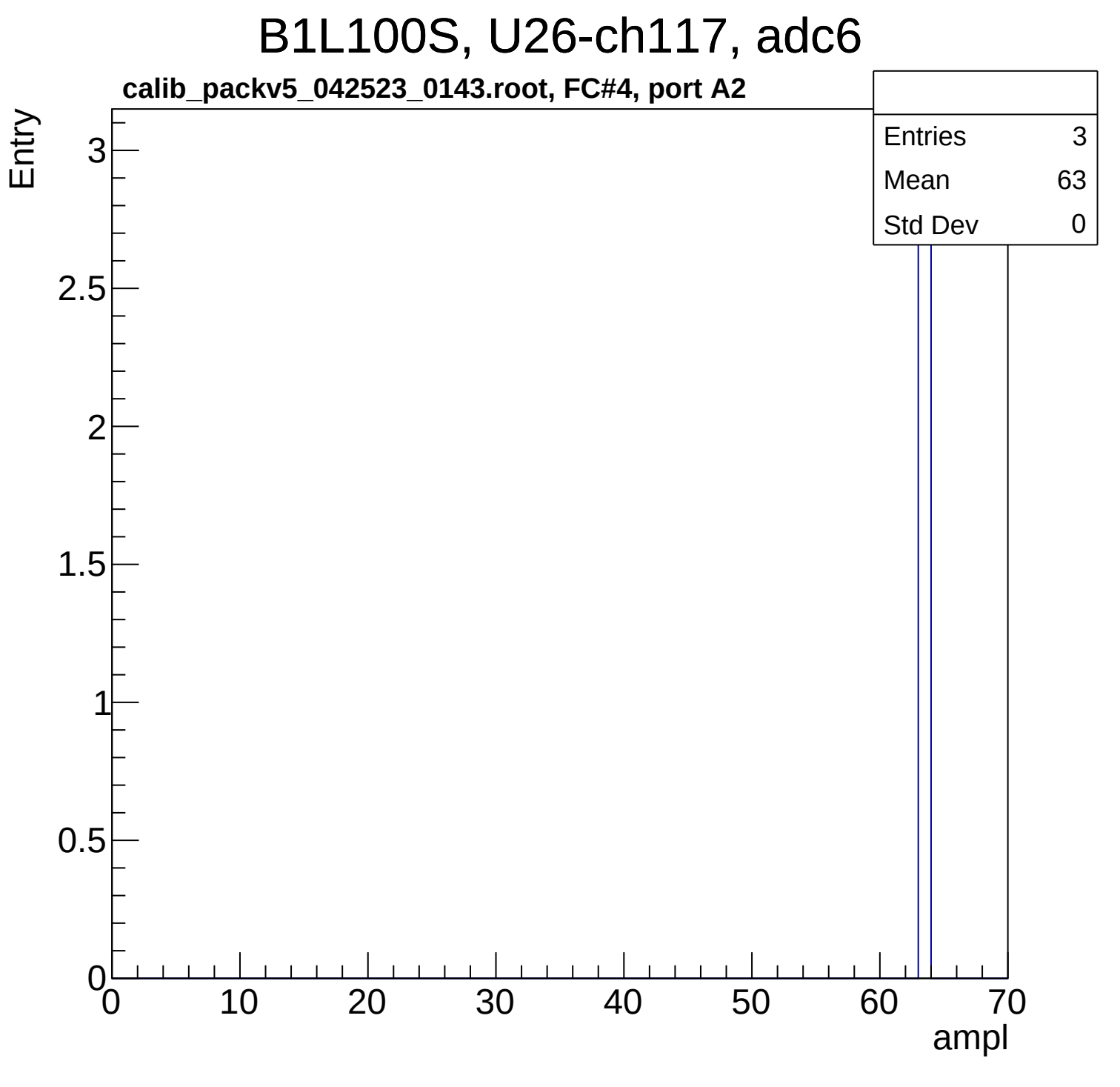
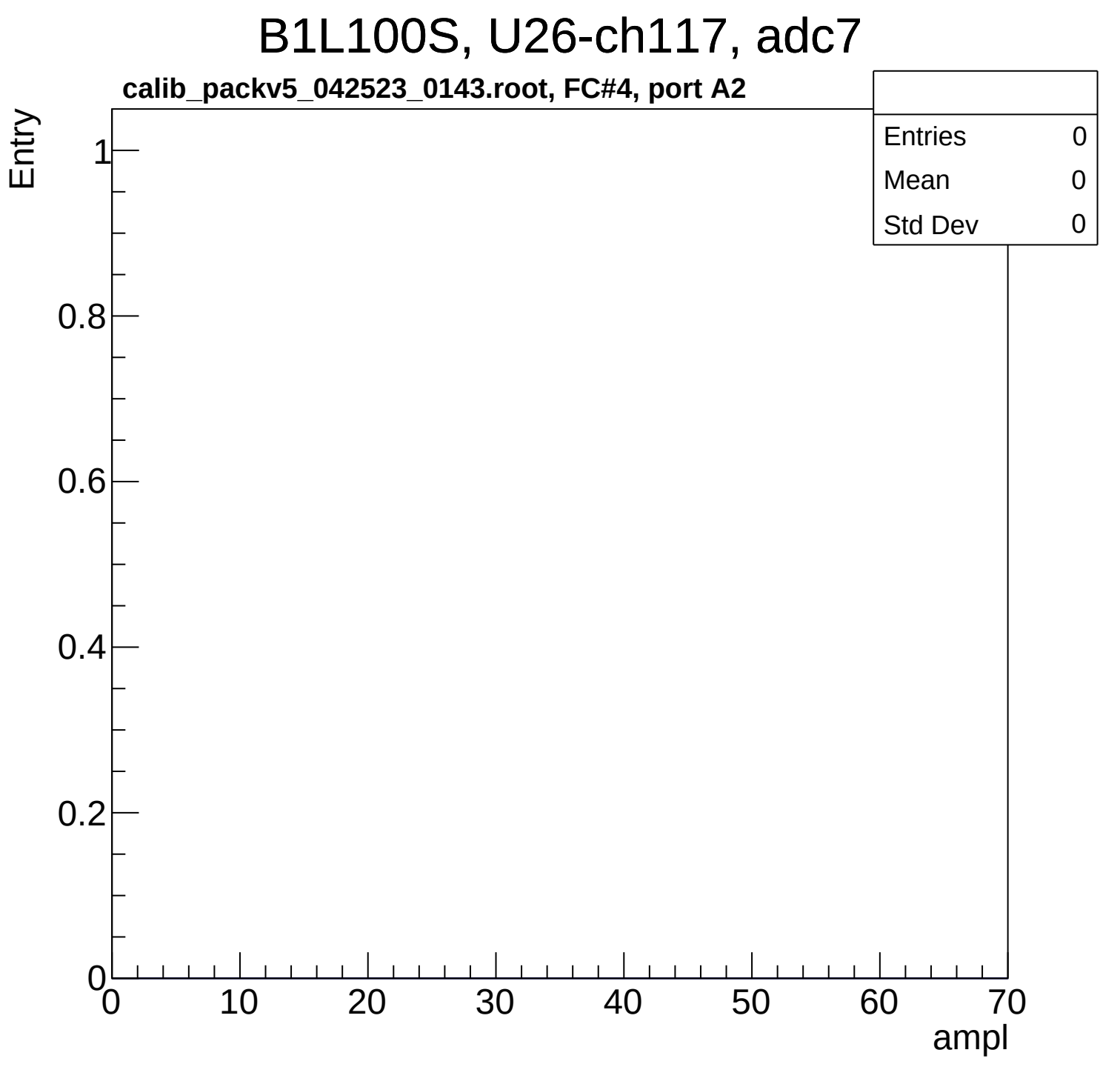
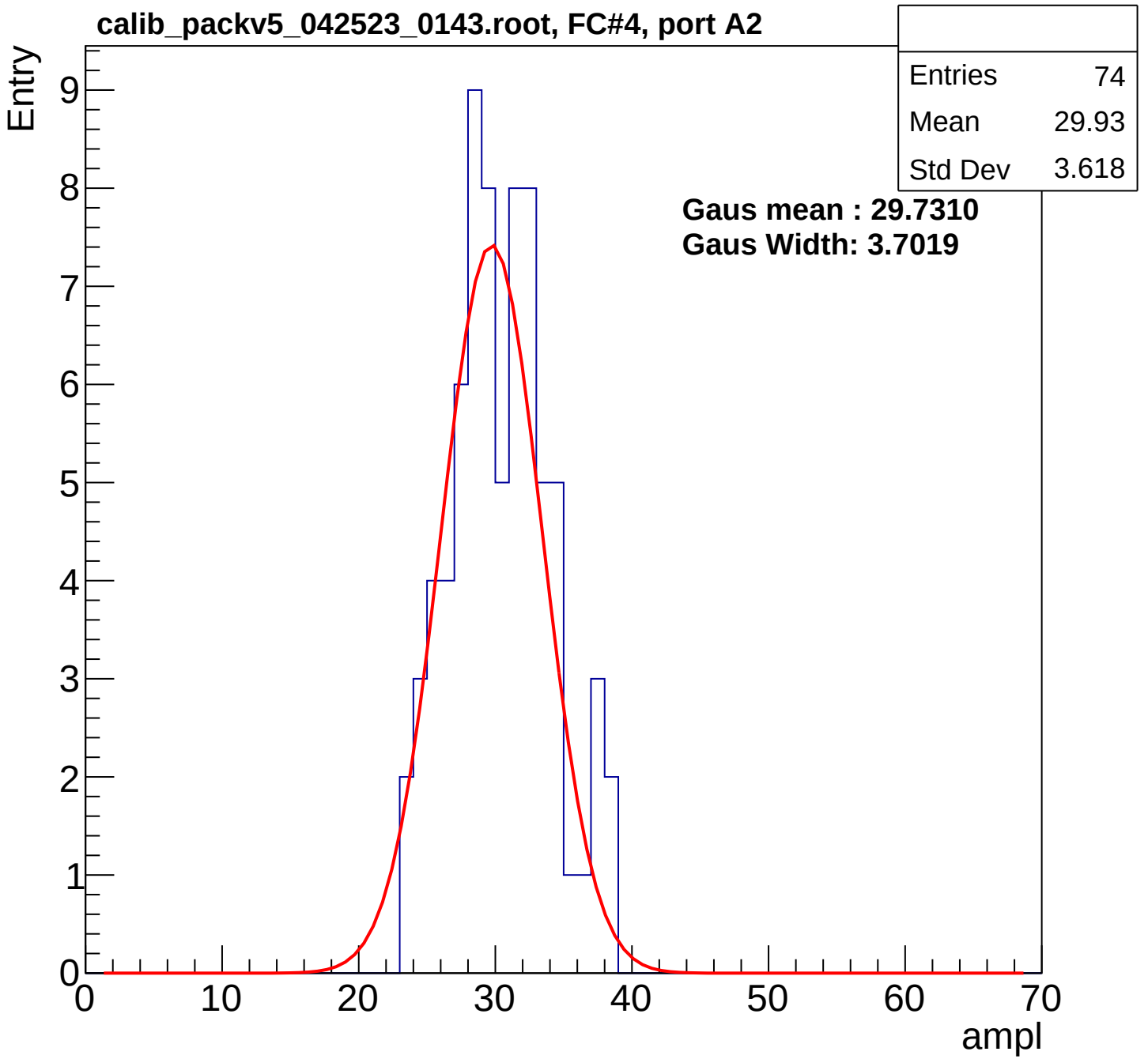

B1L100S, U26-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 51.4
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

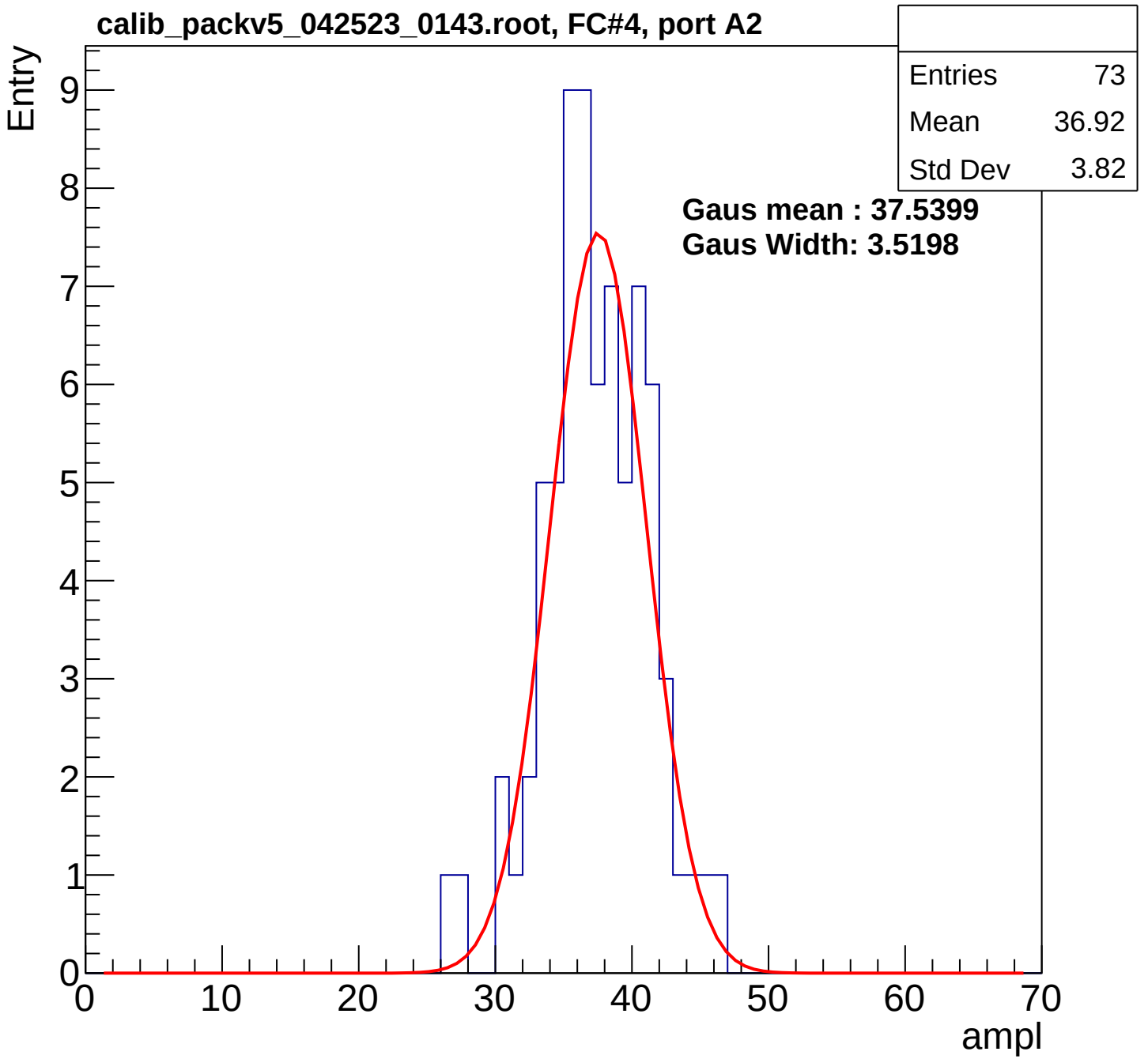

B1L100S, U26-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 57.16
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

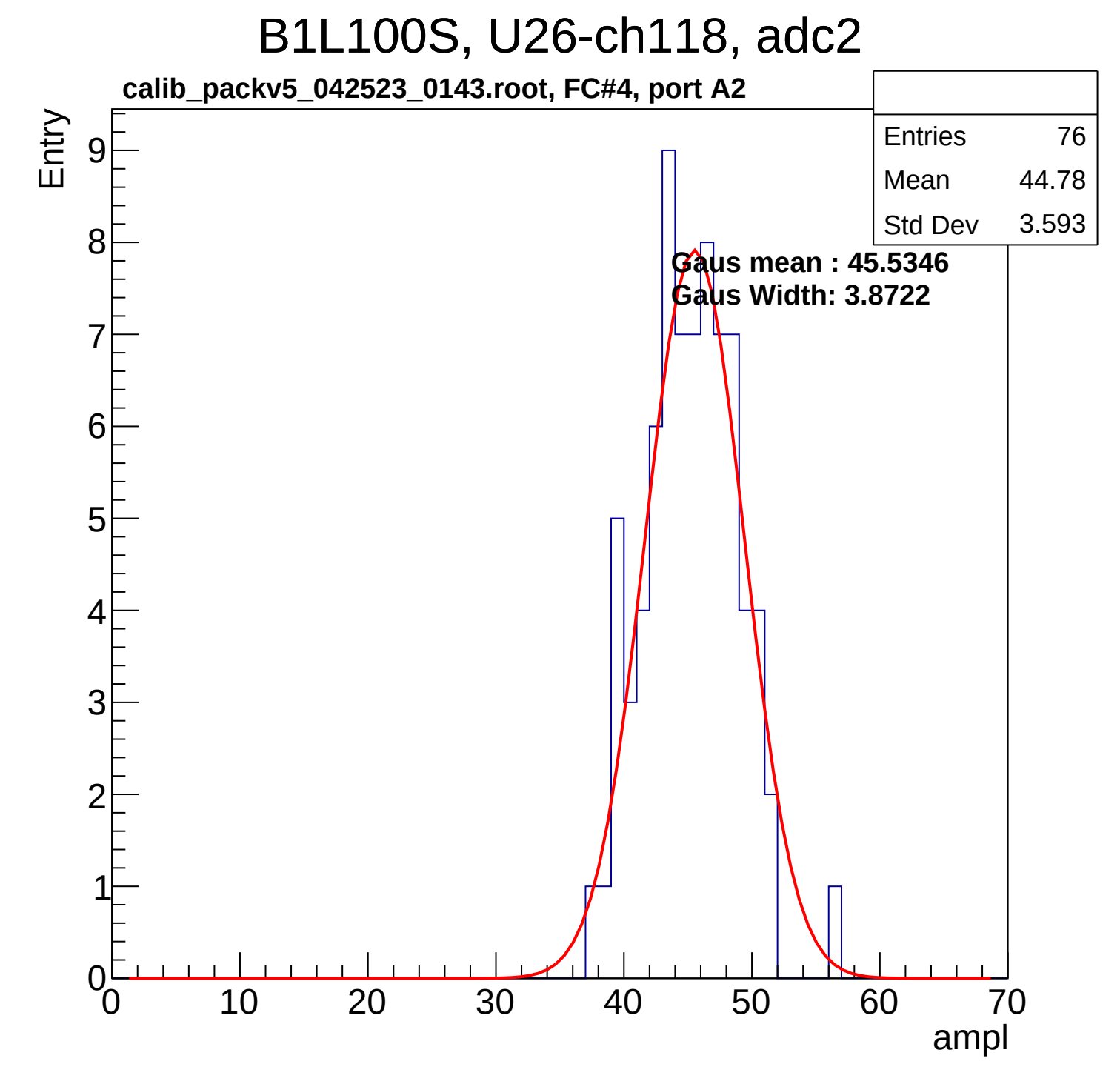

B1L100S, U26-ch117, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.35
Std Dev 10.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch117, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.93
Std Dev 3.618
Gaus mean : 29.7310
Gaus Width: 3.7019
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.92
Std Dev 3.82
Gaus mean : 37.5399
Gaus Width: 3.5198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.78
Std Dev 3.593
Gaus mean : 45.5346
Gaus Width: 3.8722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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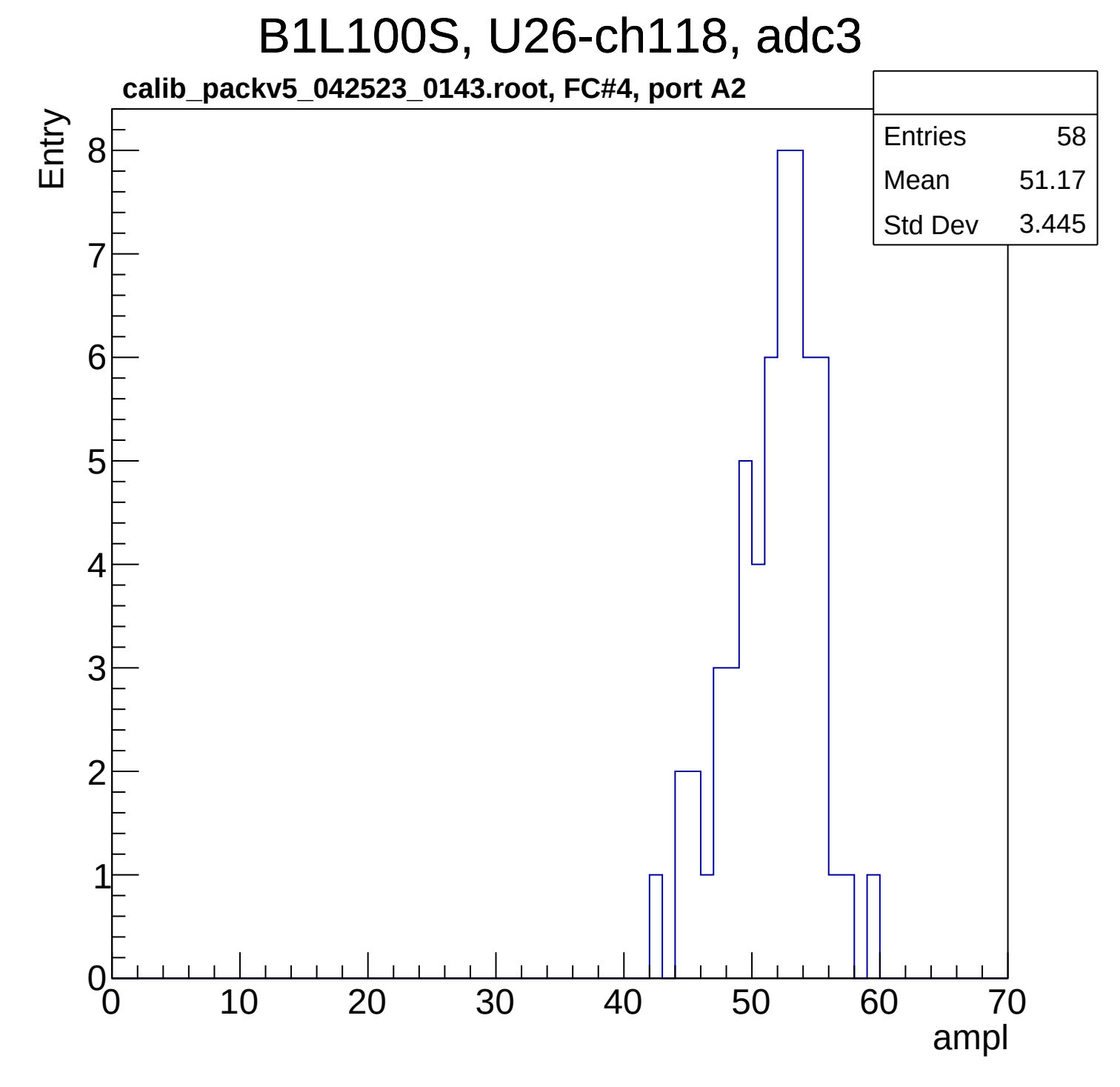
B1L100S, U26-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 51.17
Std Dev 3.445
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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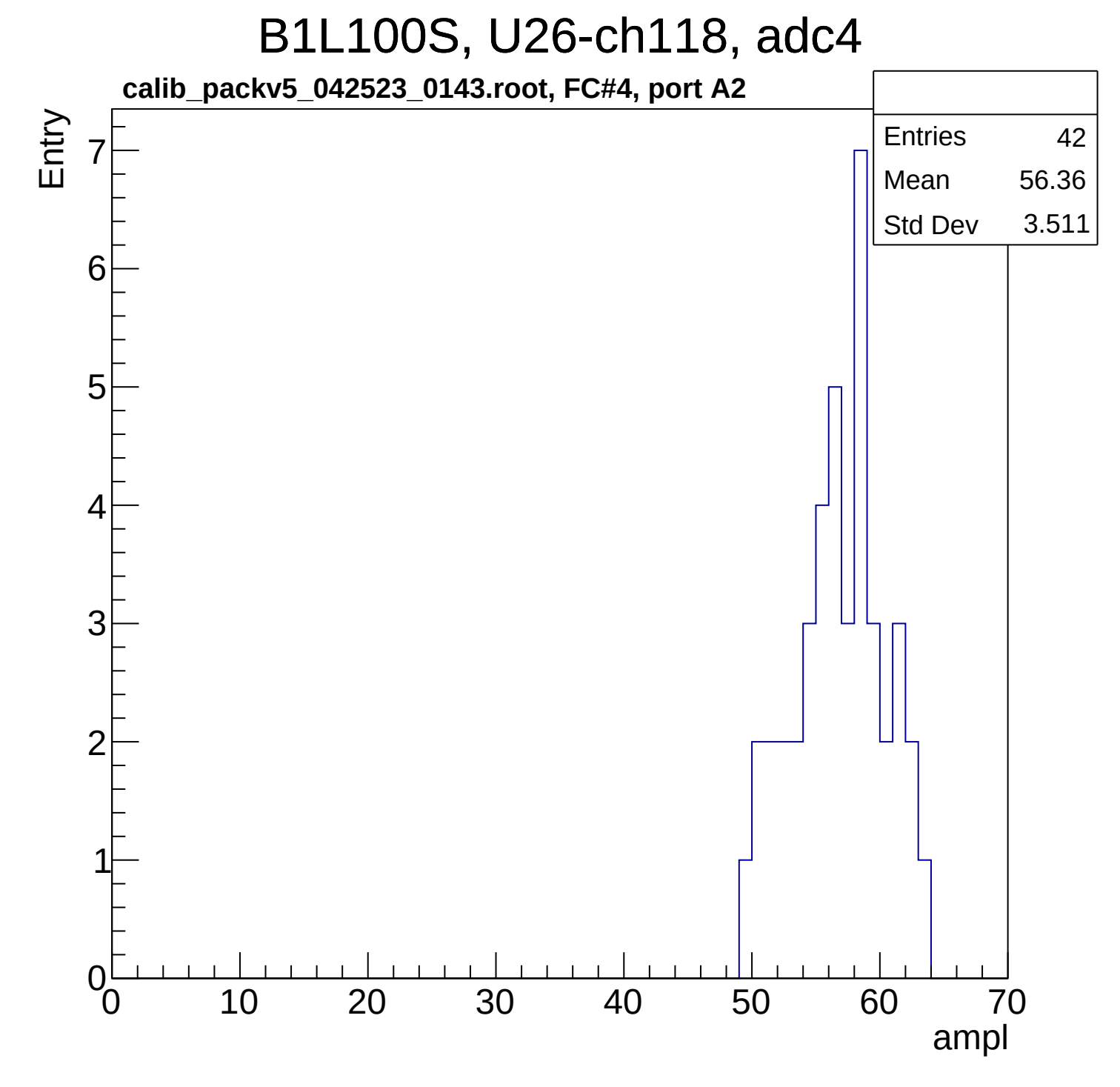
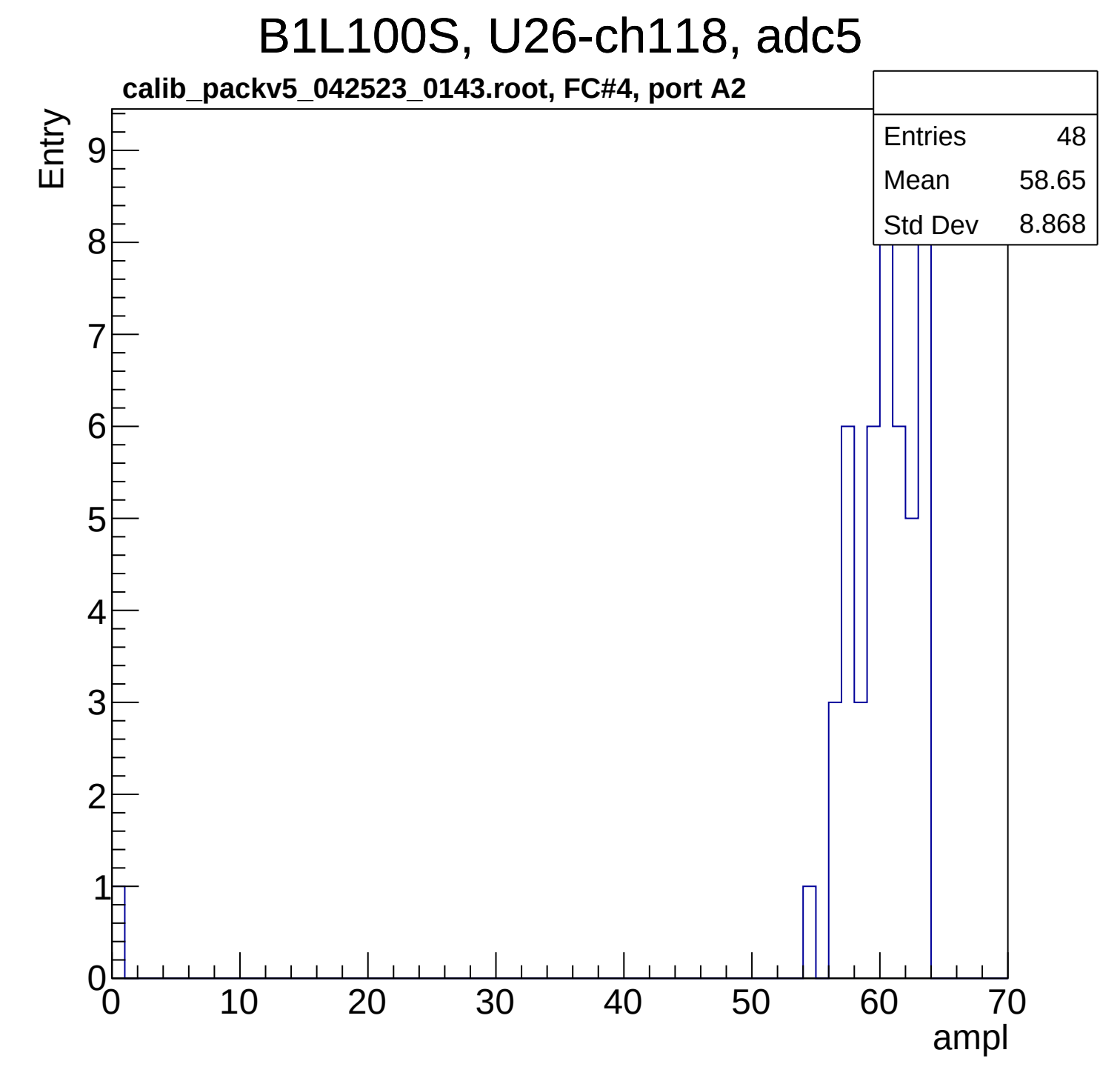
40
50
60
70


B1L100S, U26-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 56.36
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.65
Std Dev 8.868
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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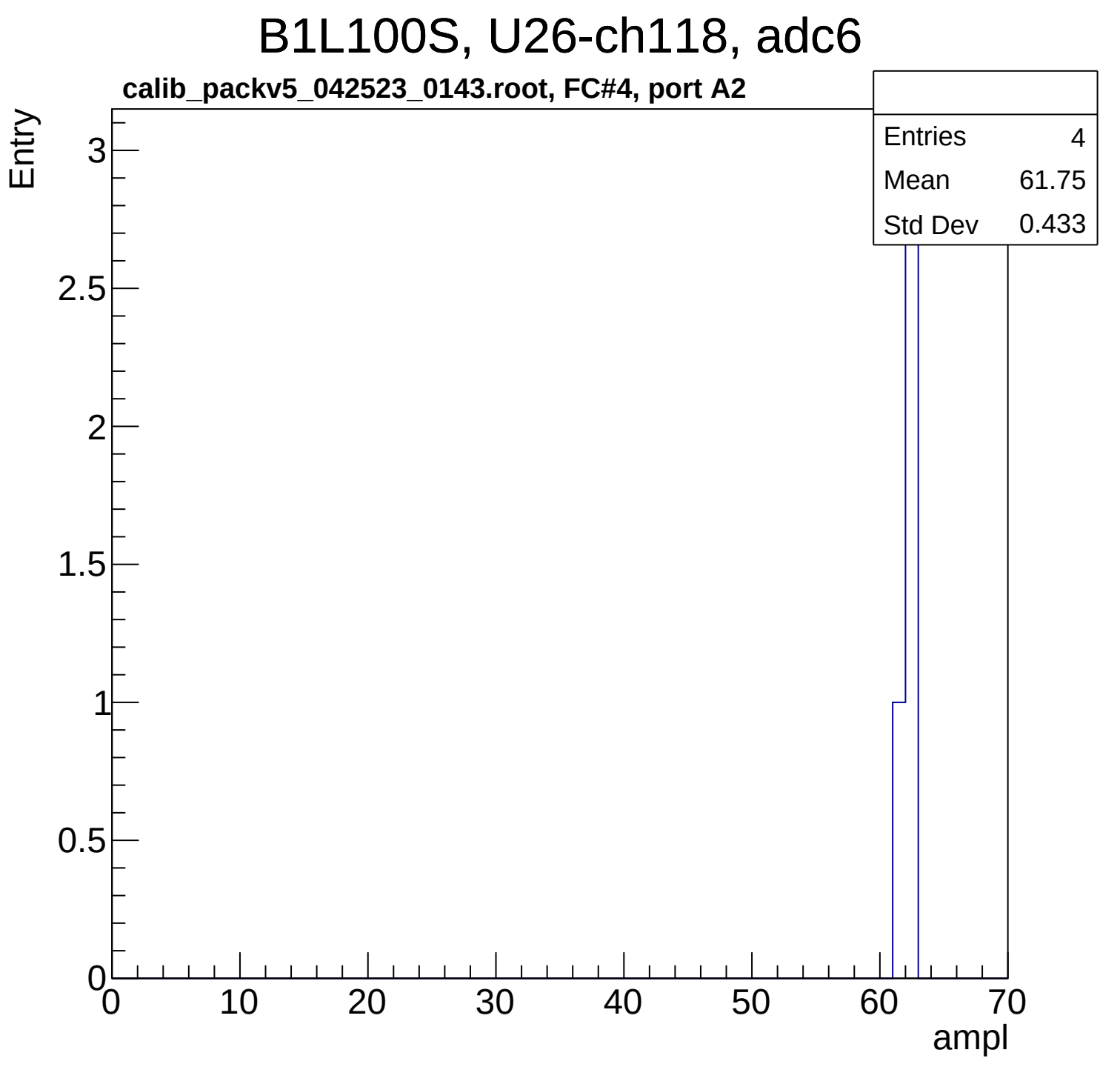
B1L100S, U26-ch118, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 0.433
Entry
ampl
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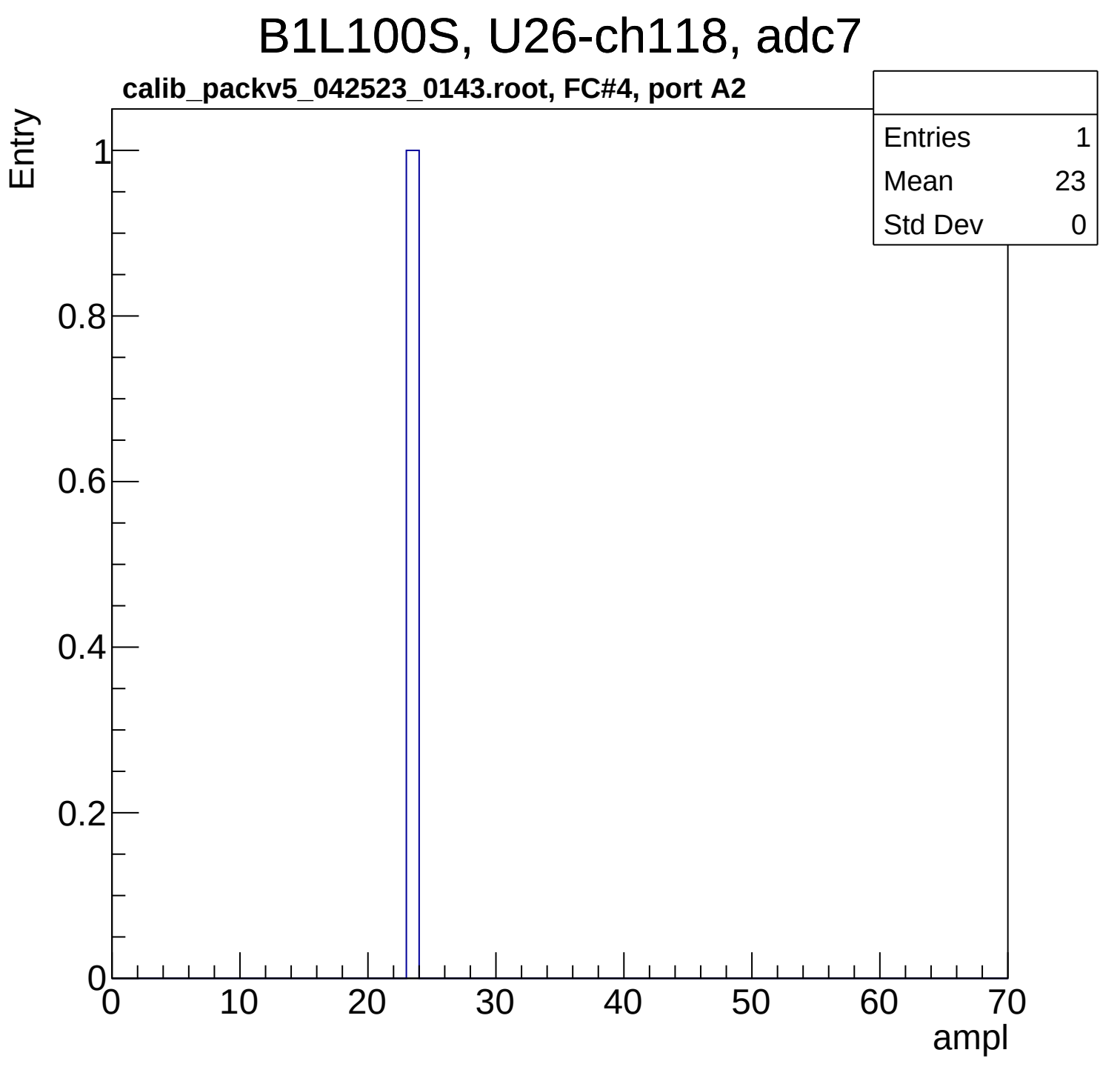
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch118, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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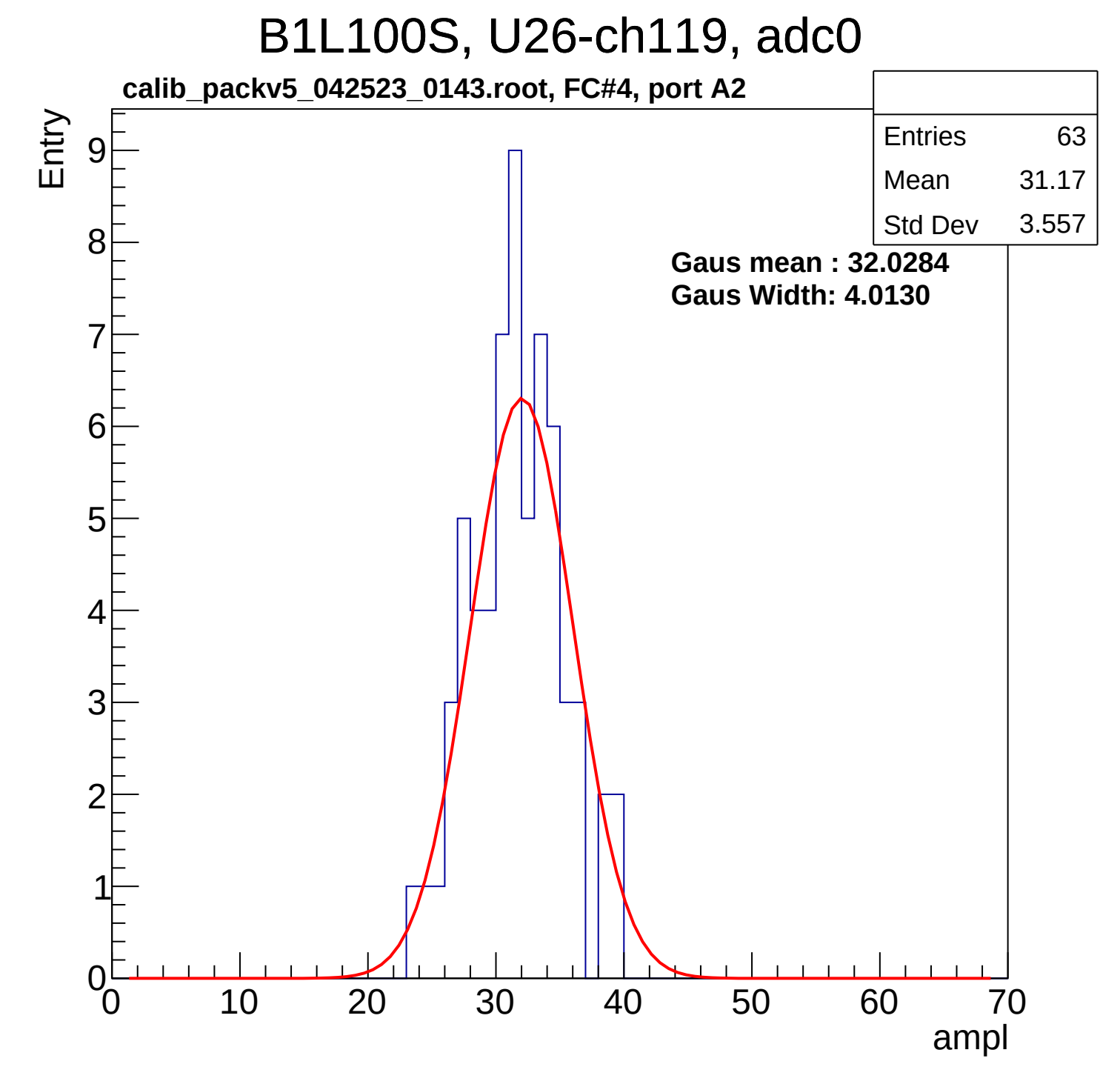
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 31.17
Std Dev 3.557
Gaus mean : 32.0284
Gaus Width: 4.0130
Entry
ampl
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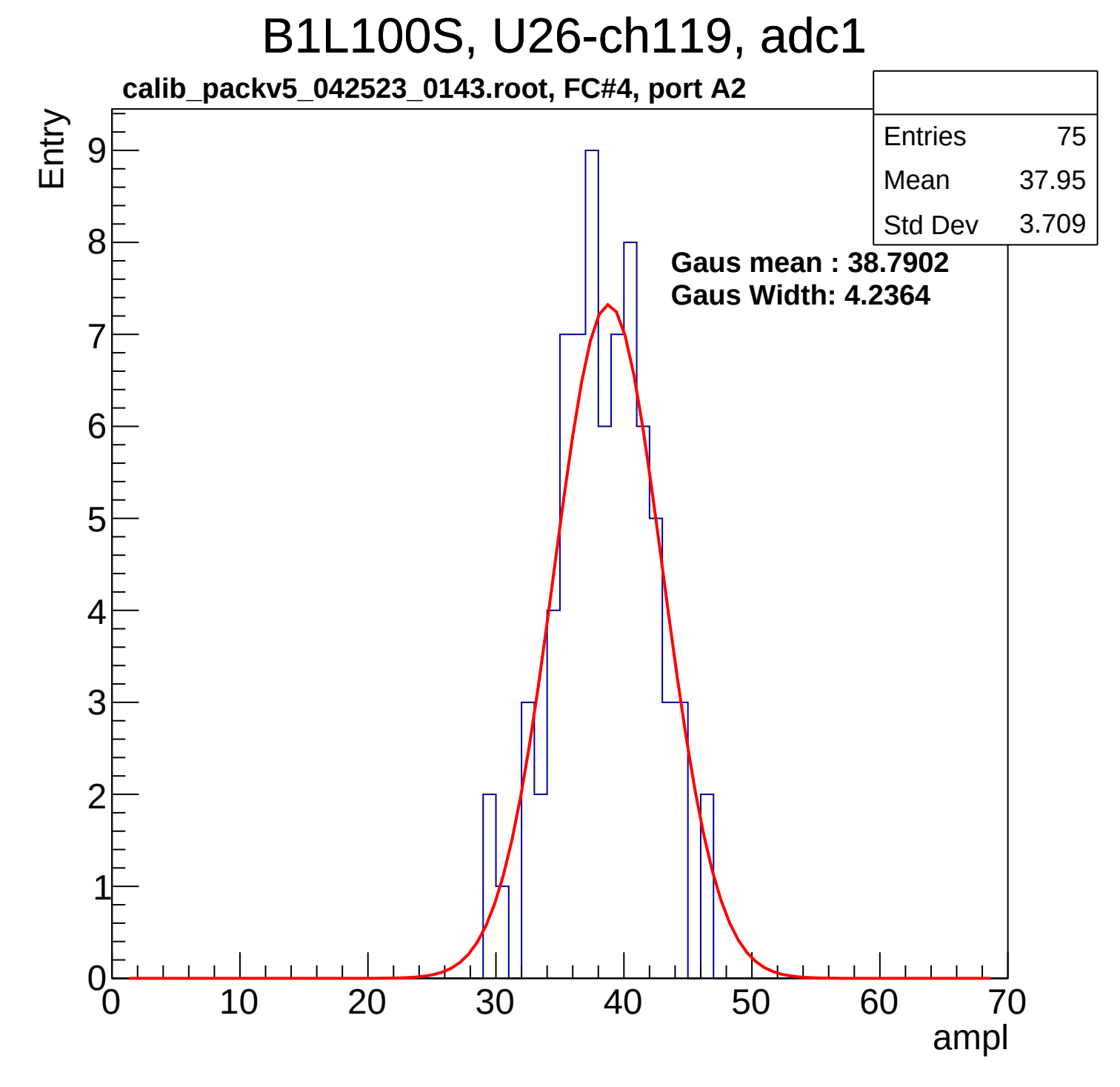
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.95
Std Dev 3.709
Gaus mean : 38.7902
Gaus Width: 4.2364
Entry
ampl
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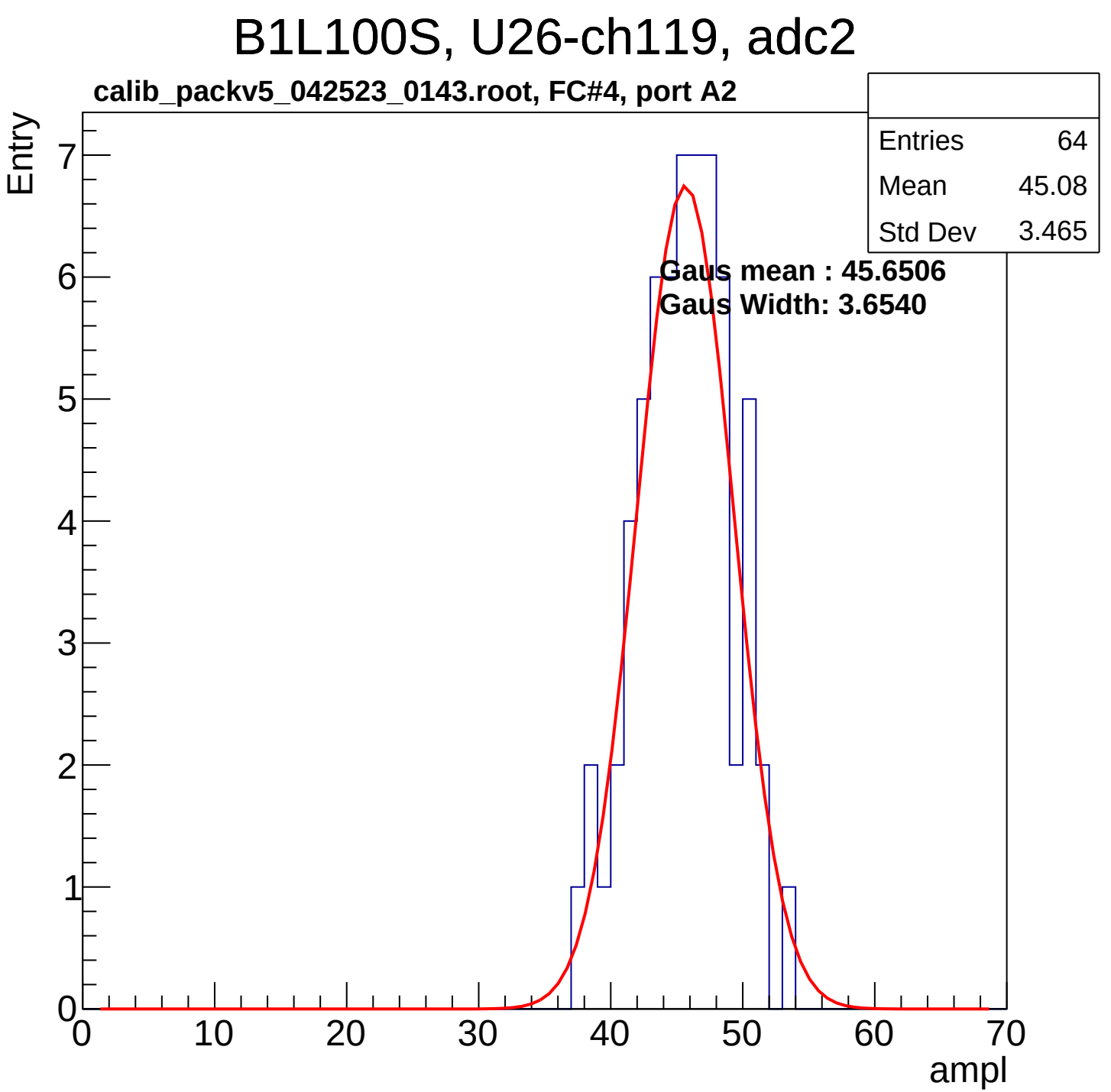
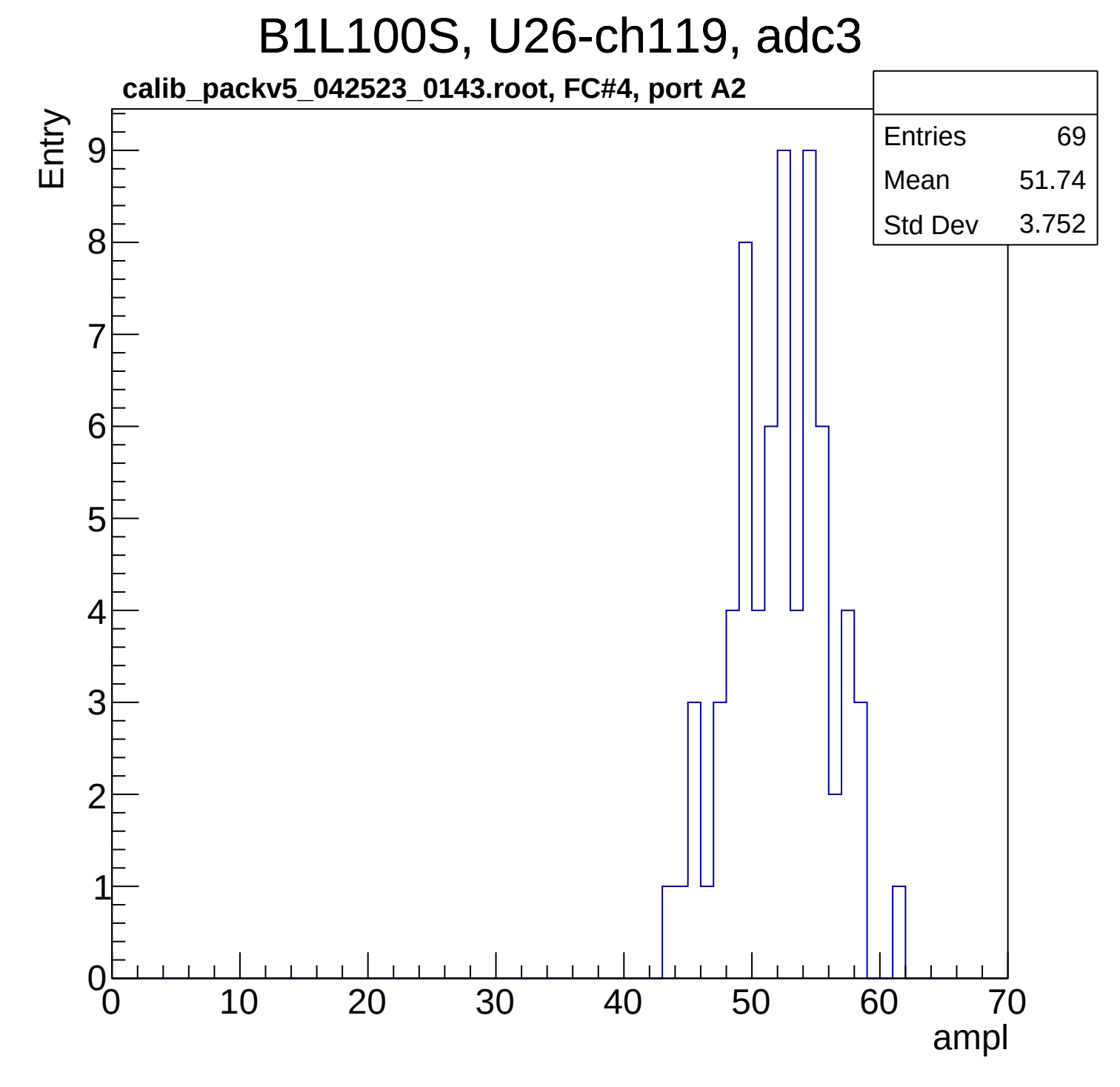
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 45.08
Std Dev 3.465
Gaus mean : 45.6506
Gaus Width: 3.6540
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U26-ch119, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.74
Std Dev 3.752
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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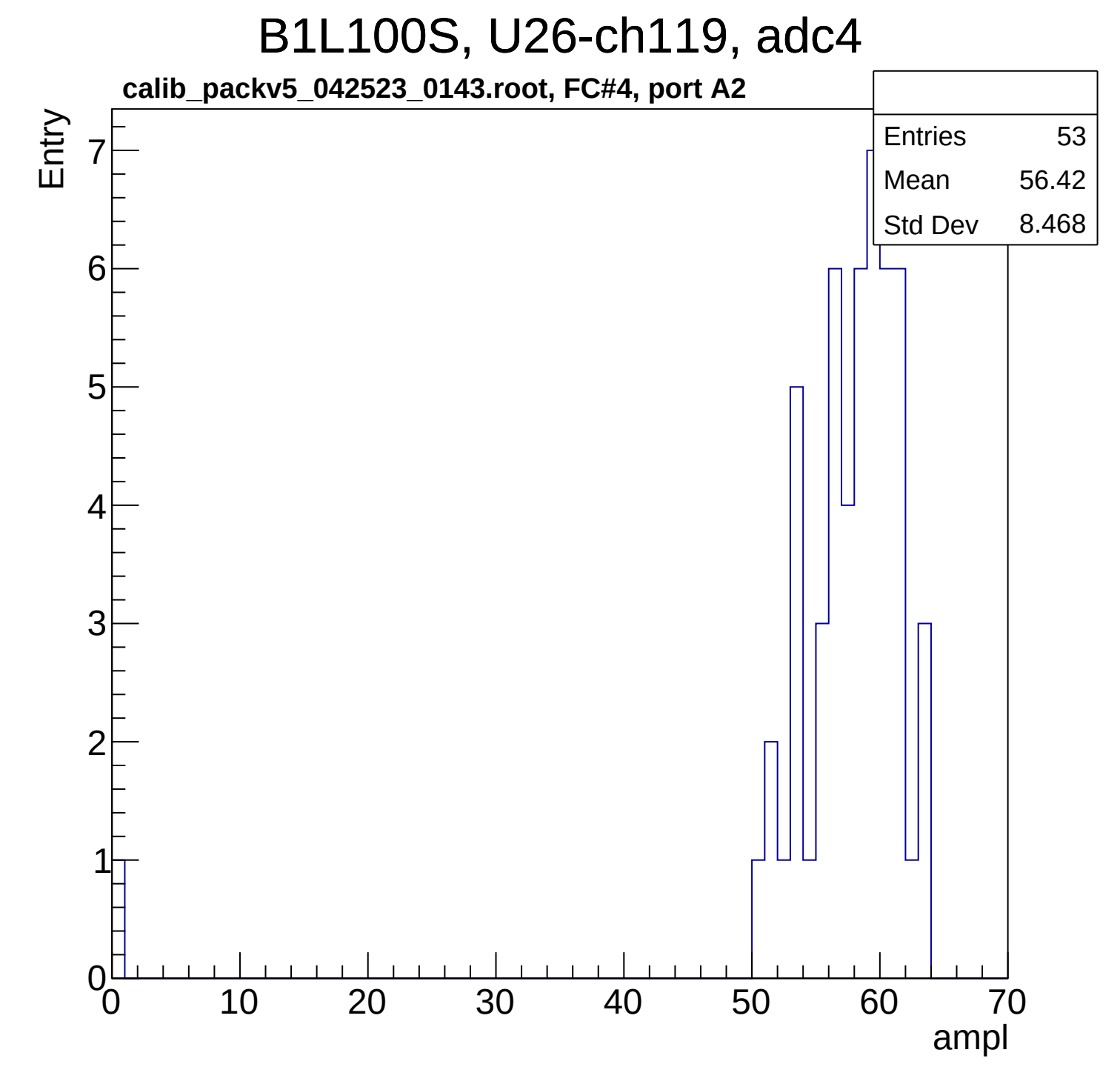
B1L100S, U26-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.42
Std Dev 8.468
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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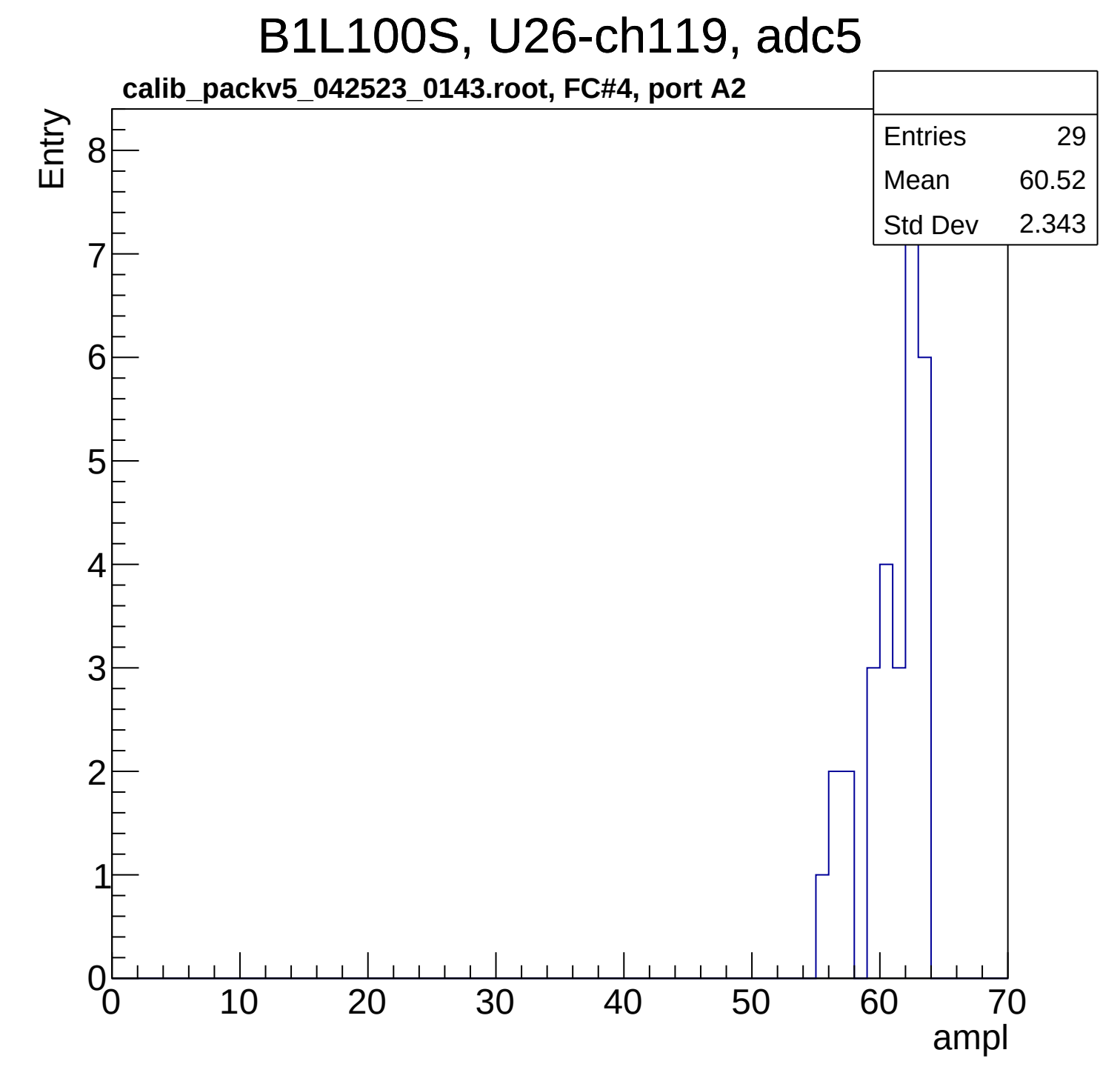
B1L100S, U26-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 60.52
Std Dev 2.343
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
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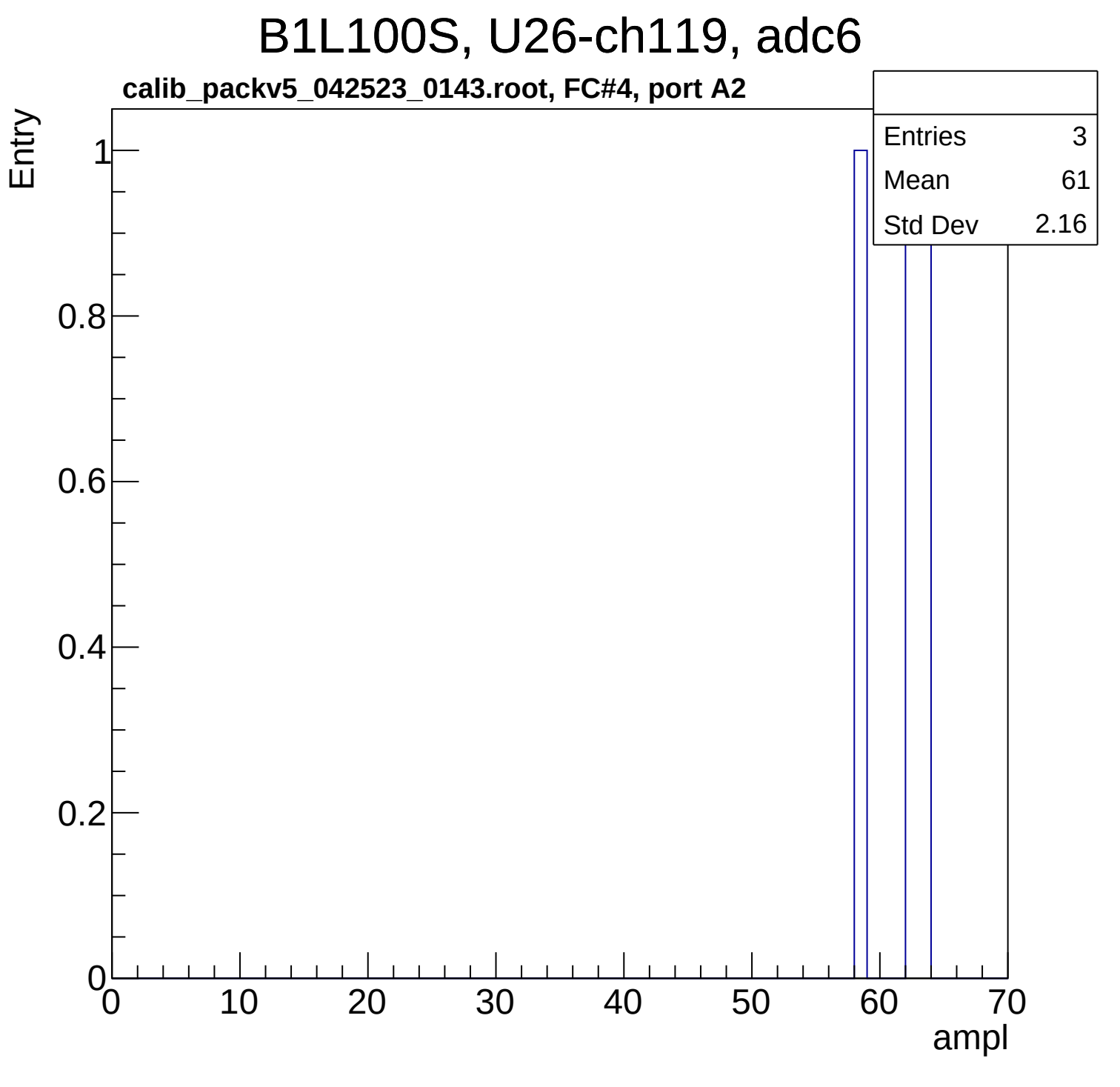
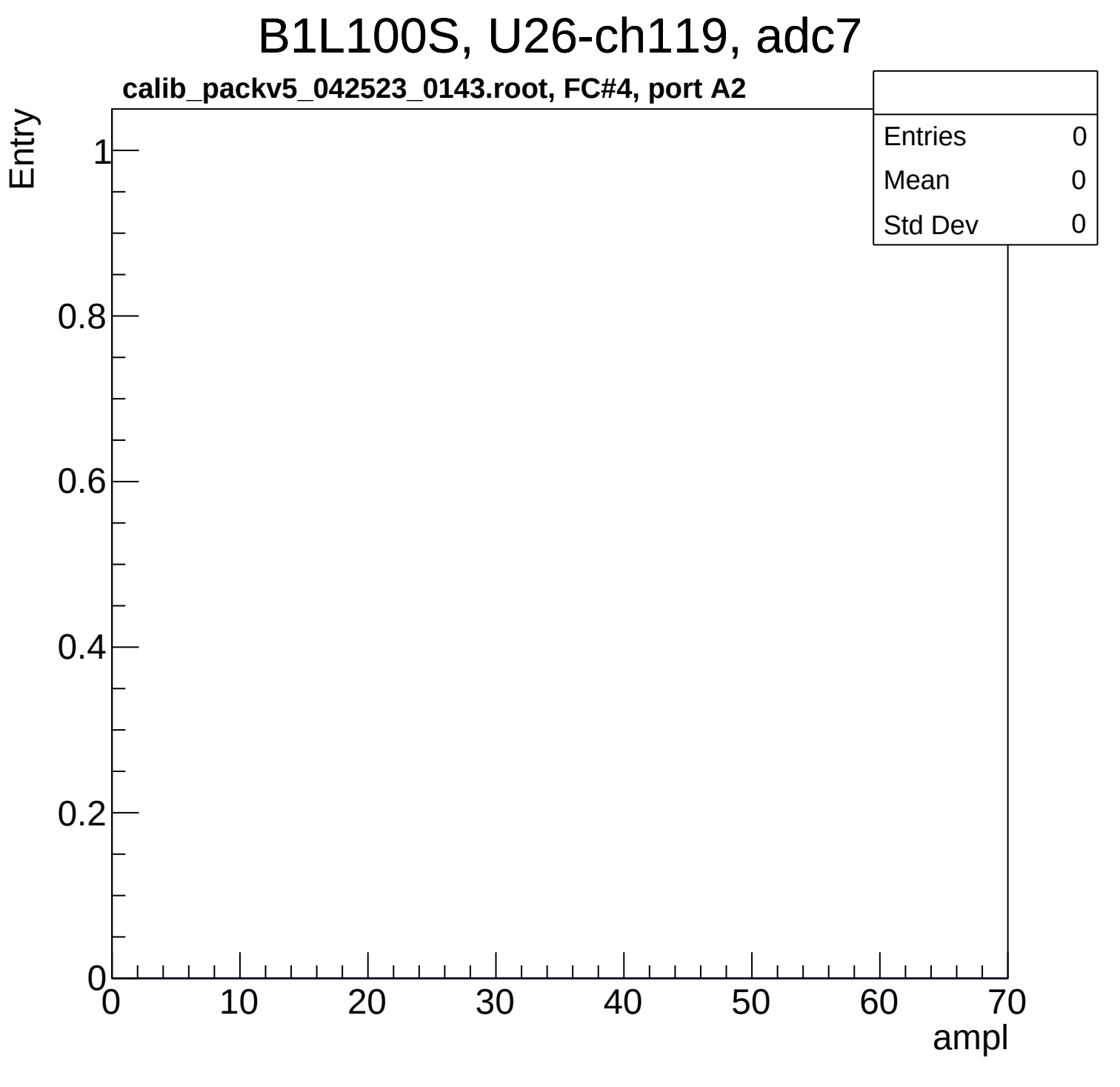
60
70


B1L100S, U26-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61
Std Dev 2.16
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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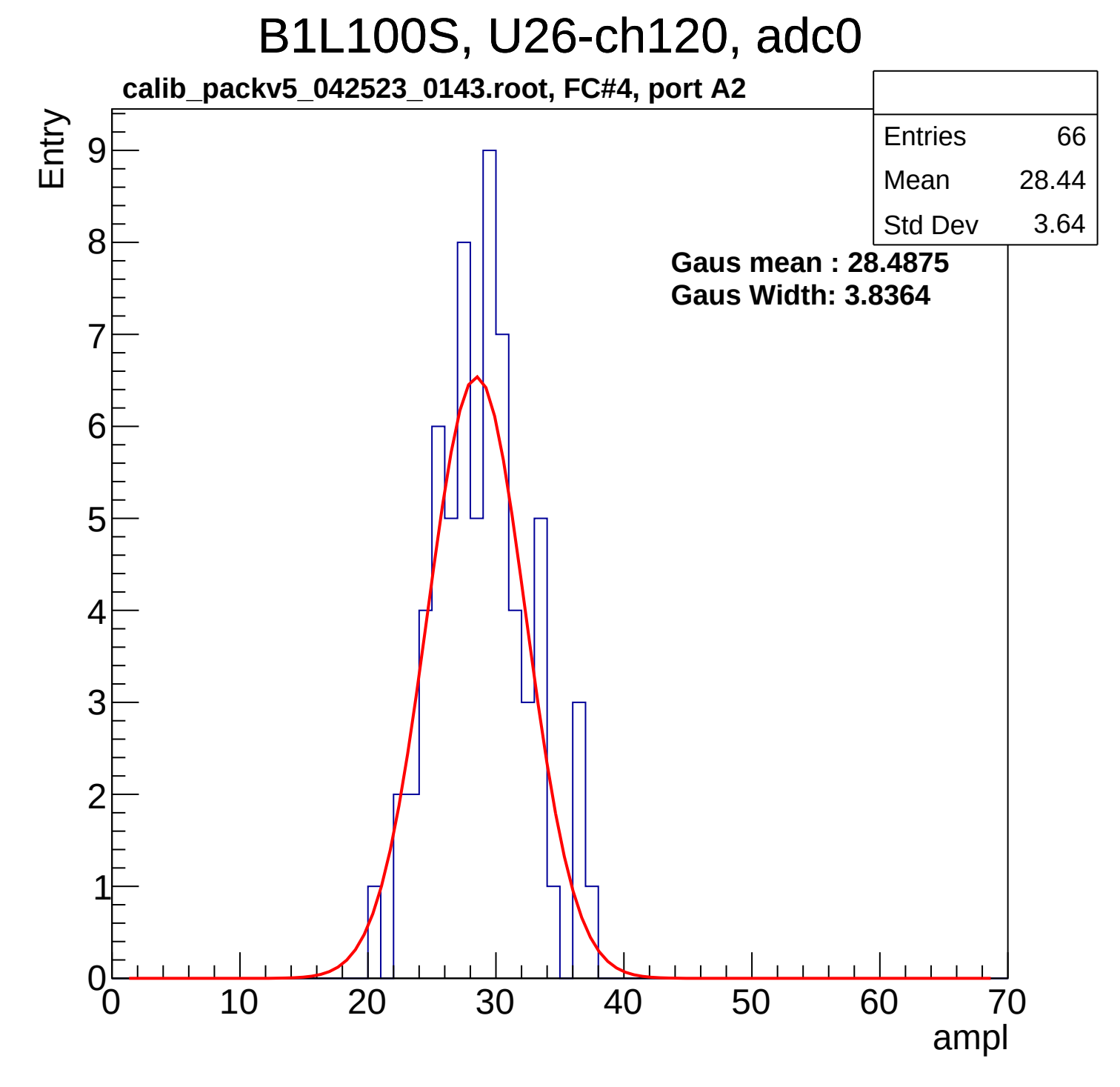
40
50
60
70
ampl


B1L100S, U26-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.44
Std Dev 3.64
Gaus mean : 28.4875
Gaus Width: 3.8364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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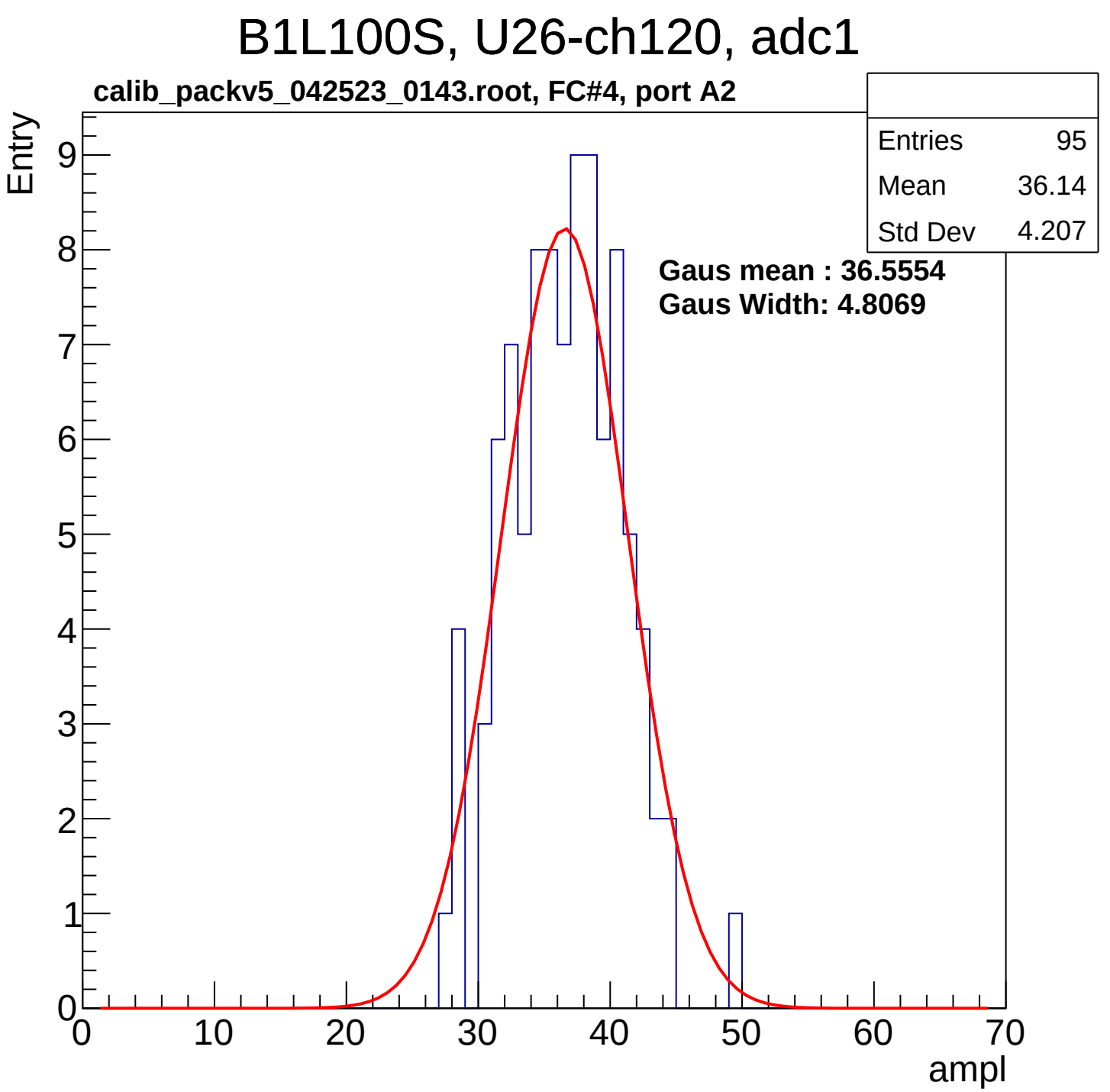
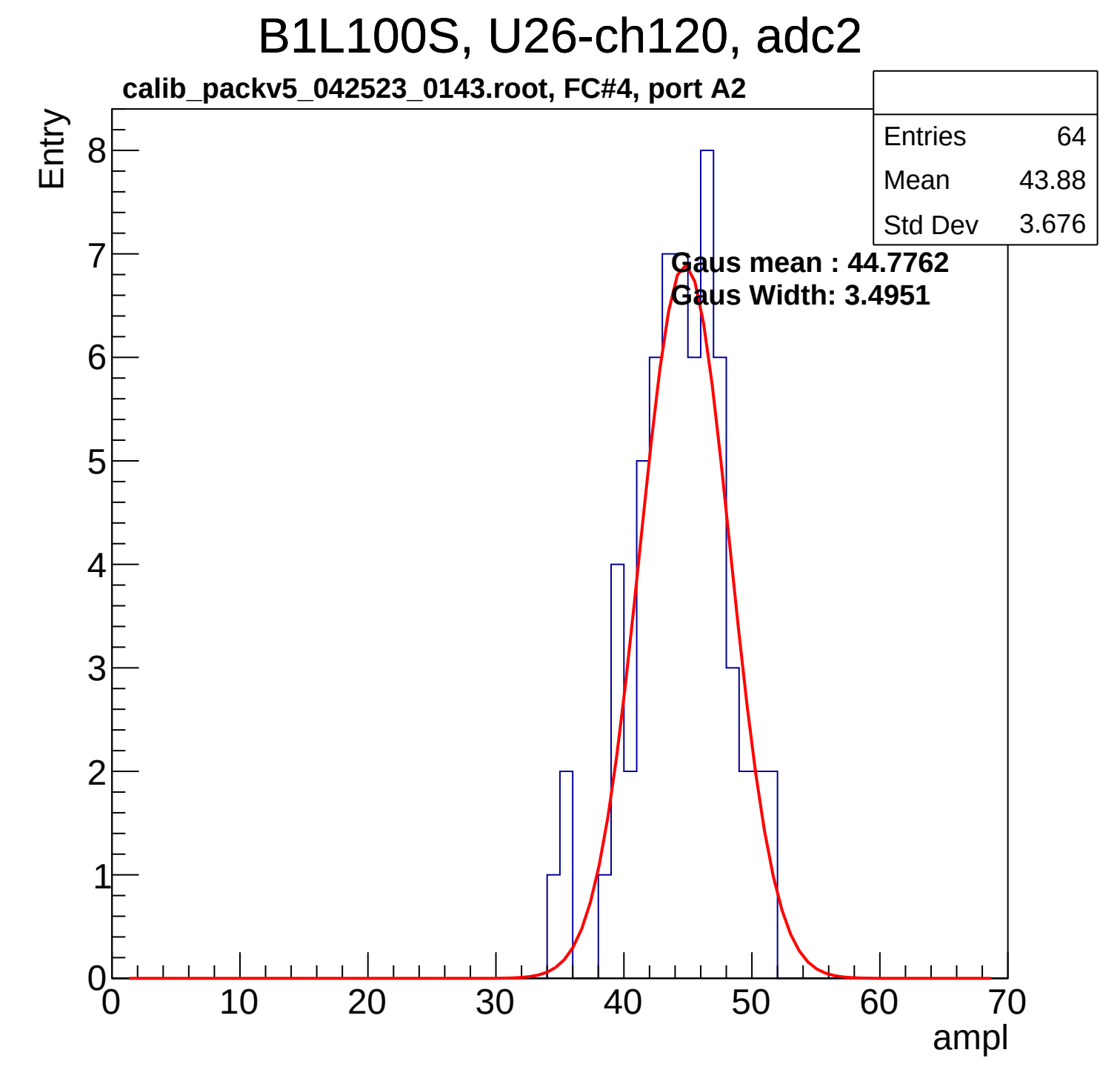

B1L100S, U26-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 36.14
Std Dev 4.207
Gaus mean : 36.5554
Gaus Width: 4.8069
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U26-ch120, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.88
Std Dev 3.676
Gaus mean : 44.7762
Gaus Width: 3.4951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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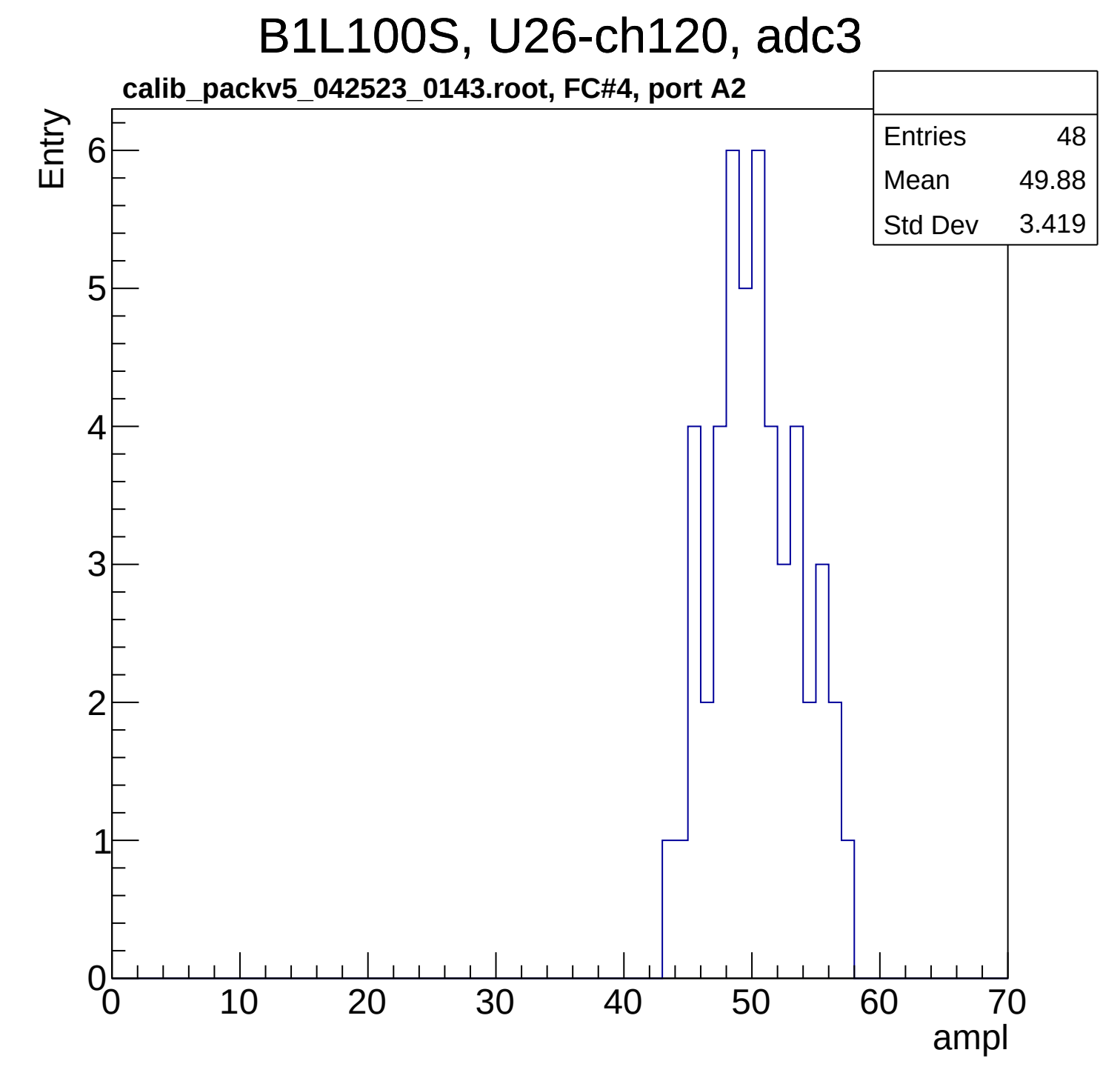
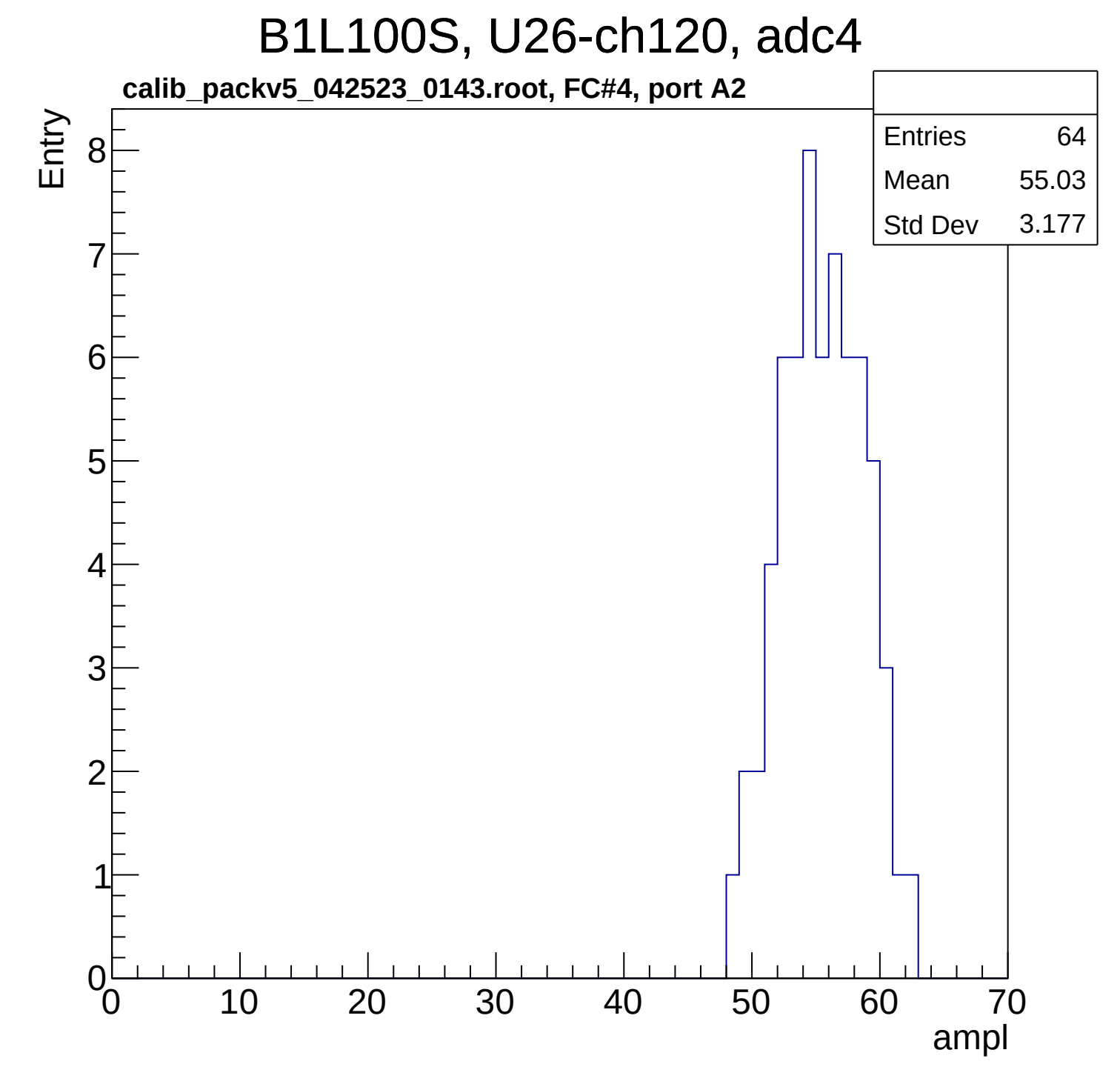
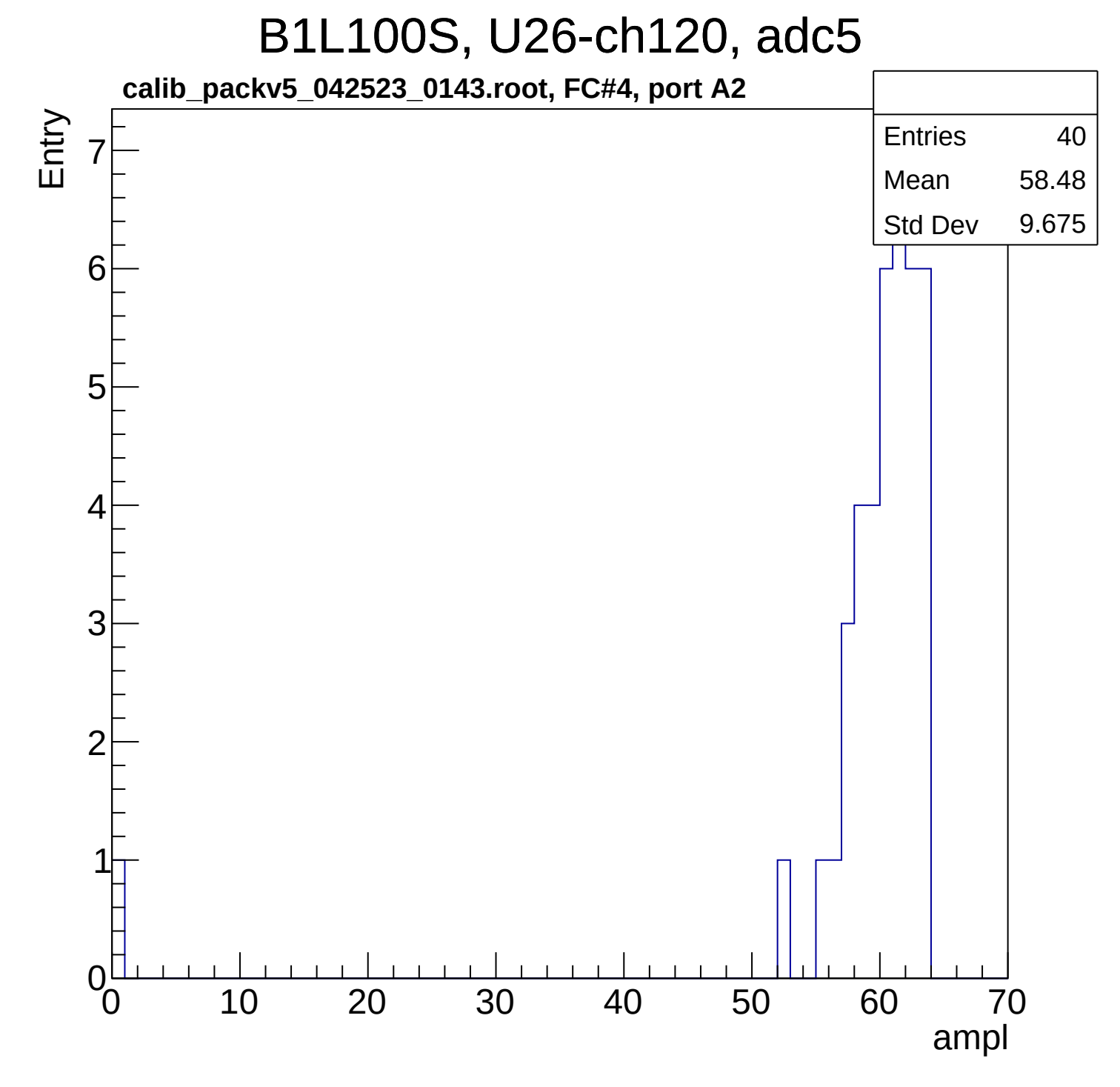

B1L100S, U26-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 49.88
Std Dev 3.419
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.03
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.48
Std Dev 9.675
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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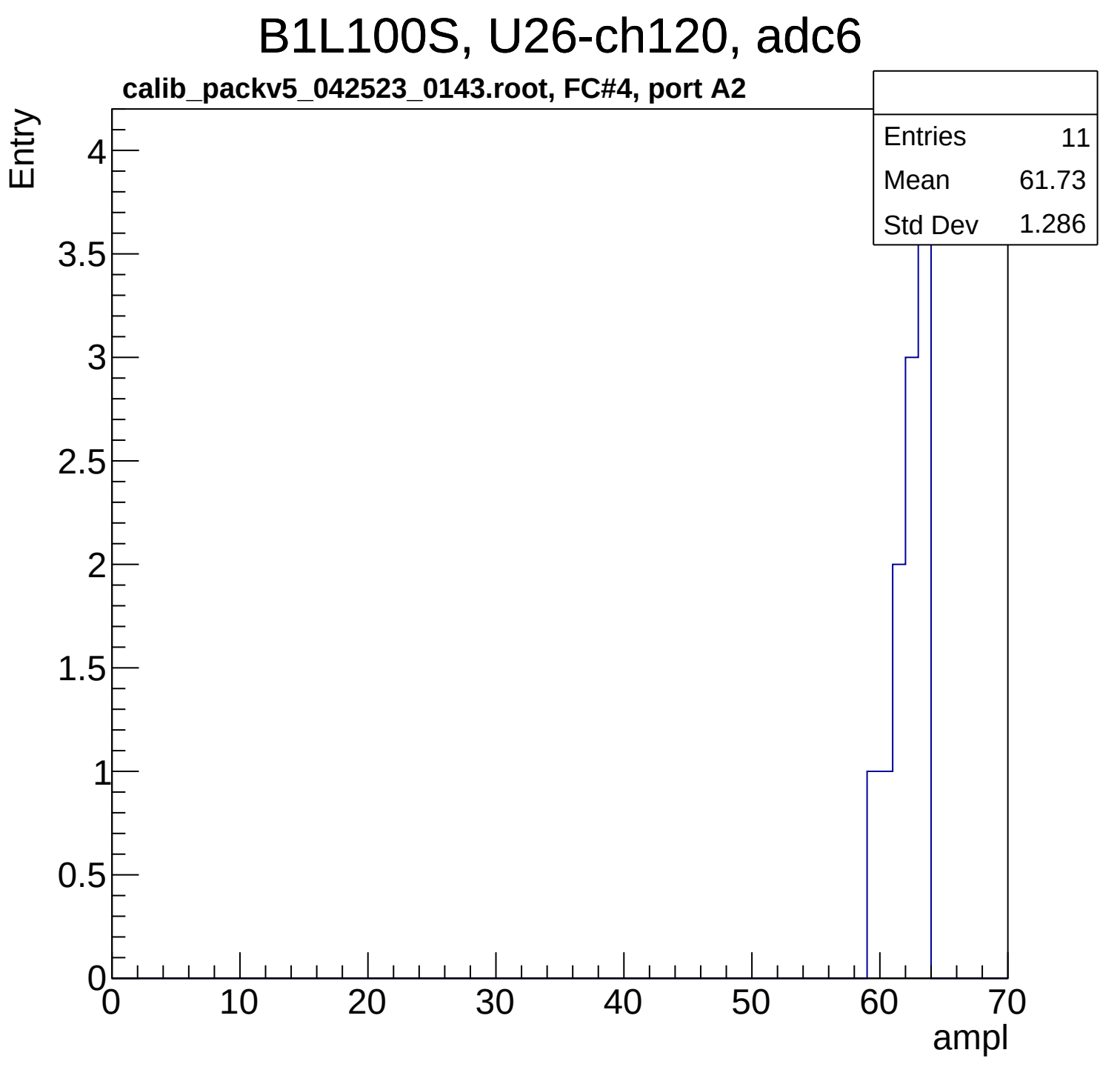
B1L100S, U26-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.73
Std Dev 1.286
Entry
ampl
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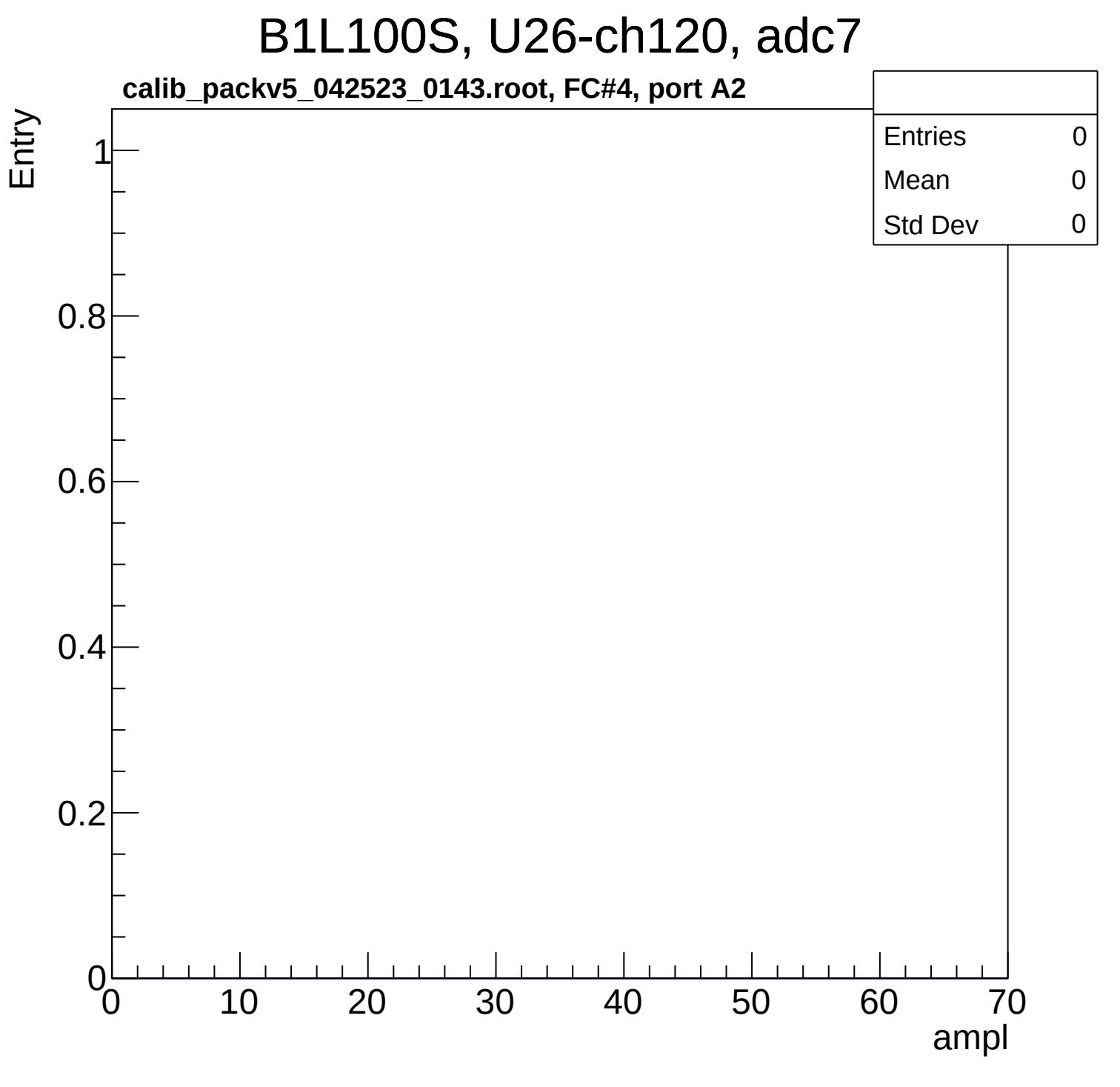
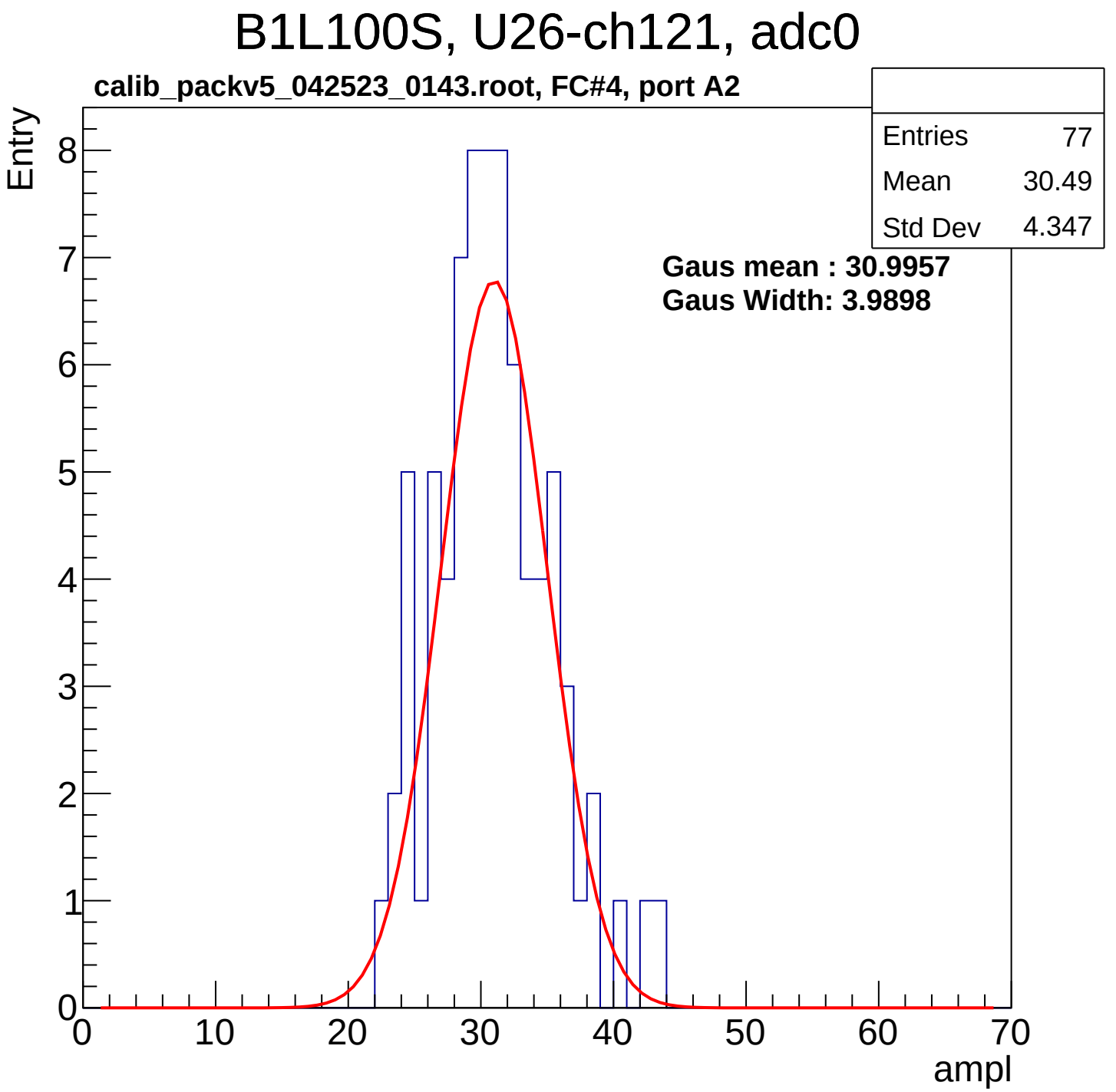
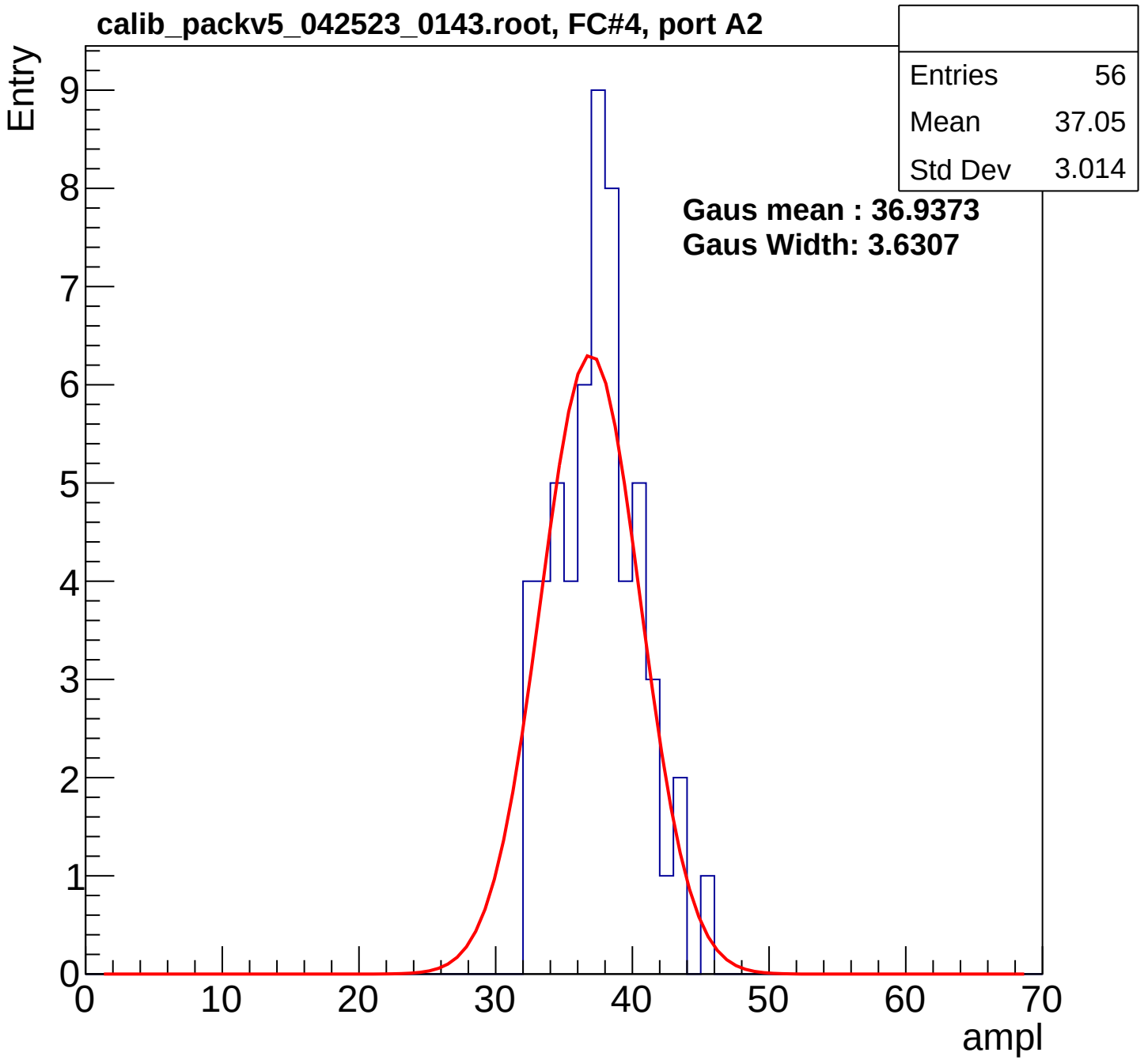
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U26-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 30.49
Std Dev 4.347
Gaus mean : 30.9957
Gaus Width: 3.9898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 37.05
Std Dev 3.014
Gaus mean : 36.9373
Gaus Width: 3.6307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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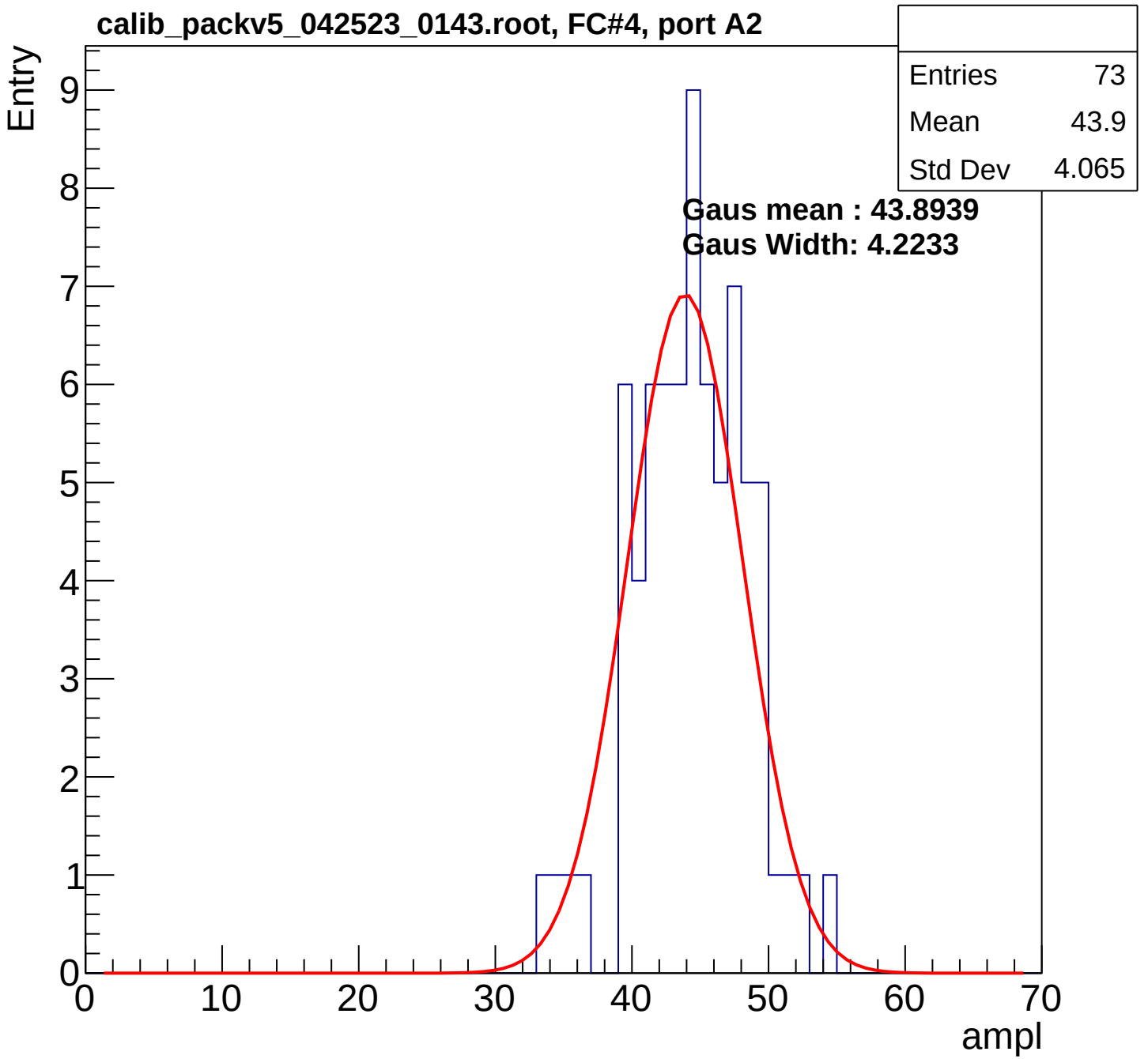
B1L100S, U26-ch121, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.9
Std Dev 4.065
Gaus mean : 43.8939
Gaus Width: 4.2233
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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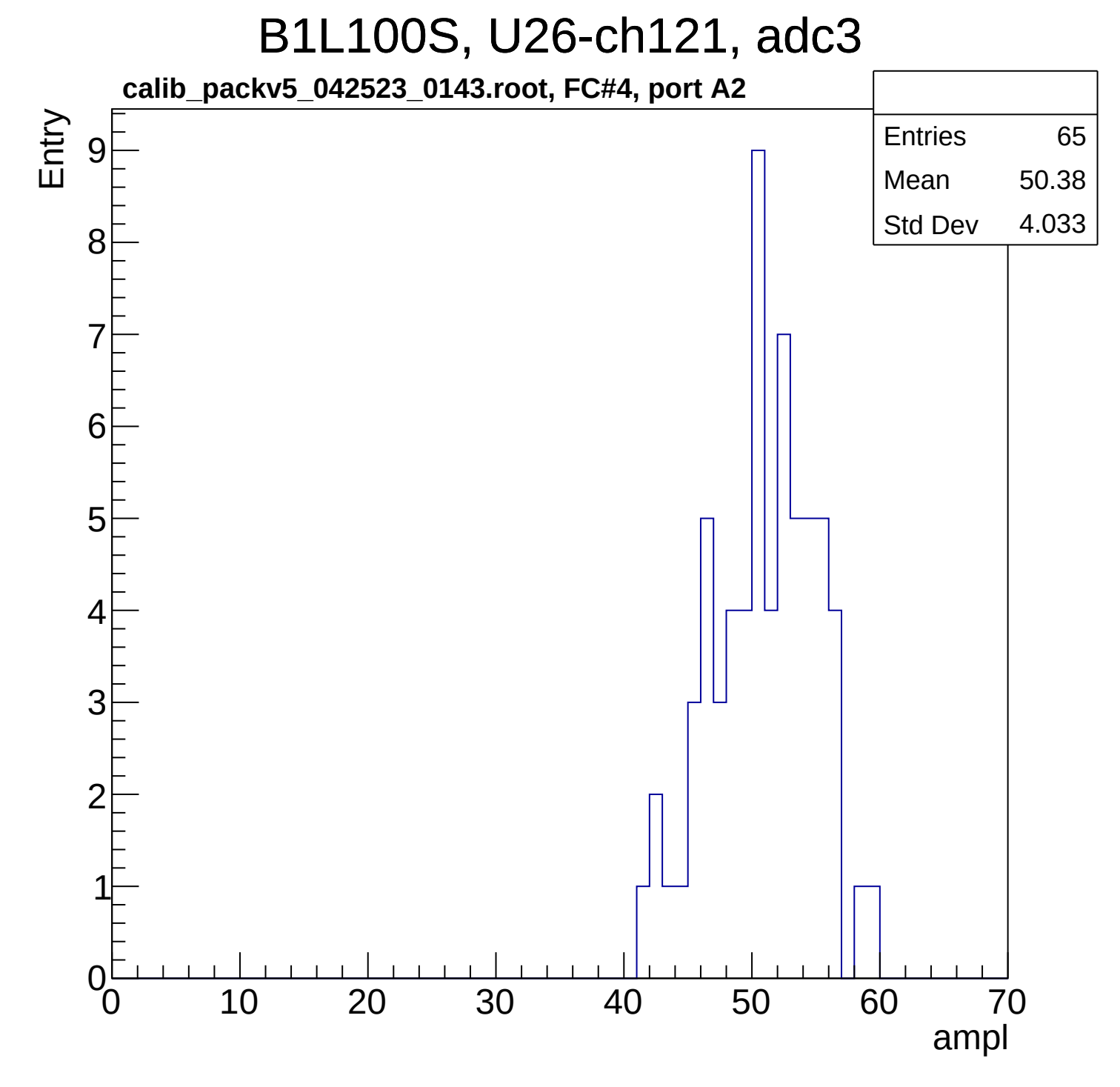
B1L100S, U26-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.38
Std Dev 4.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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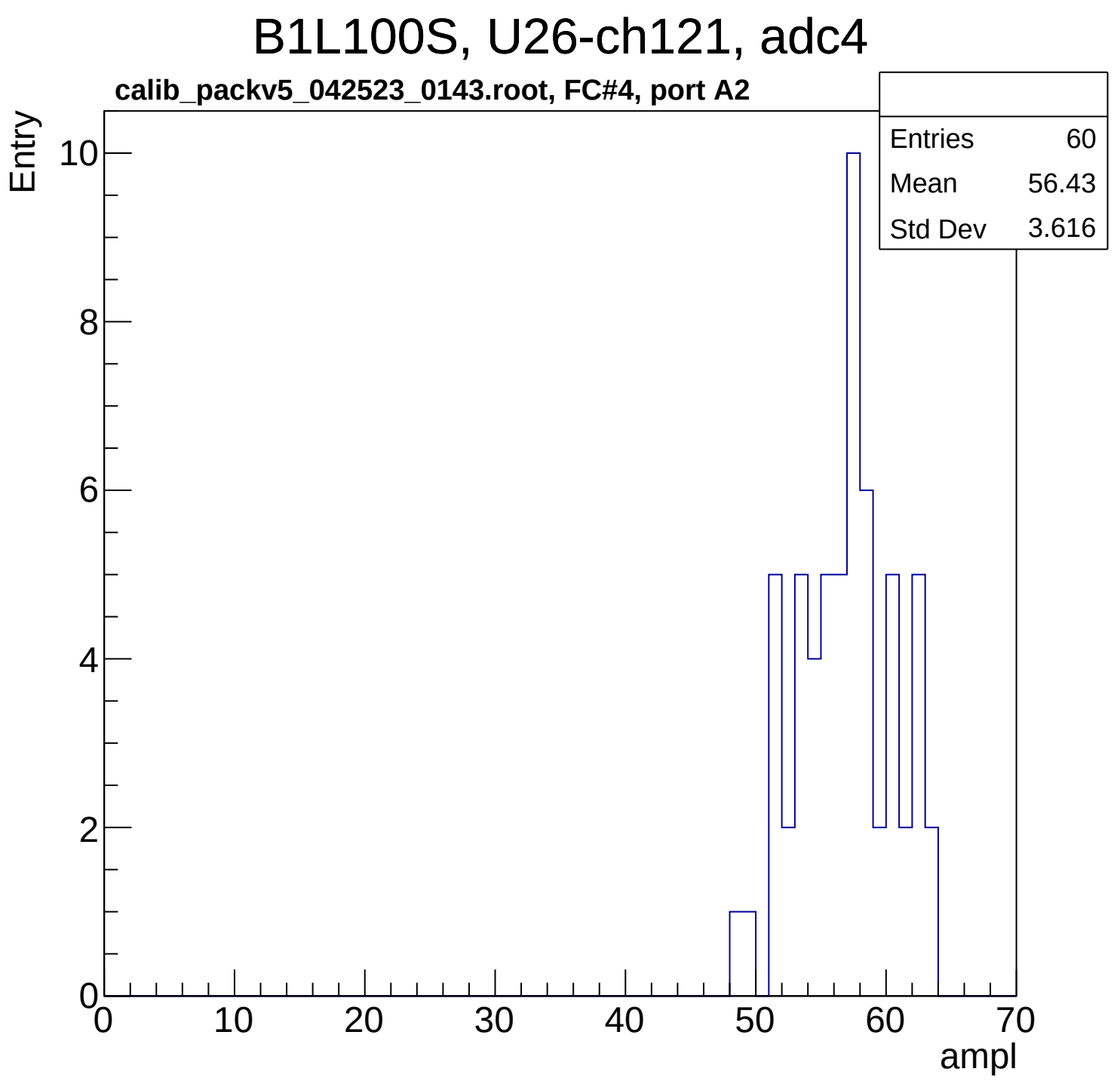
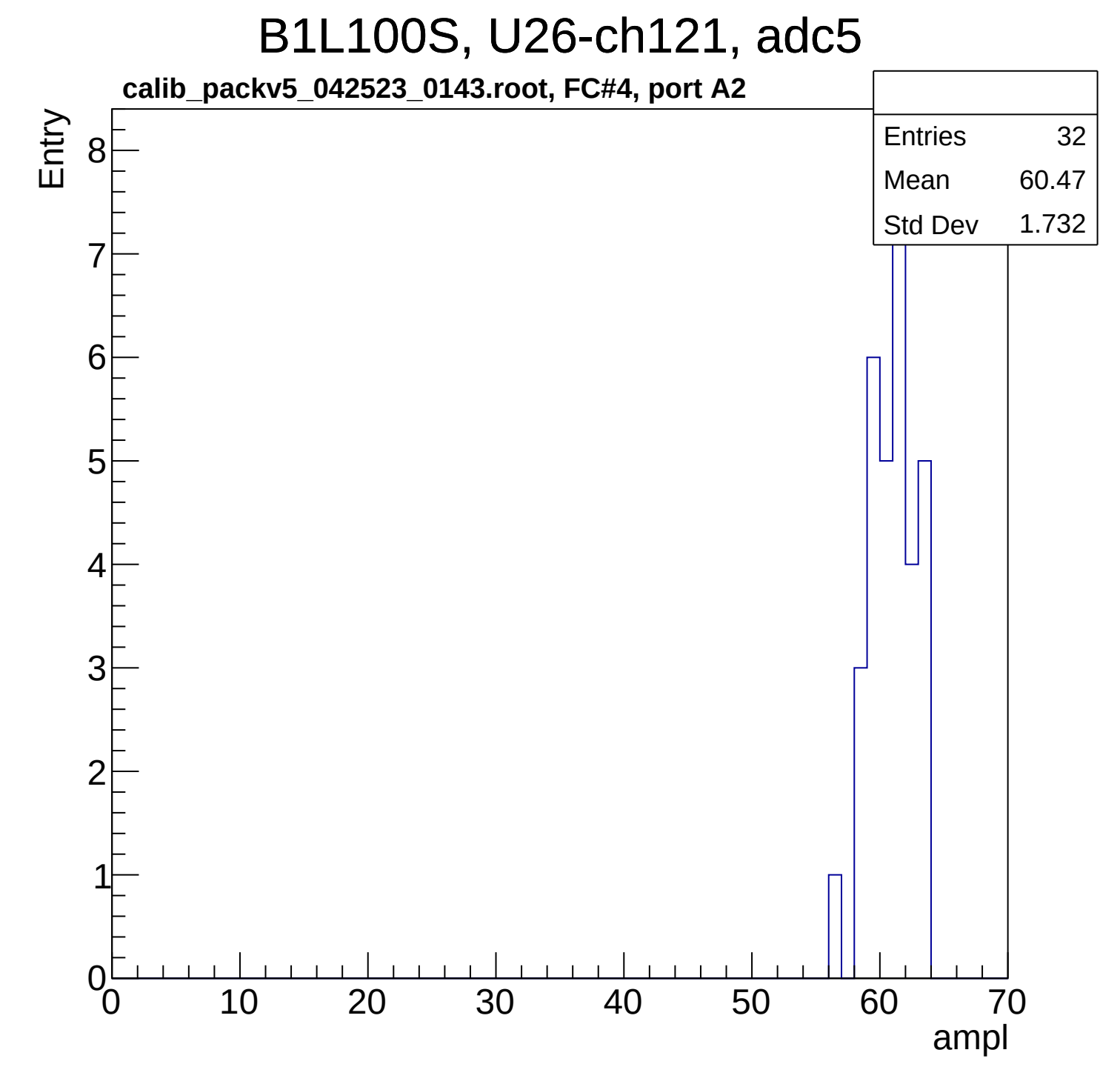
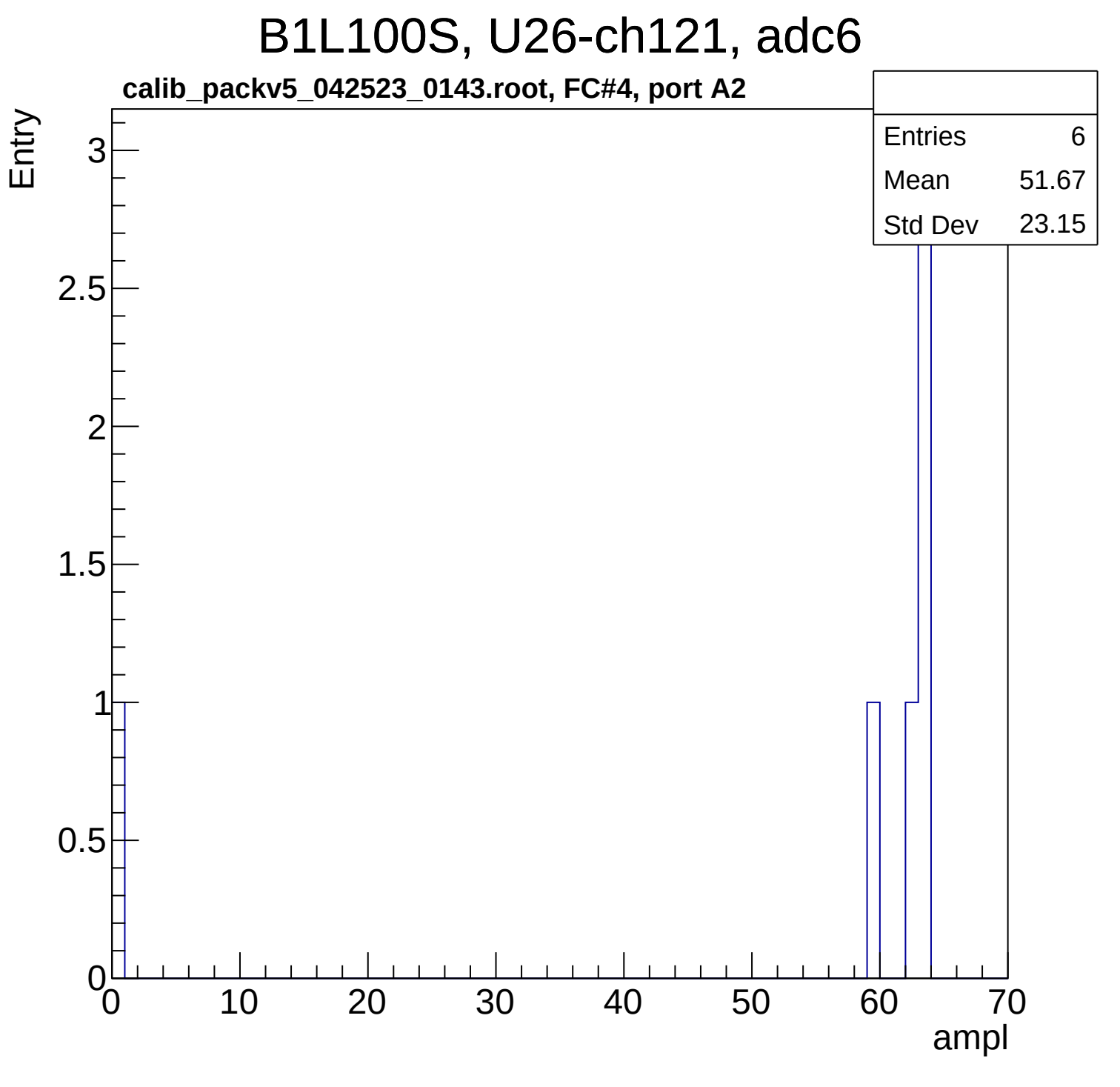
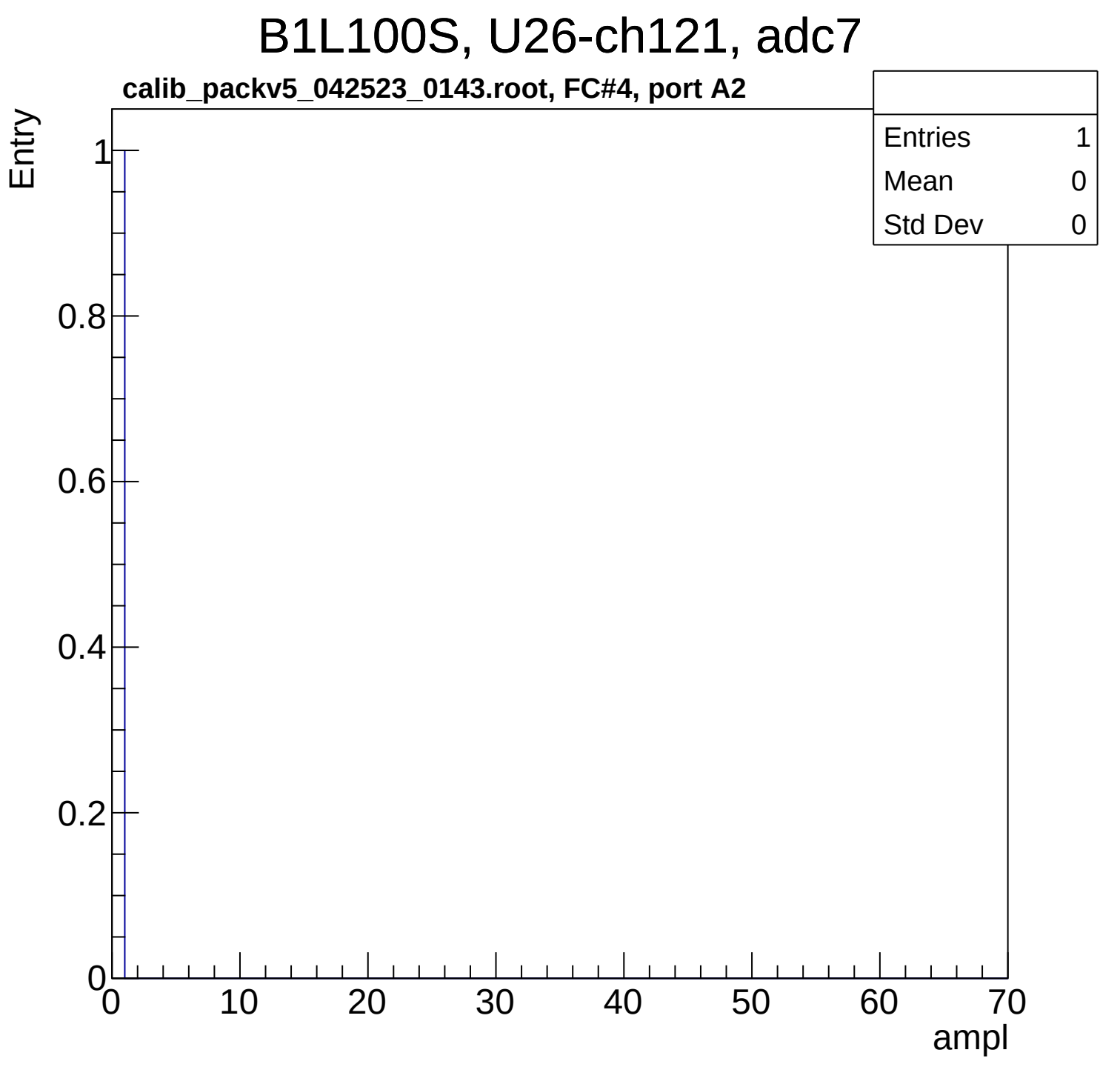
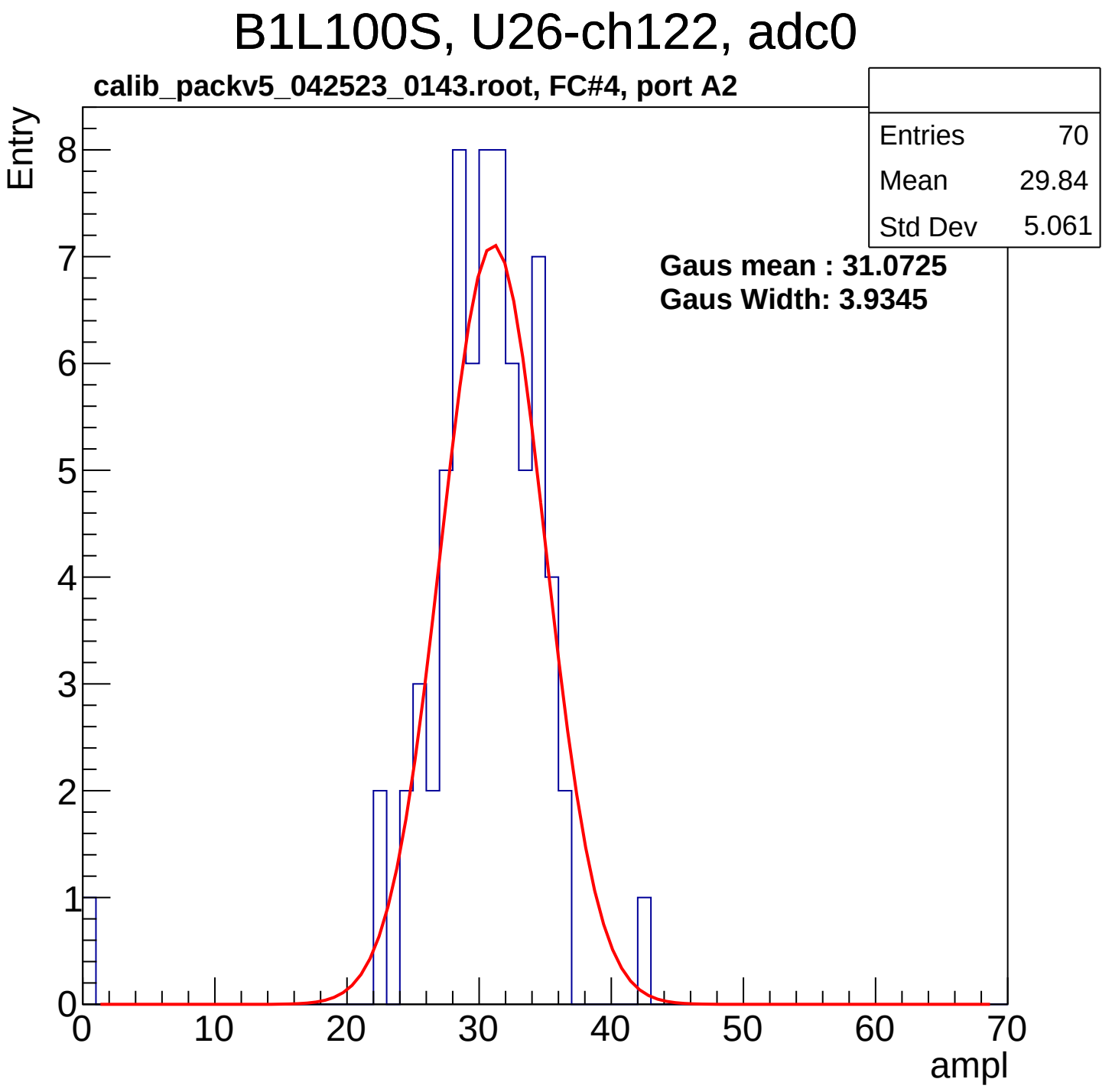

B1L100S, U26-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.43
Std Dev 3.616
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch121, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 60.47
Std Dev 1.732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.67
Std Dev 23.15
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 29.84
Std Dev 5.061
Gaus mean : 31.0725
Gaus Width: 3.9345
Entry
ampl
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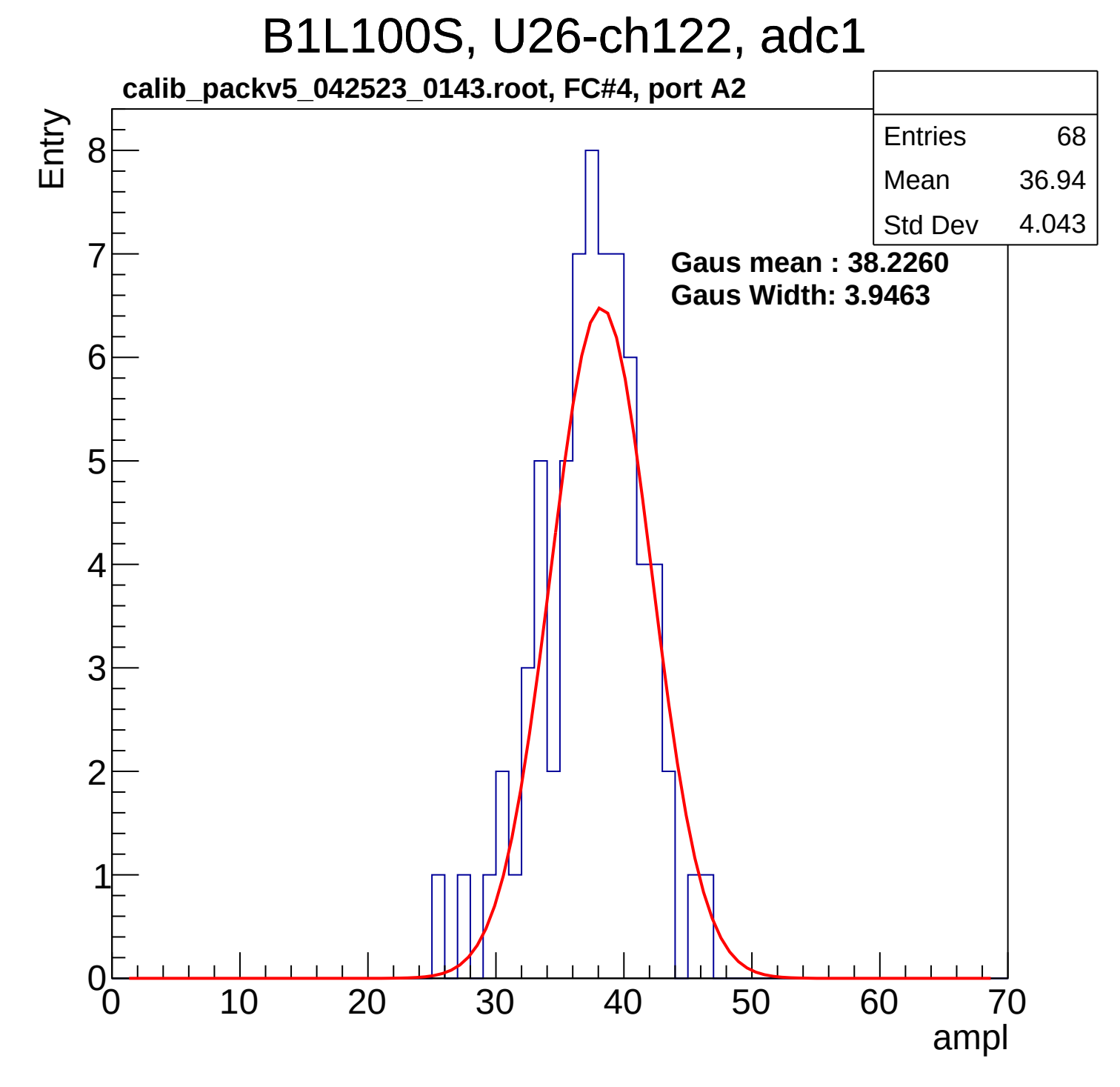
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.94
Std Dev 4.043
Gaus mean : 38.2260
Gaus Width: 3.9463
Entry
ampl
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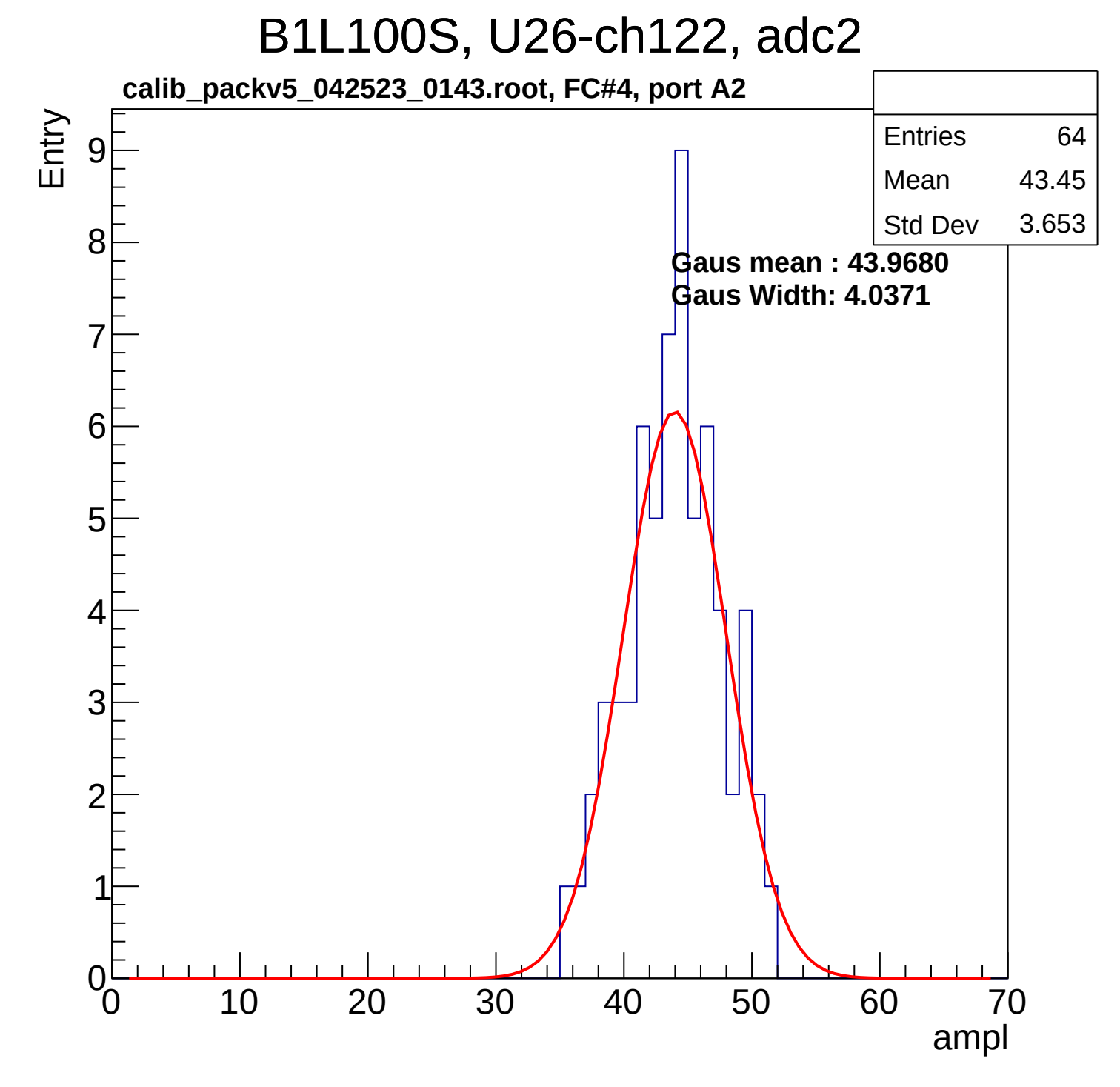
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.45
Std Dev 3.653
Gaus mean : 43.9680
Gaus Width: 4.0371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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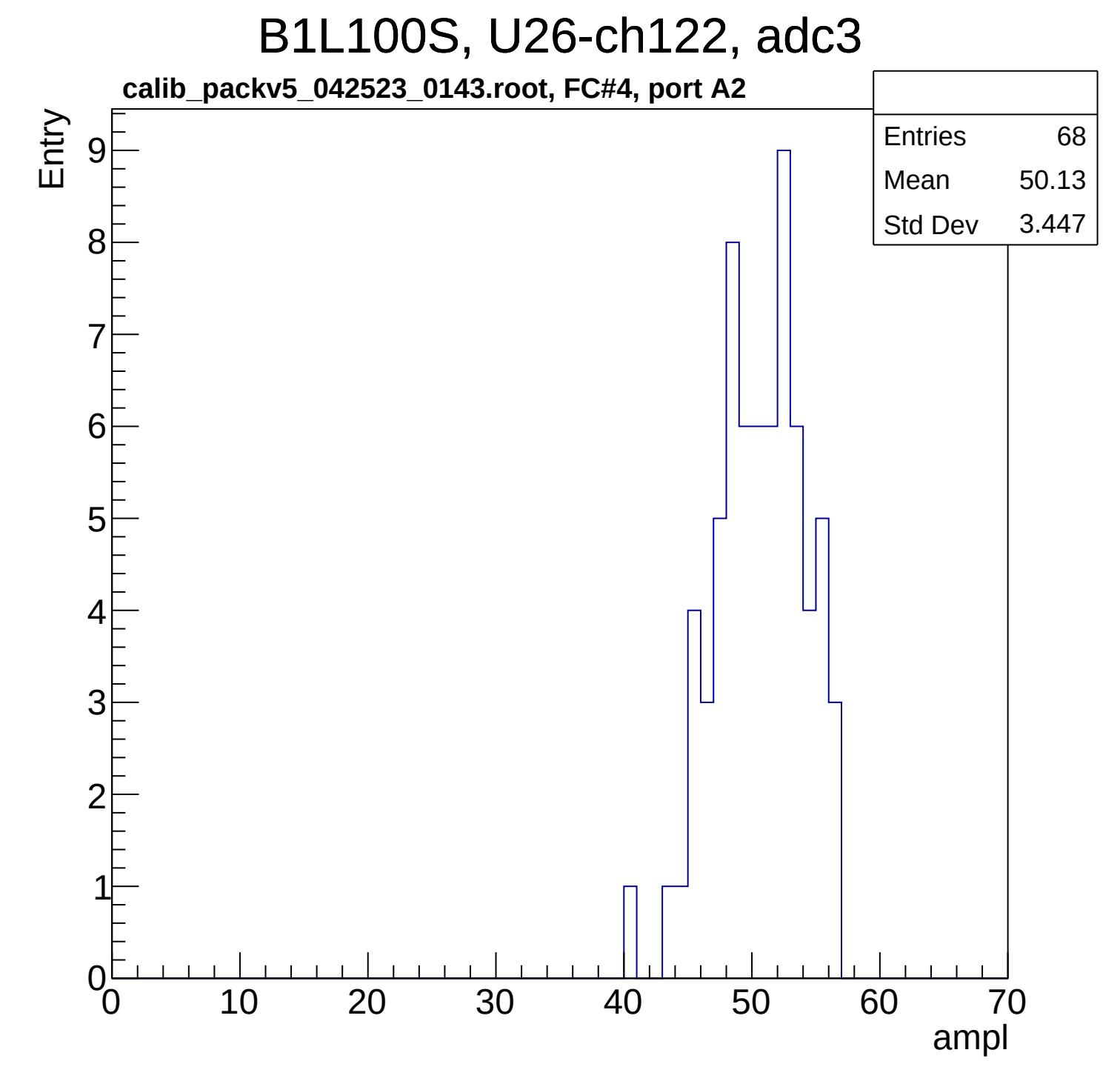
B1L100S, U26-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.13
Std Dev 3.447
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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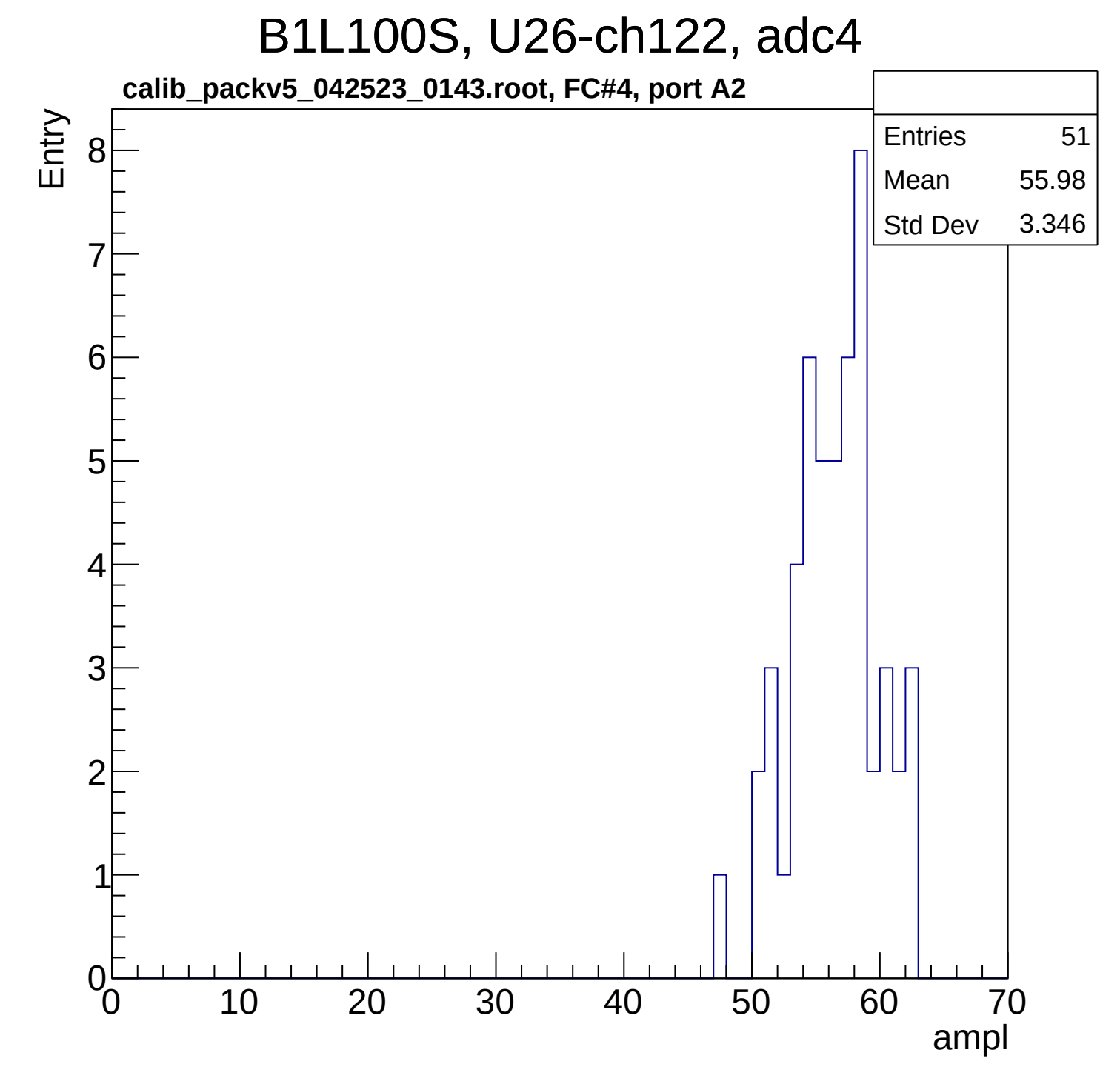
40
50
60
70


B1L100S, U26-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.98
Std Dev 3.346
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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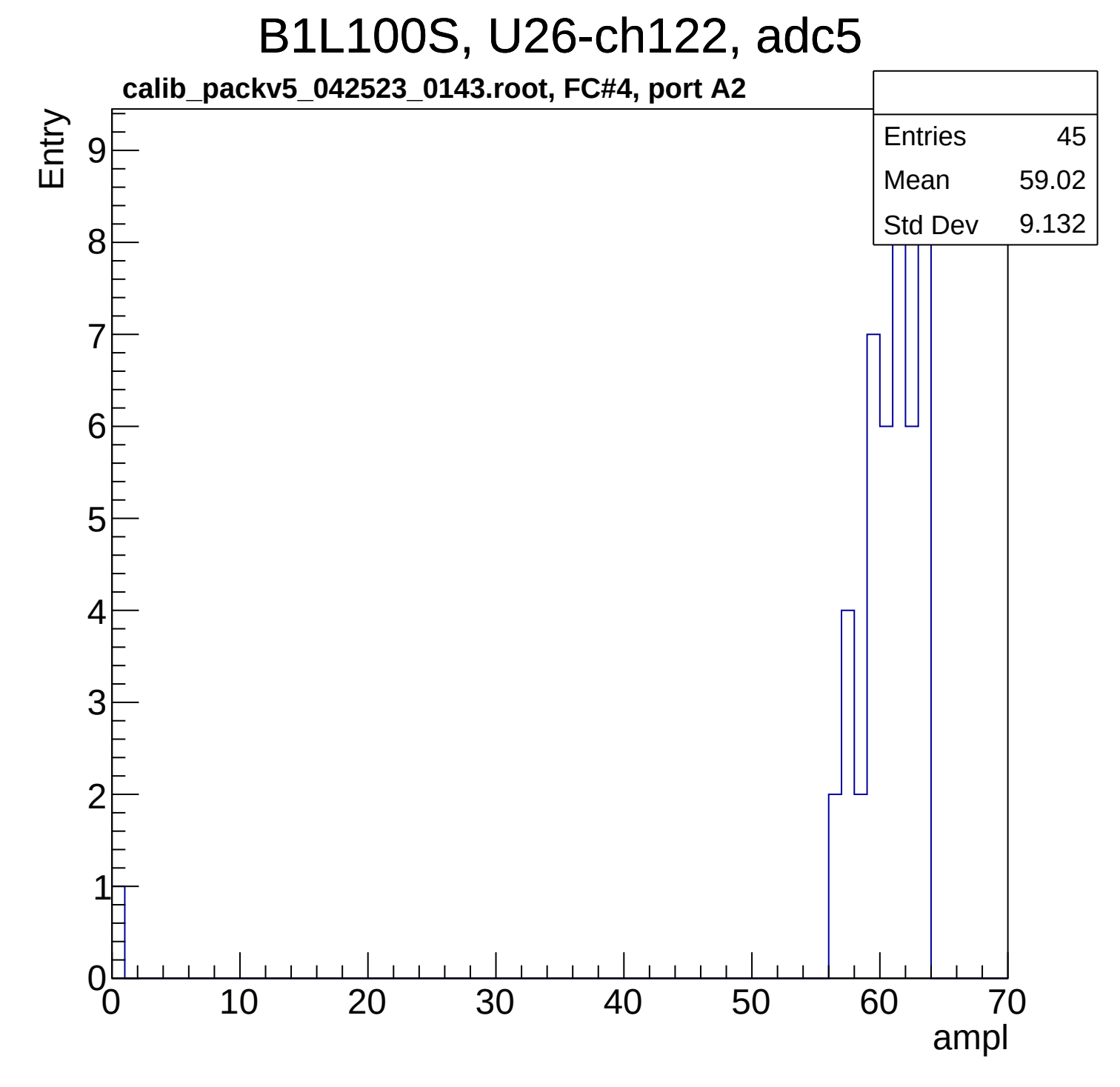
60
70


B1L100S, U26-ch122, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.02
Std Dev 9.132
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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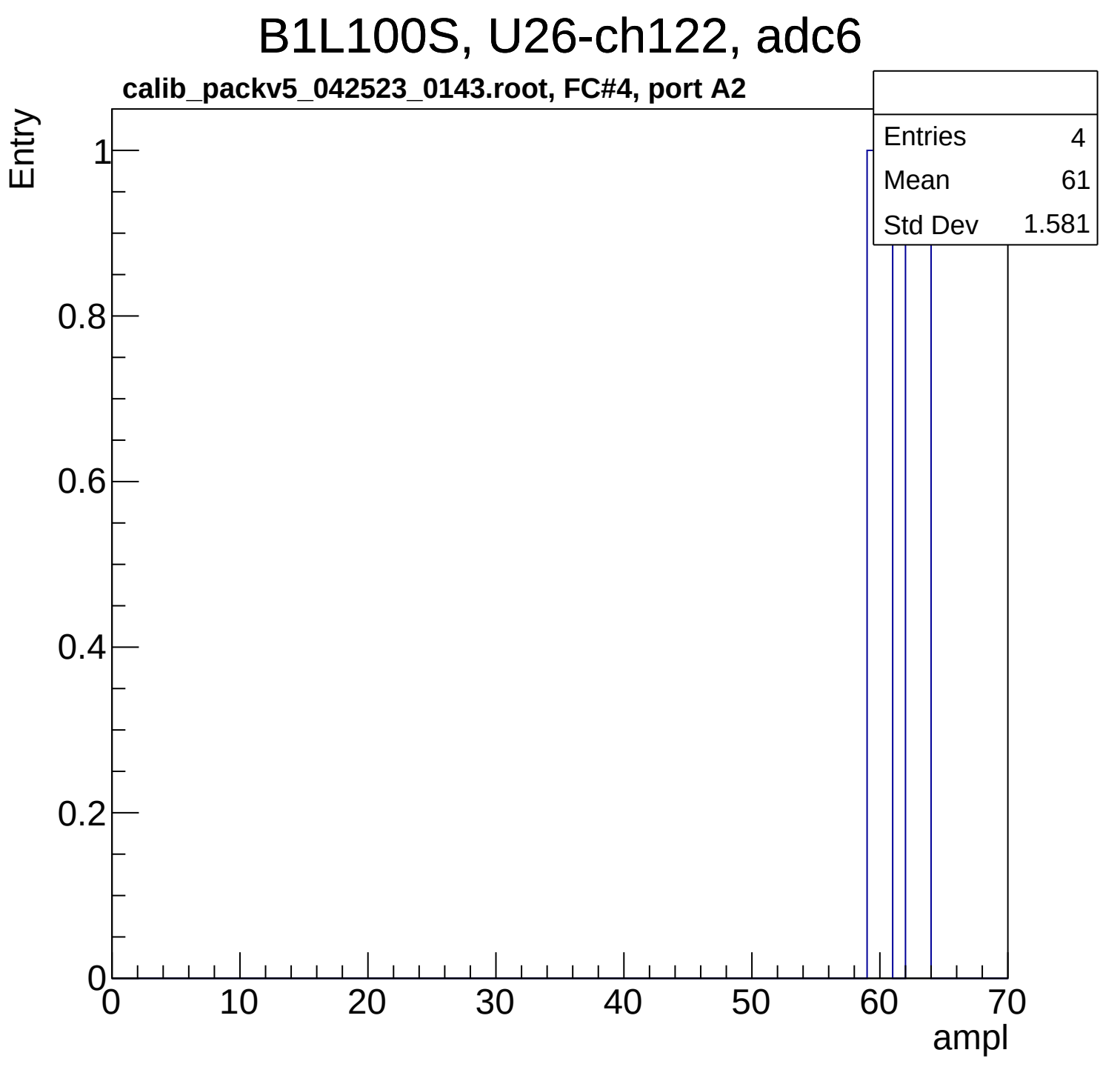
B1L100S, U26-ch122, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61
Std Dev 1.581
Entry
ampl
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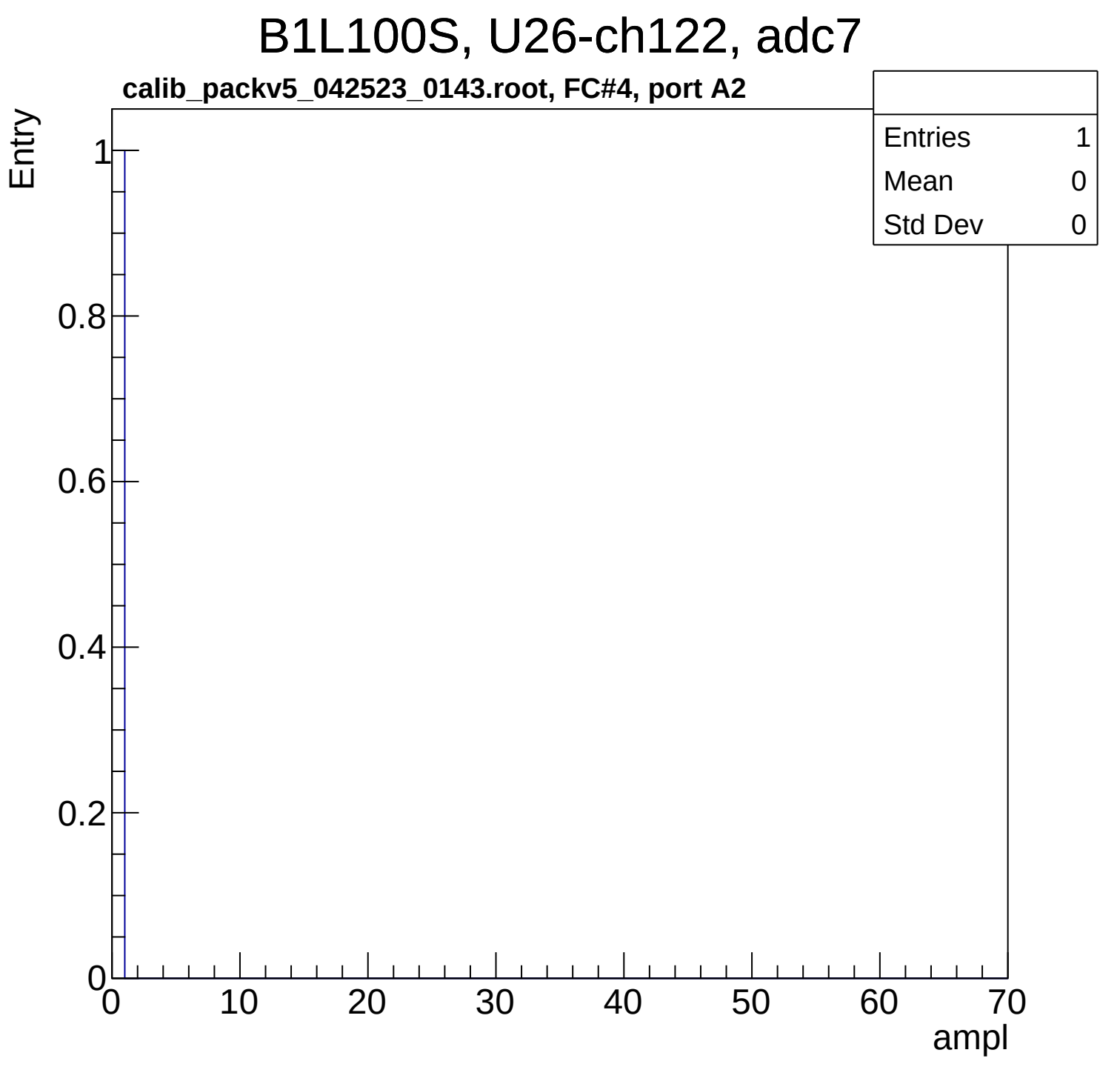
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U26-ch122, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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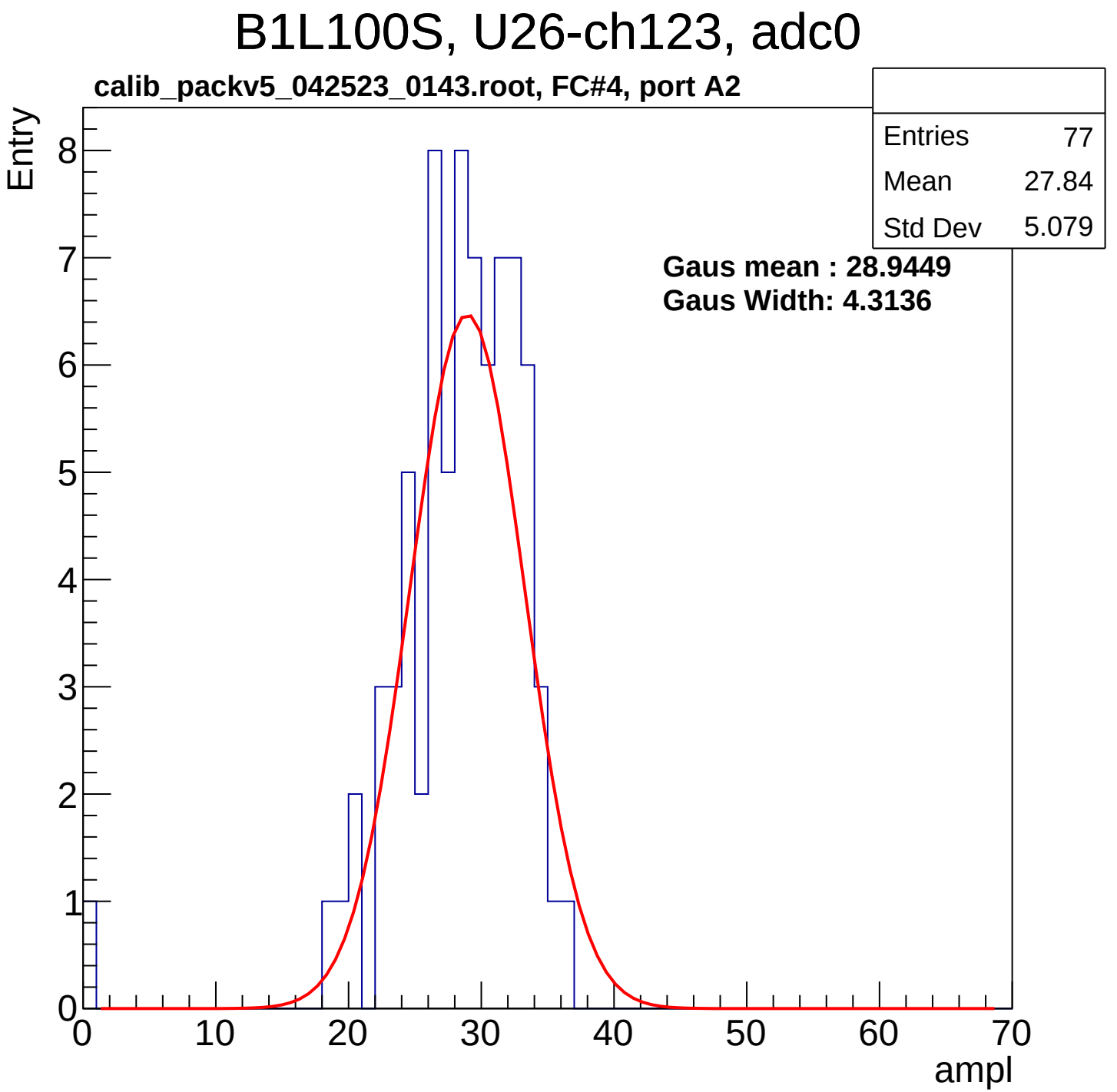
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 27.84
Std Dev 5.079
Gaus mean : 28.9449
Gaus Width: 4.3136
Entry
ampl
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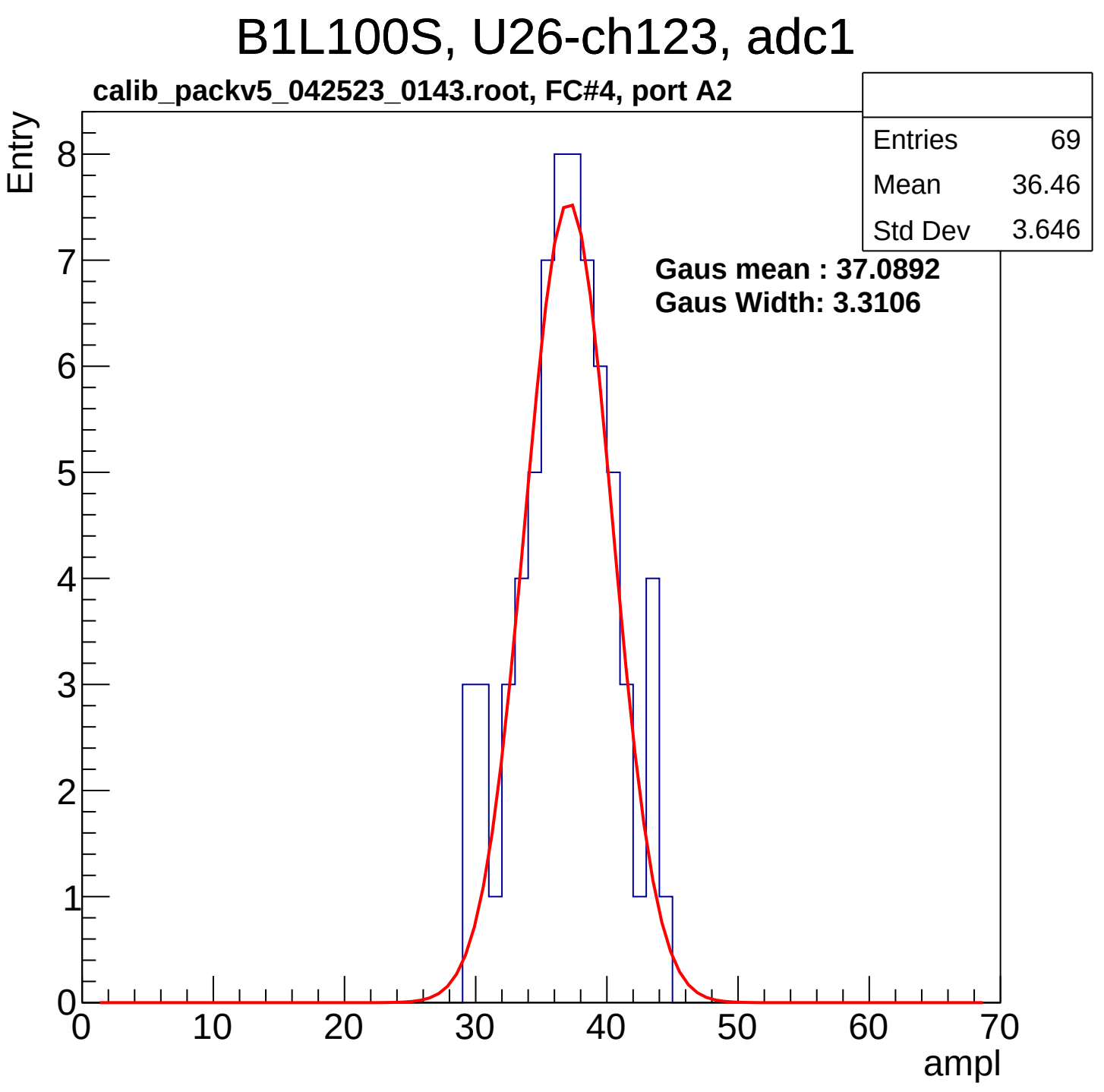
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.46
Std Dev 3.646
Gaus mean : 37.0892
Gaus Width: 3.3106
Entry
ampl
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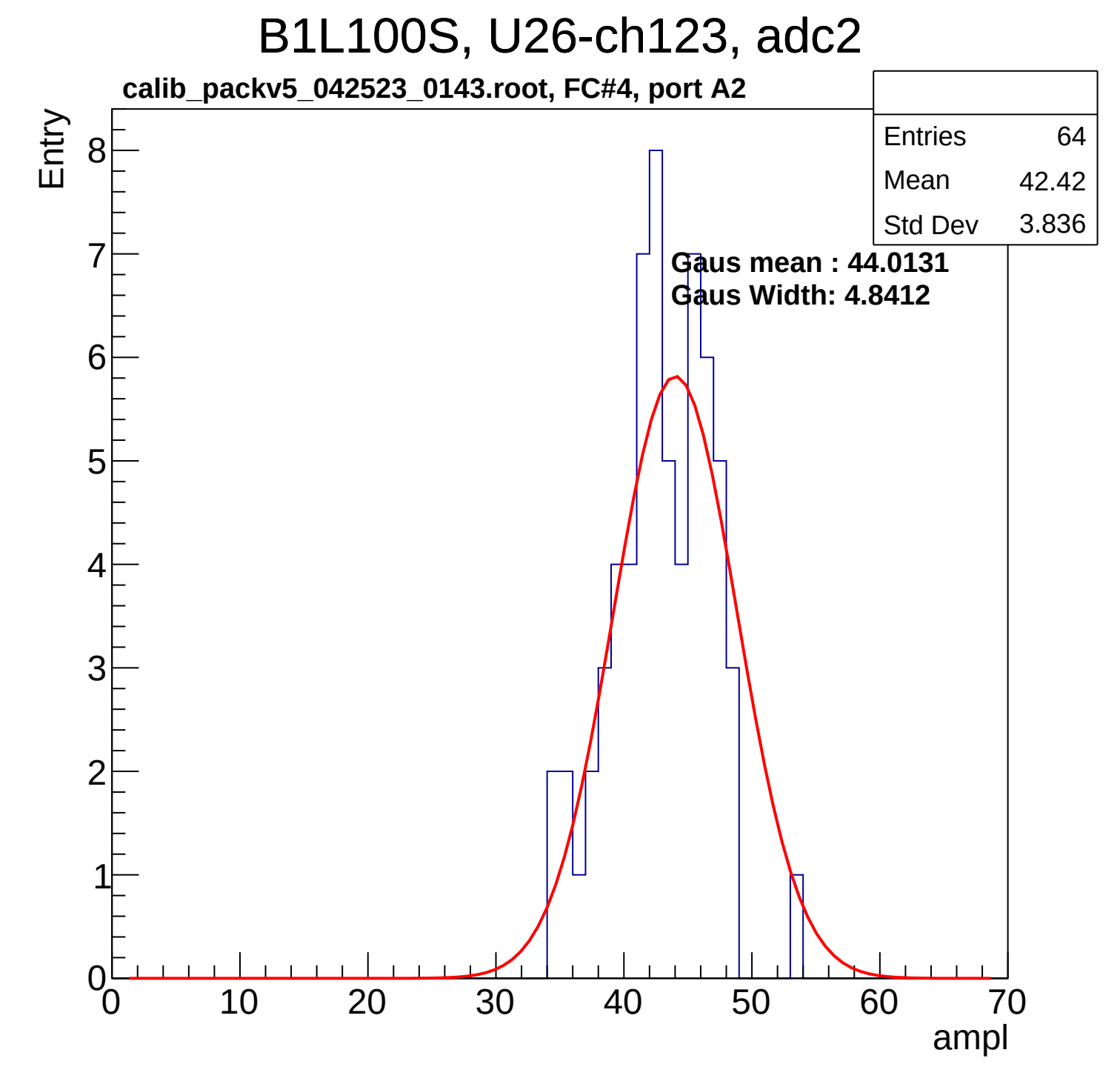
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.42
Std Dev 3.836
Gaus mean : 44.0131
Gaus Width: 4.8412
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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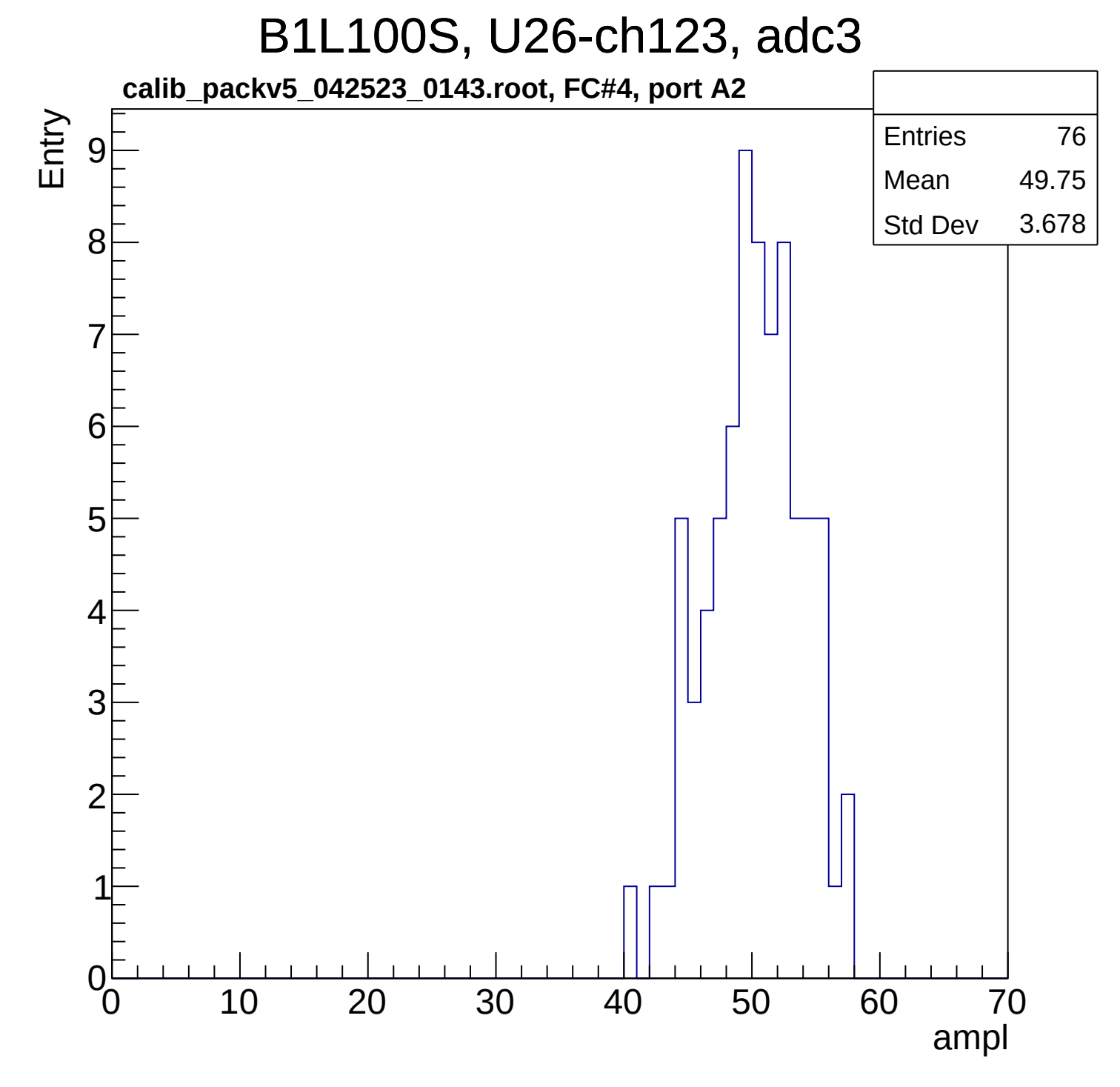
B1L100S, U26-ch123, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.75
Std Dev 3.678
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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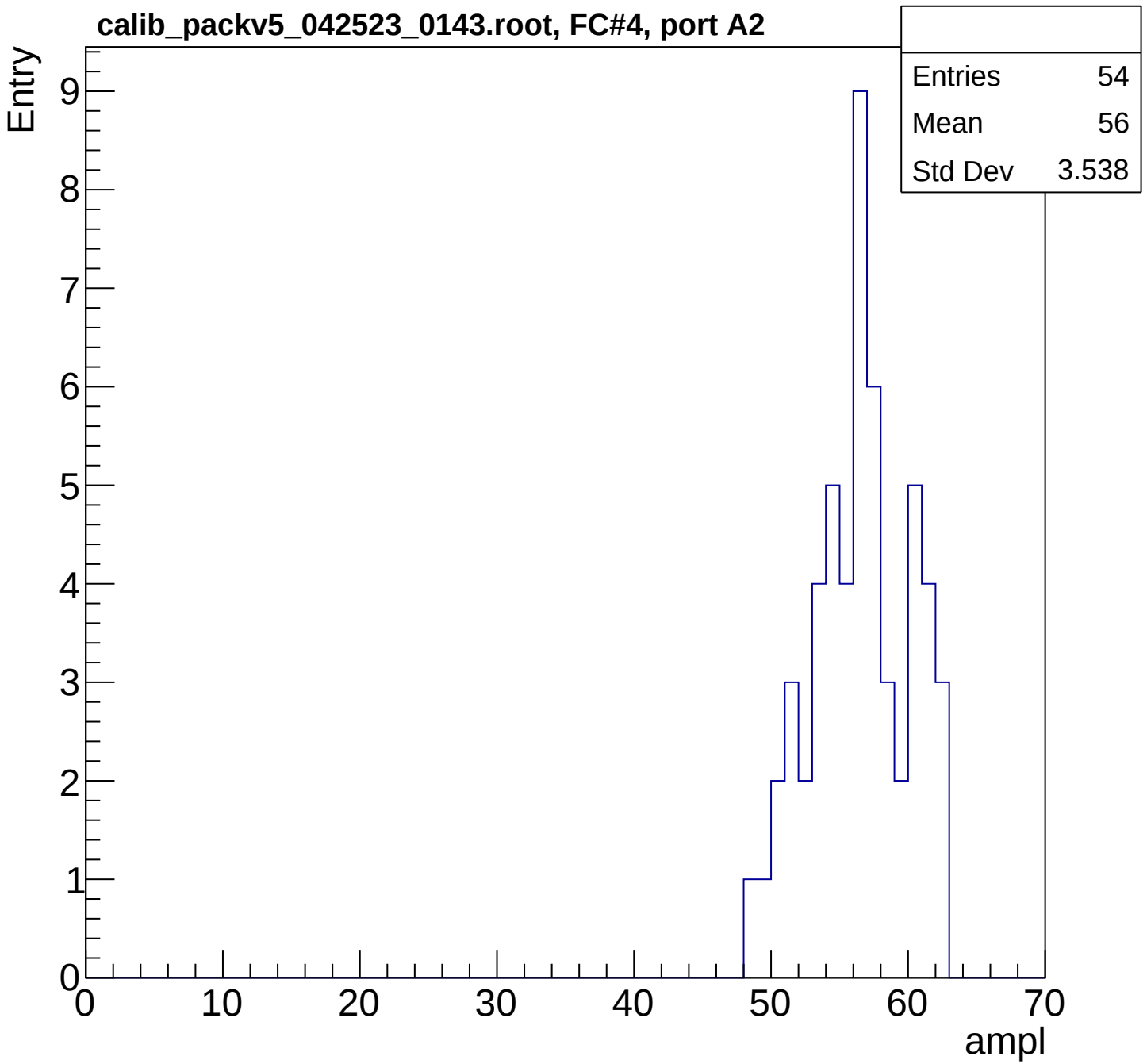
B1L100S, U26-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56
Std Dev 3.538
Entry
ampl
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2
3
4
5
6
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0
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40
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60
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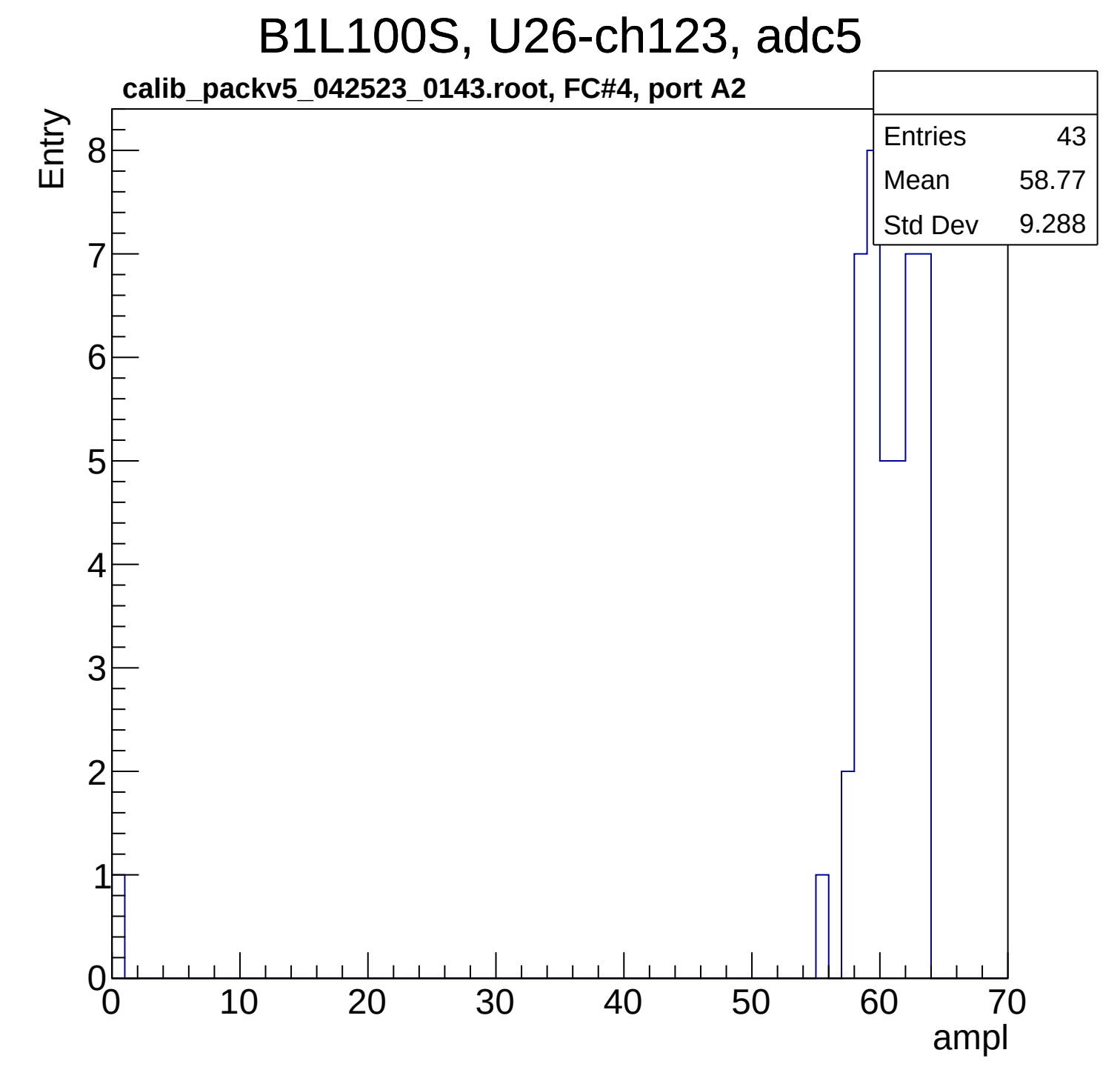
B1L100S, U26-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.77
Std Dev 9.288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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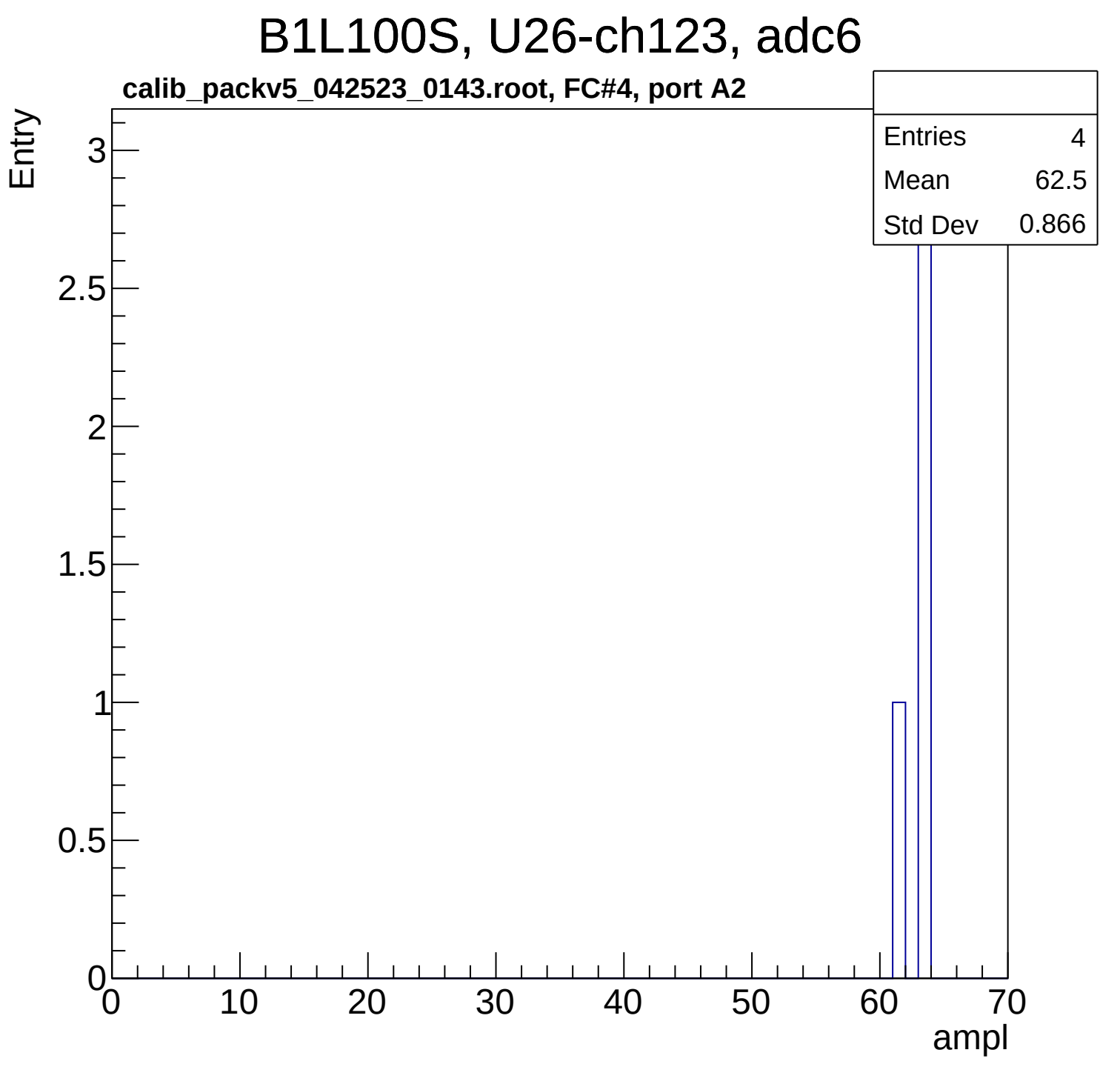
40
50
60
70


B1L100S, U26-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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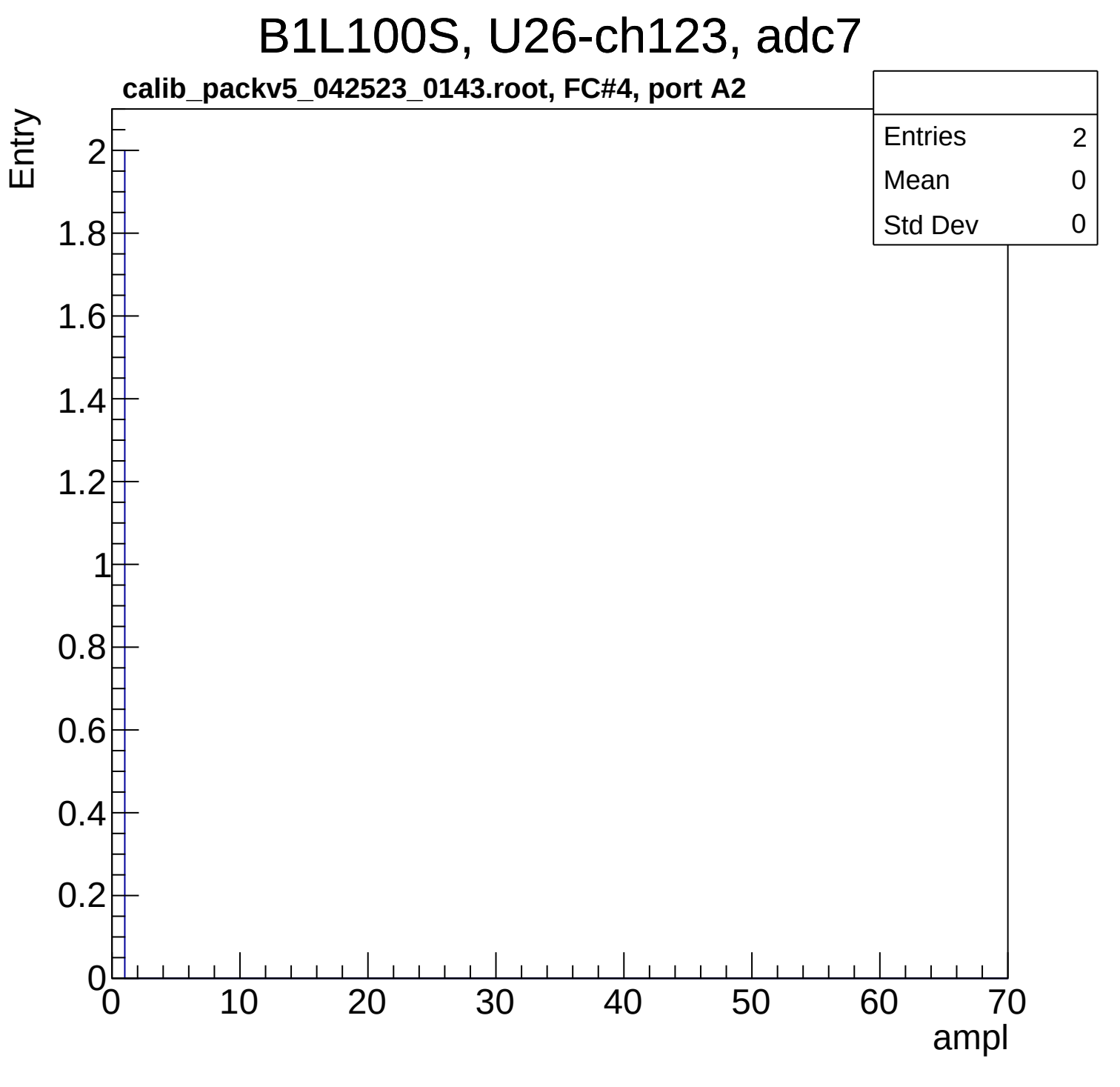
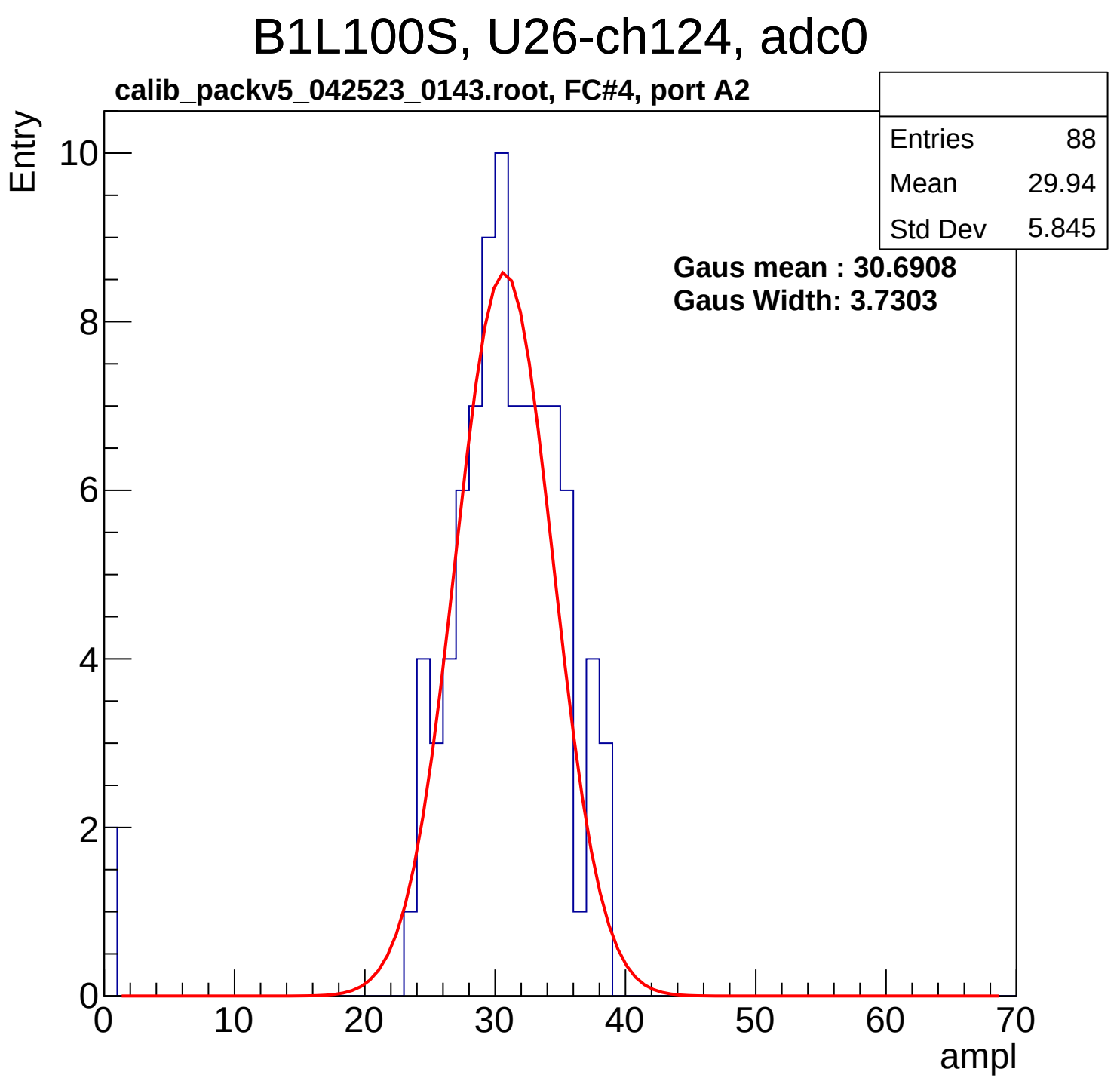
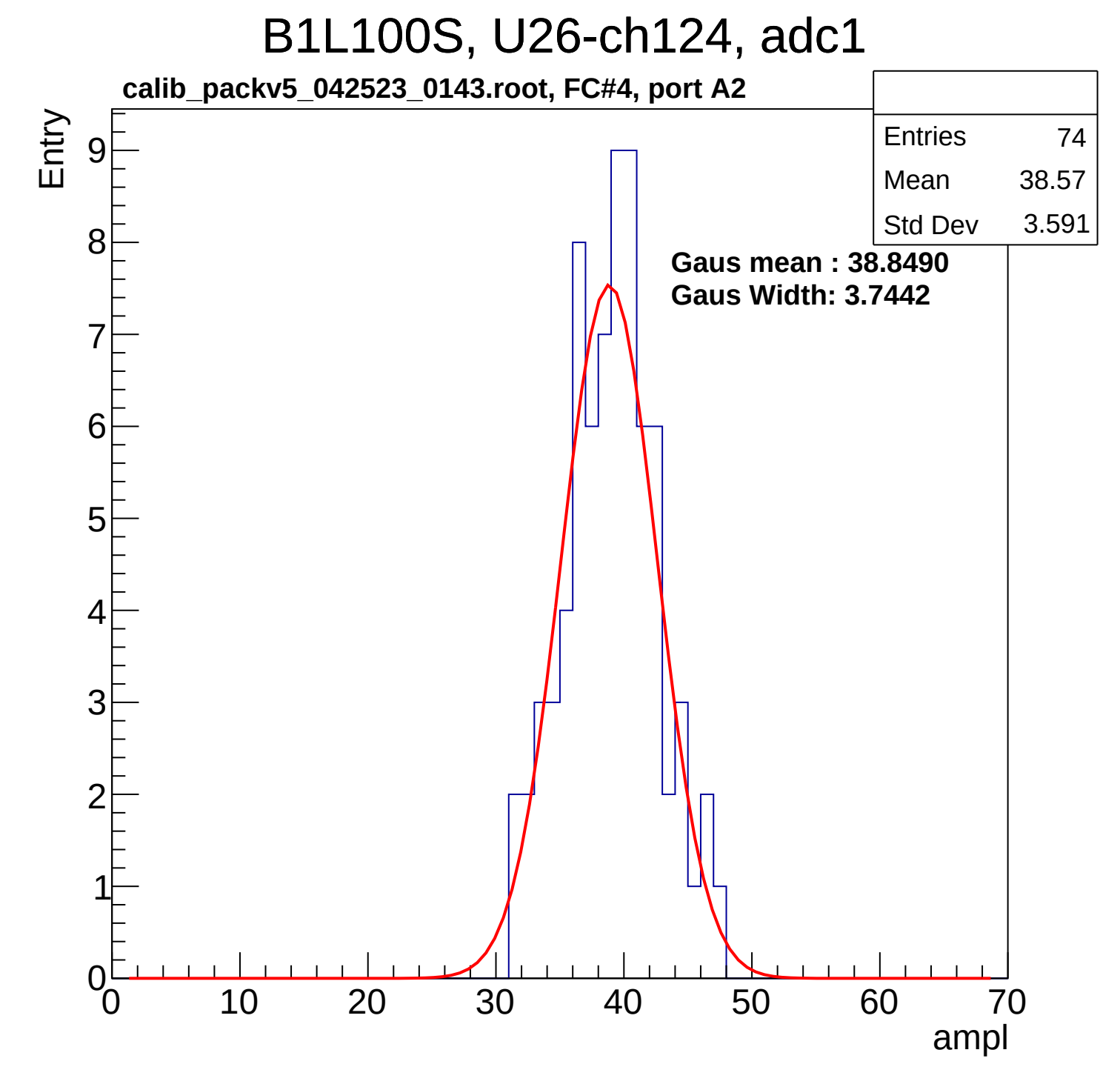

B1L100S, U26-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 29.94
Std Dev 5.845
Gaus mean : 30.6908
Gaus Width: 3.7303
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 38.57
Std Dev 3.591
Gaus mean : 38.8490
Gaus Width: 3.7442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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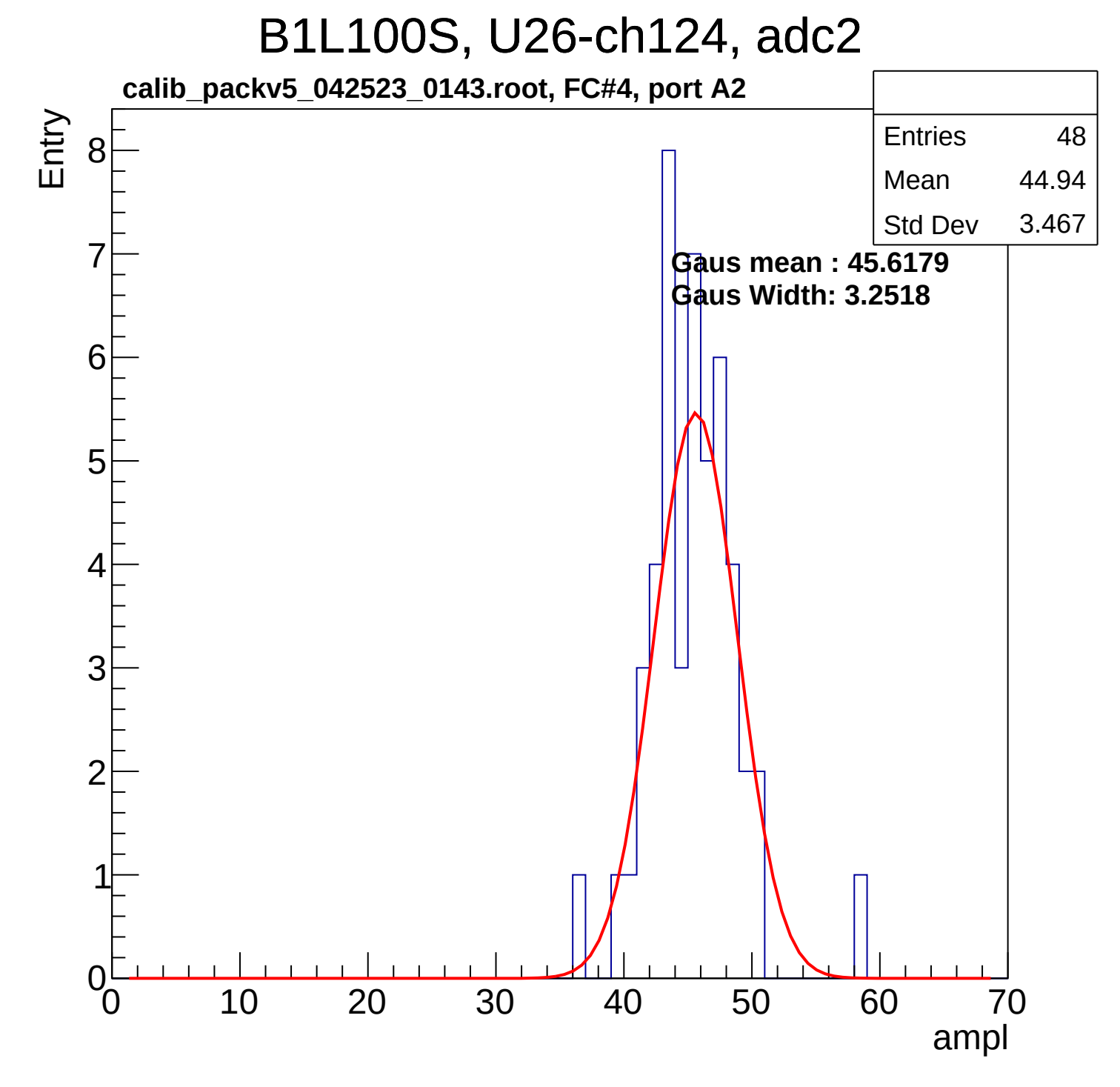
B1L100S, U26-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 44.94
Std Dev 3.467
Gaus mean : 45.6179
Gaus Width: 3.2518
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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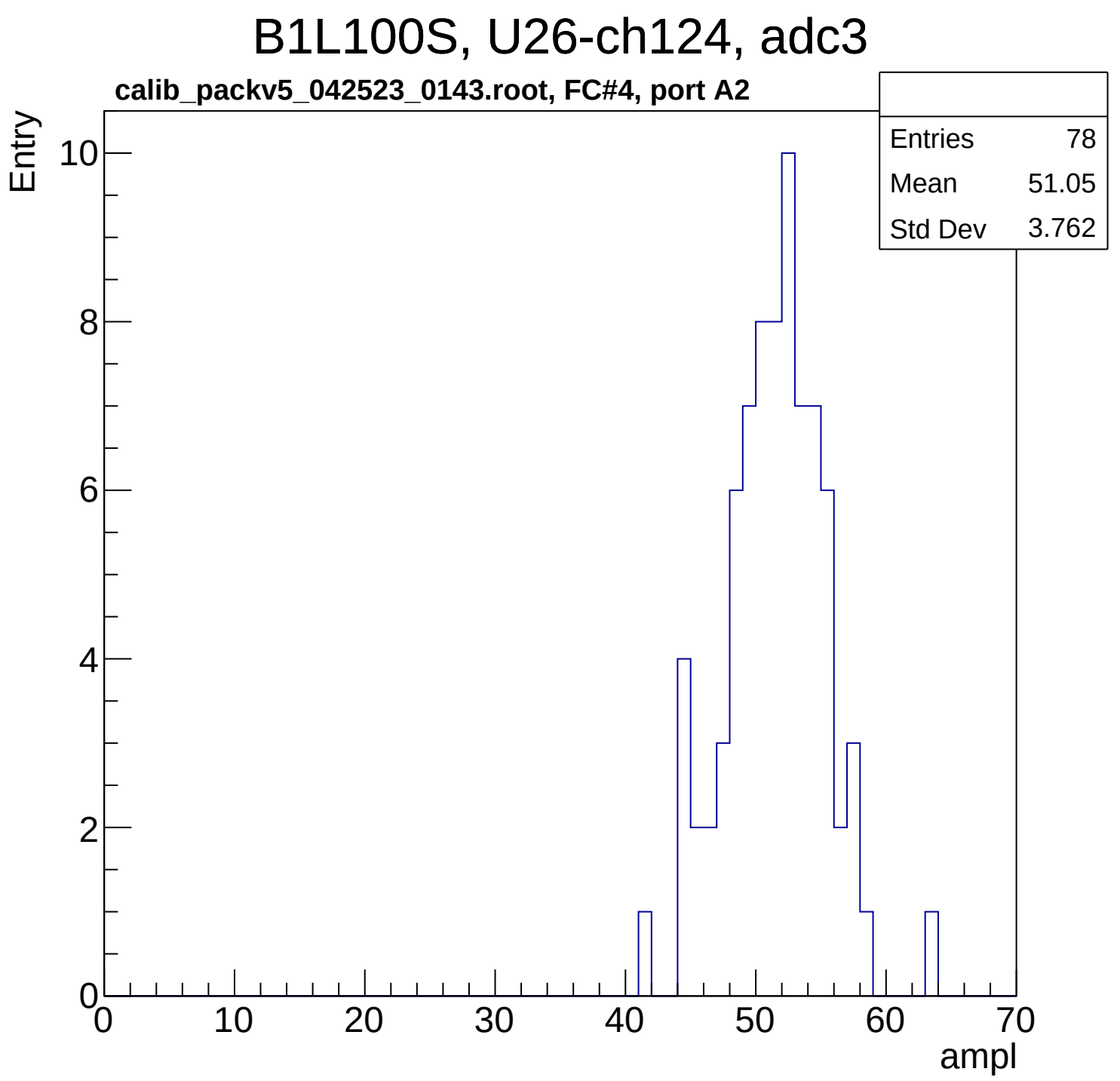
B1L100S, U26-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 51.05
Std Dev 3.762
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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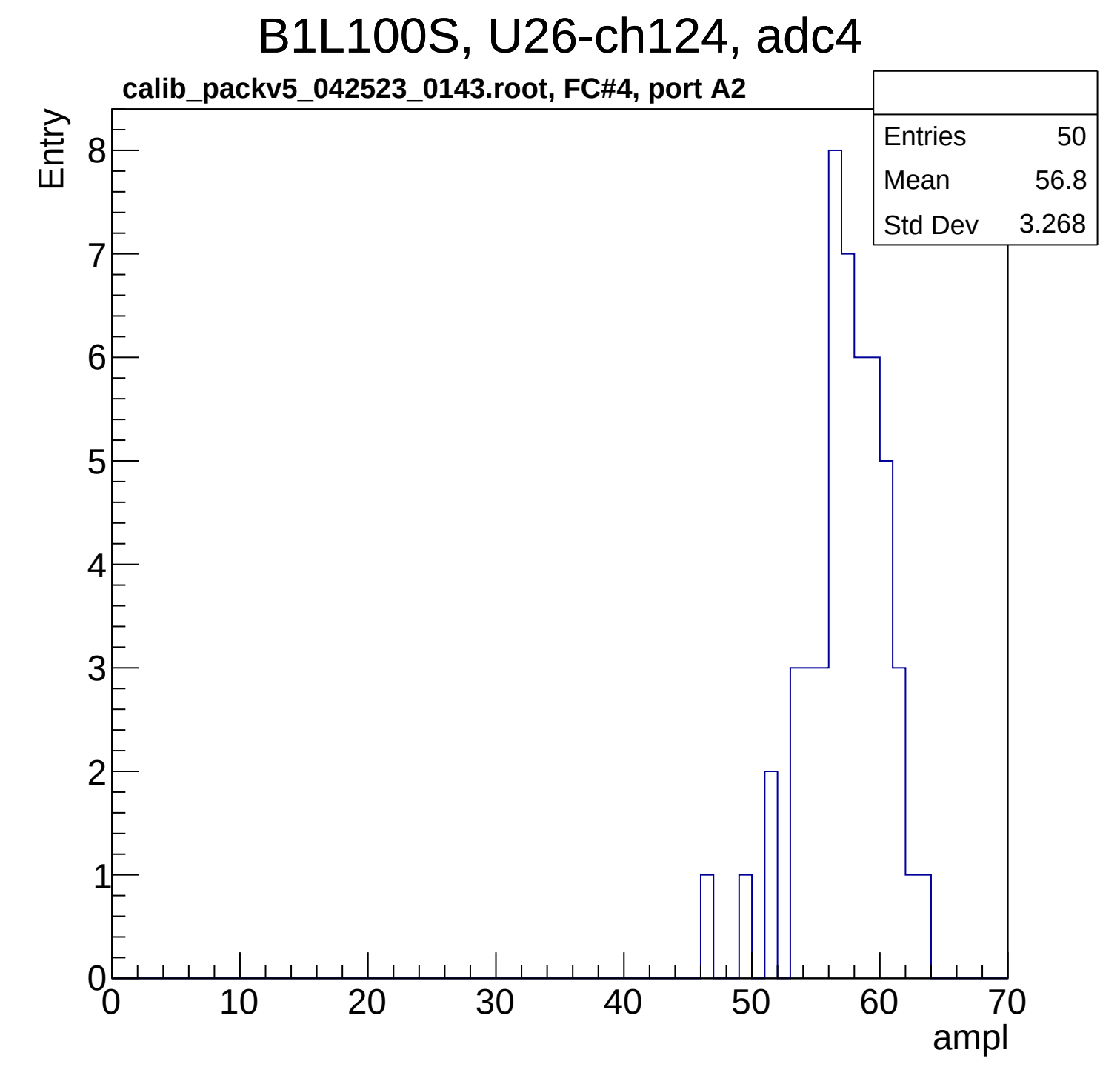
B1L100S, U26-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 56.8
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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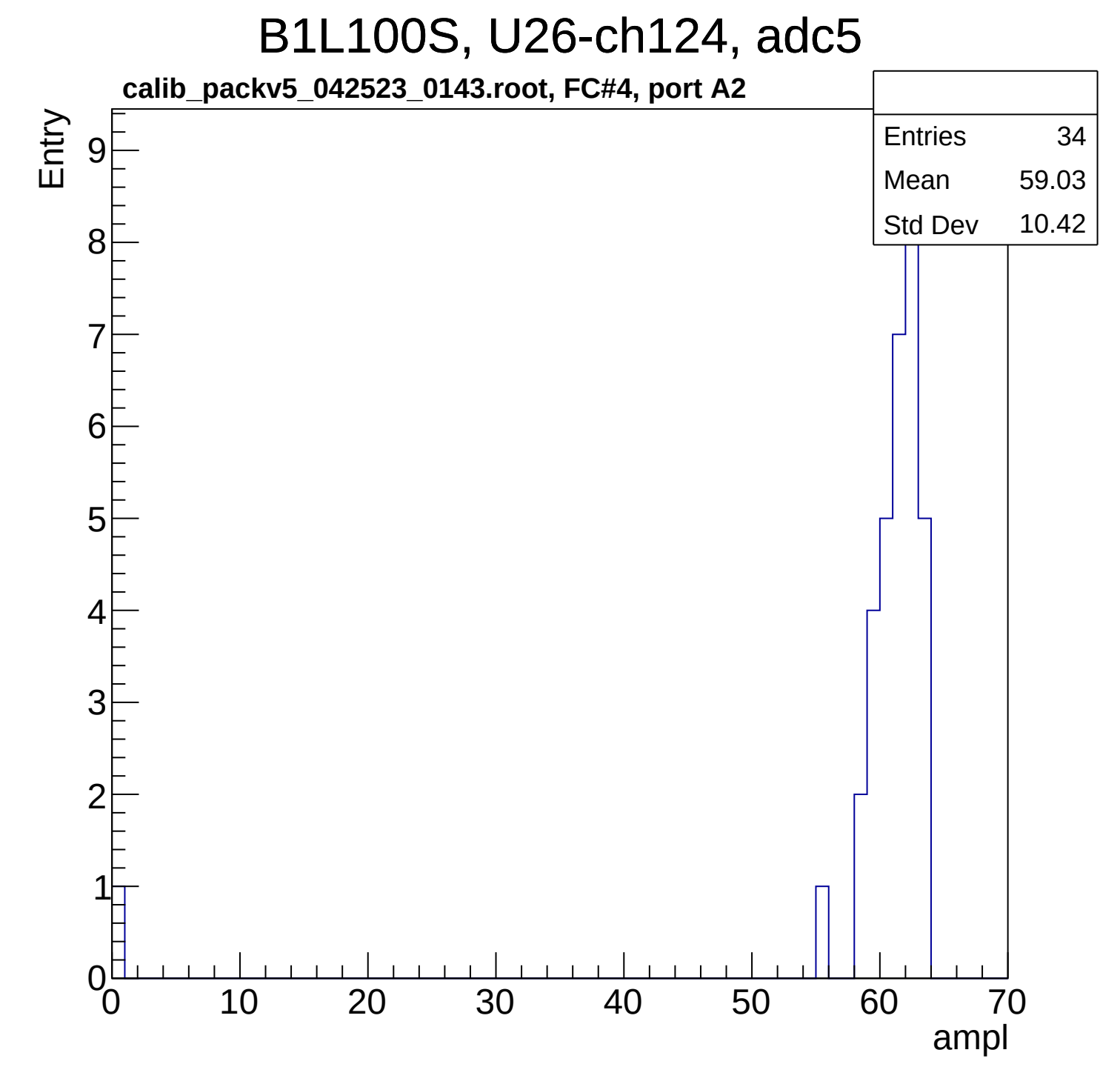
B1L100S, U26-ch124, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59.03
Std Dev 10.42
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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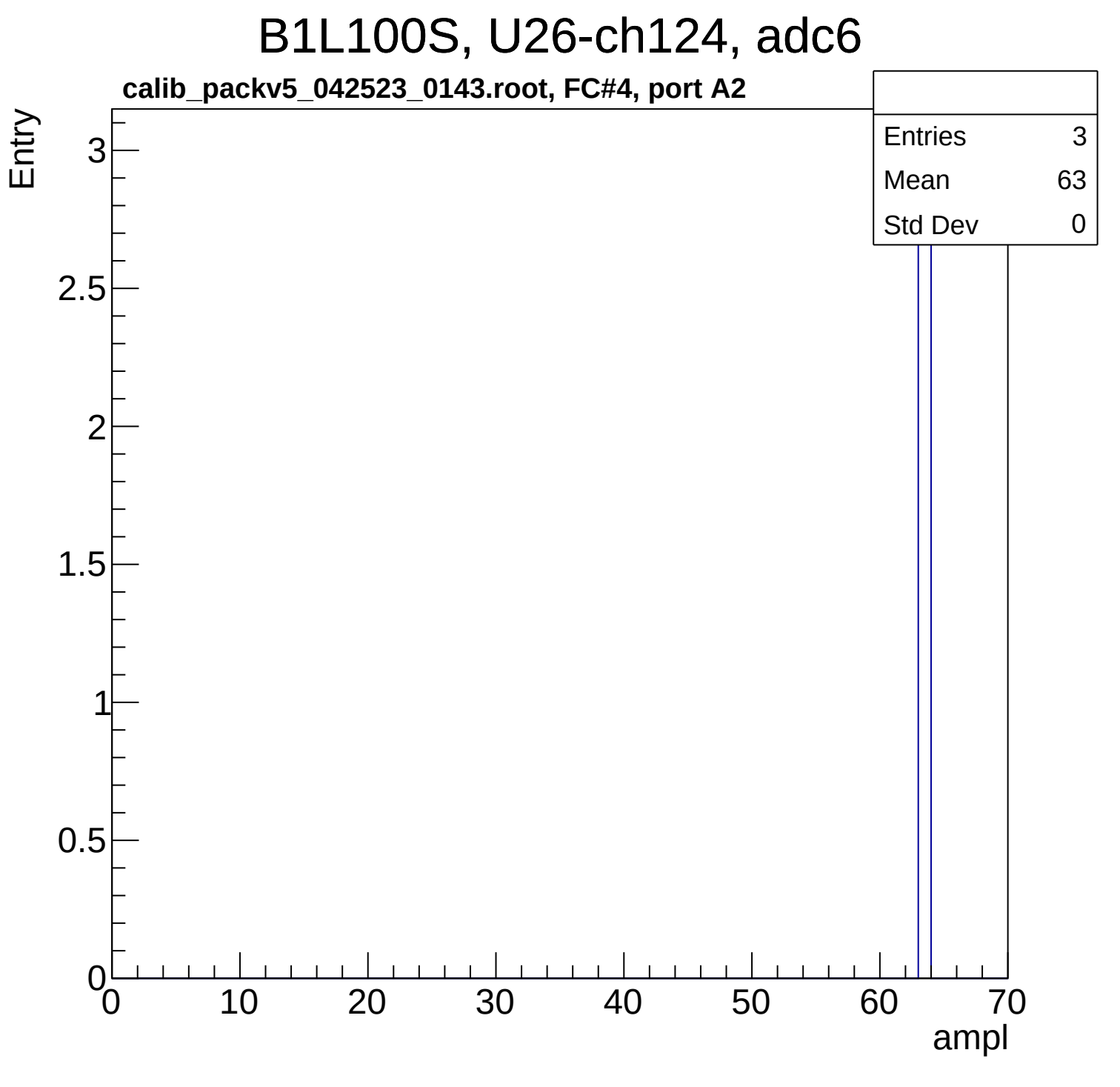
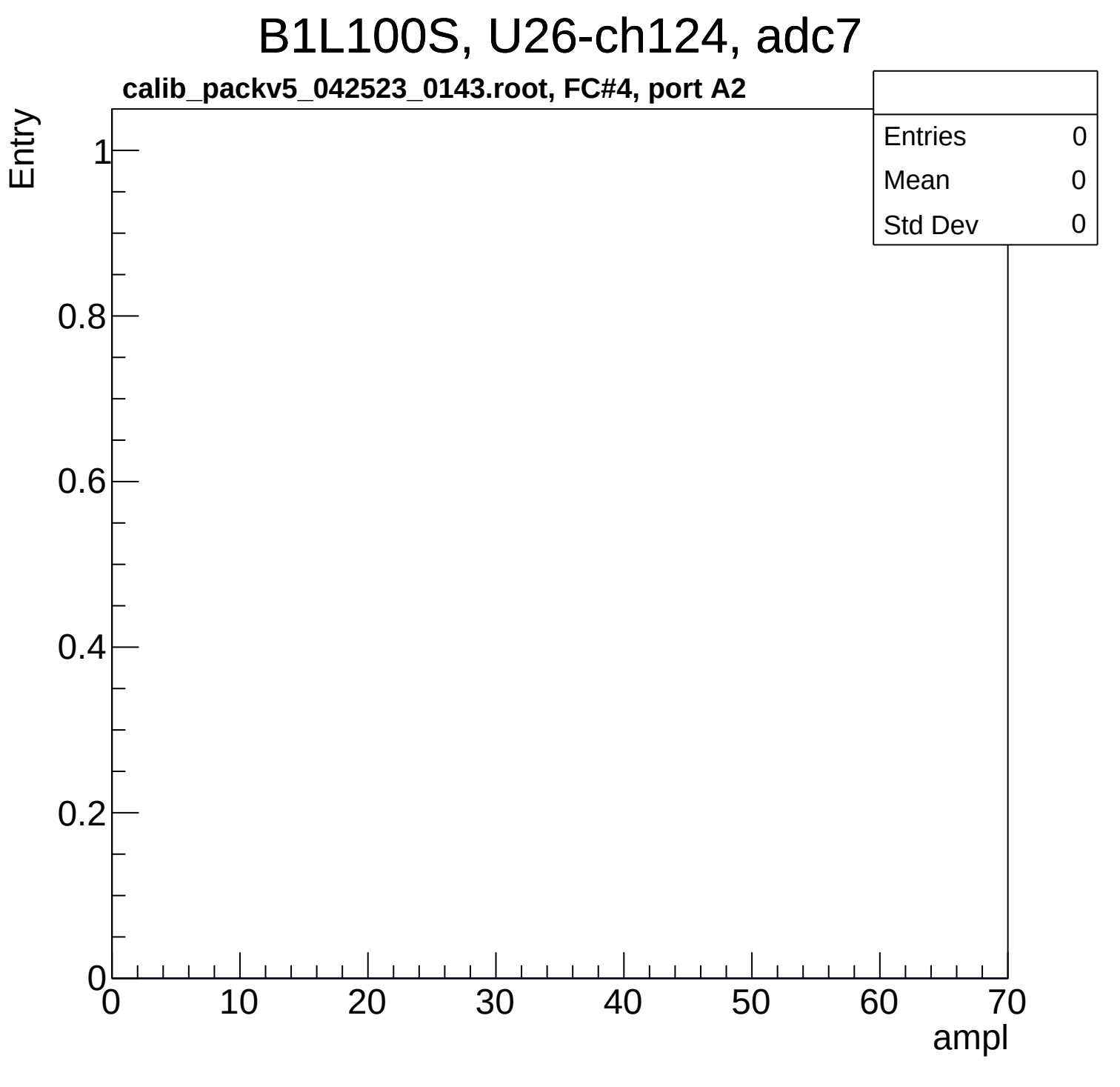
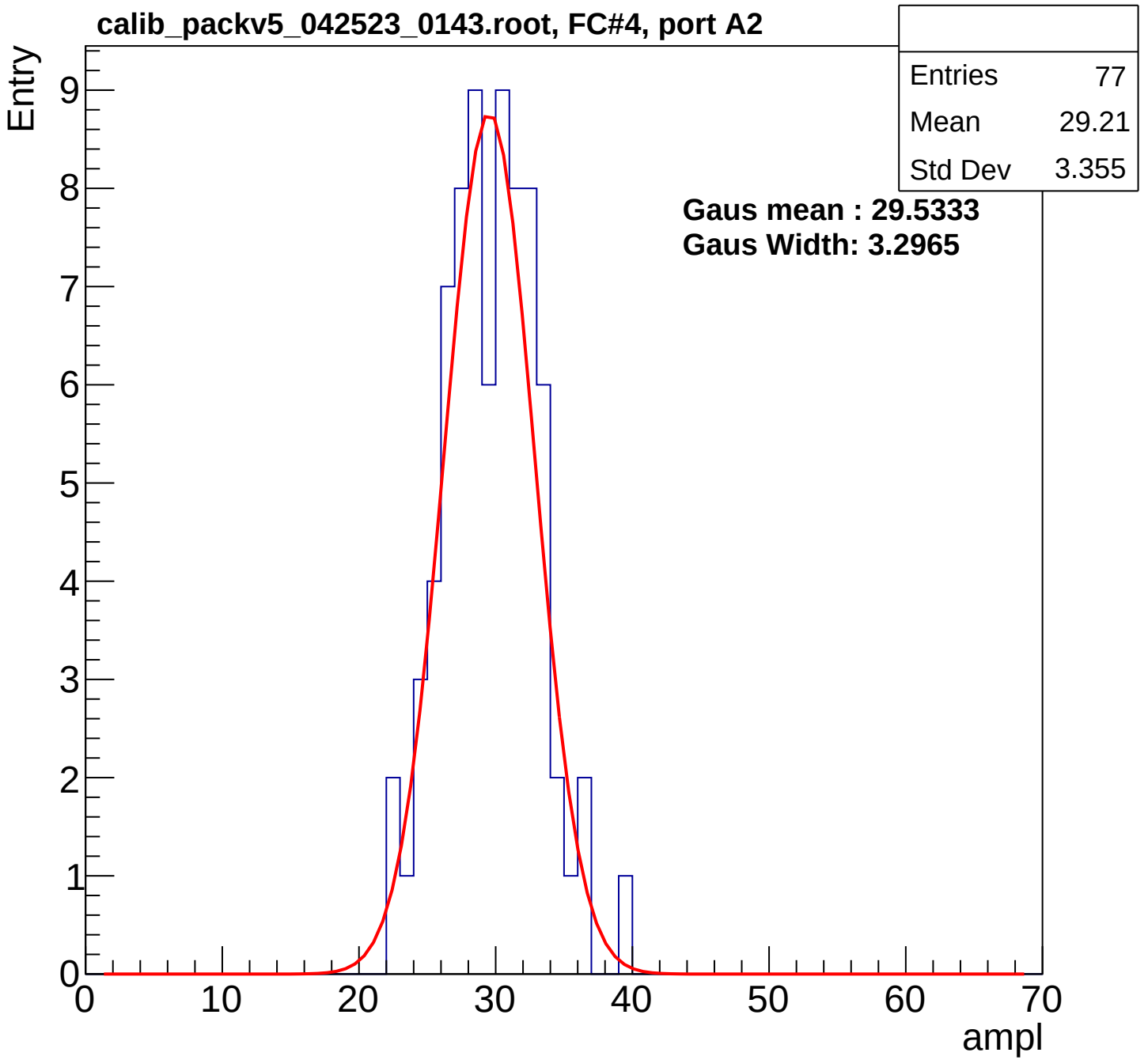
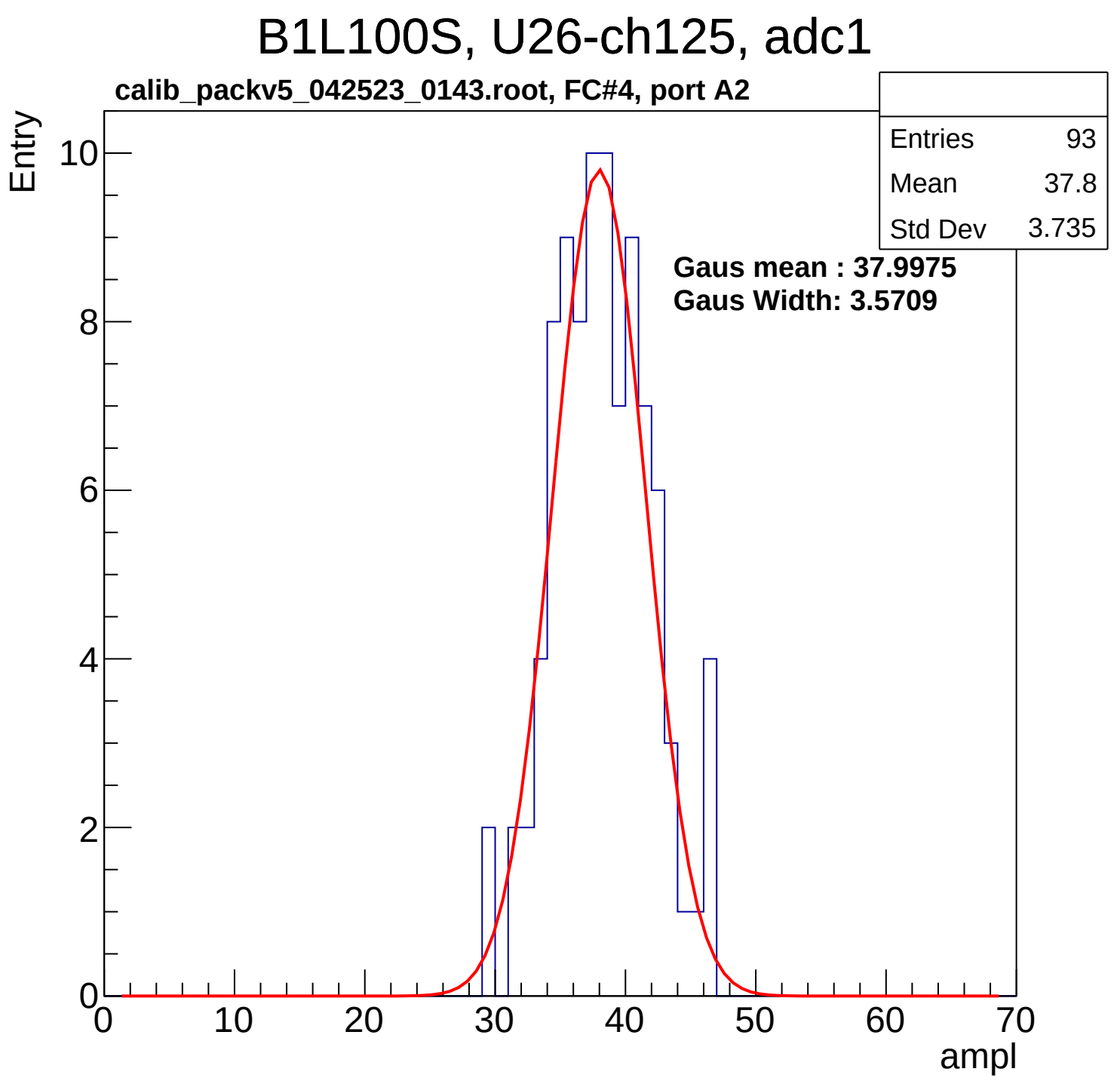
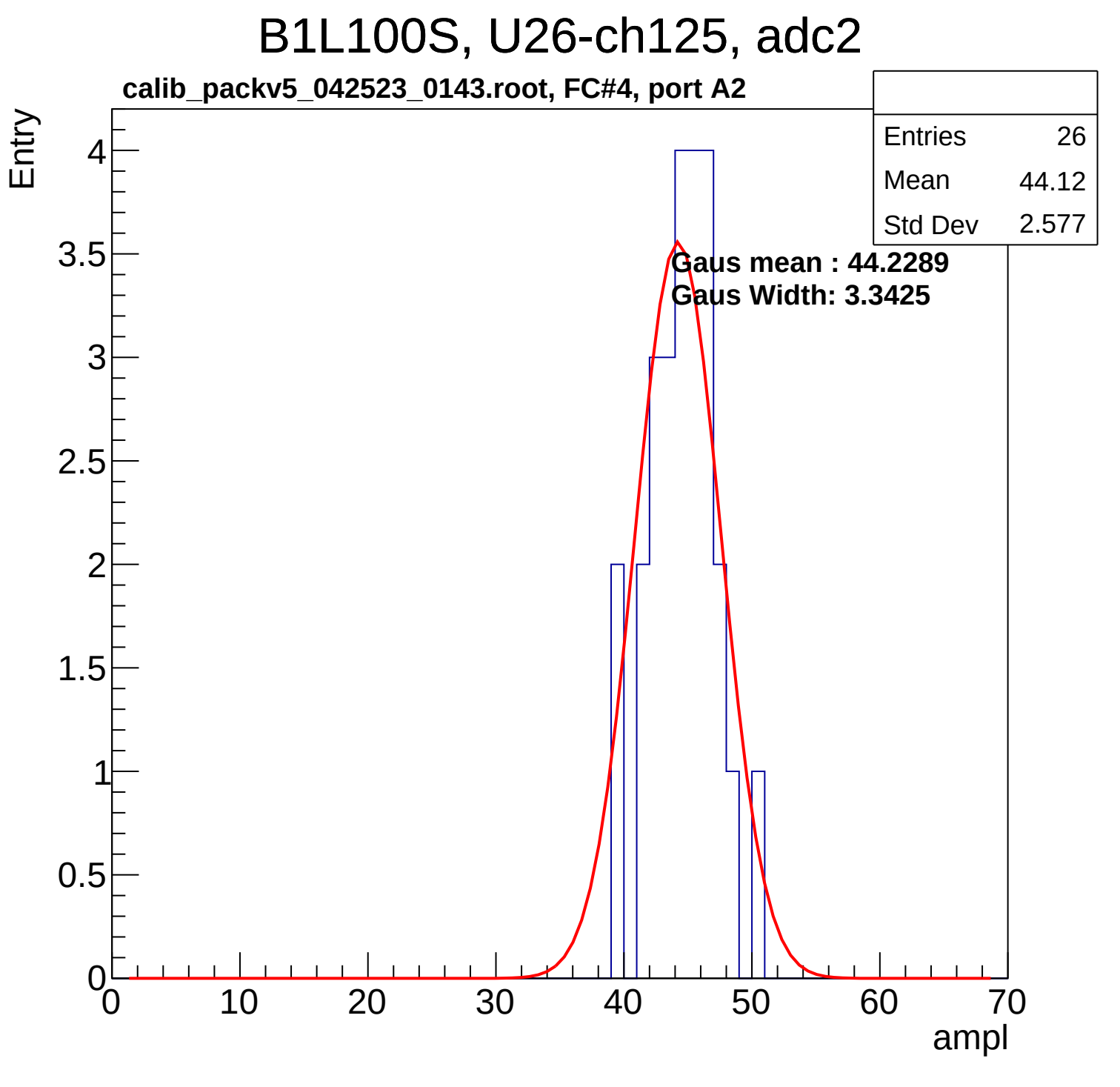

B1L100S, U26-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.21
Std Dev 3.355
Gaus mean : 29.5333
Gaus Width: 3.2965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 93
Mean 37.8
Std Dev 3.735
Gaus mean : 37.9975
Gaus Width: 3.5709
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U26-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 44.12
Std Dev 2.577
Gaus mean : 44.2289
Gaus Width: 3.3425
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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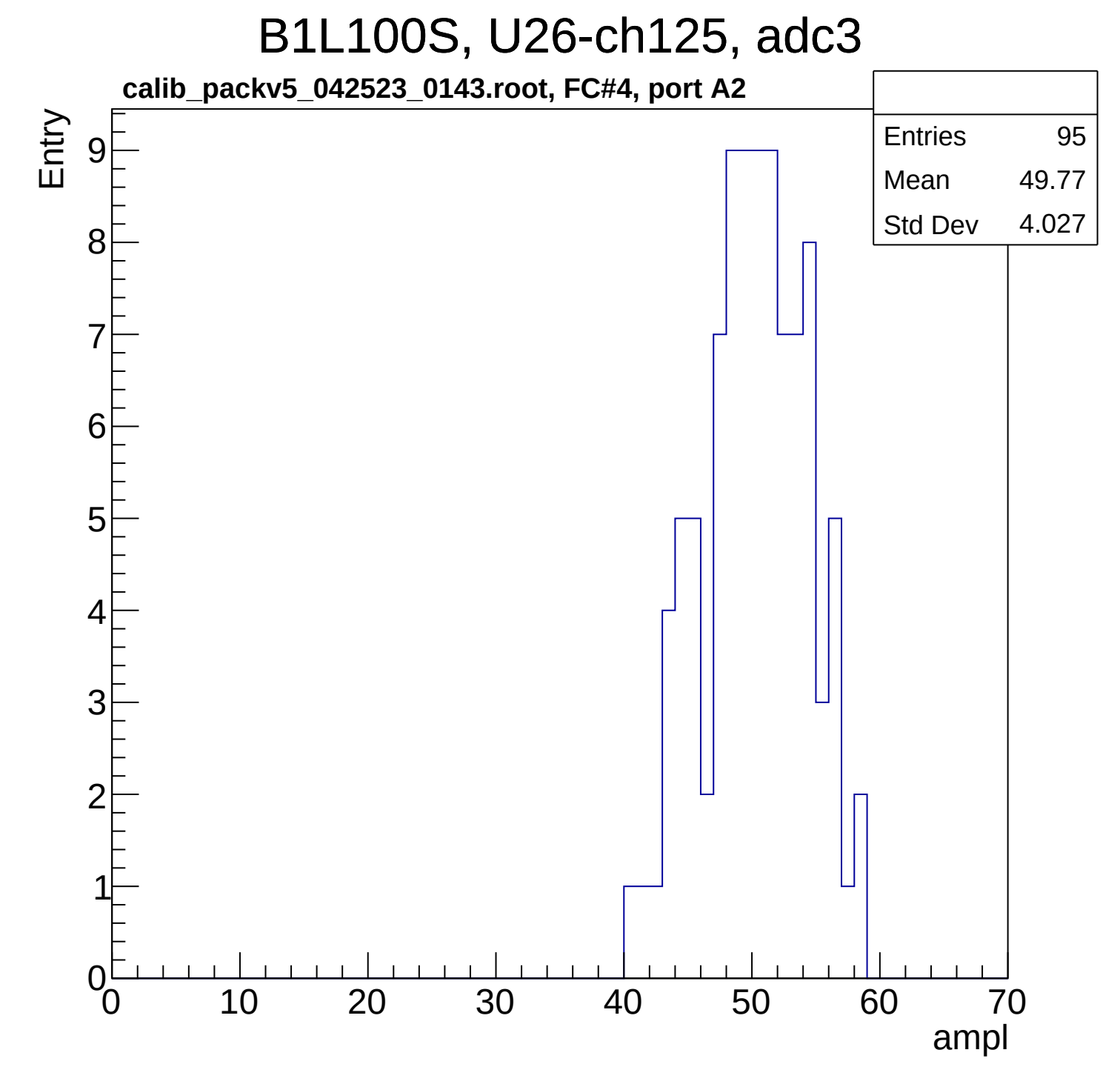
B1L100S, U26-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 95
Mean 49.77
Std Dev 4.027
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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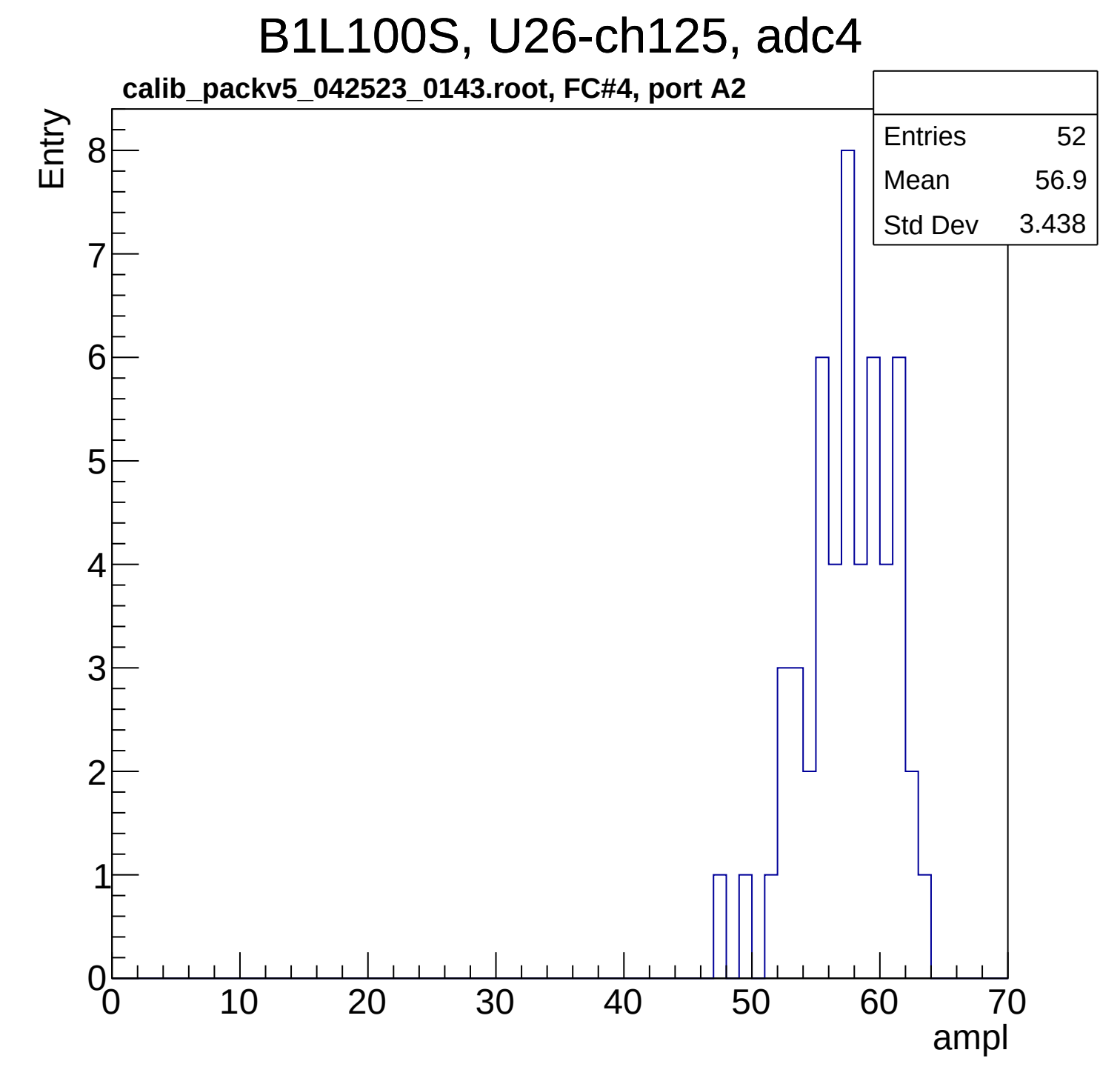
B1L100S, U26-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.9
Std Dev 3.438
Entry
ampl
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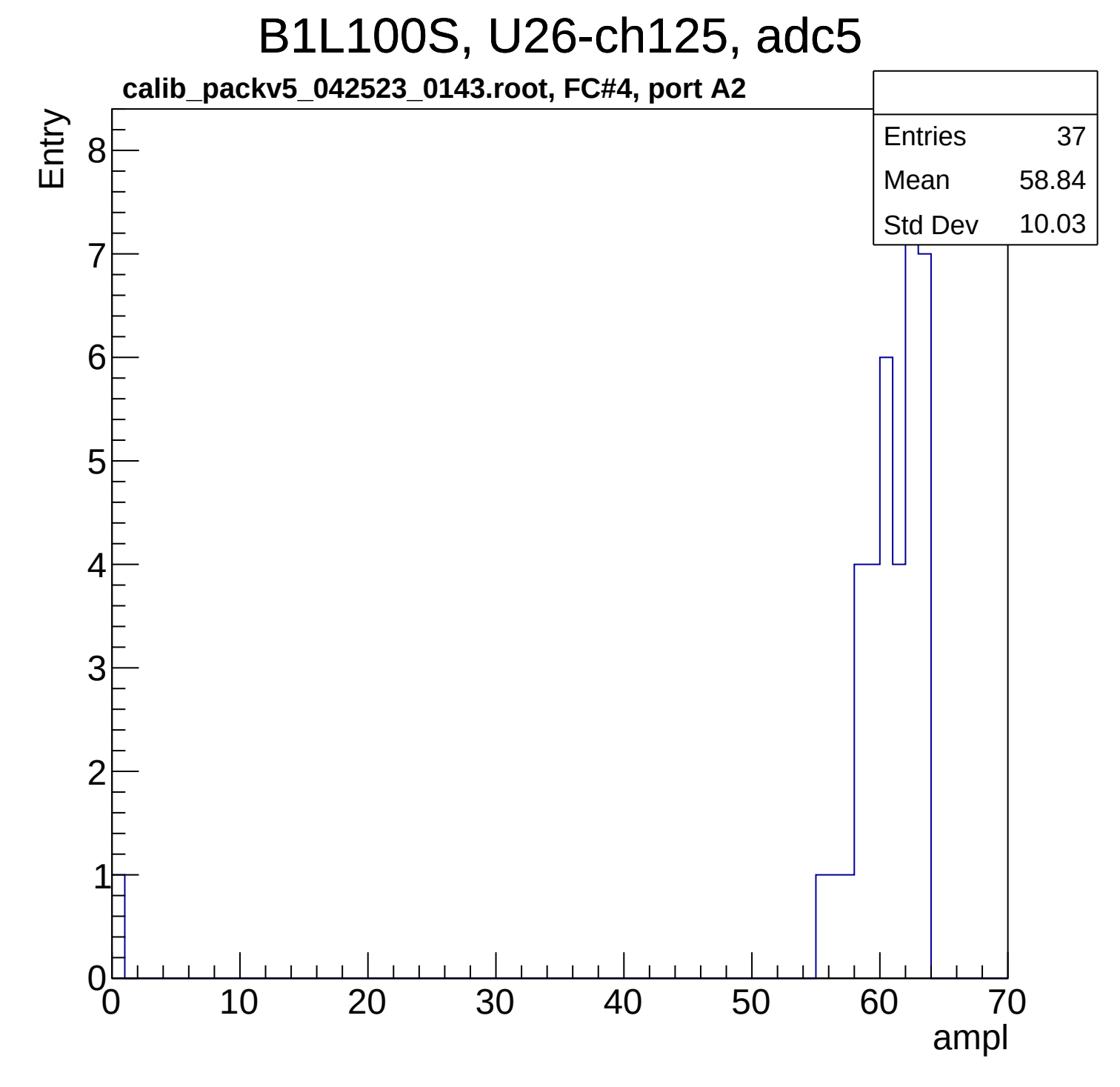
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.84
Std Dev 10.03
Entry
ampl
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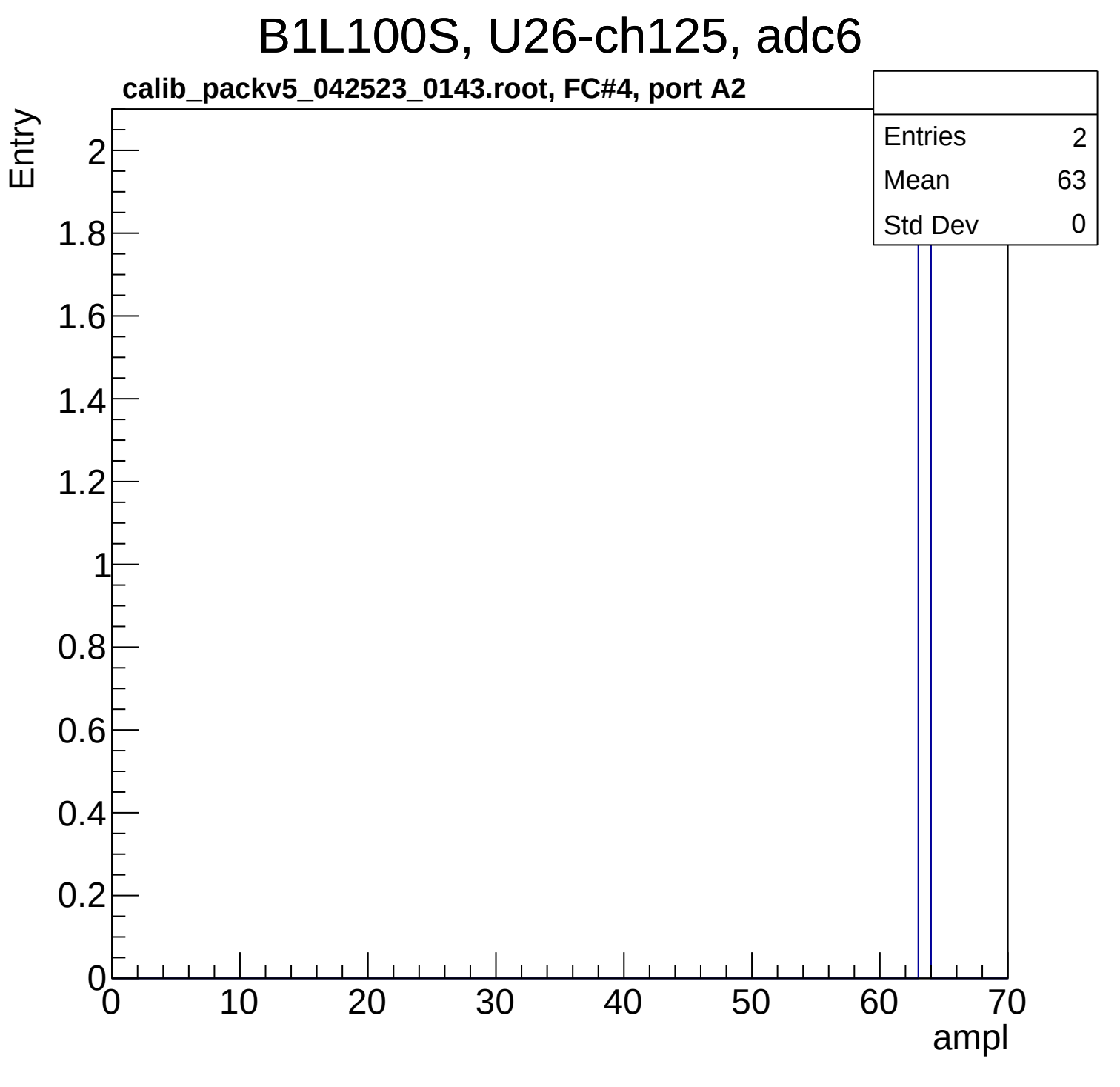
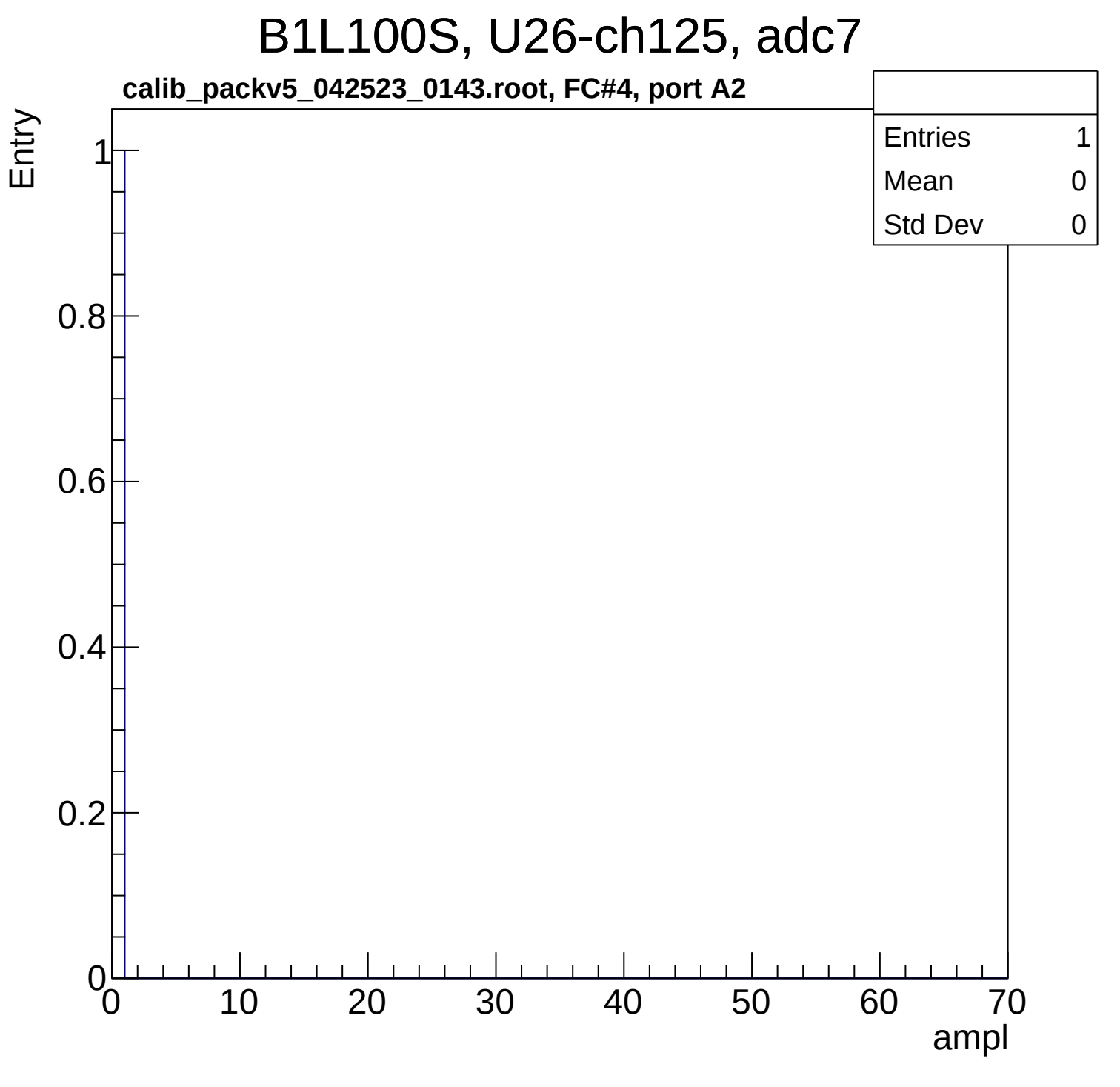
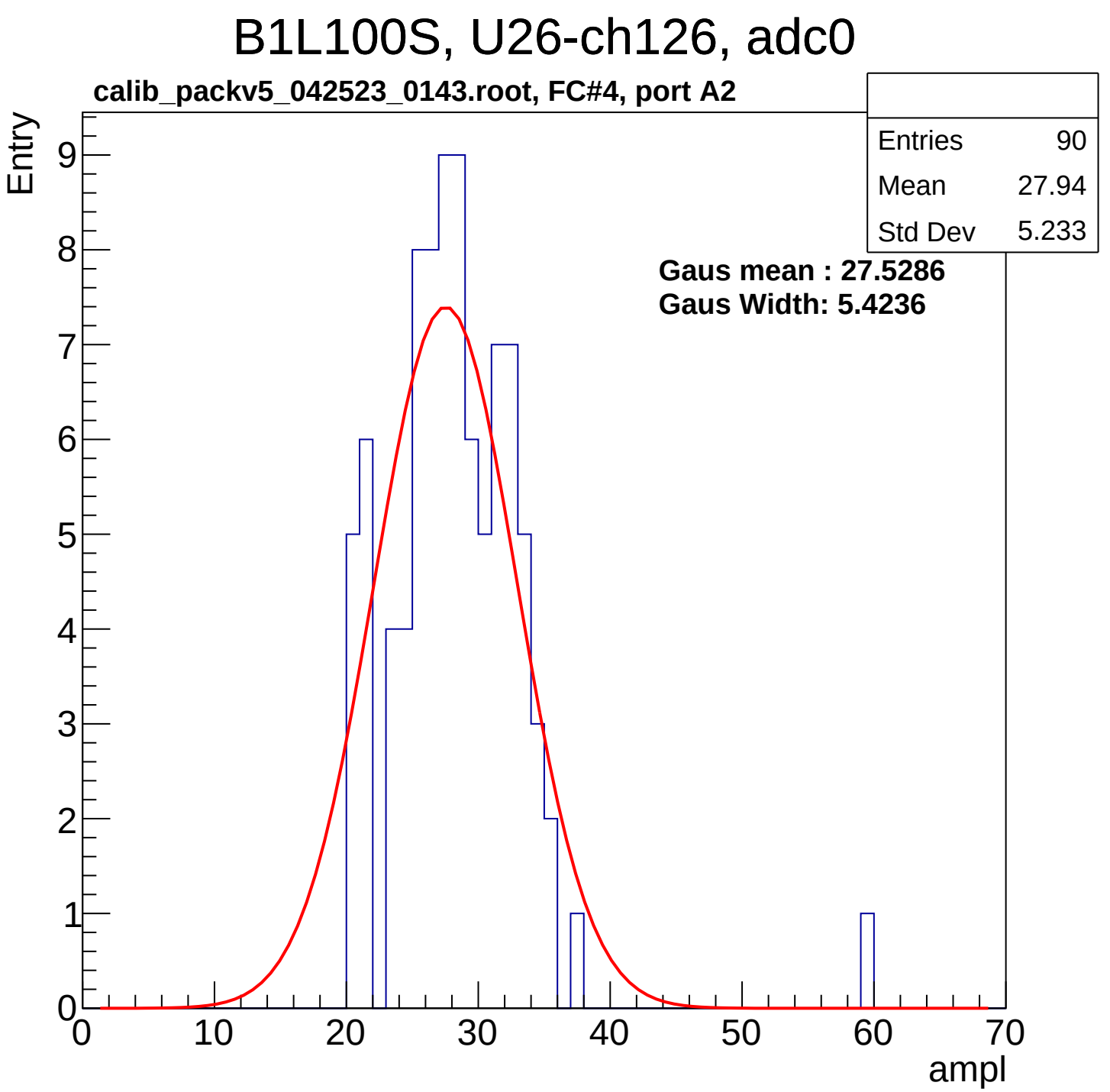
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U26-ch125, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 27.94
Std Dev 5.233
Gaus mean : 27.5286
Gaus Width: 5.4236
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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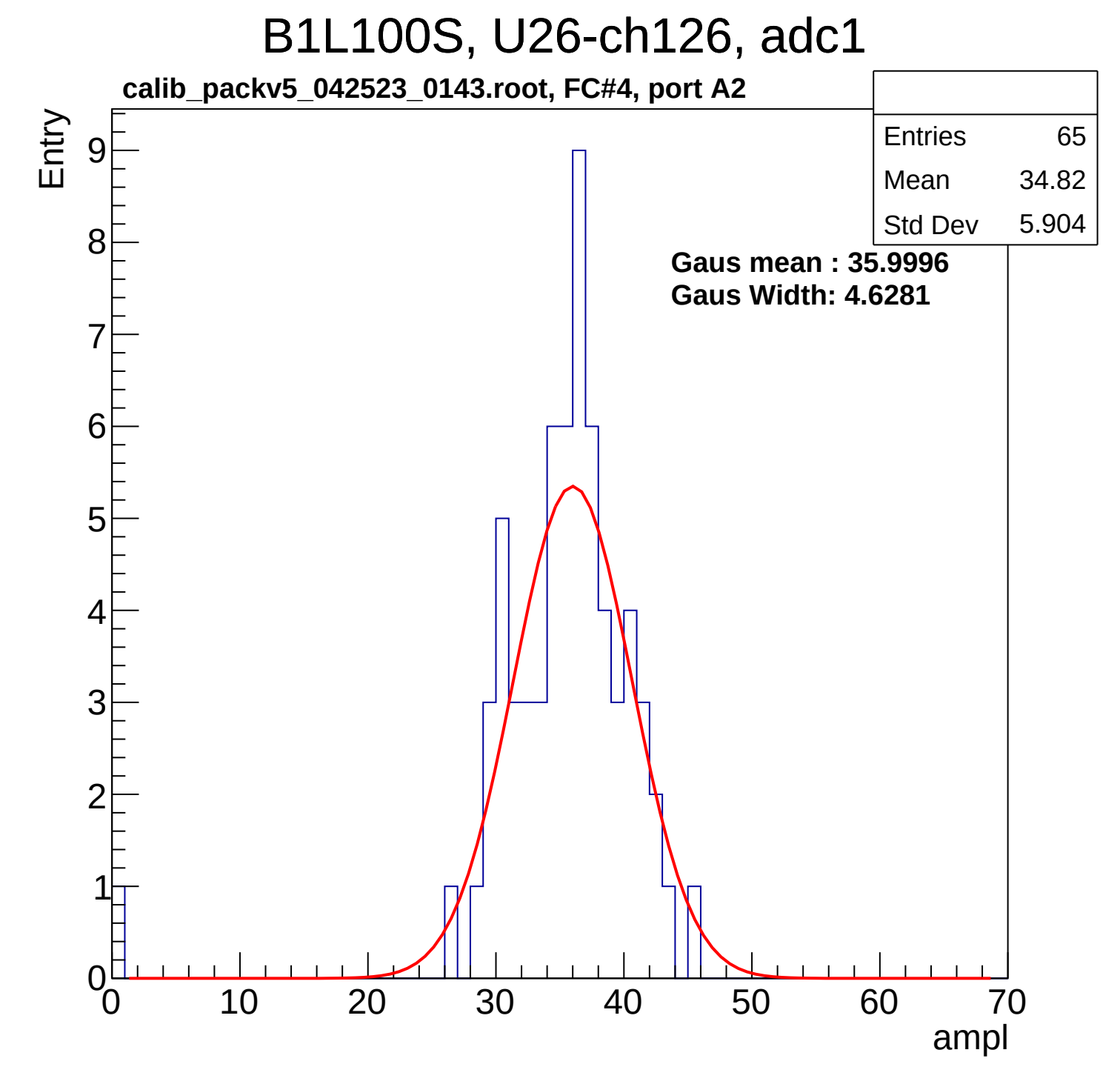
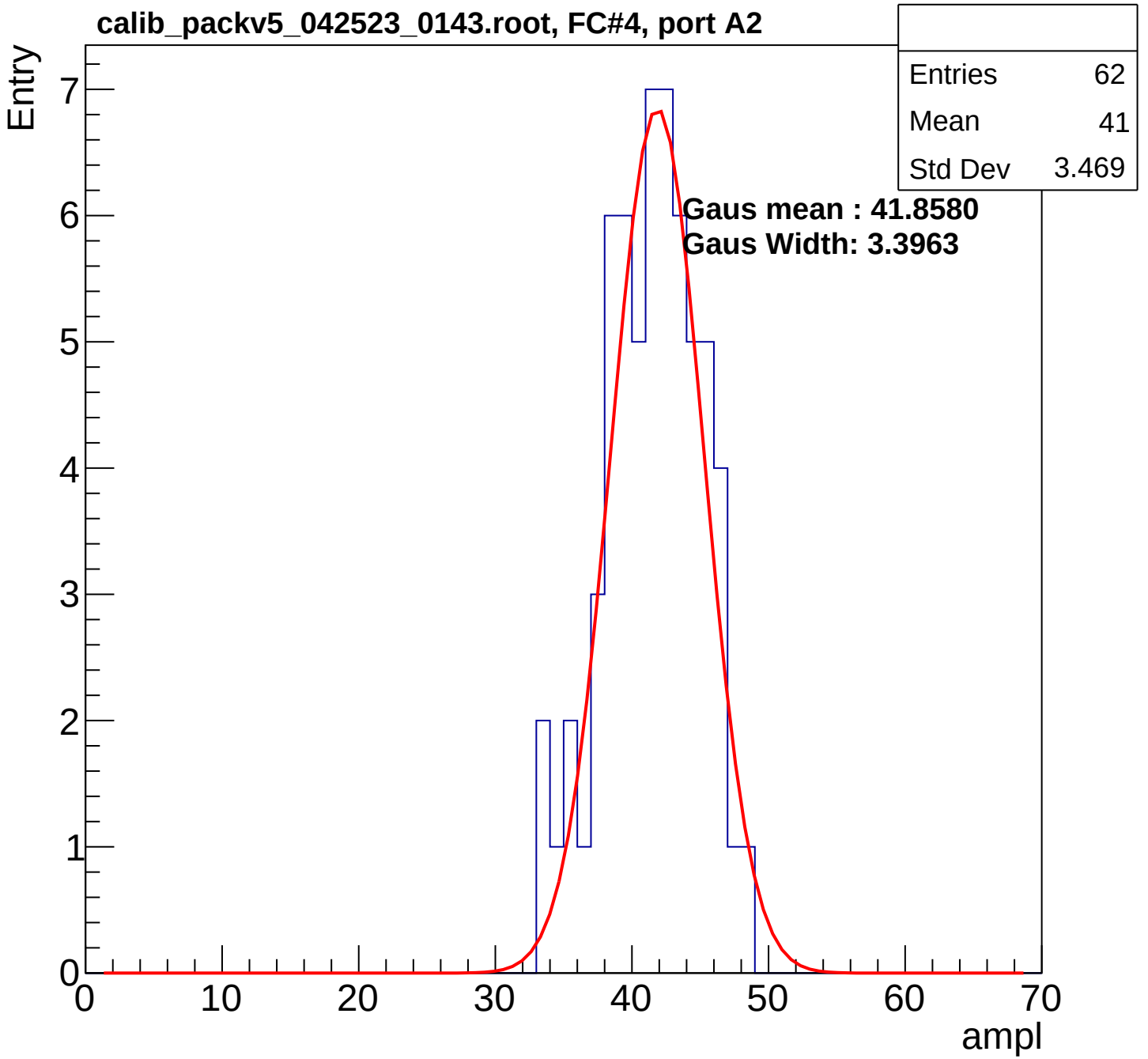
40
50
60
70


B1L100S, U26-ch126, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 34.82
Std Dev 5.904
Gaus mean : 35.9996
Gaus Width: 4.6281
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 41
Std Dev 3.469
Gaus mean : 41.8580
Gaus Width: 3.3963
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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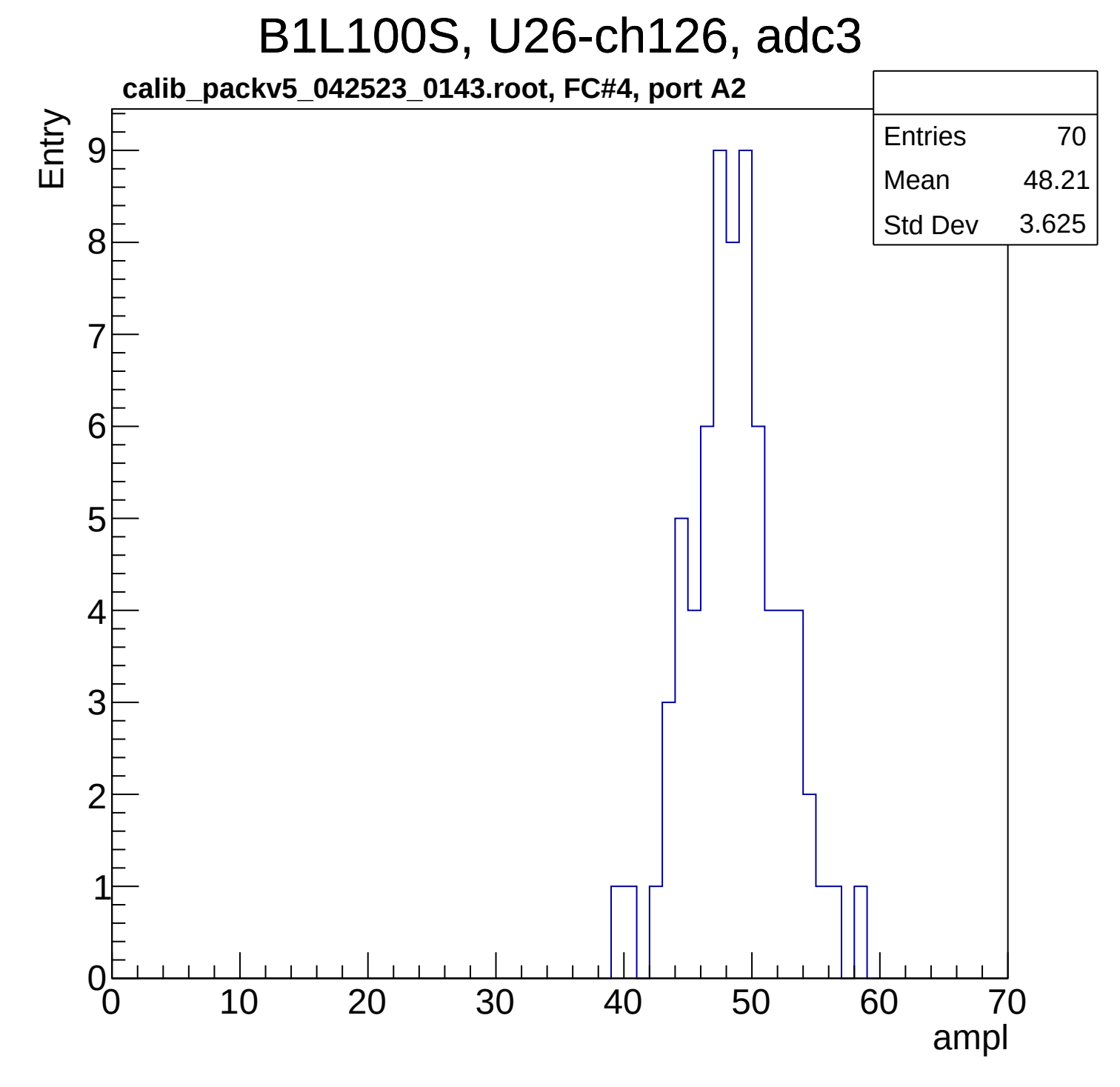
B1L100S, U26-ch126, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 48.21
Std Dev 3.625
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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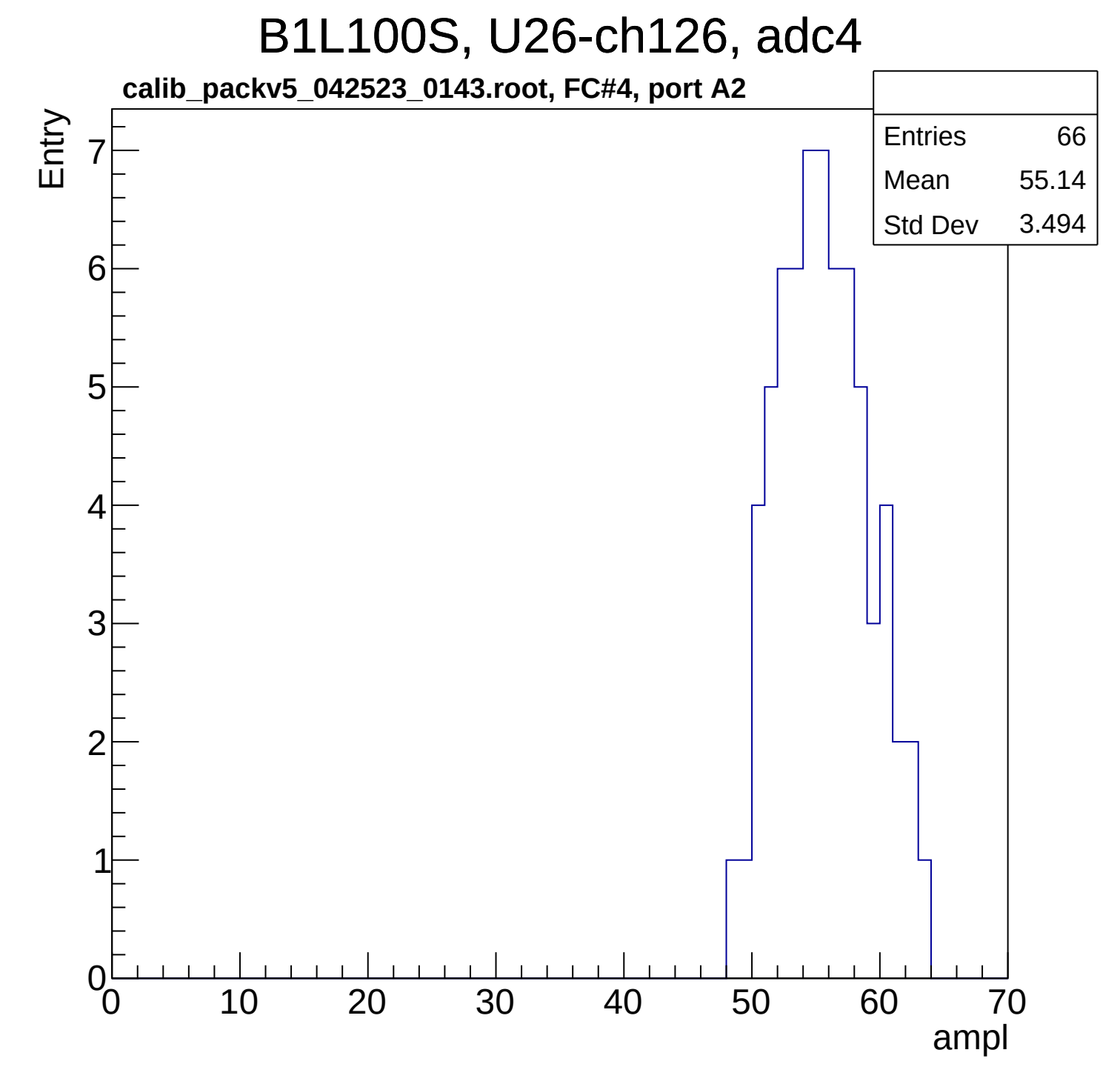
B1L100S, U26-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.14
Std Dev 3.494
Entry
ampl
0
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4
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6
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0
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20
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40
50
60
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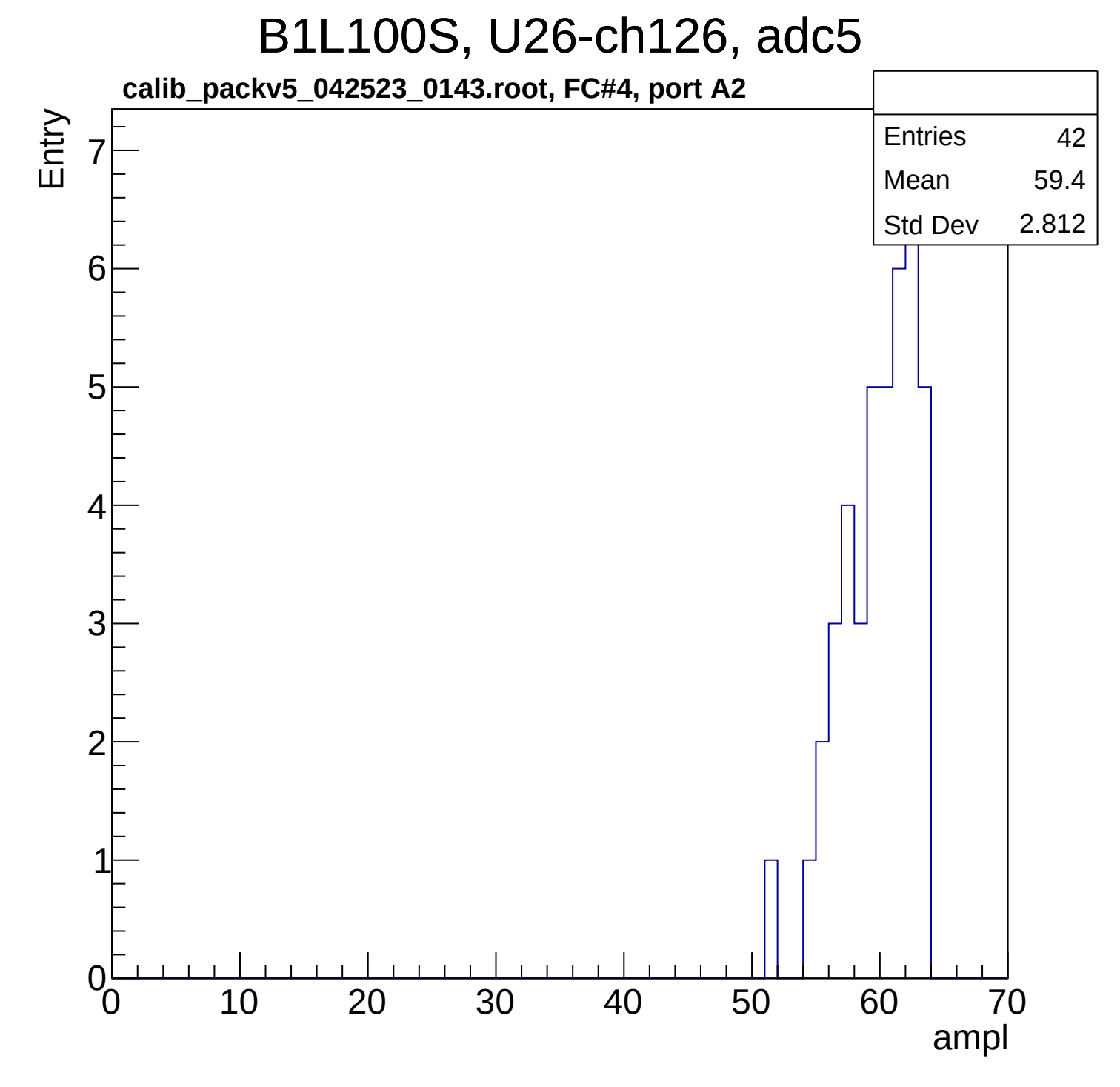
B1L100S, U26-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.4
Std Dev 2.812
Entry
ampl
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2
3
4
5
6
7
0
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40
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60
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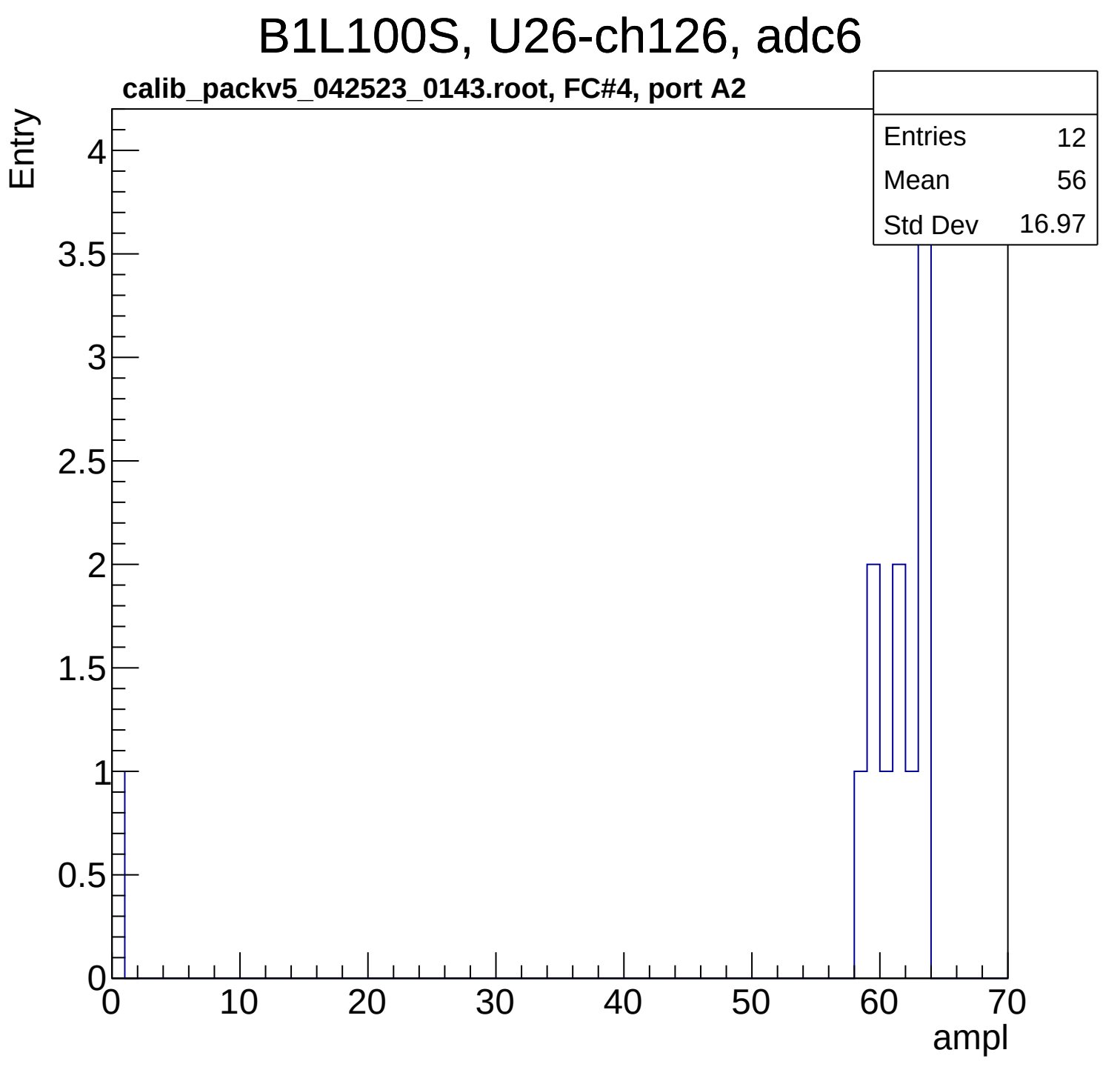
B1L100S, U26-ch126, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56
Std Dev 16.97
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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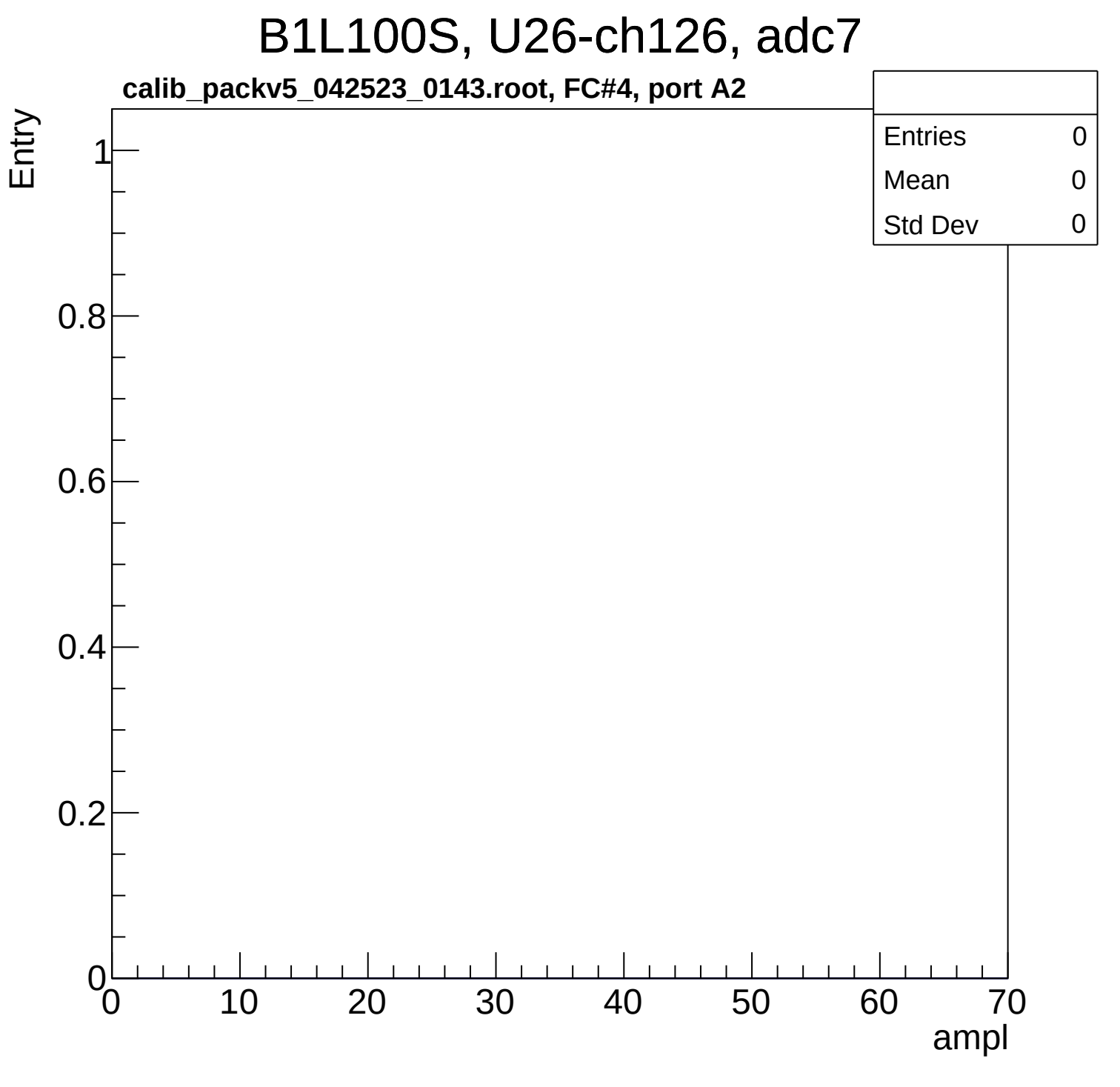
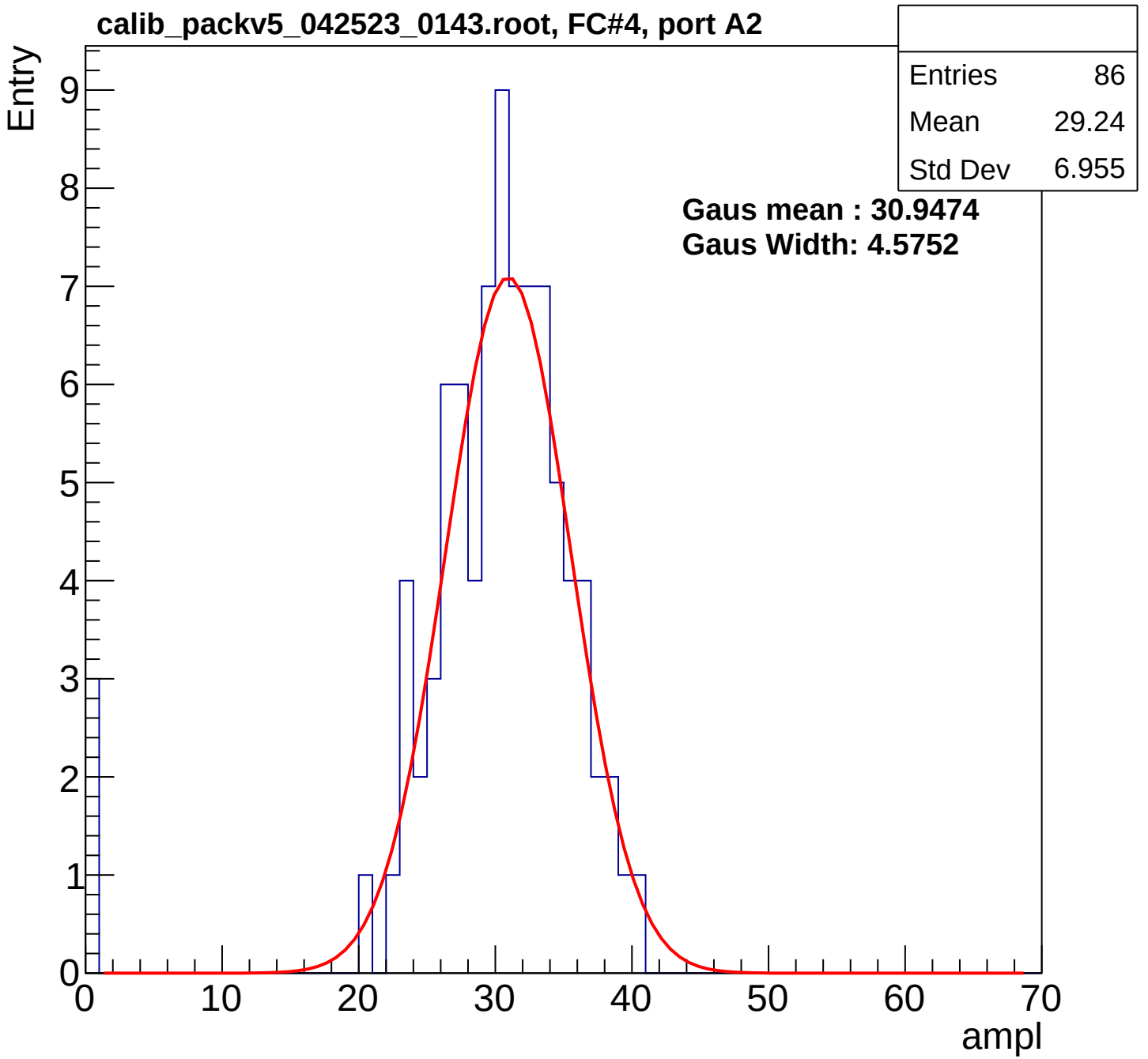
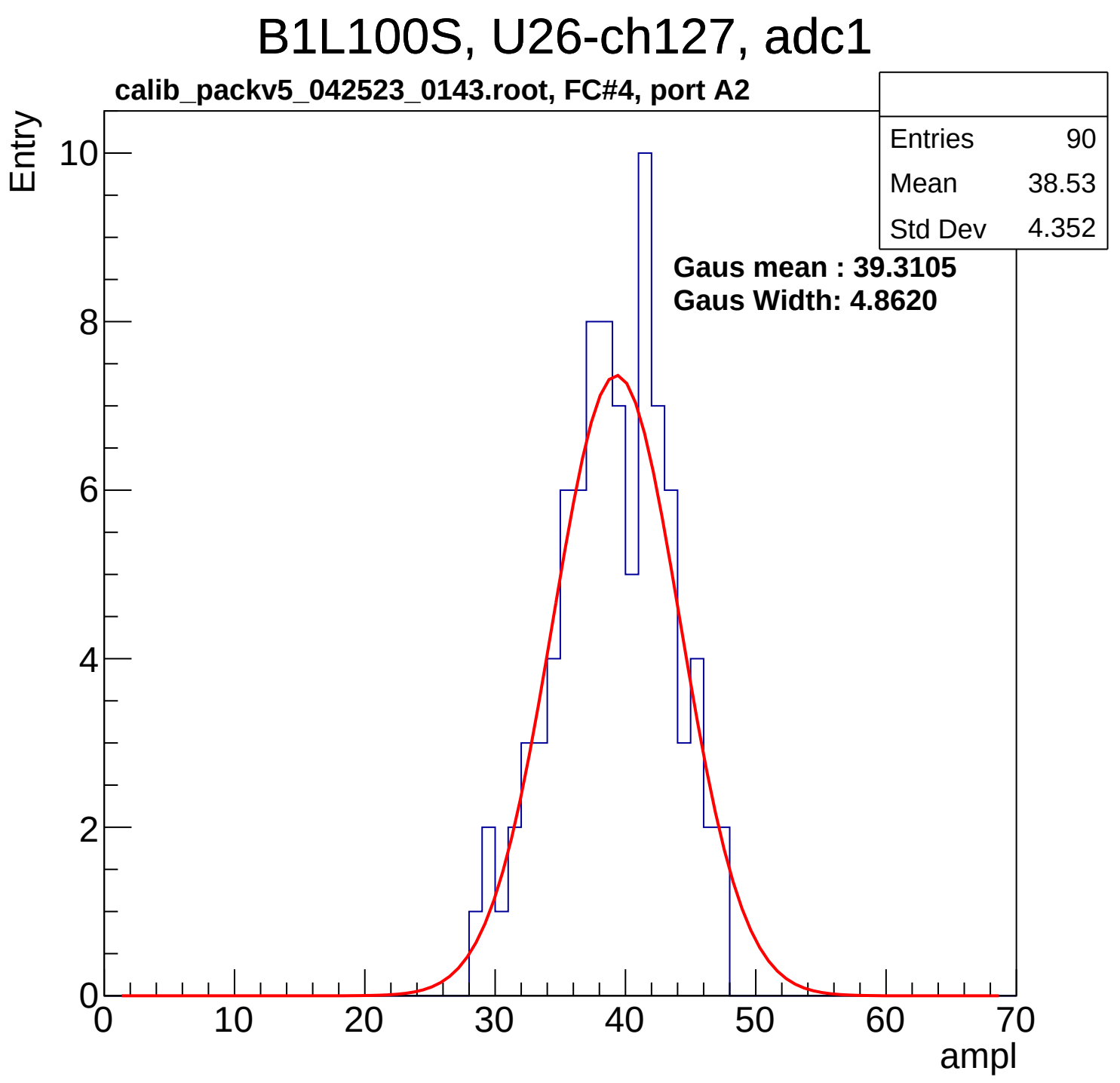

B1L100S, U26-ch126, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 29.24
Std Dev 6.955
Gaus mean : 30.9474
Gaus Width: 4.5752
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U26-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 38.53
Std Dev 4.352
Gaus mean : 39.3105
Gaus Width: 4.8620
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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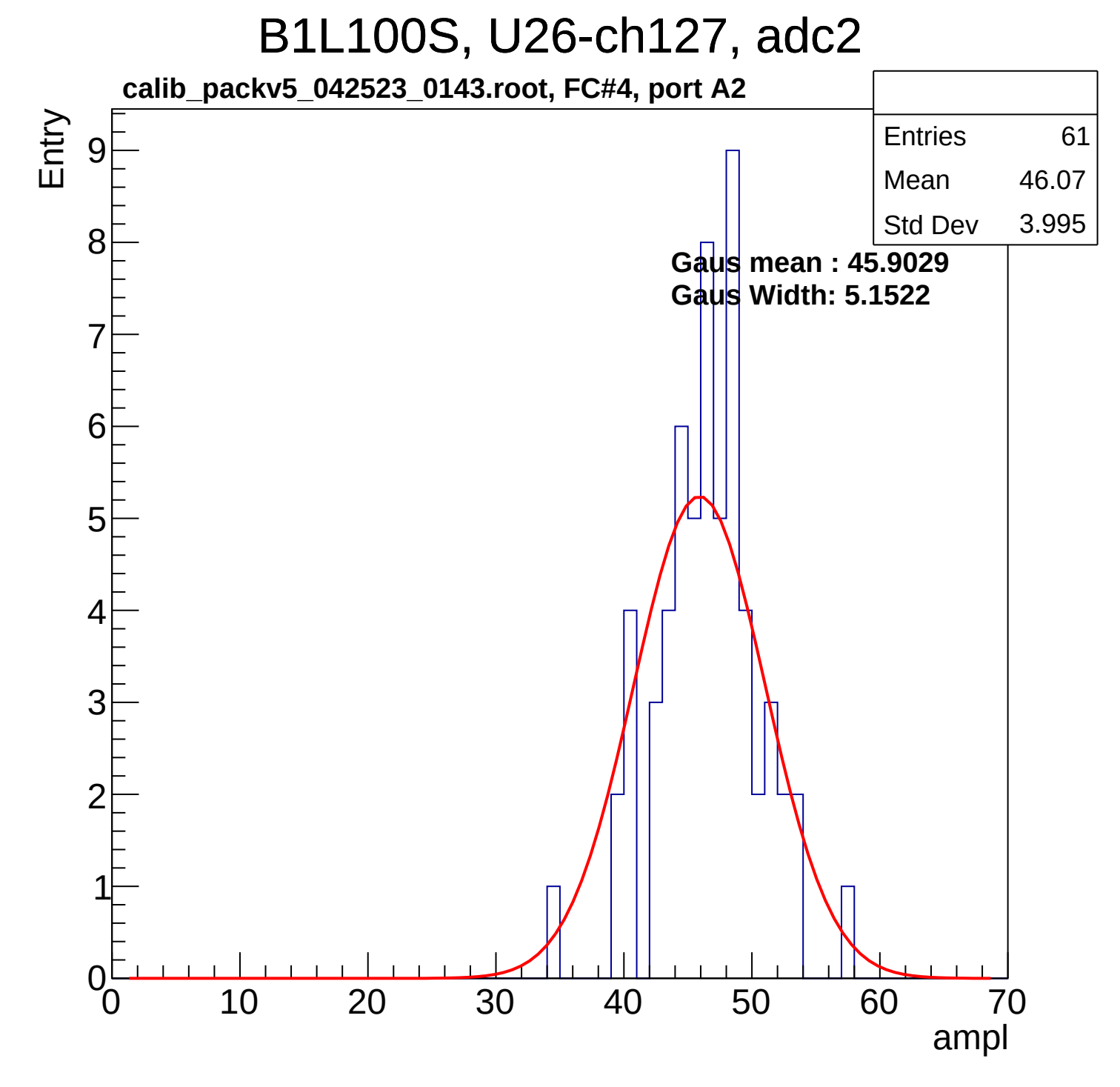
B1L100S, U26-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 46.07
Std Dev 3.995
Gaus mean : 45.9029
Gaus Width: 5.1522
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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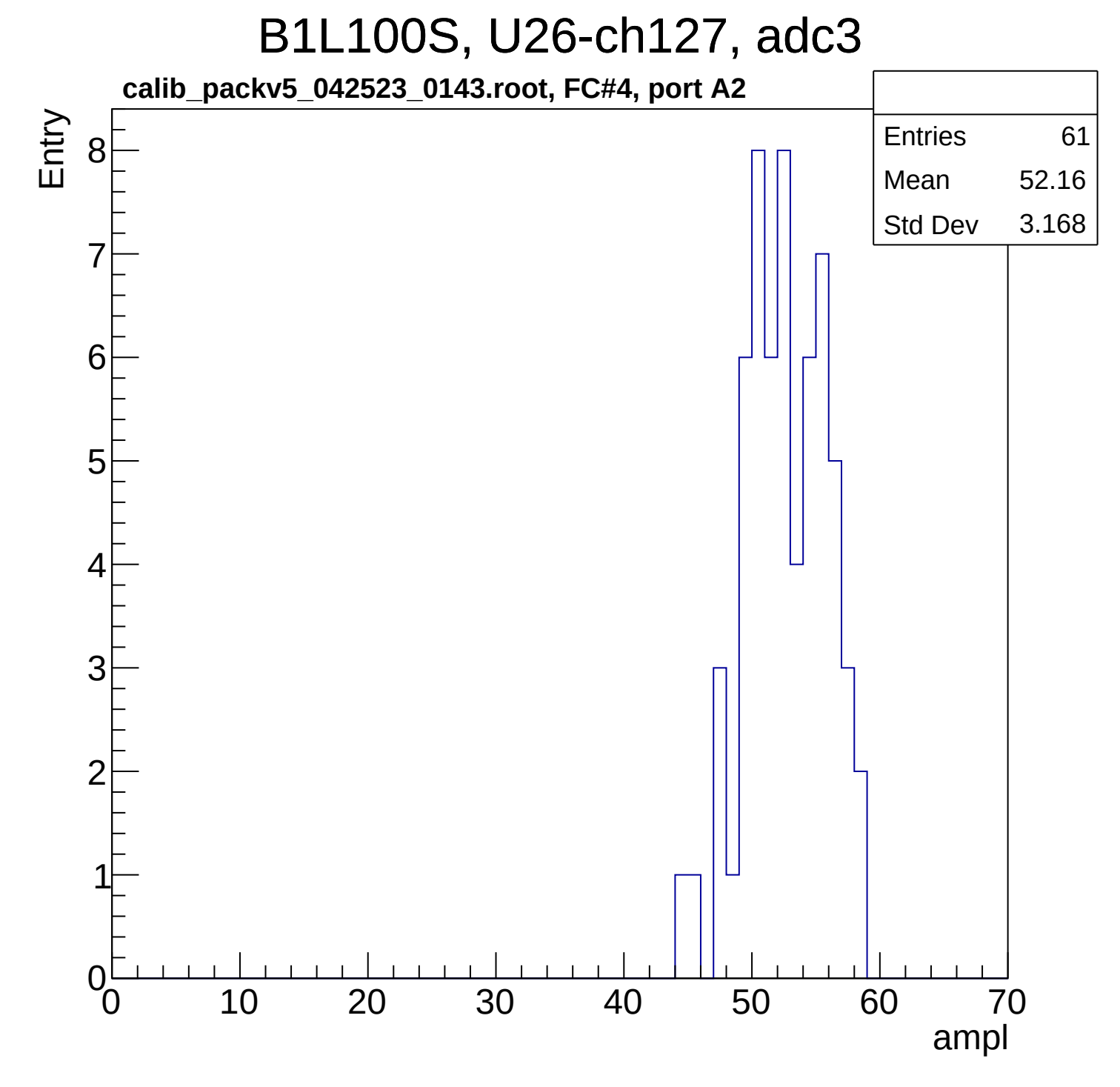
B1L100S, U26-ch127, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 52.16
Std Dev 3.168
Entry
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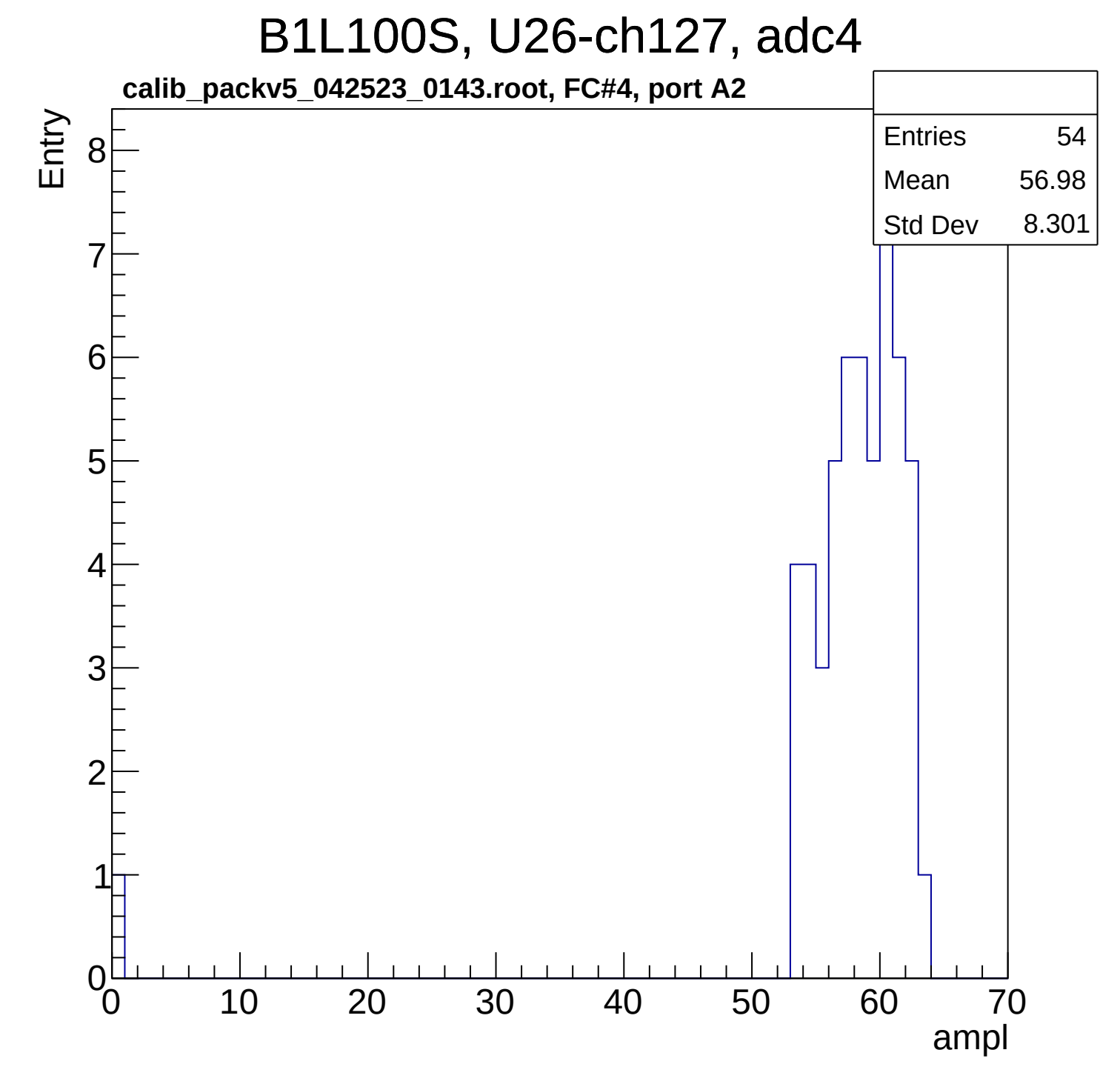
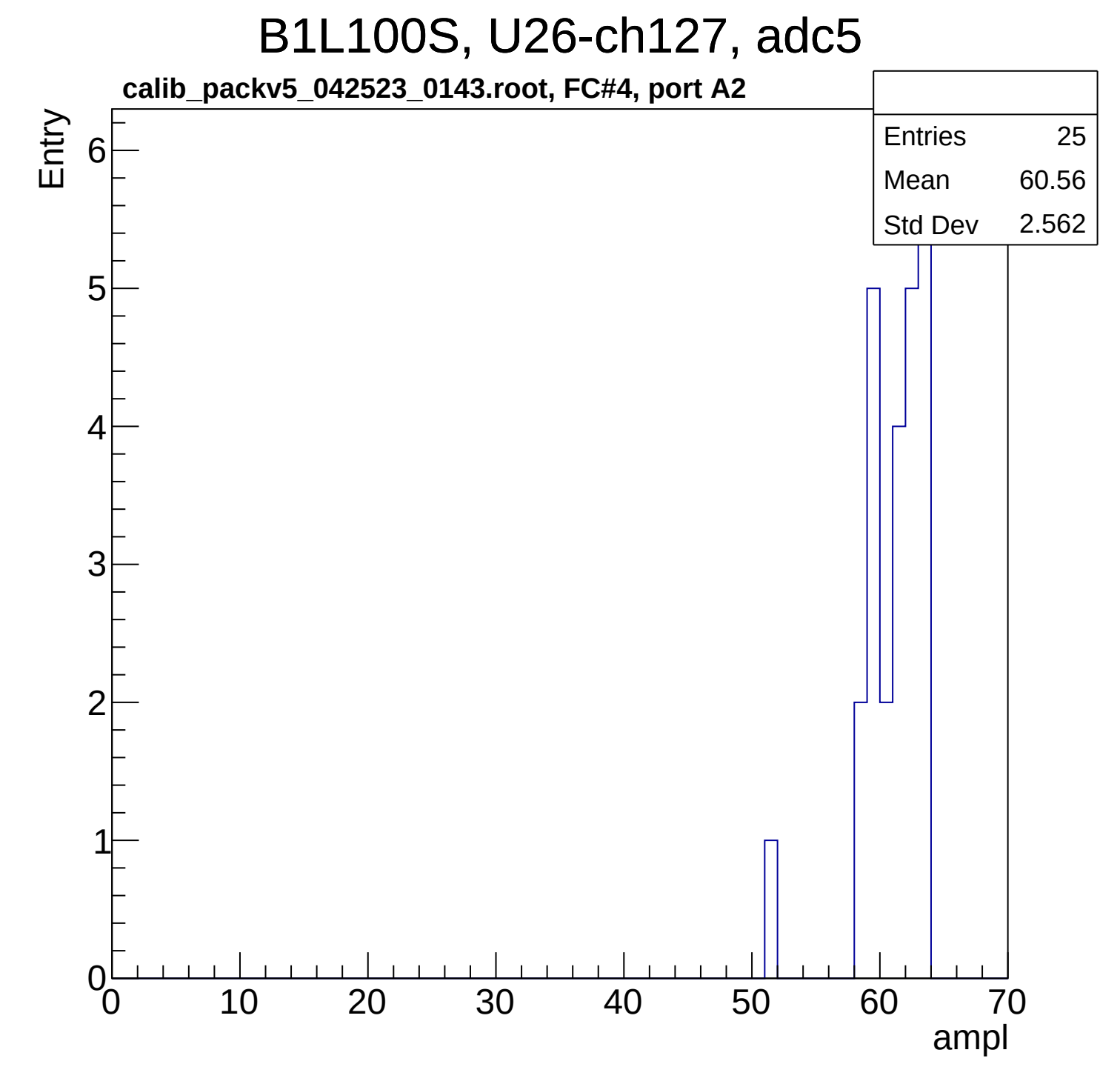
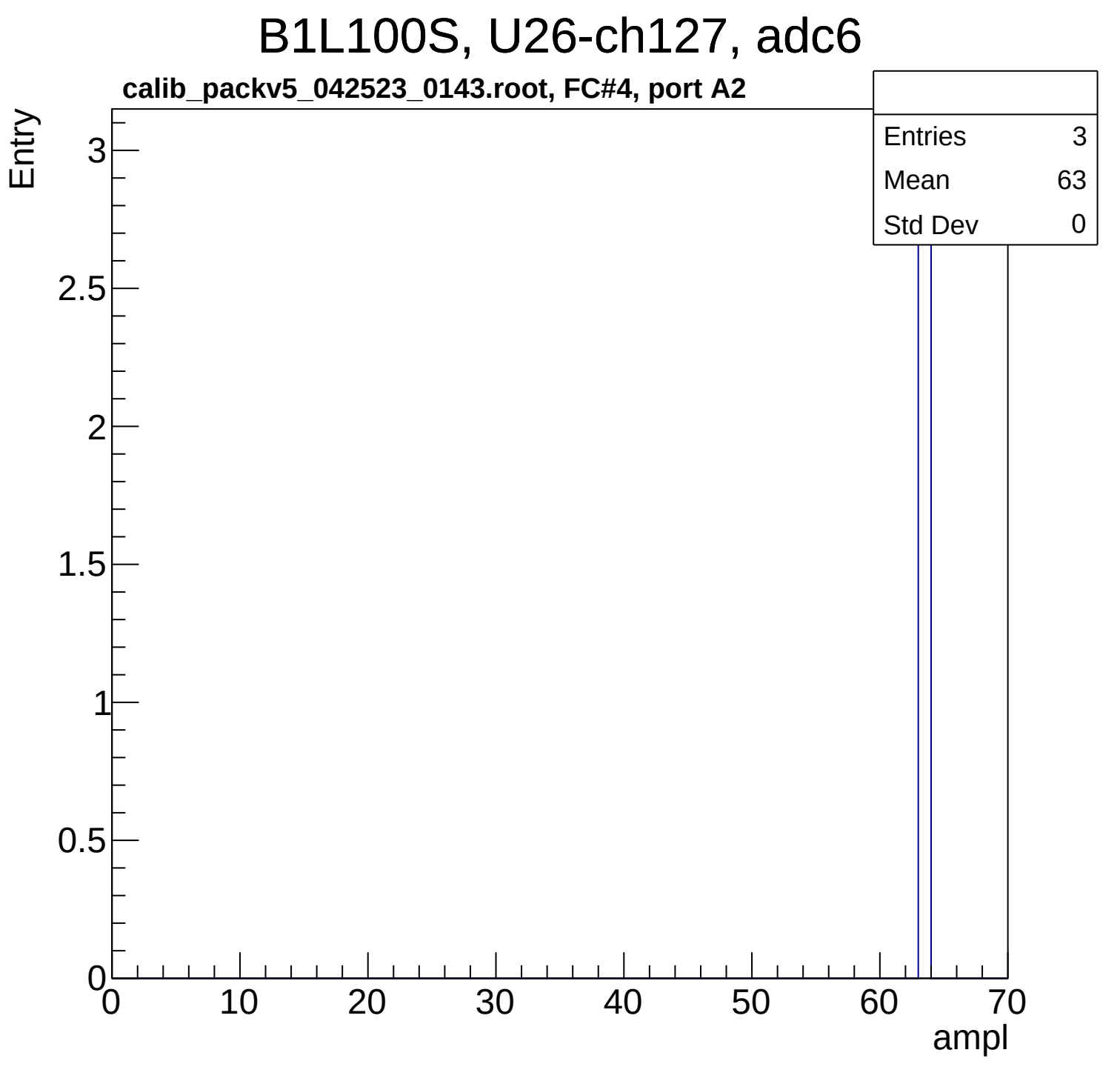
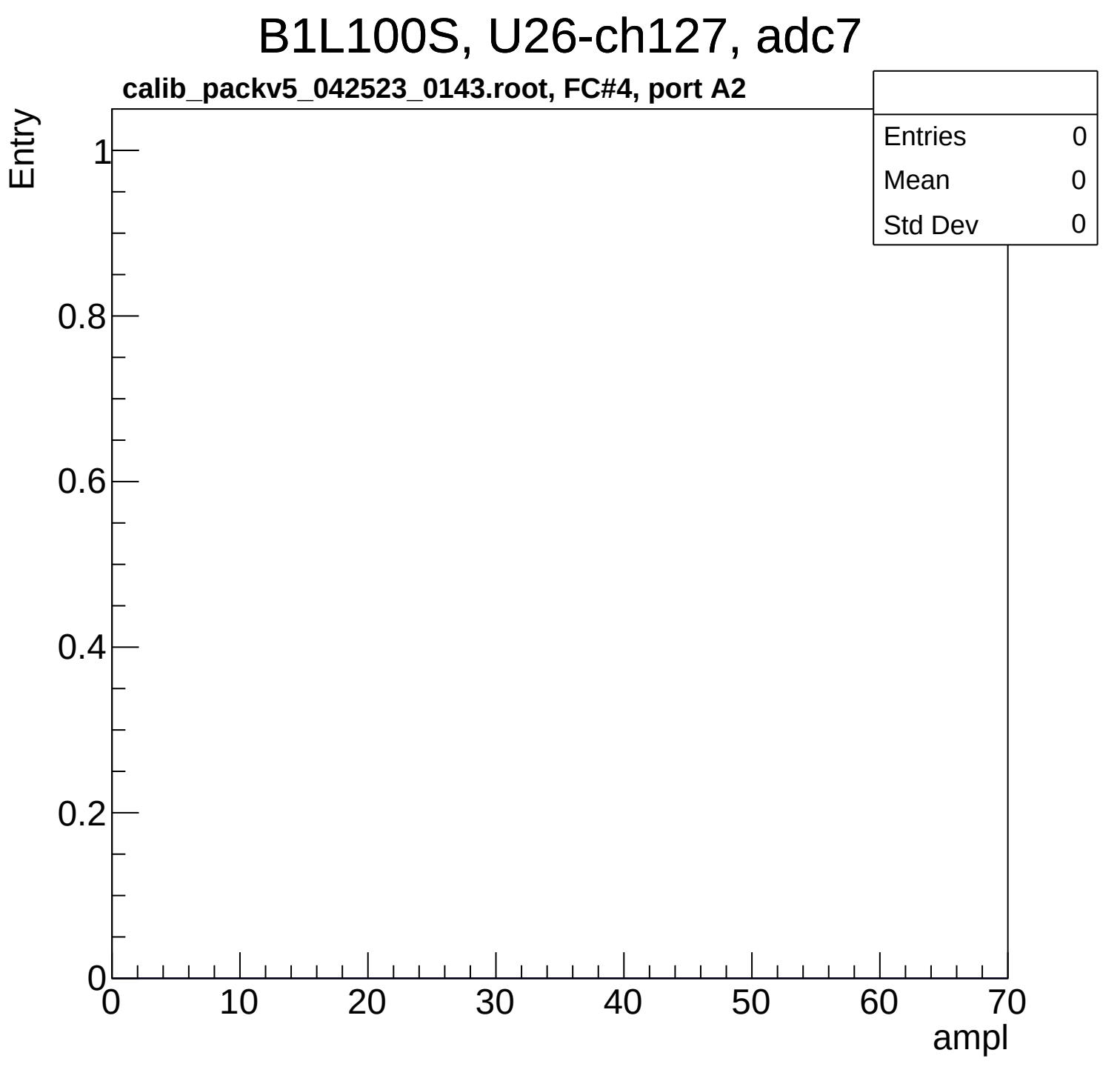
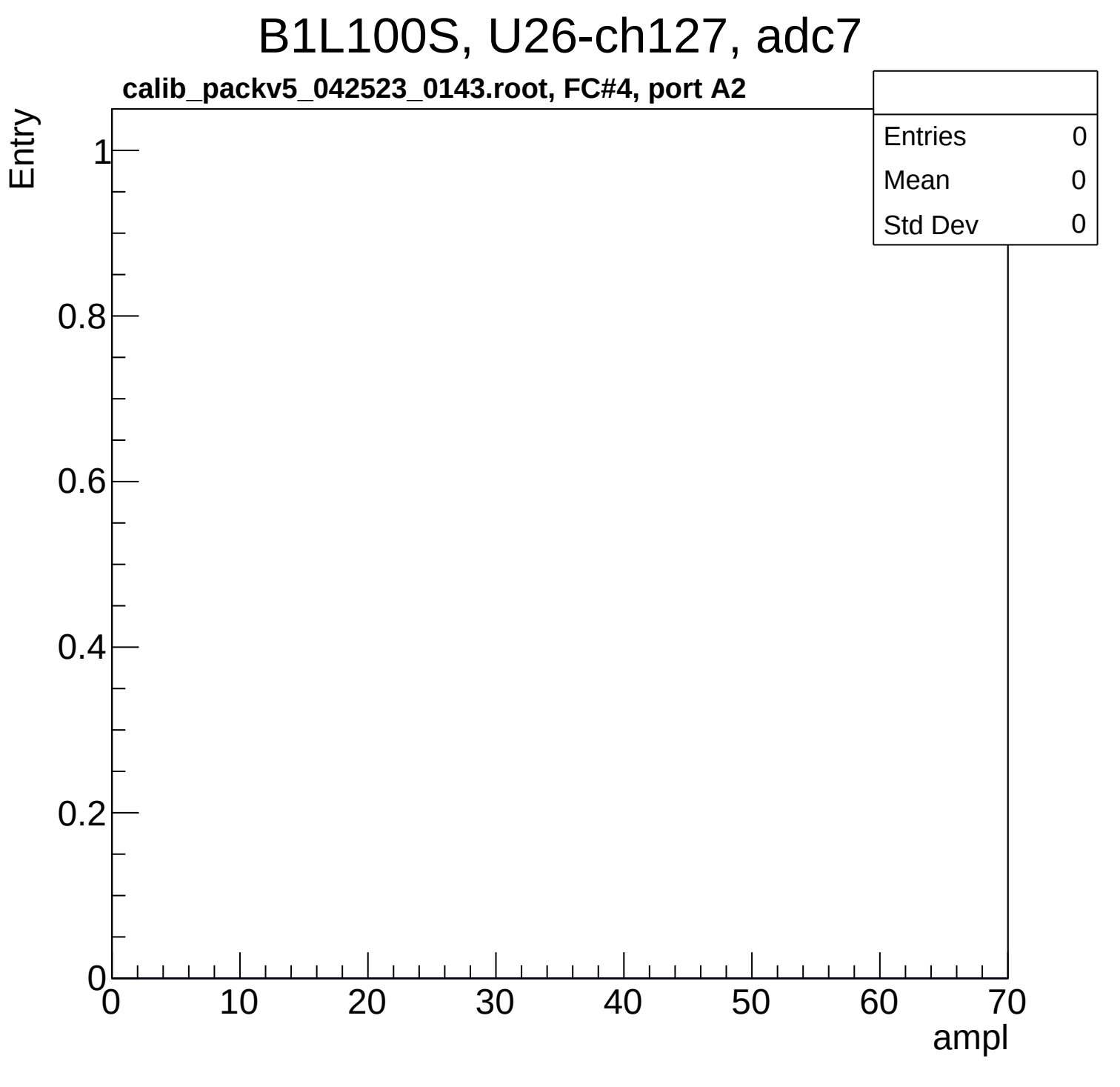
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.98
Std Dev 8.301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U26-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 25
Mean 60.56
Std Dev 2.562
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U26-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U26-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U26-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl