

B1L102S, U23-ch0

calib_packv5_042523_0143.root, FC#11, port A2

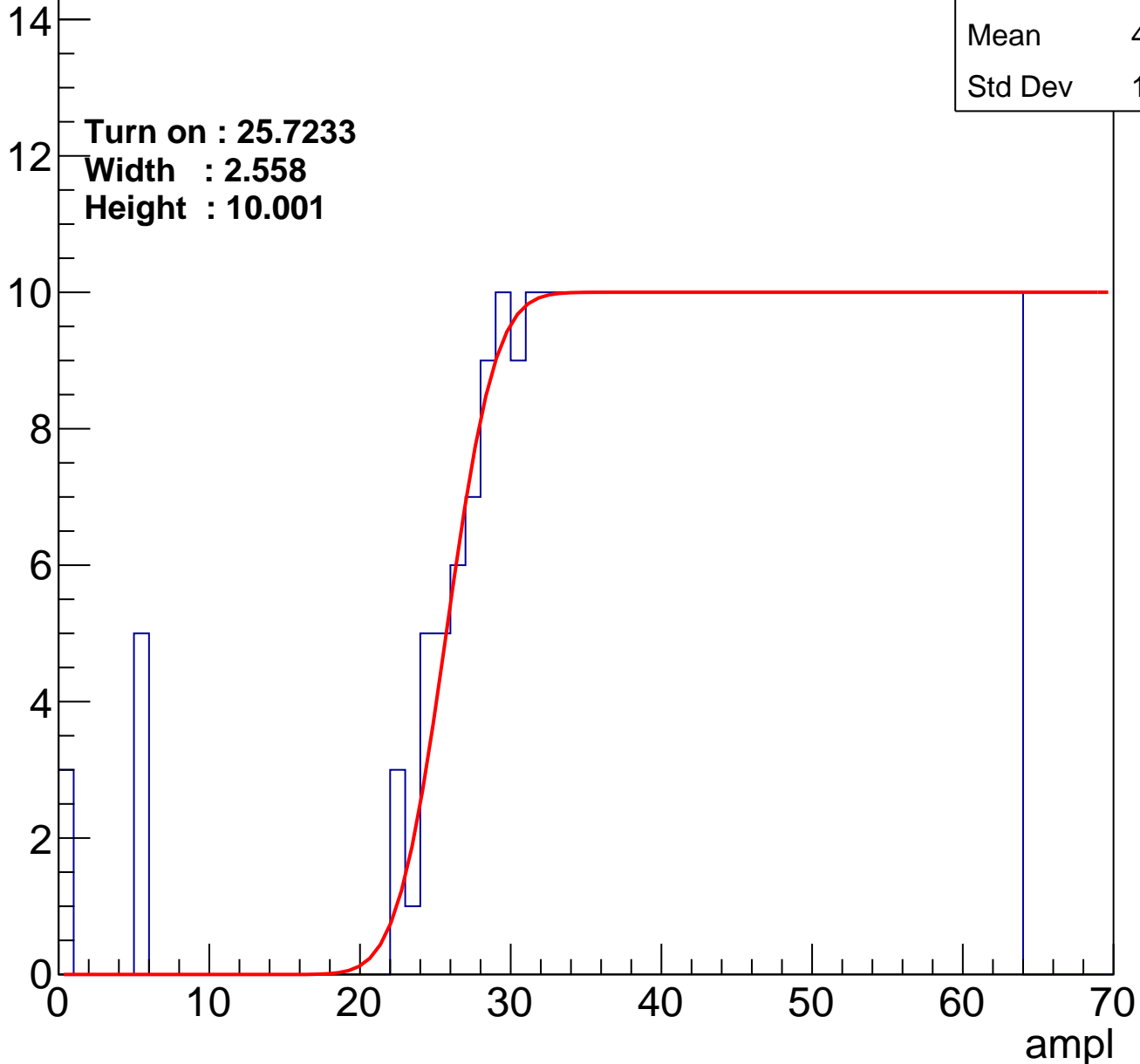
Entries	393
Mean	43.32
Std Dev	12.57

Turn on : 25.7233

Width : 2.558

Height : 10.001

Entry



B1L102S, U23-ch1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 26.6660

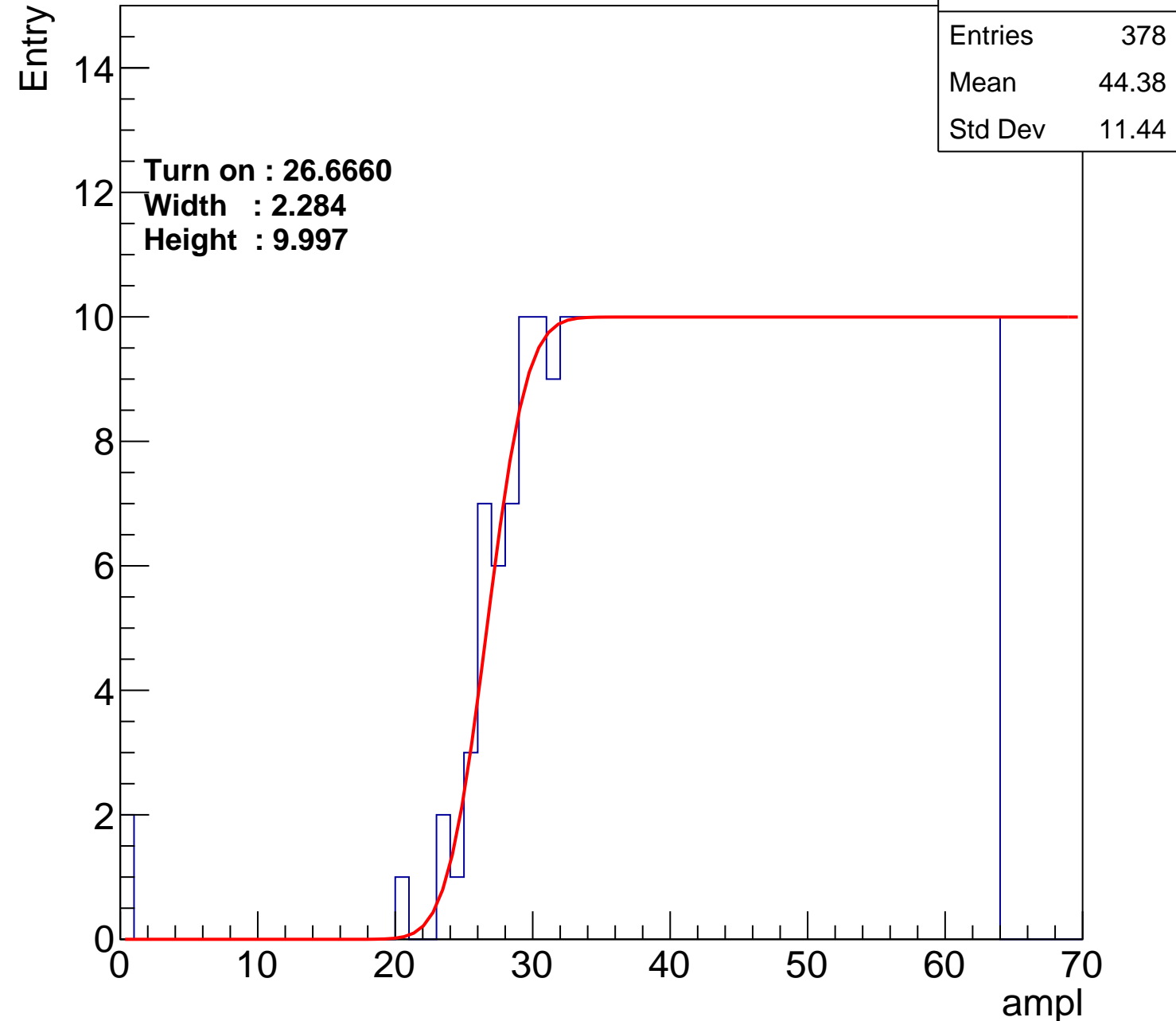
Width : 2.284

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch2

calib_packv5_042523_0143.root, FC#11, port A2

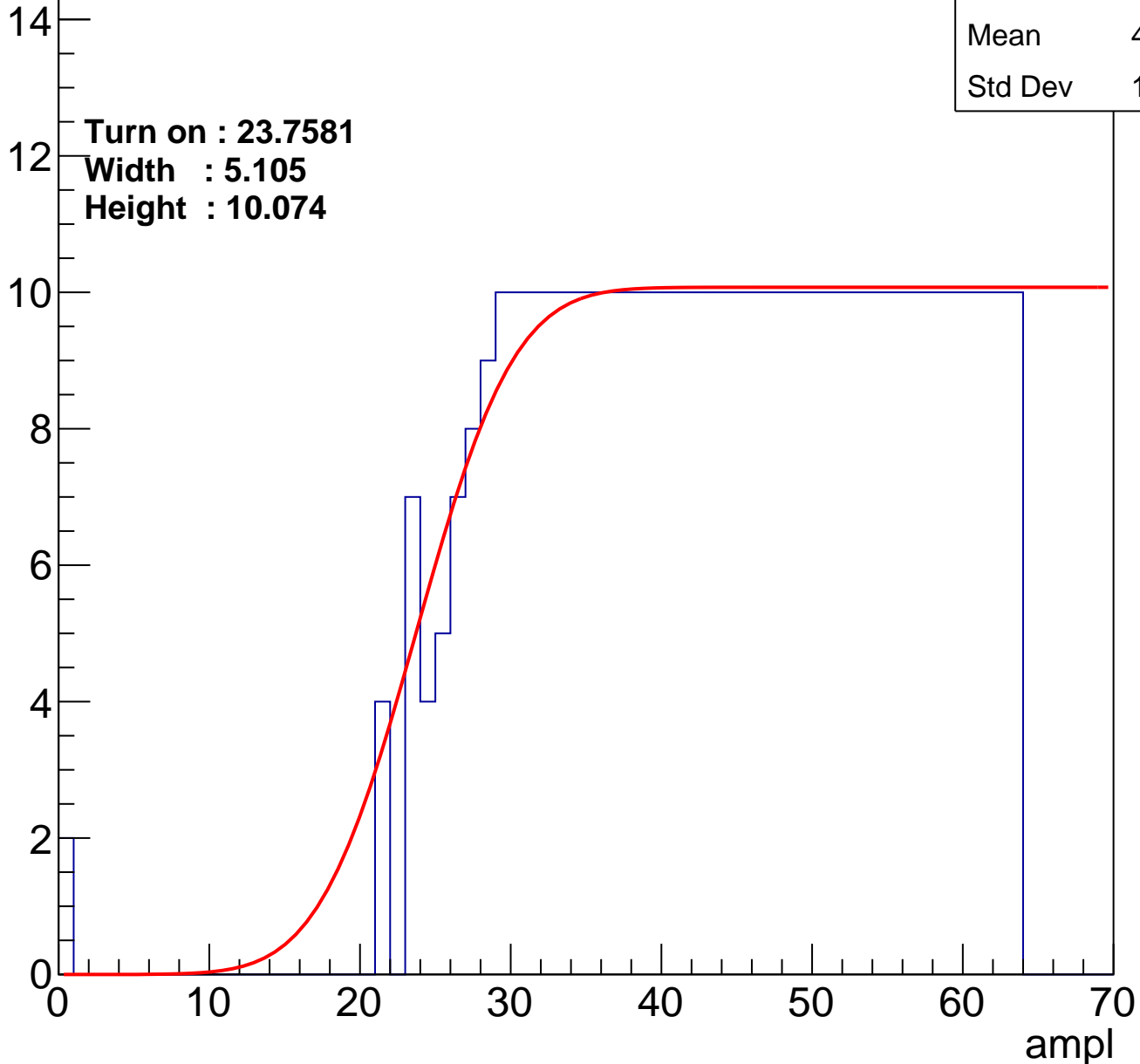
Entries	396
Mean	43.47
Std Dev	11.93

Turn on : 23.7581

Width : 5.105

Height : 10.074

Entry



B1L102S, U23-ch3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.23
Std Dev	10.82

Turn on : 28.1302

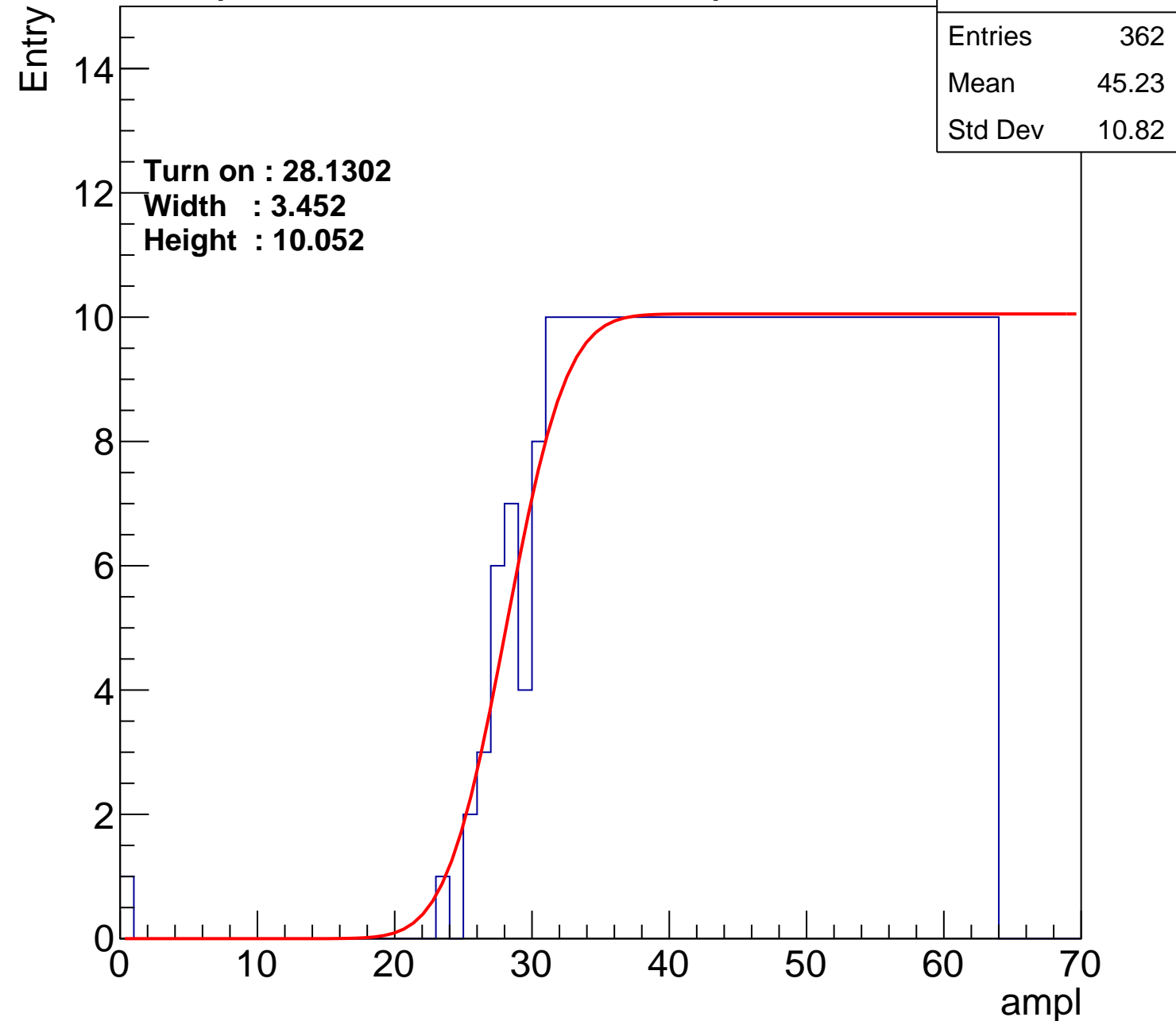
Width : 3.452

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 28.0378

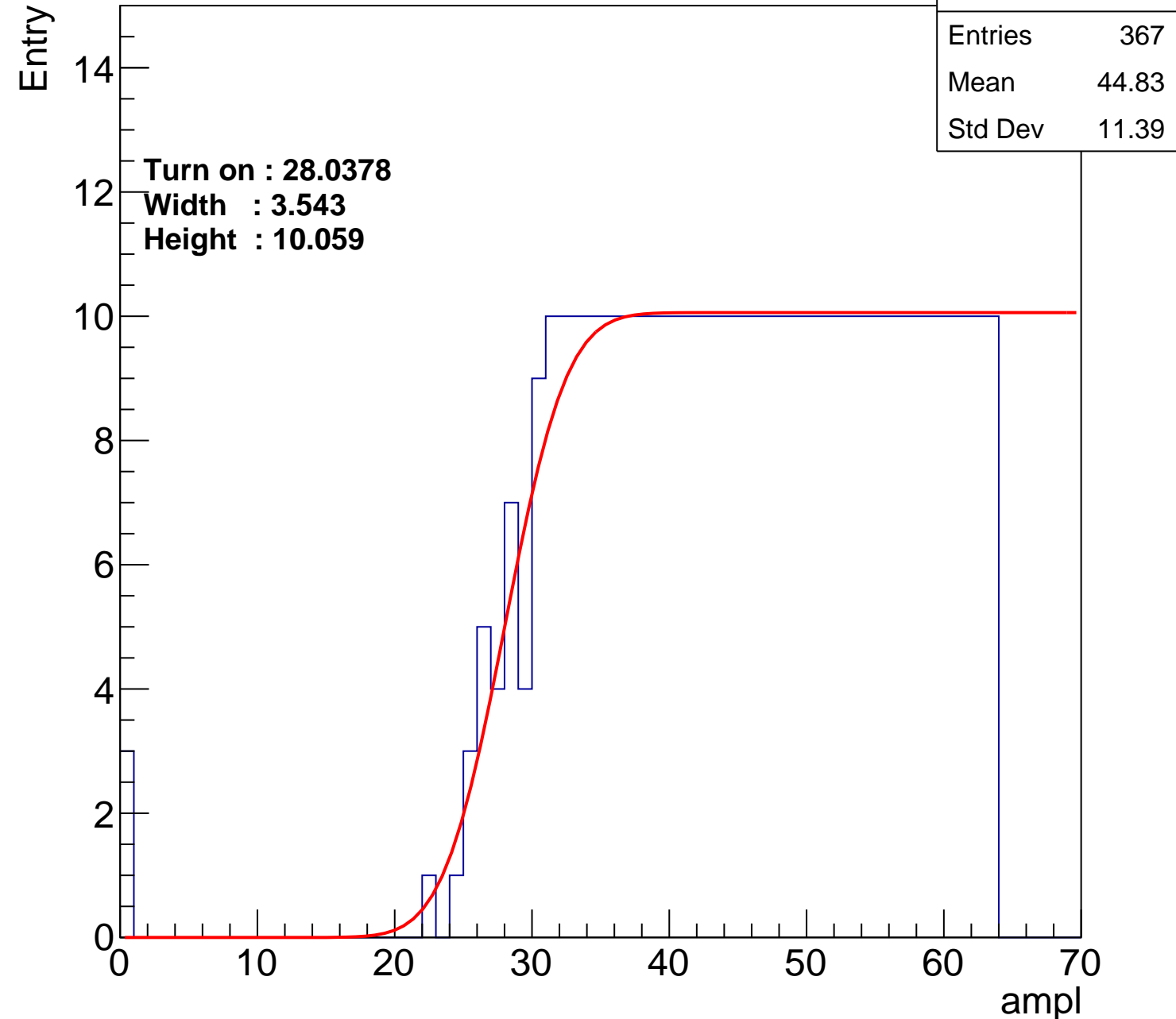
Width : 3.543

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.6486

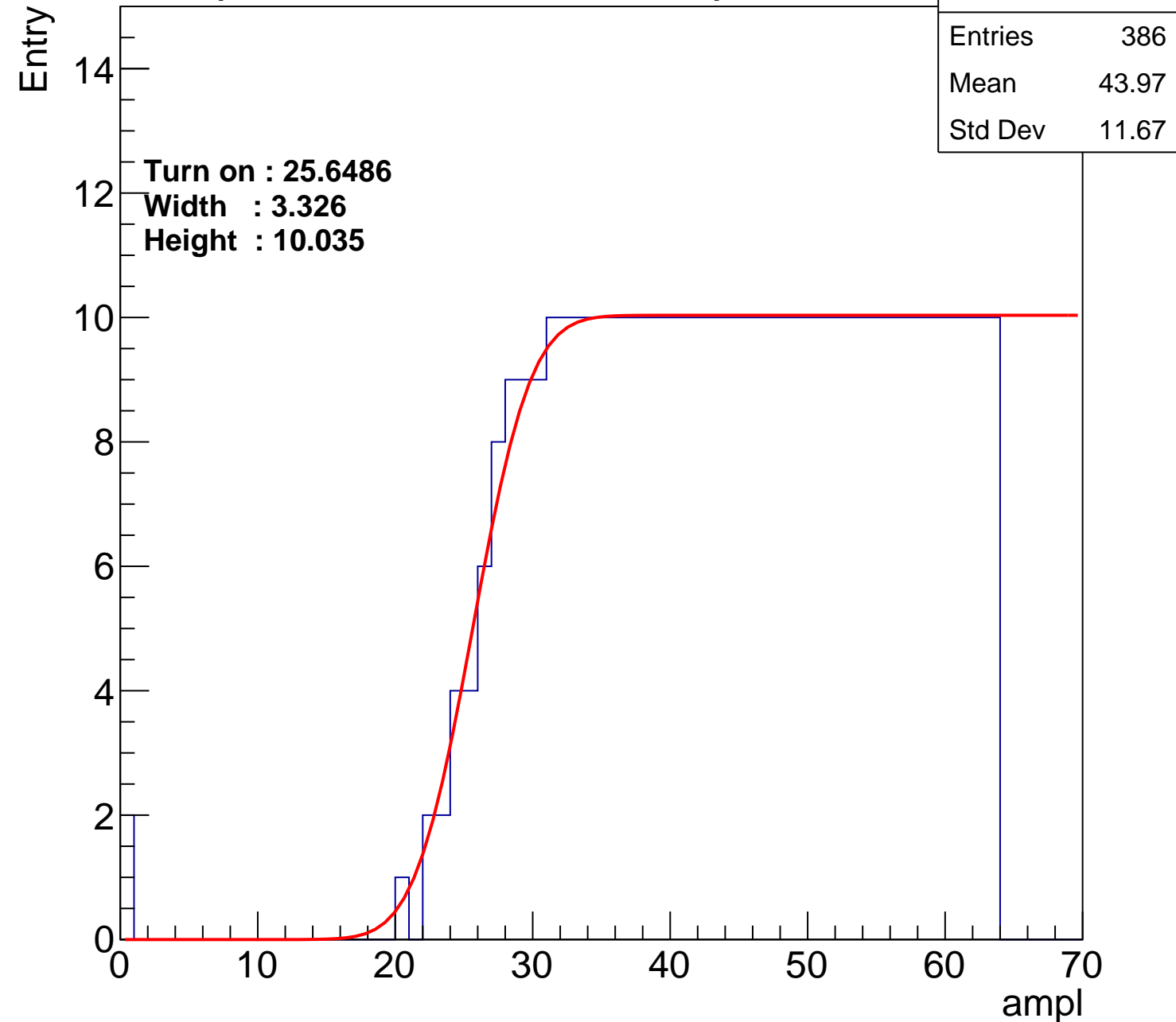
Width : 3.326

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch6

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.24
Std Dev	12.43

Turn on : 25.0177

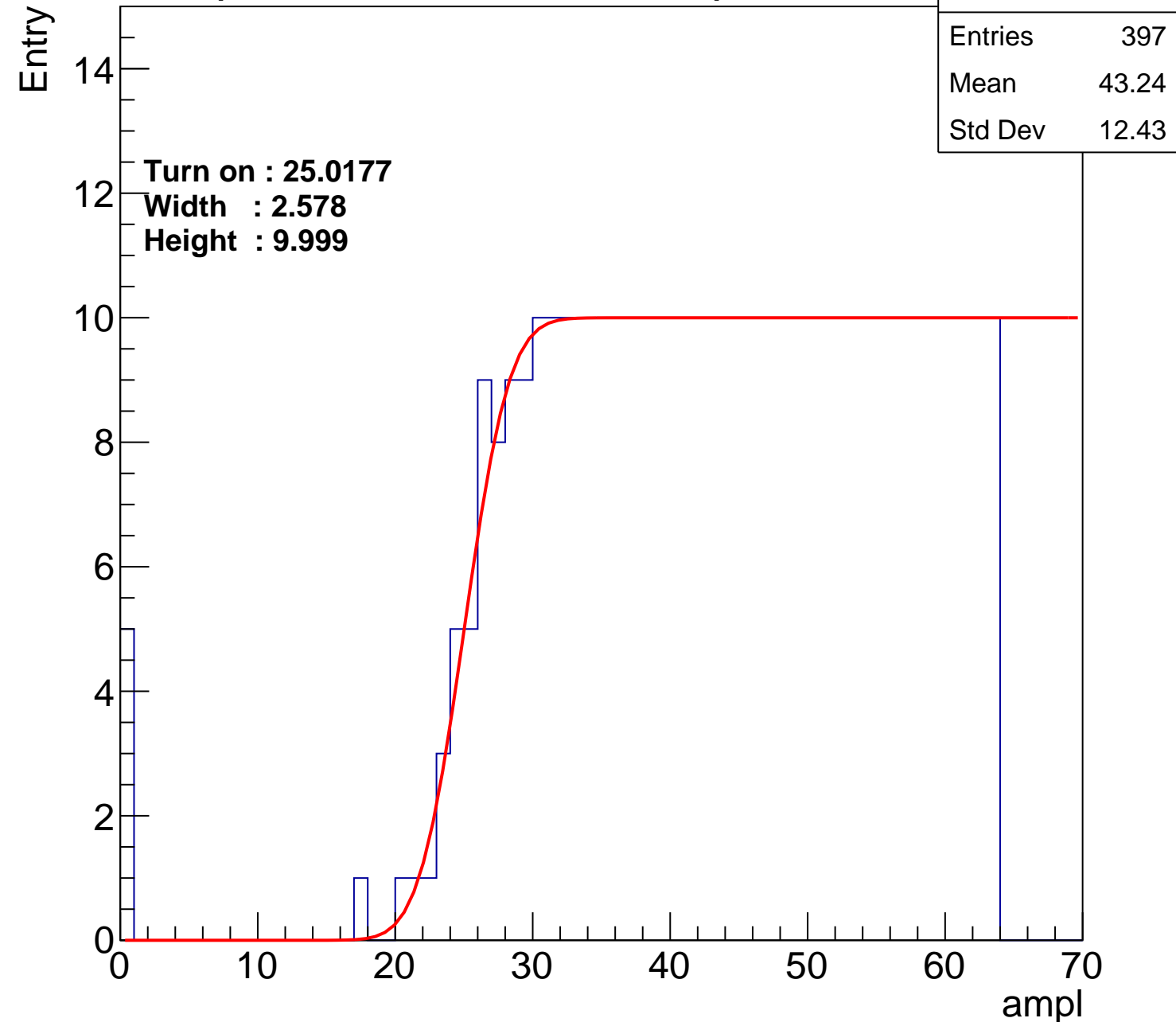
Width : 2.578

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch7

calib_packv5_042523_0143.root, FC#11, port A2

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.9480

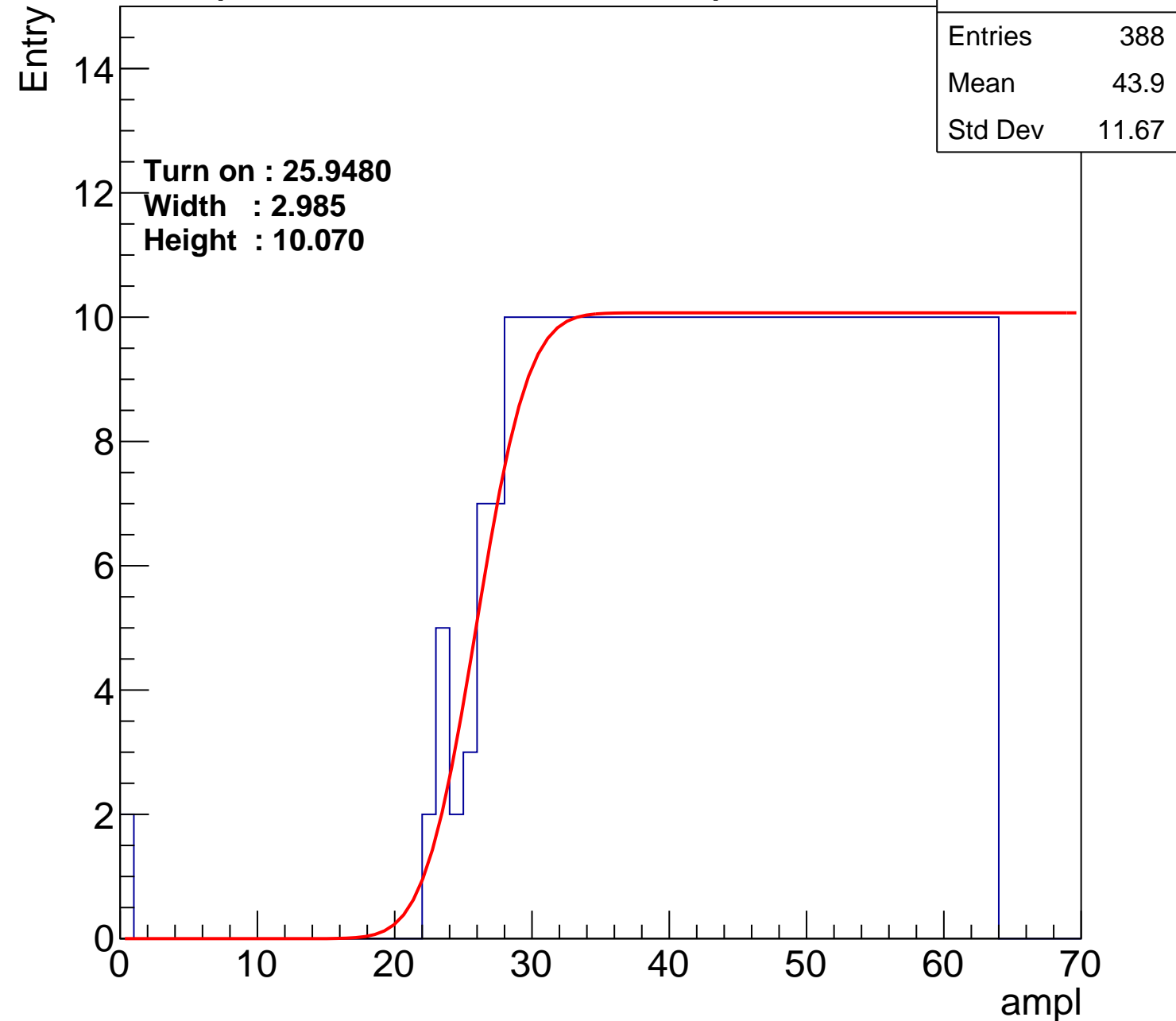
Width : 2.985

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch8

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.89
Std Dev	11.98

Turn on : 26.0758

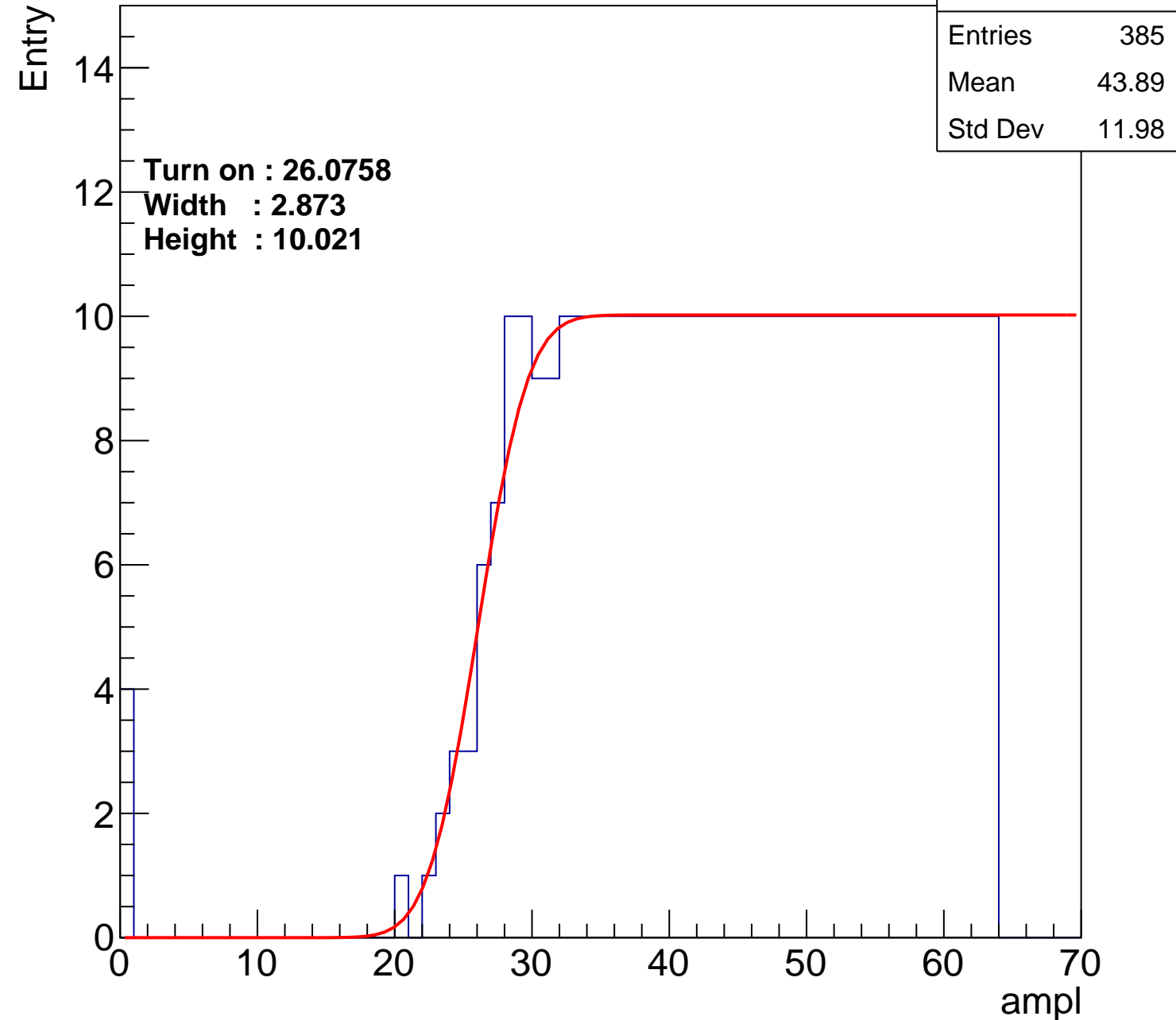
Width : 2.873

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch9

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.1389

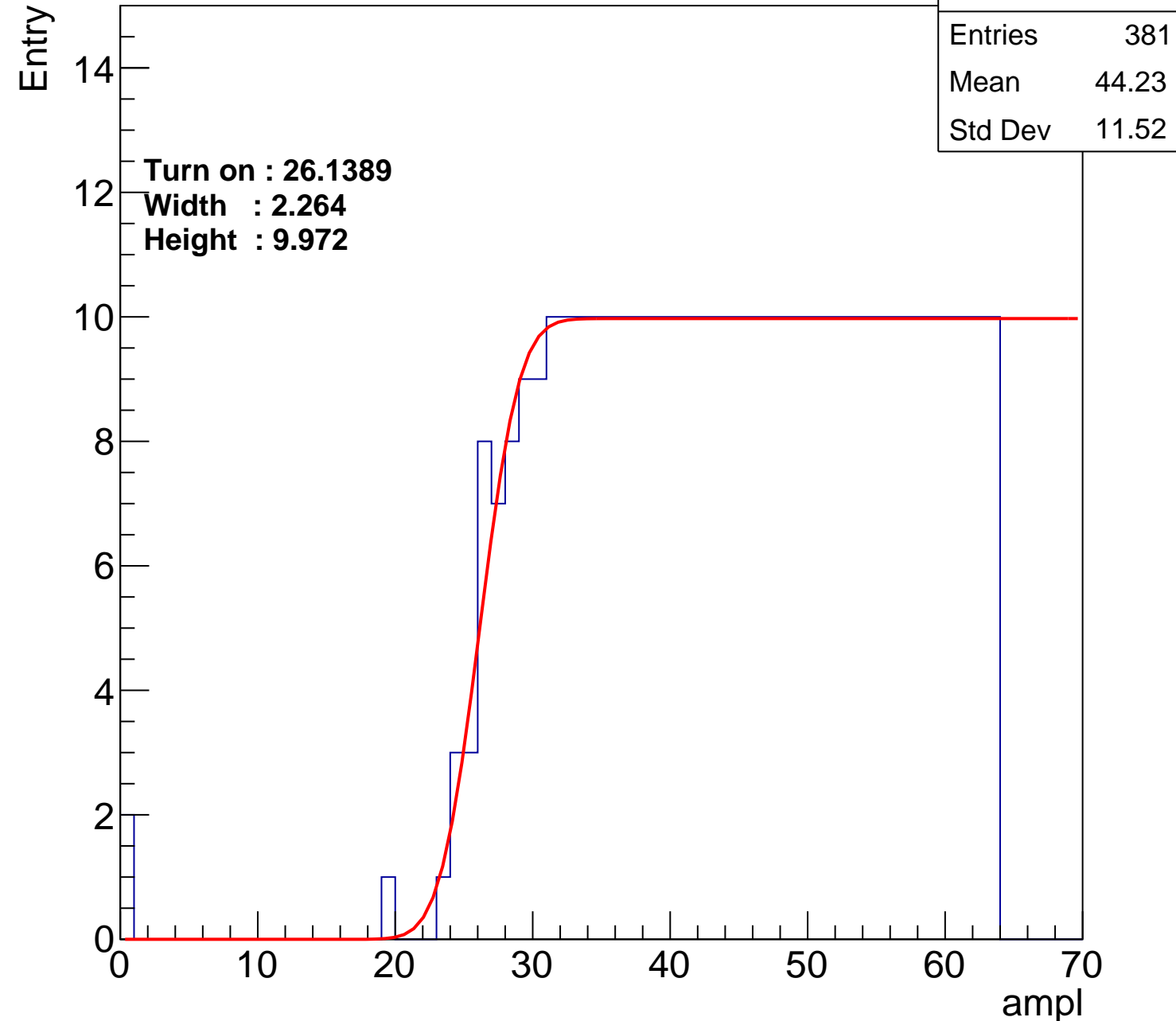
Width : 2.264

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch10

calib_packv5_042523_0143.root, FC#11, port A2

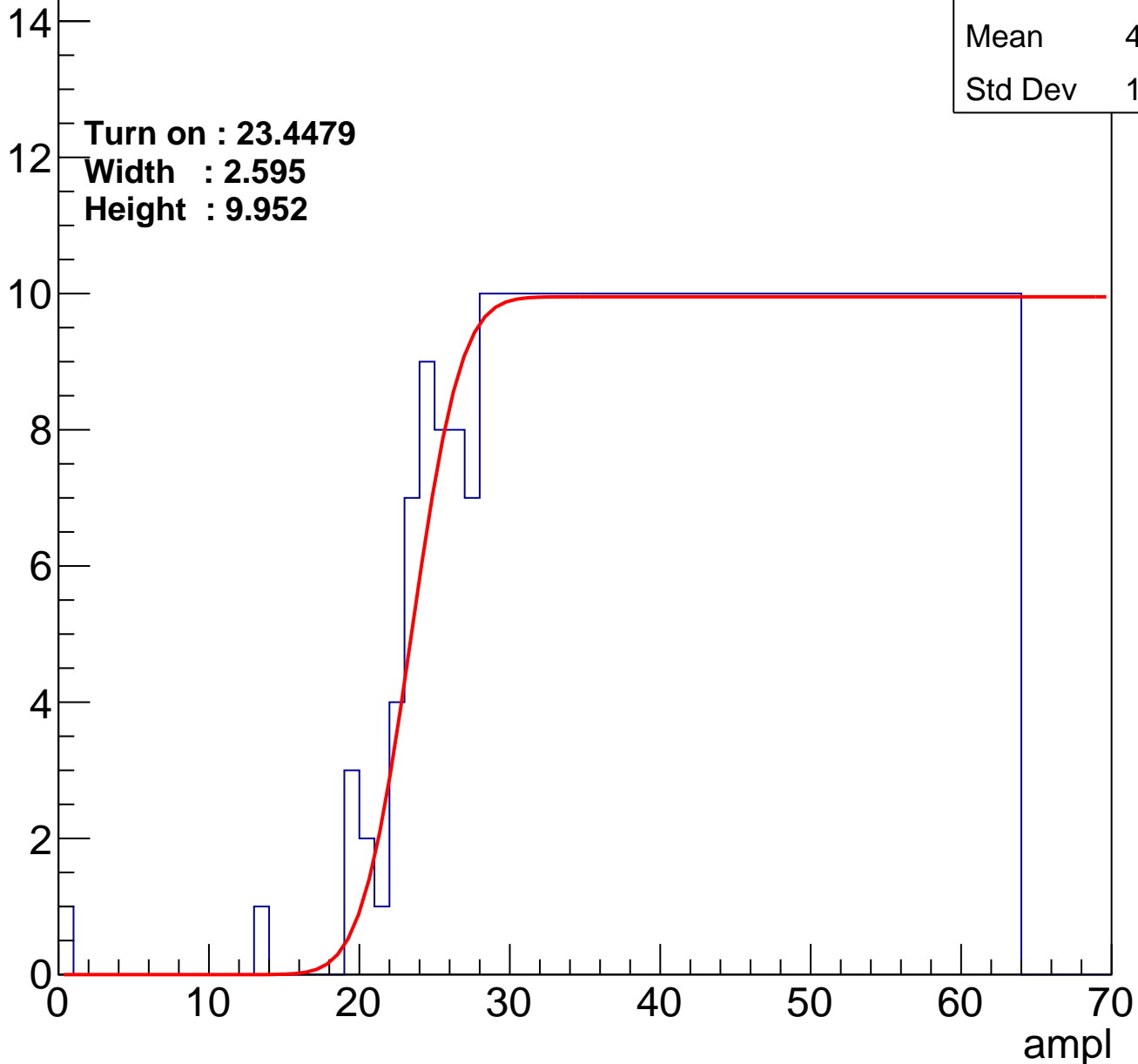
Entries	411
Mean	42.76
Std Dev	12.24

Turn on : 23.4479

Width : 2.595

Height : 9.952

Entry



B1L102S, U23-ch11

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 25.7687

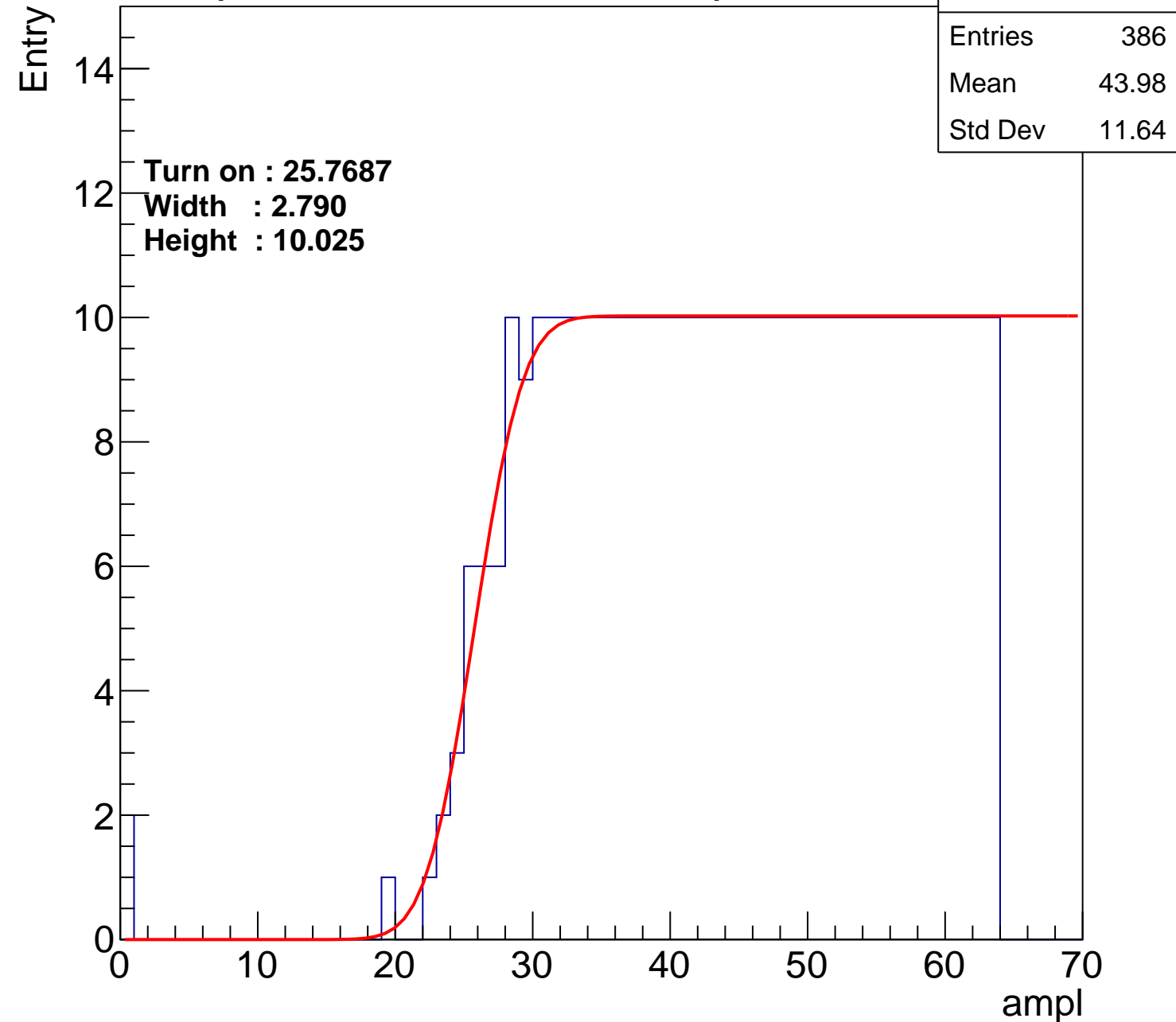
Width : 2.790

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch12

calib_packv5_042523_0143.root, FC#11, port A2

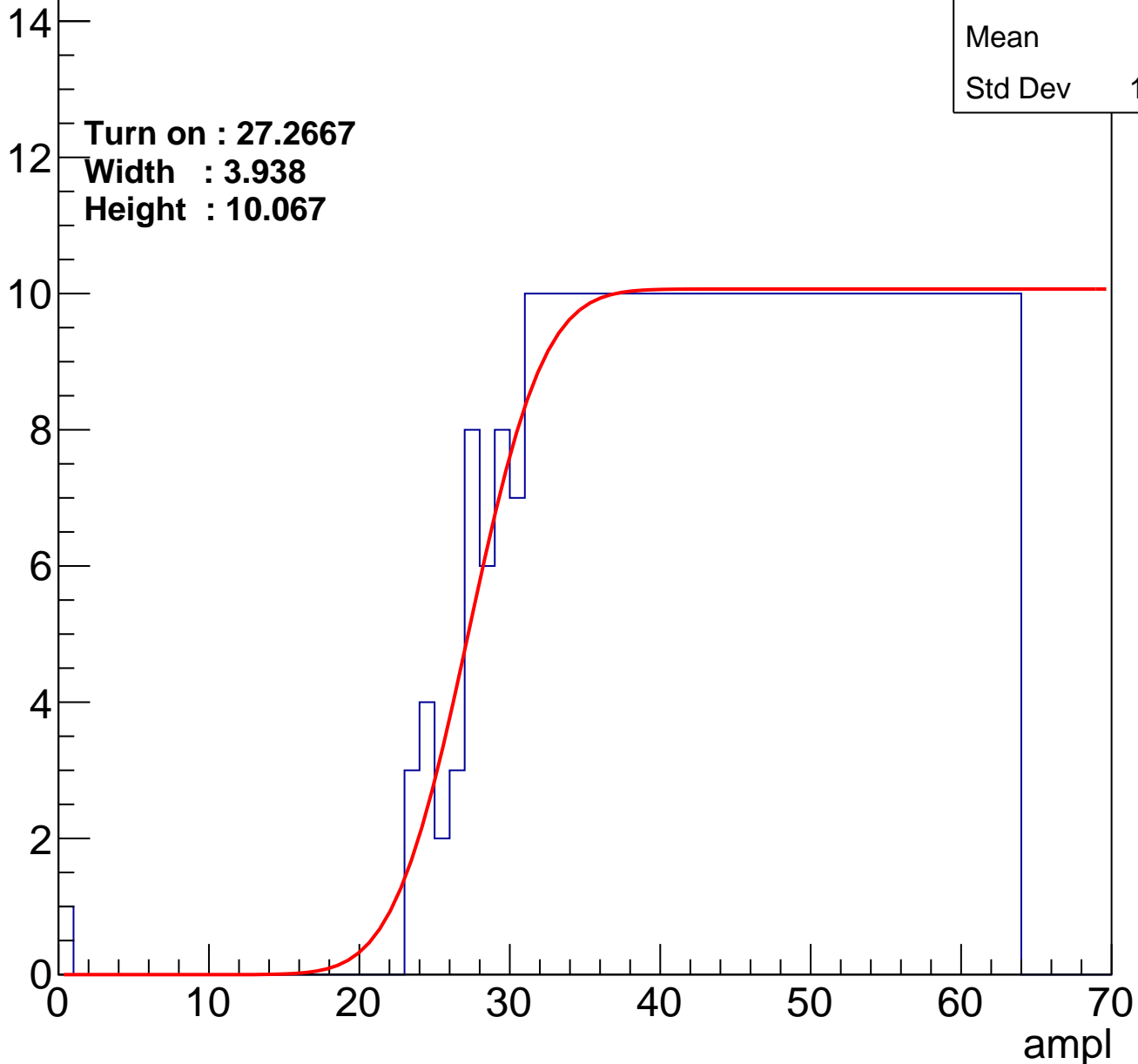
Entries	372
Mean	44.7
Std Dev	11.15

Turn on : 27.2667

Width : 3.938

Height : 10.067

Entry



B1L102S, U23-ch13

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.18
Std Dev	11.73

Turn on : 26.3975

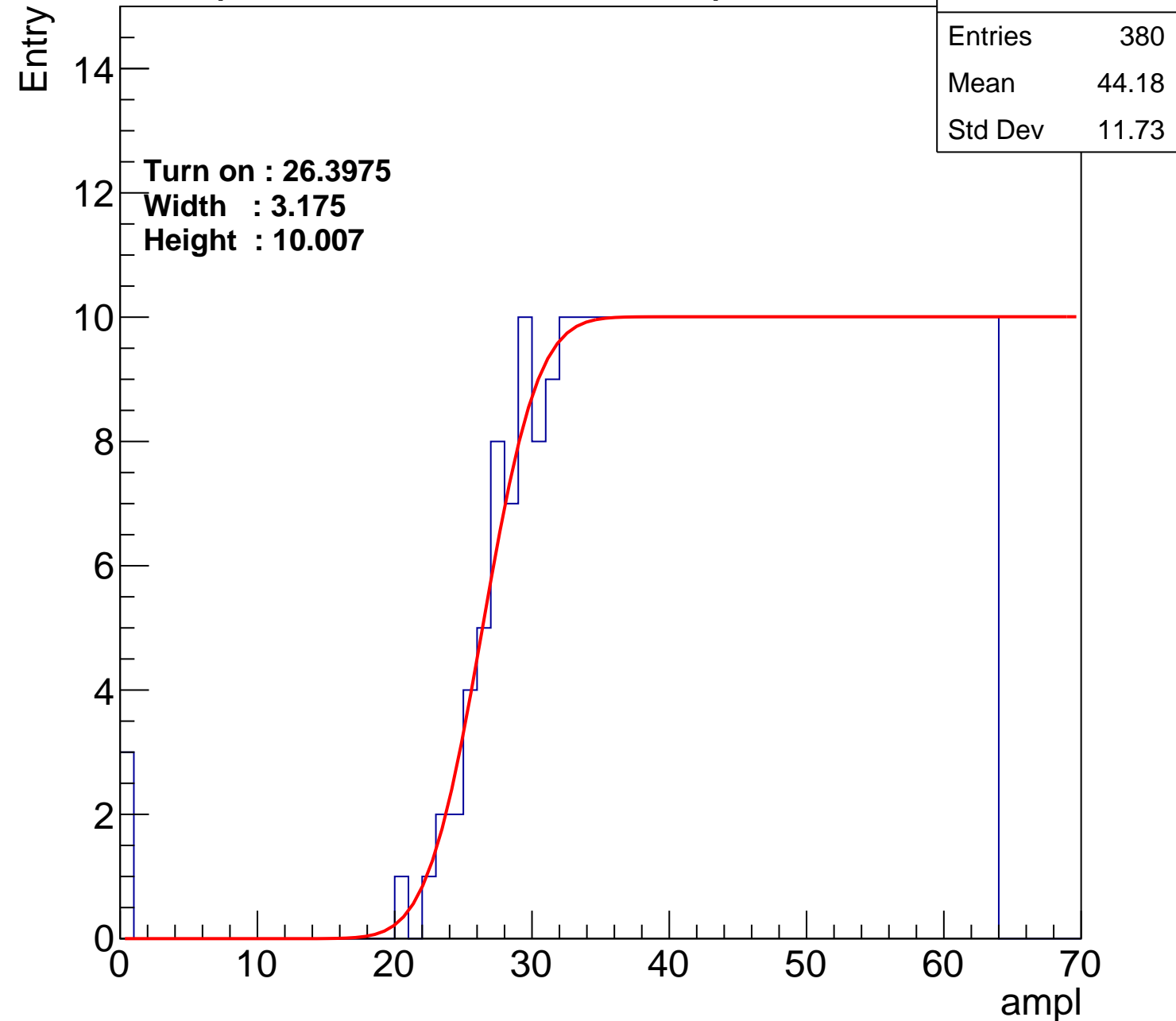
Width : 3.175

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch14

calib_packv5_042523_0143.root, FC#11, port A2

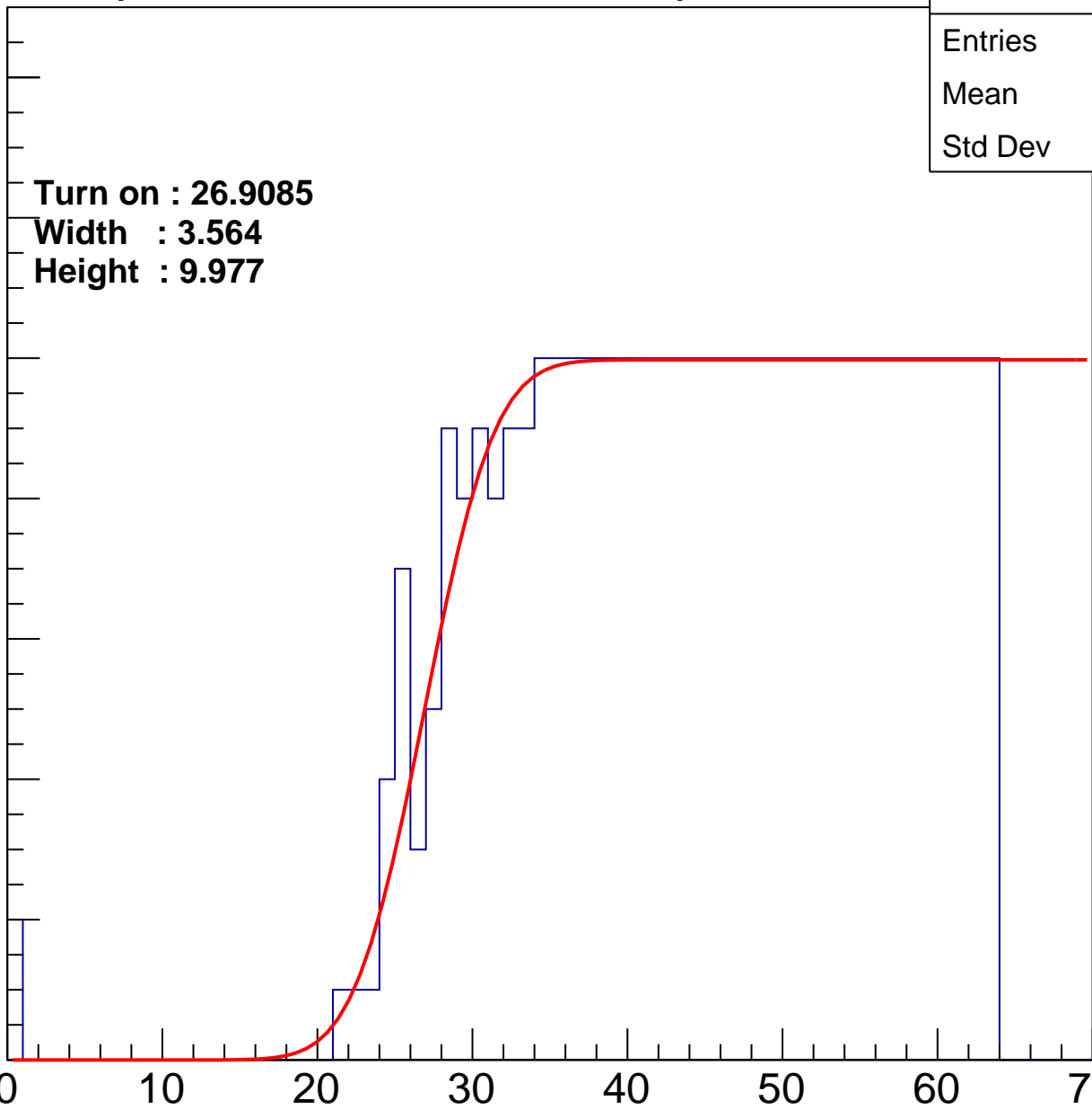
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9085
Width : 3.564
Height : 9.977

Entries	376
Mean	44.38
Std Dev	11.53

ampl



B1L102S, U23-ch15

calib_packv5_042523_0143.root, FC#11, port A2

Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.4048

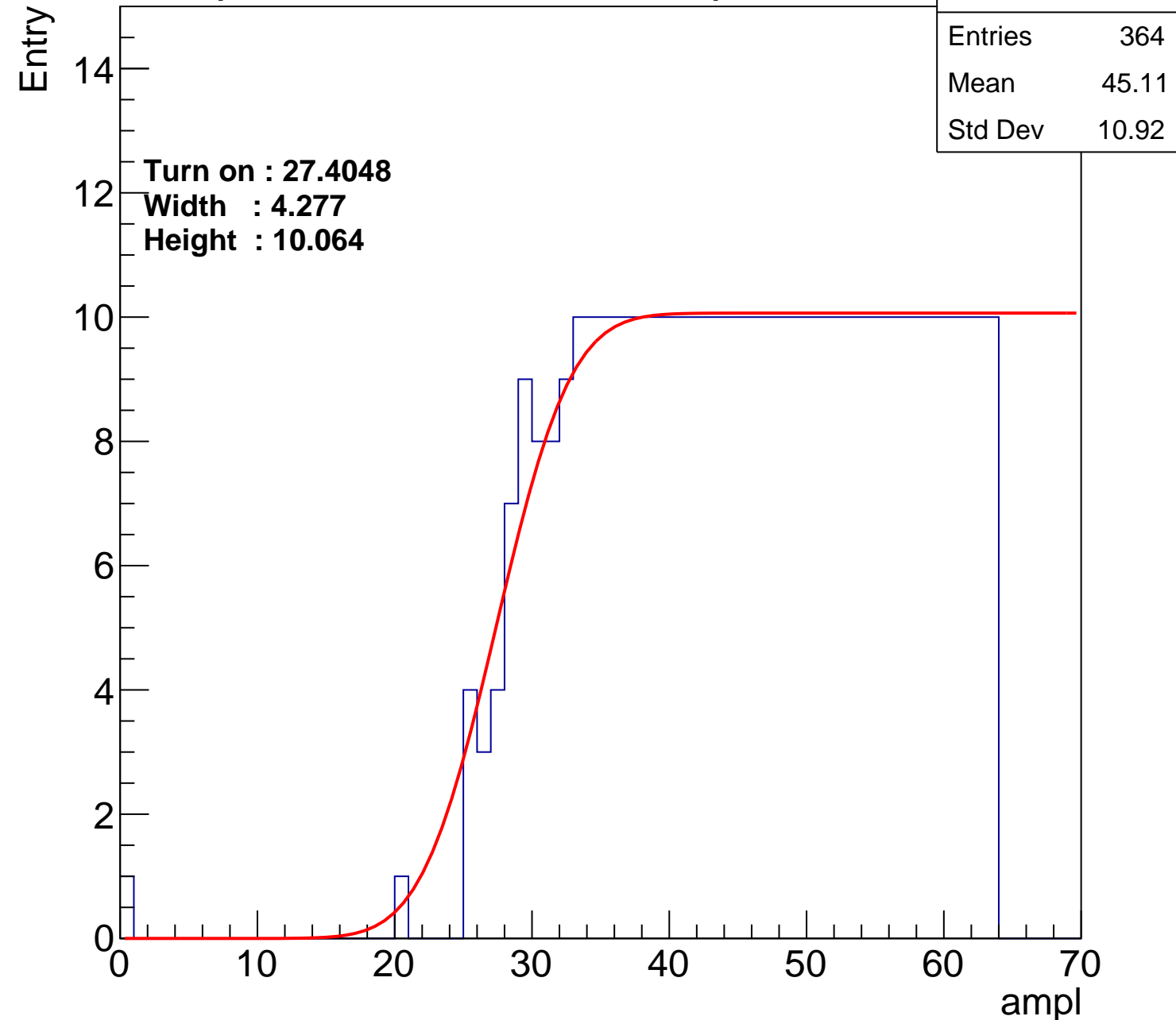
Width : 4.277

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch16

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.12
Std Dev	11.09

Turn on : 27.6777

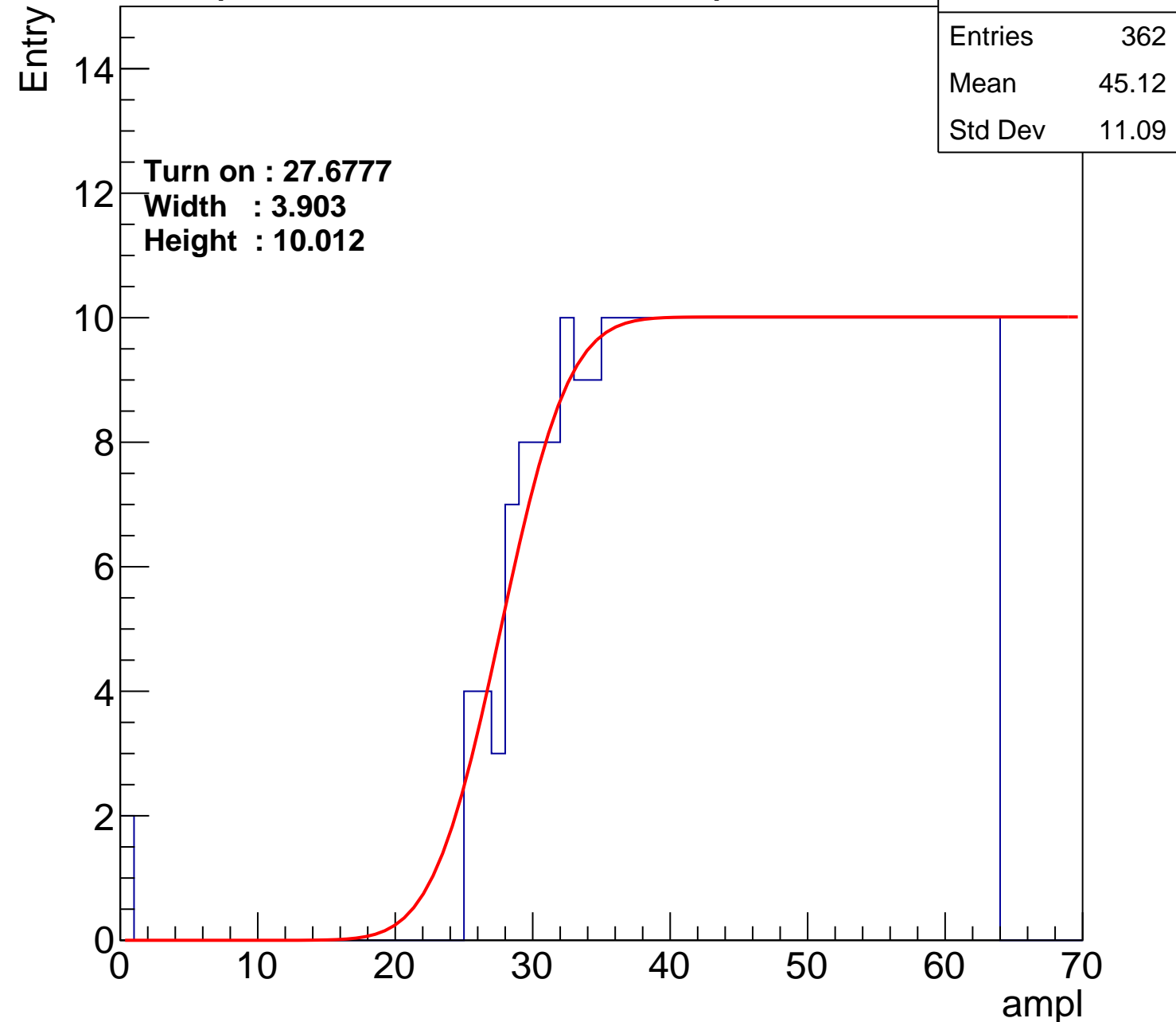
Width : 3.903

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch17

calib_packv5_042523_0143.root, FC#11, port A2

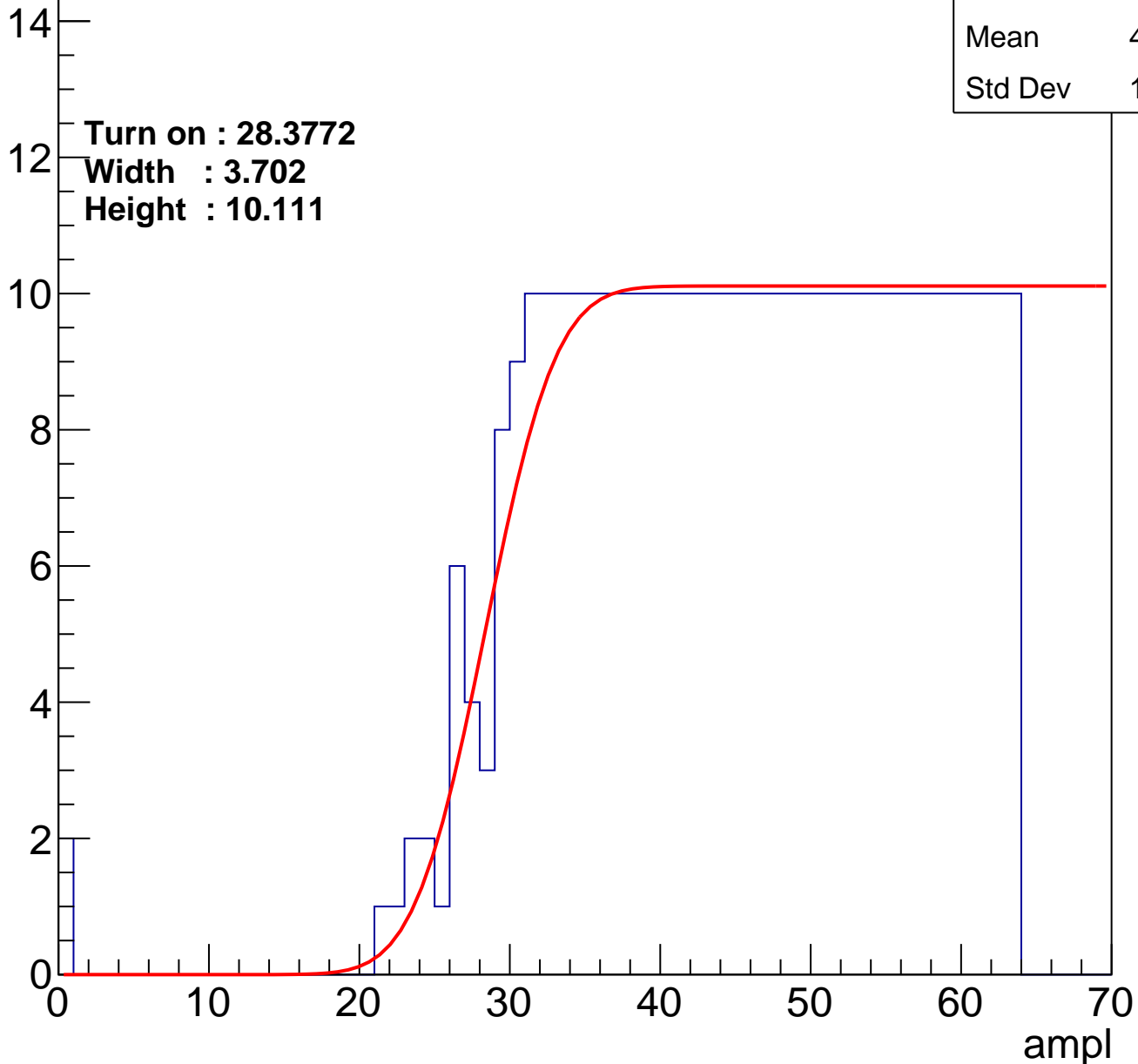
Entries	369
Mean	44.78
Std Dev	11.29

Turn on : 28.3772

Width : 3.702

Height : 10.111

Entry



B1L102S, U23-ch18

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	42.99
Std Dev	12.66

Turn on : 24.2759

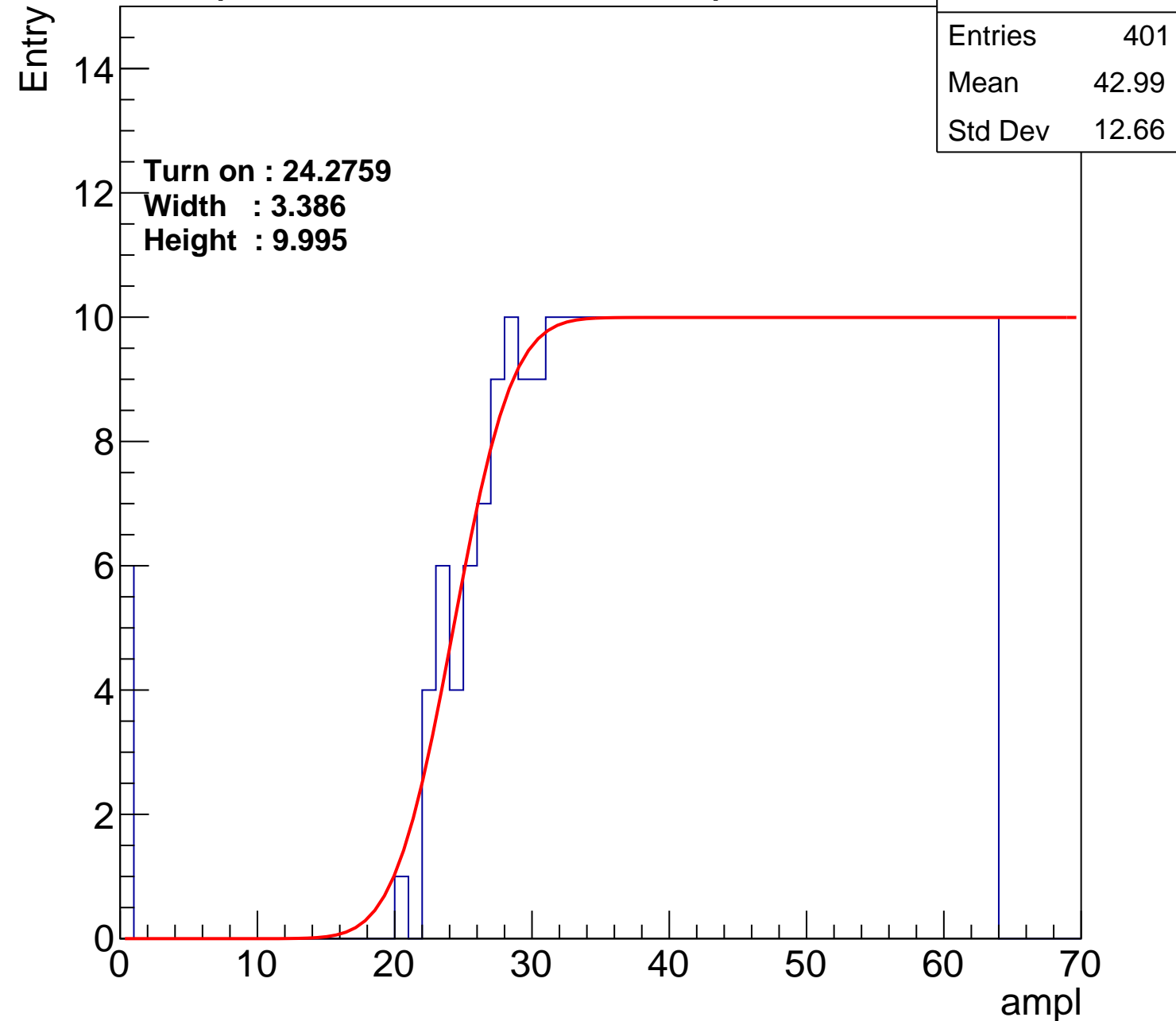
Width : 3.386

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch19

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.21
Std Dev	11.81

Turn on : 26.6357

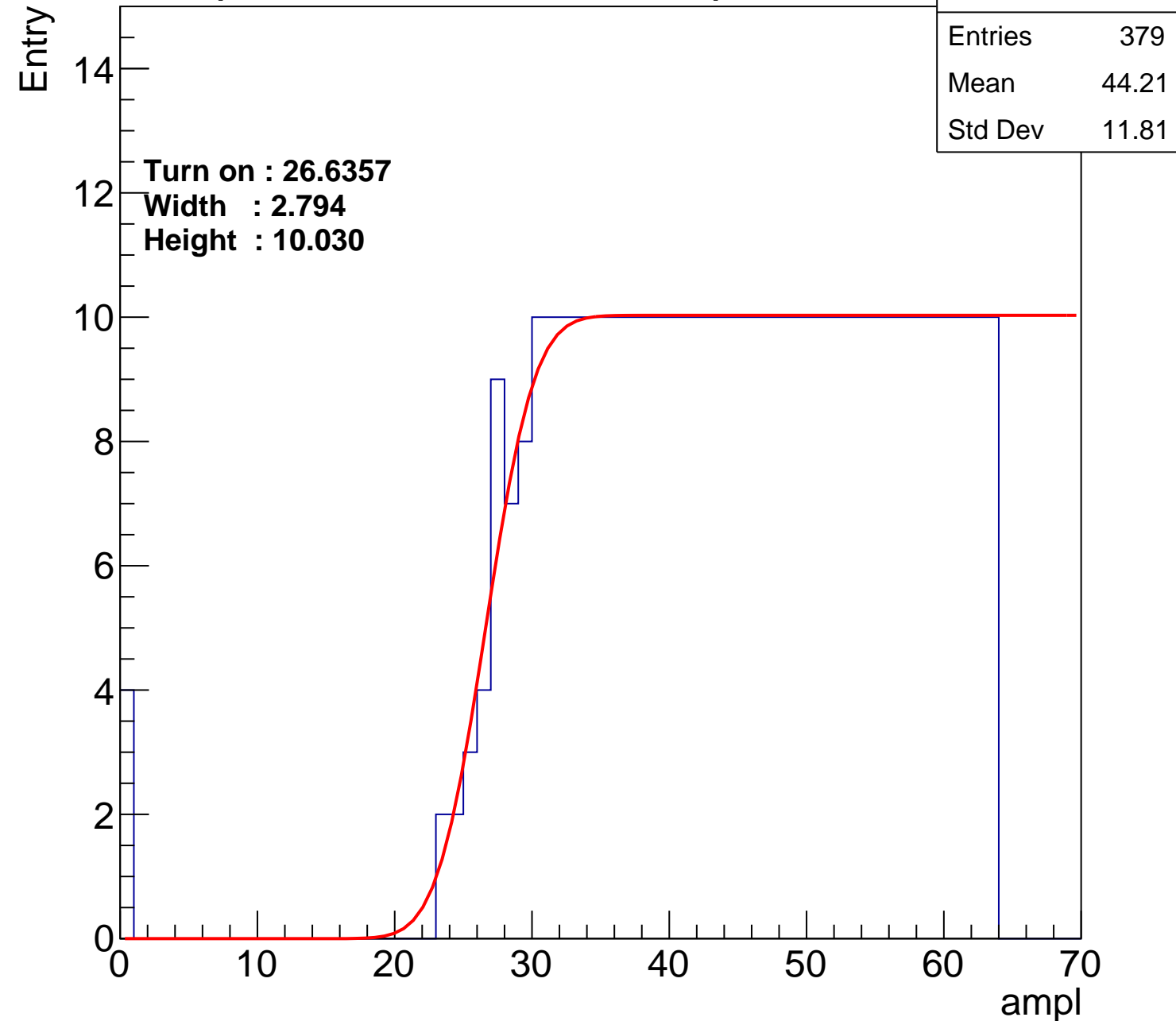
Width : 2.794

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch20

calib_packv5_042523_0143.root, FC#11, port A2

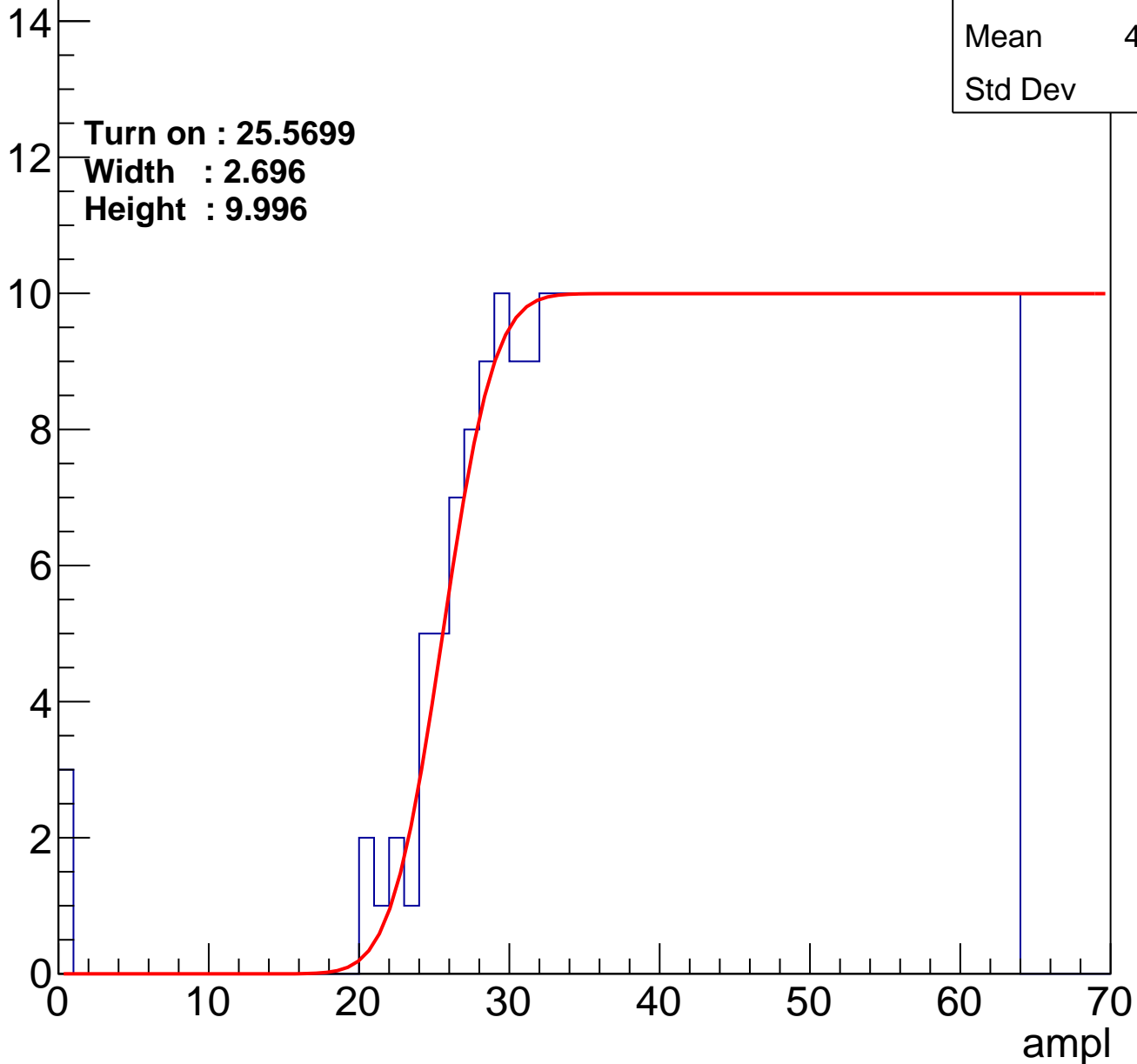
Entries	391
Mean	43.64
Std Dev	12

Turn on : 25.5699

Width : 2.696

Height : 9.996

Entry



B1L102S, U23-ch21

calib_packv5_042523_0143.root, FC#11, port A2

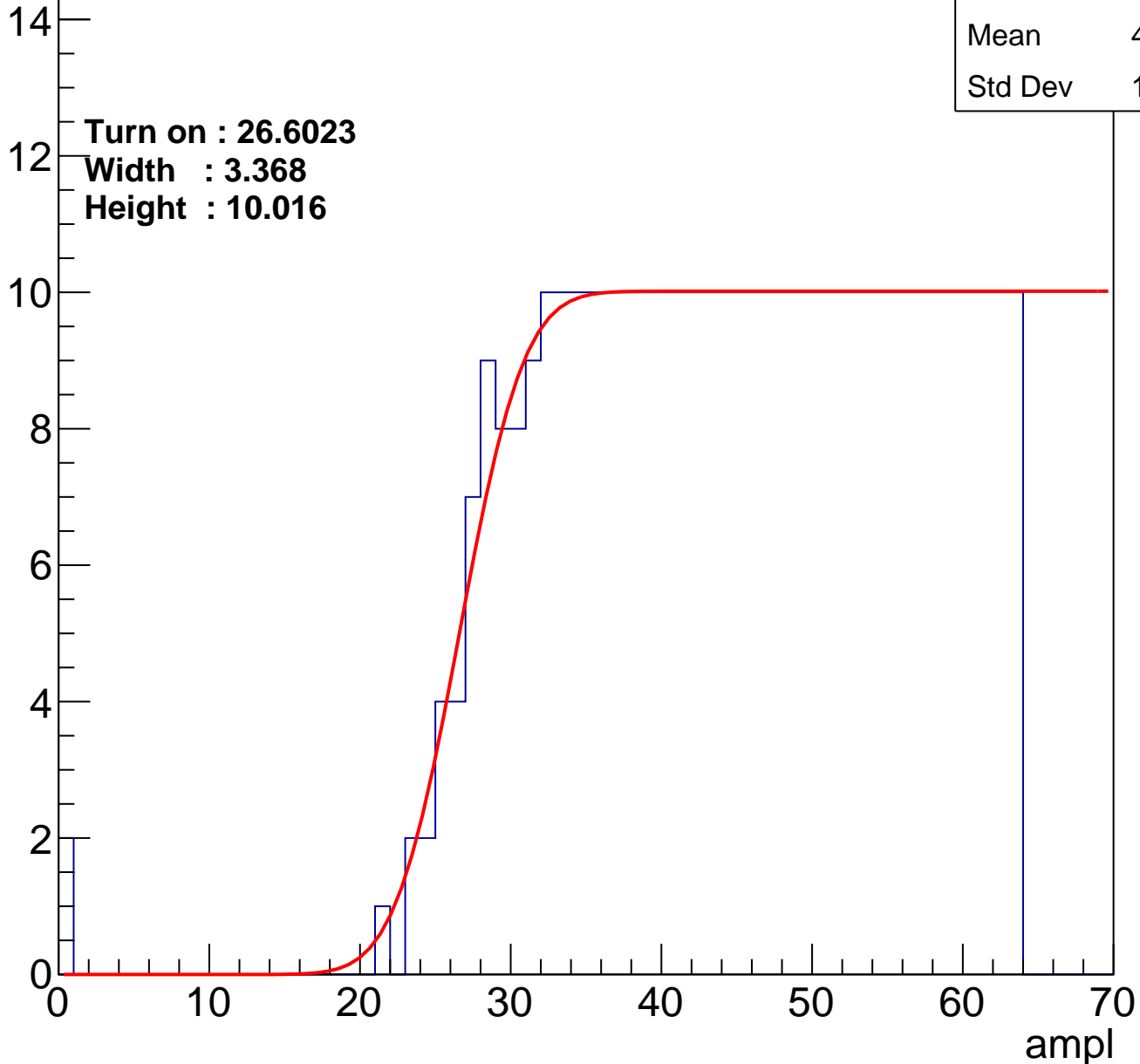
Entries	376
Mean	44.44
Std Dev	11.43

Turn on : 26.6023

Width : 3.368

Height : 10.016

Entry



B1L102S, U23-ch22

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 26.6306

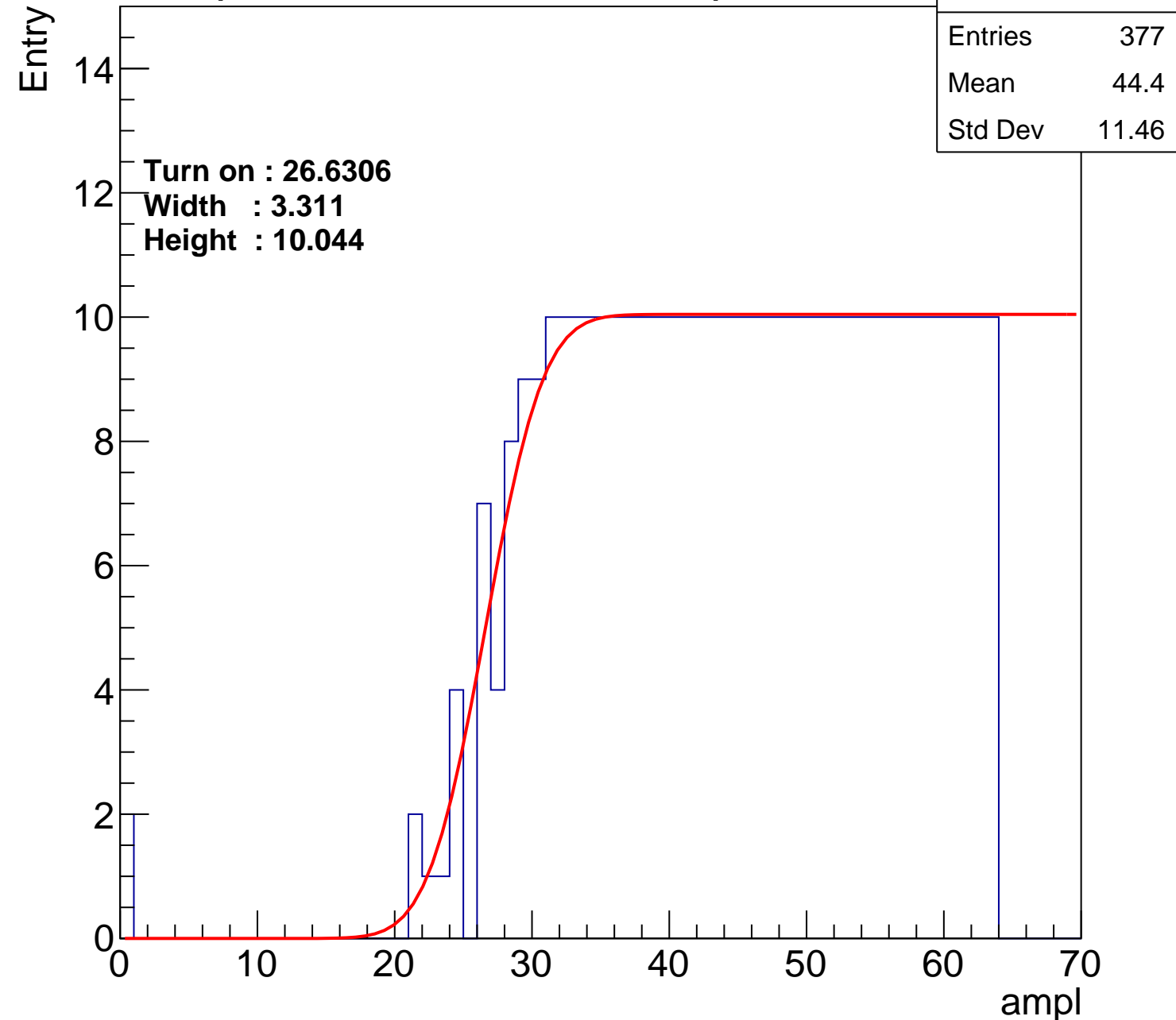
Width : 3.311

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch23

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.62
Std Dev	11.85

Turn on : 25.0982

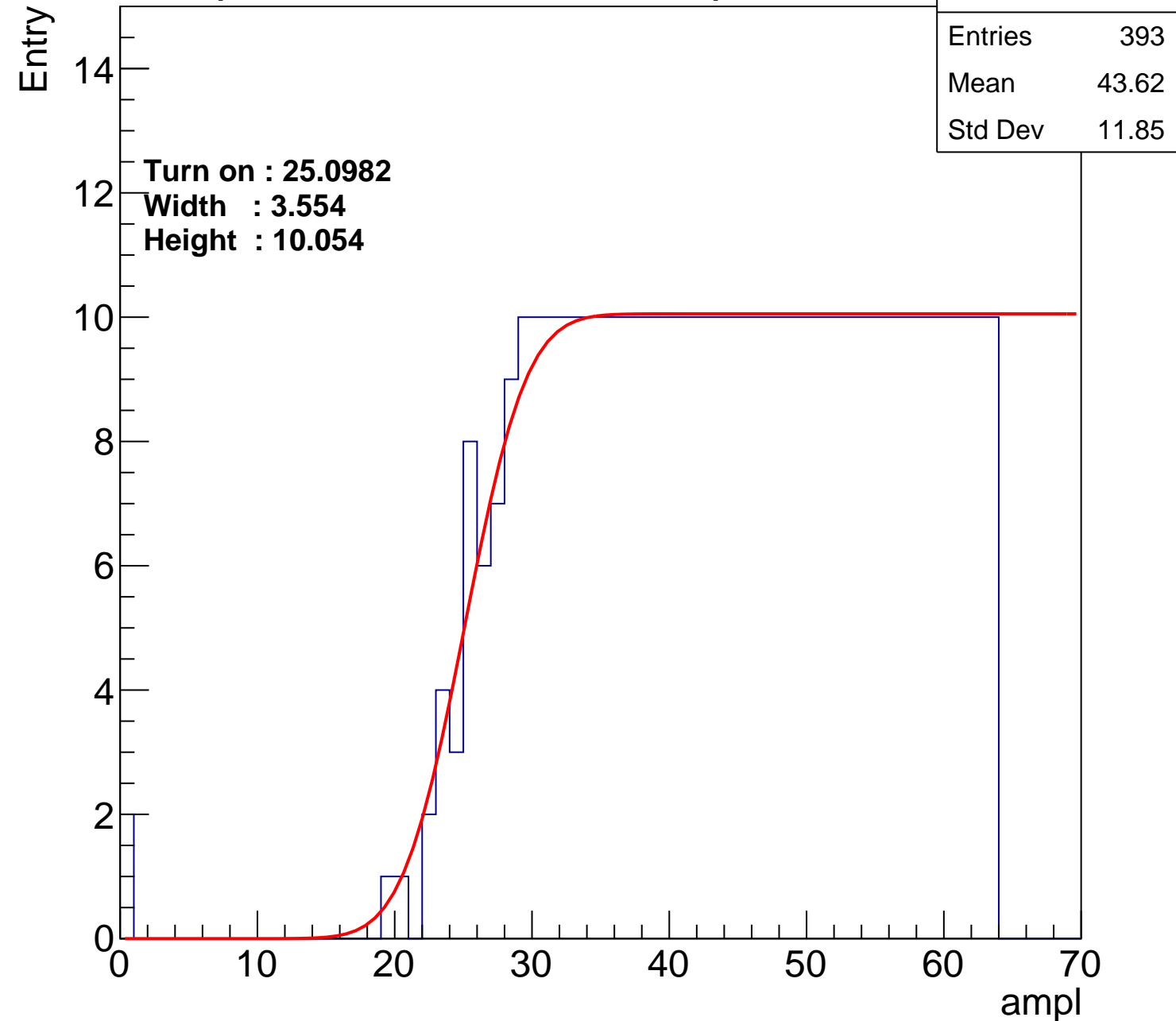
Width : 3.554

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch24

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.21
Std Dev	11.06

Turn on : 28.2685

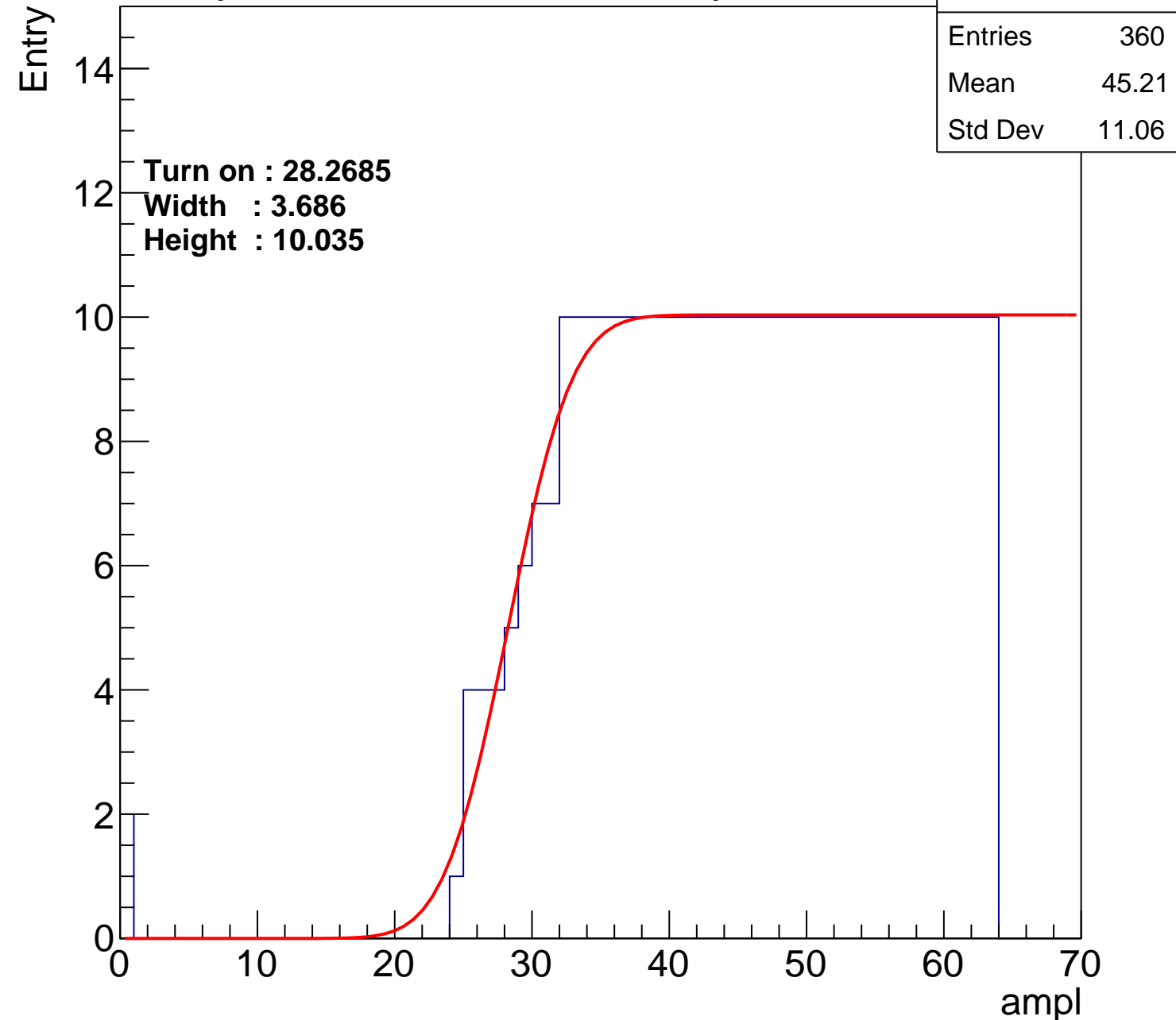
Width : 3.686

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch25

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 26.8794

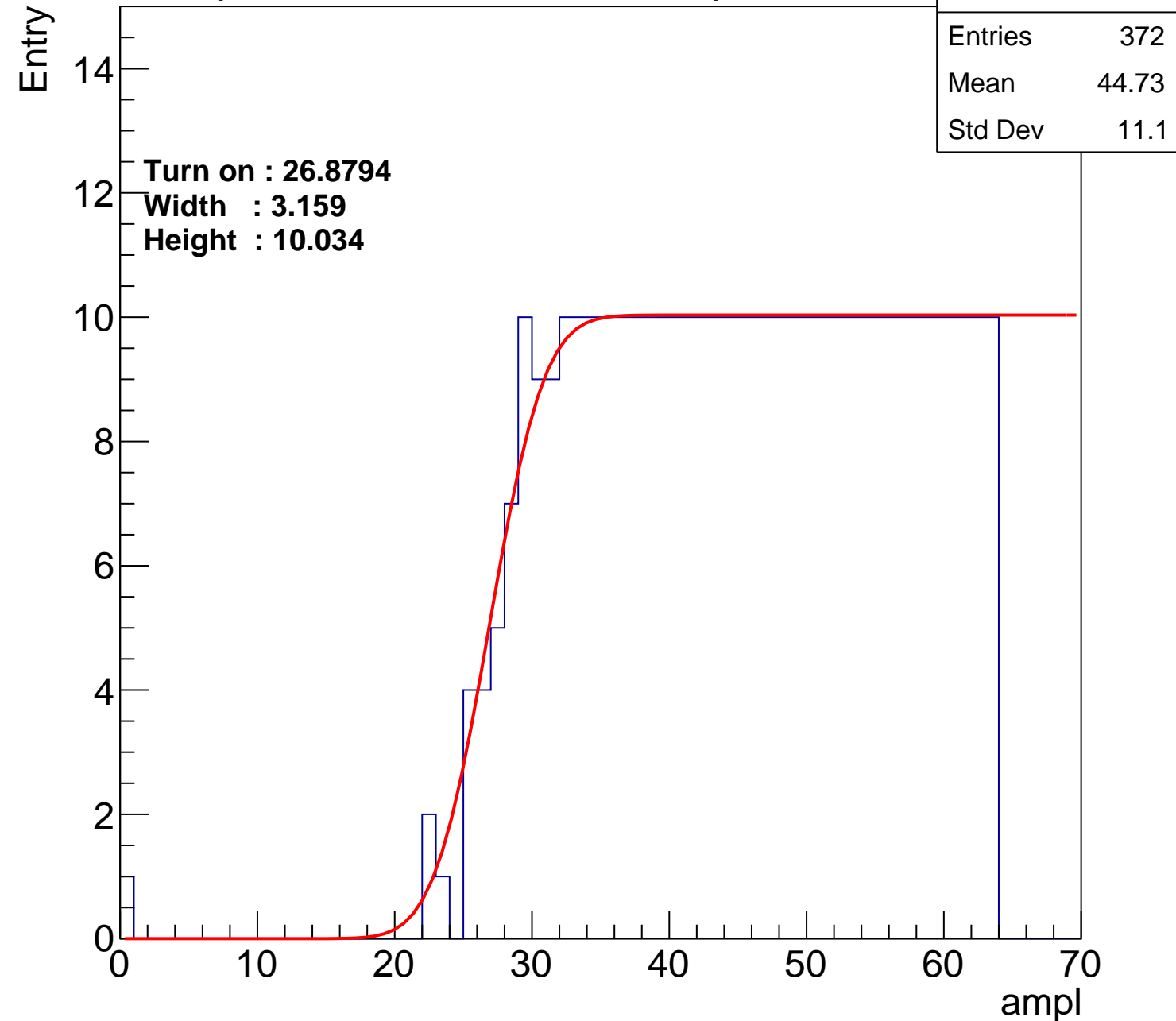
Width : 3.159

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch26

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.04
Std Dev	11.79

Turn on : 26.6420

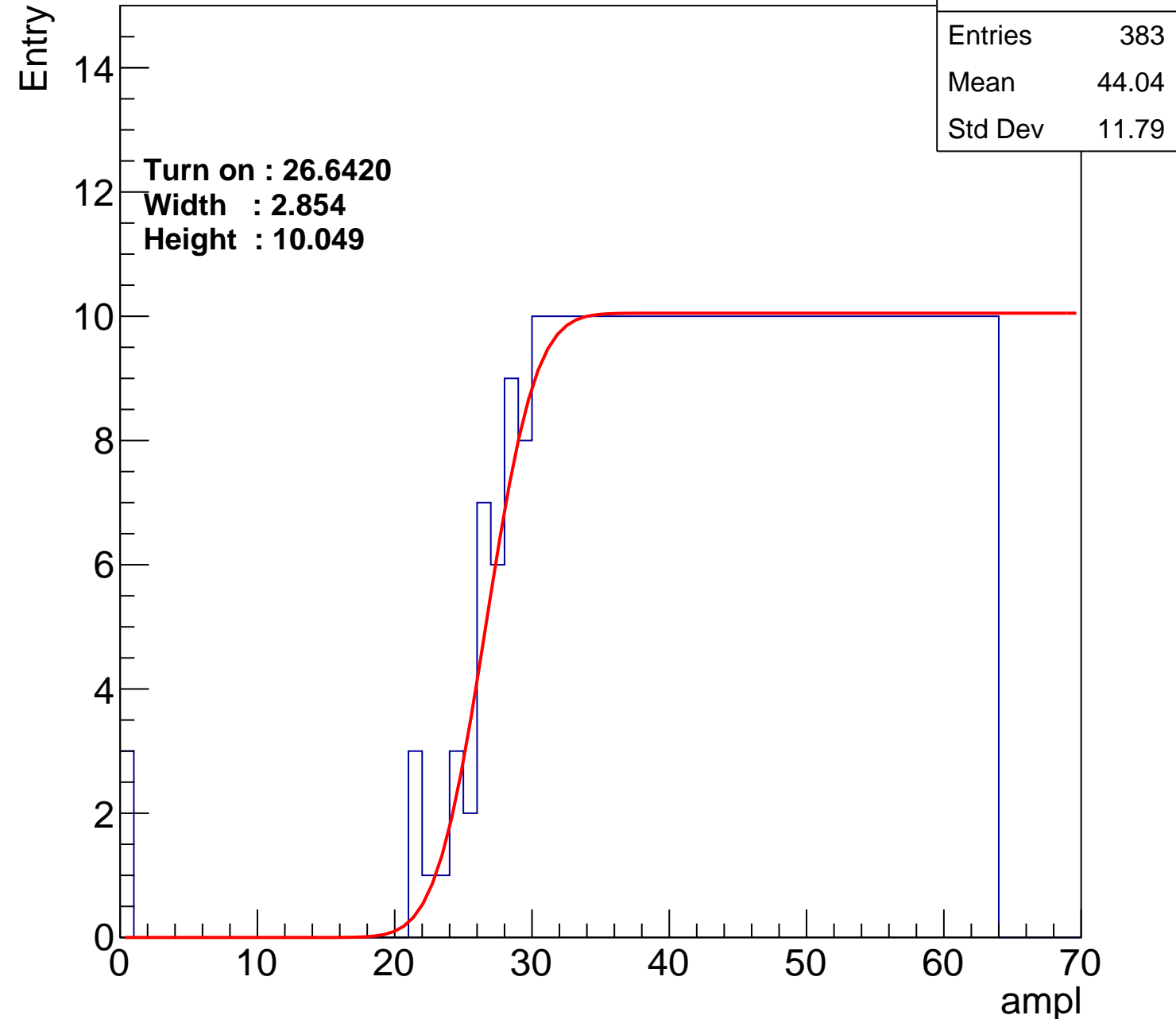
Width : 2.854

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch27

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.5147

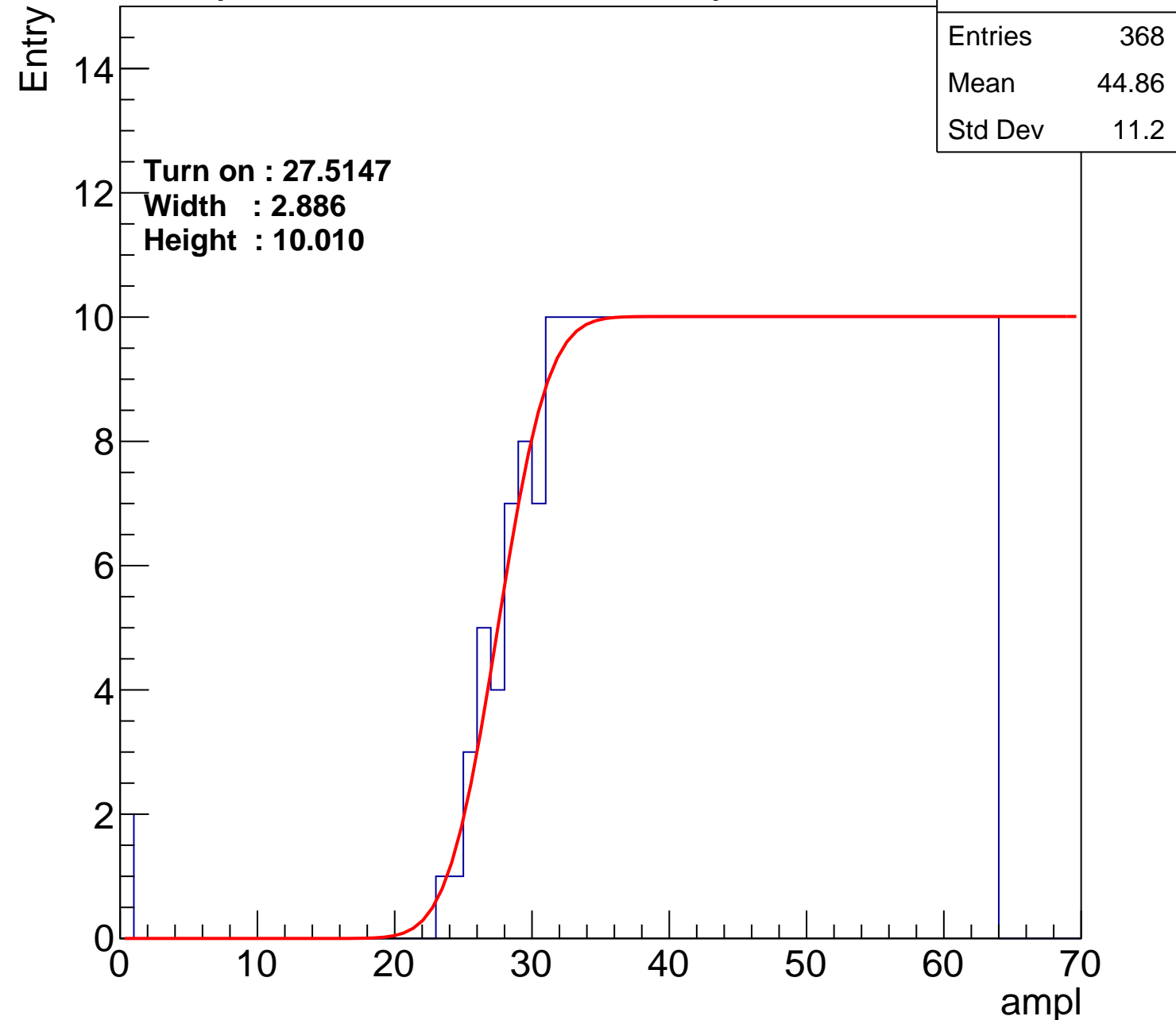
Width : 2.886

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch28

calib_packv5_042523_0143.root, FC#11, port A2

Entries	397
Mean	43.25
Std Dev	12.41

Turn on : 24.6578

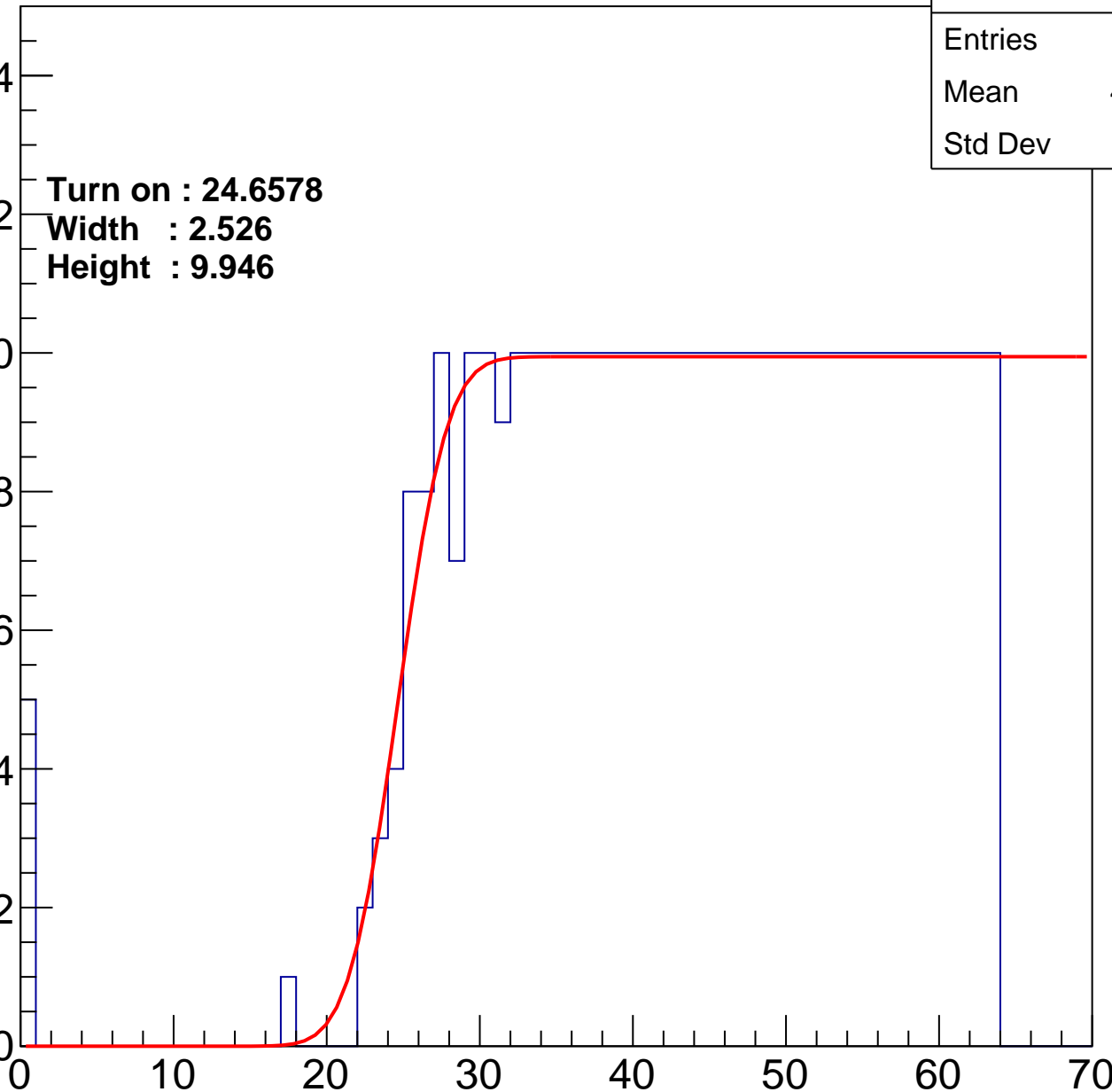
Width : 2.526

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch29

calib_packv5_042523_0143.root, FC#11, port A2

Entries	351
Mean	45.83
Std Dev	10.54

Turn on : 29.3890

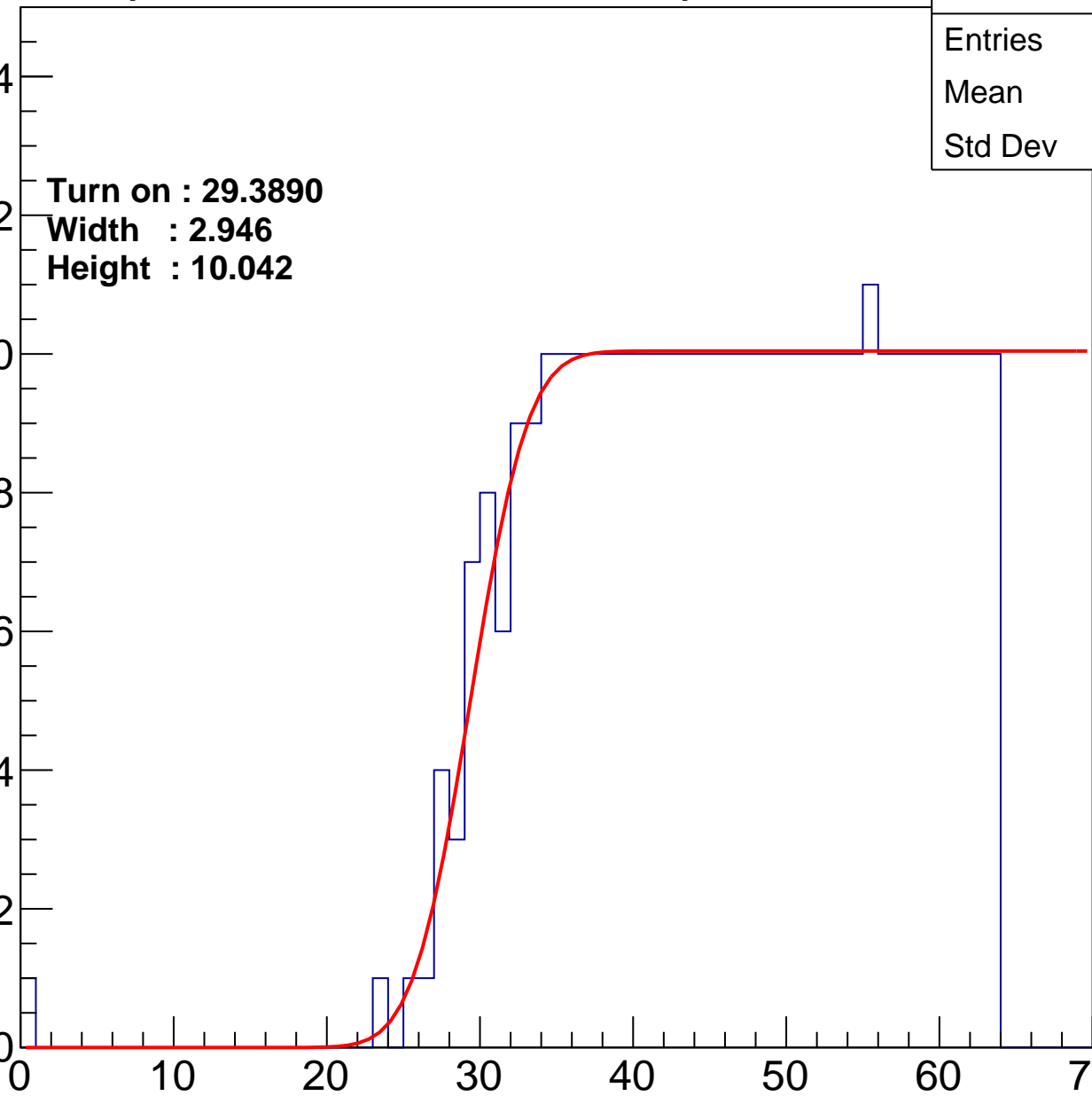
Width : 2.946

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch30

calib_packv5_042523_0143.root, FC#11, port A2

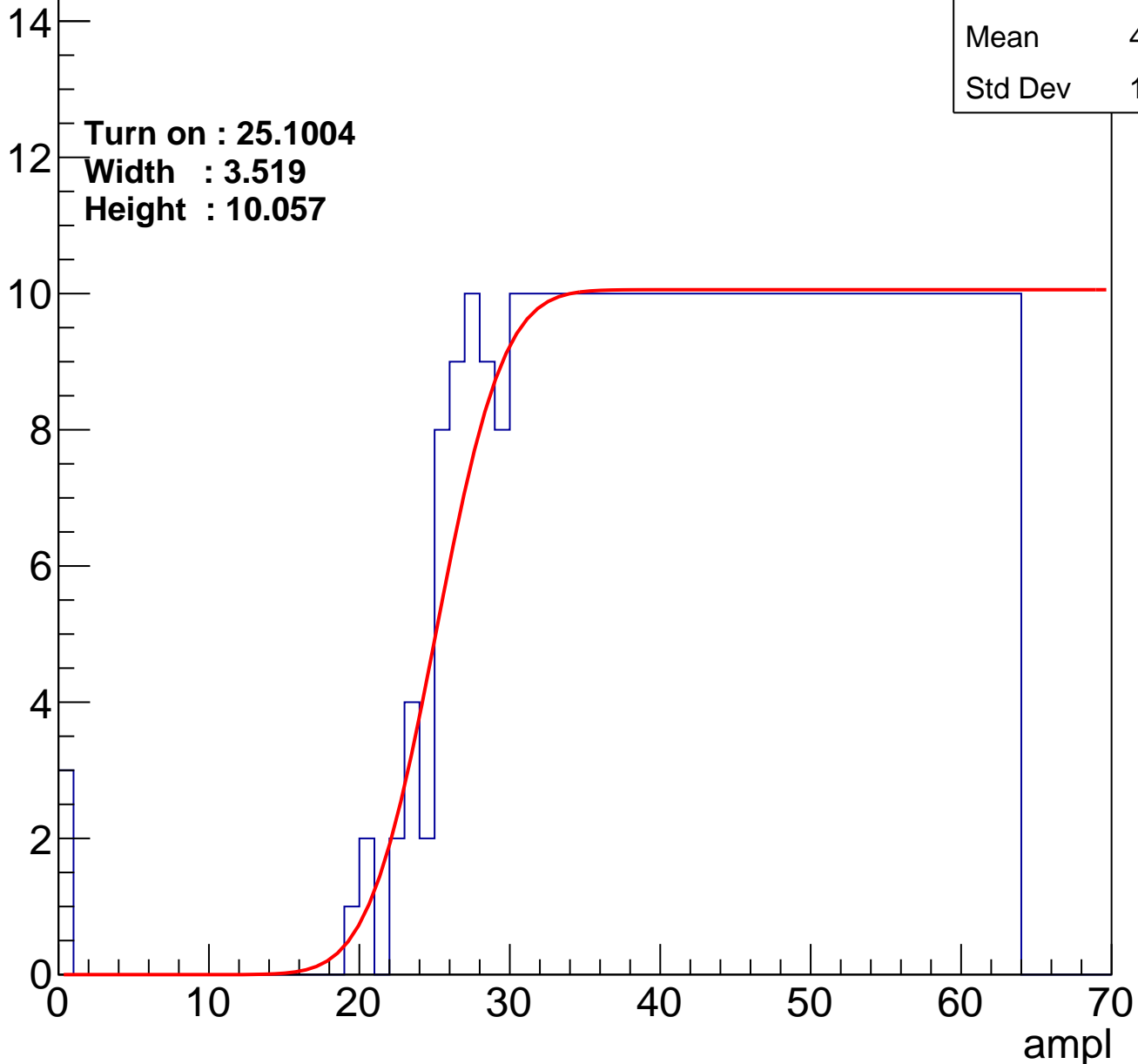
Entries	398
Mean	43.32
Std Dev	12.13

Turn on : 25.1004

Width : 3.519

Height : 10.057

Entry



B1L102S, U23-ch31

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.37
Std Dev	11.7

Turn on : 26.4014

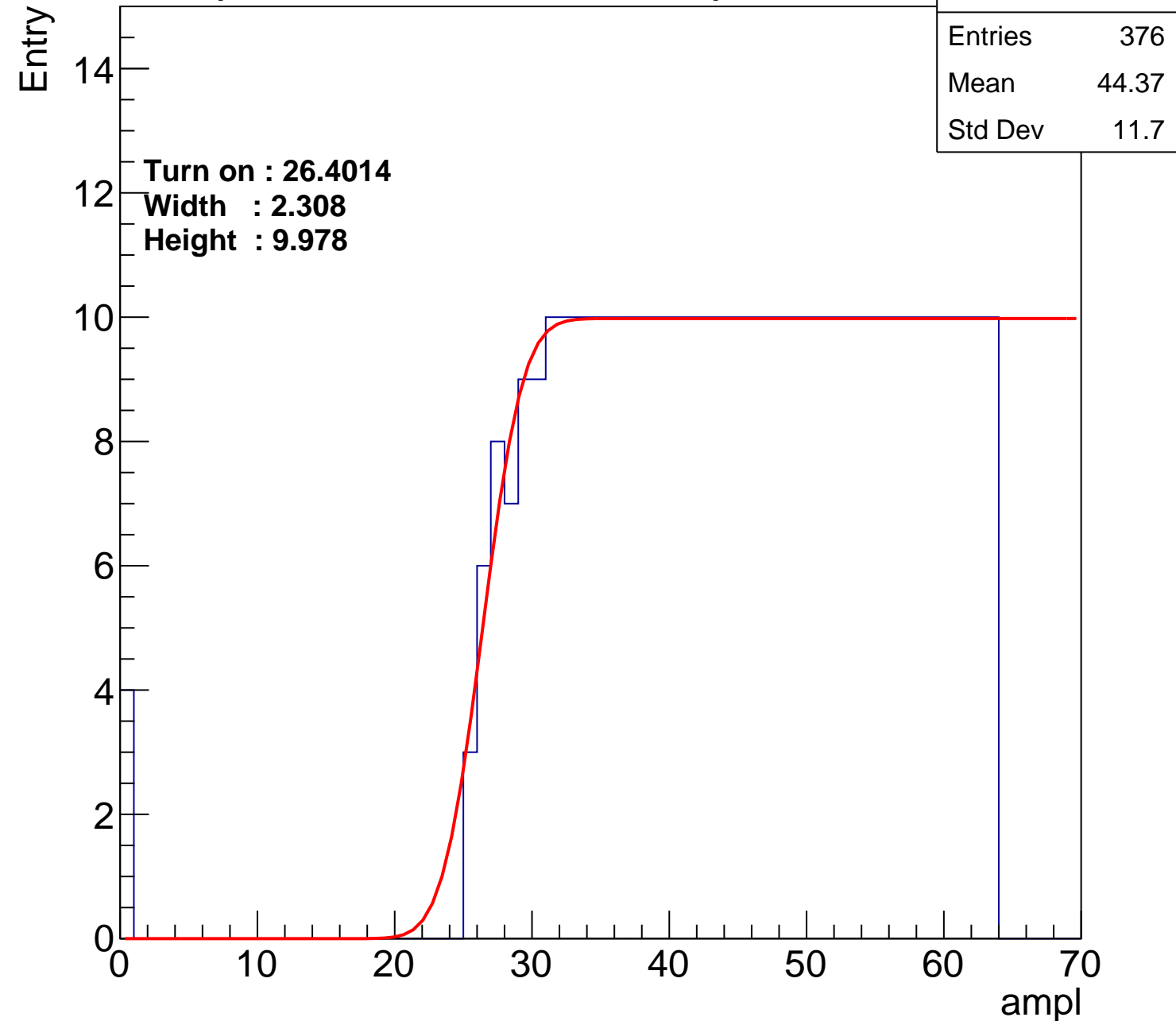
Width : 2.308

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch32

calib_packv5_042523_0143.root, FC#11, port A2

Entries	369
Mean	44.82
Std Dev	11.21

Turn on : 27.3662

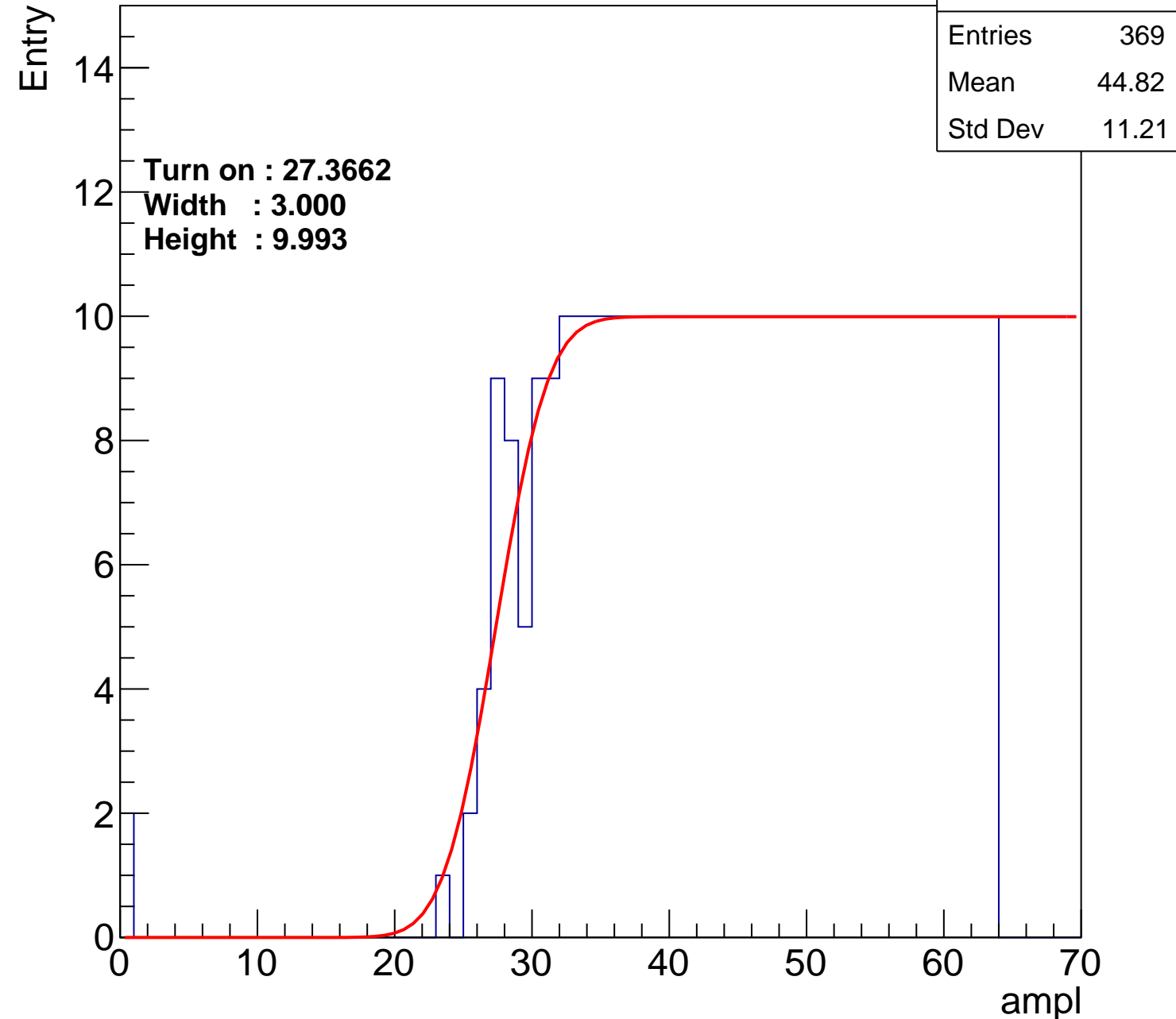
Width : 3.000

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch33

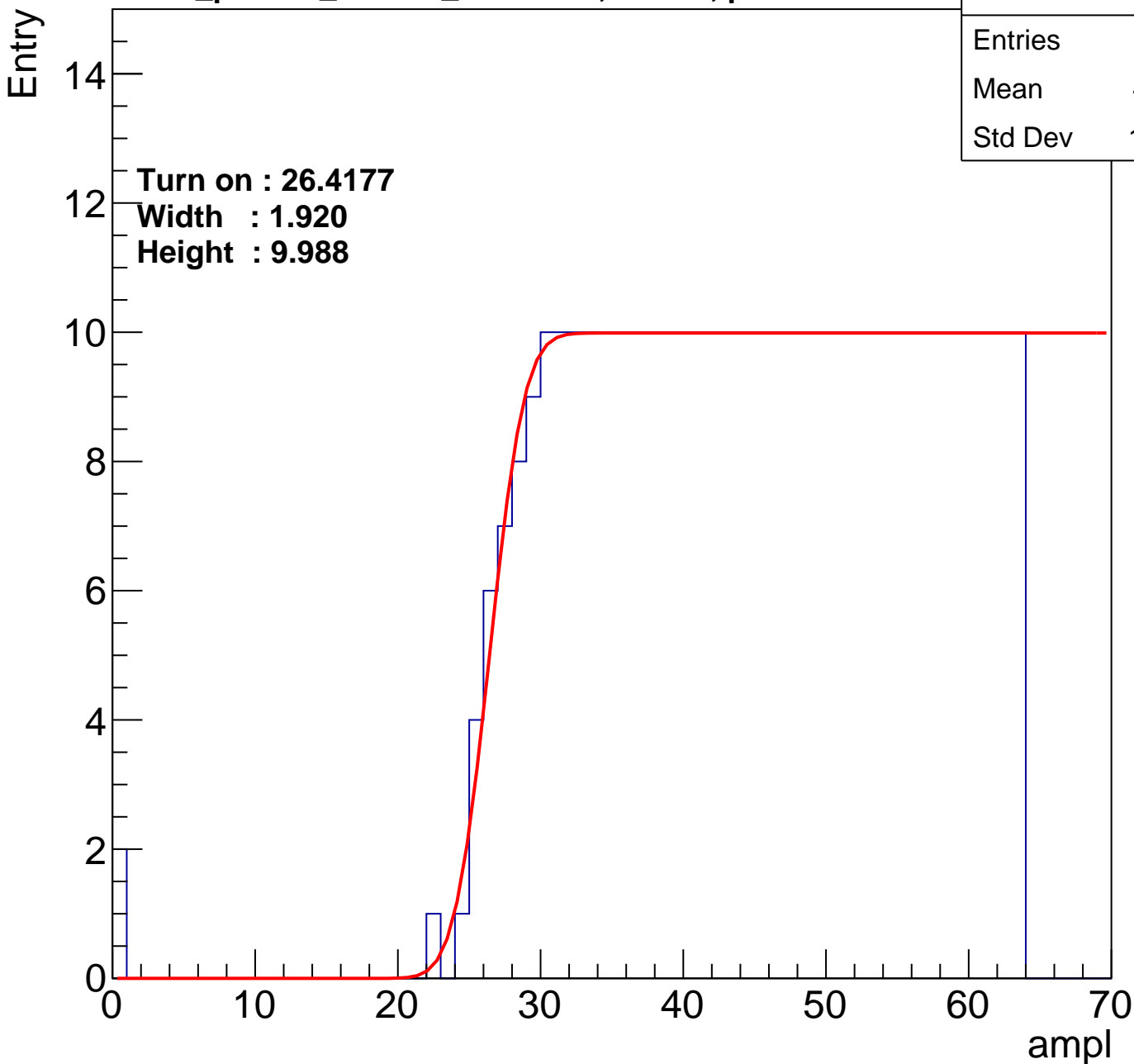
calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.41
Std Dev	11.39

Turn on : 26.4177

Width : 1.920

Height : 9.988



B1L102S, U23-ch34

calib_packv5_042523_0143.root, FC#11, port A2

Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.0748

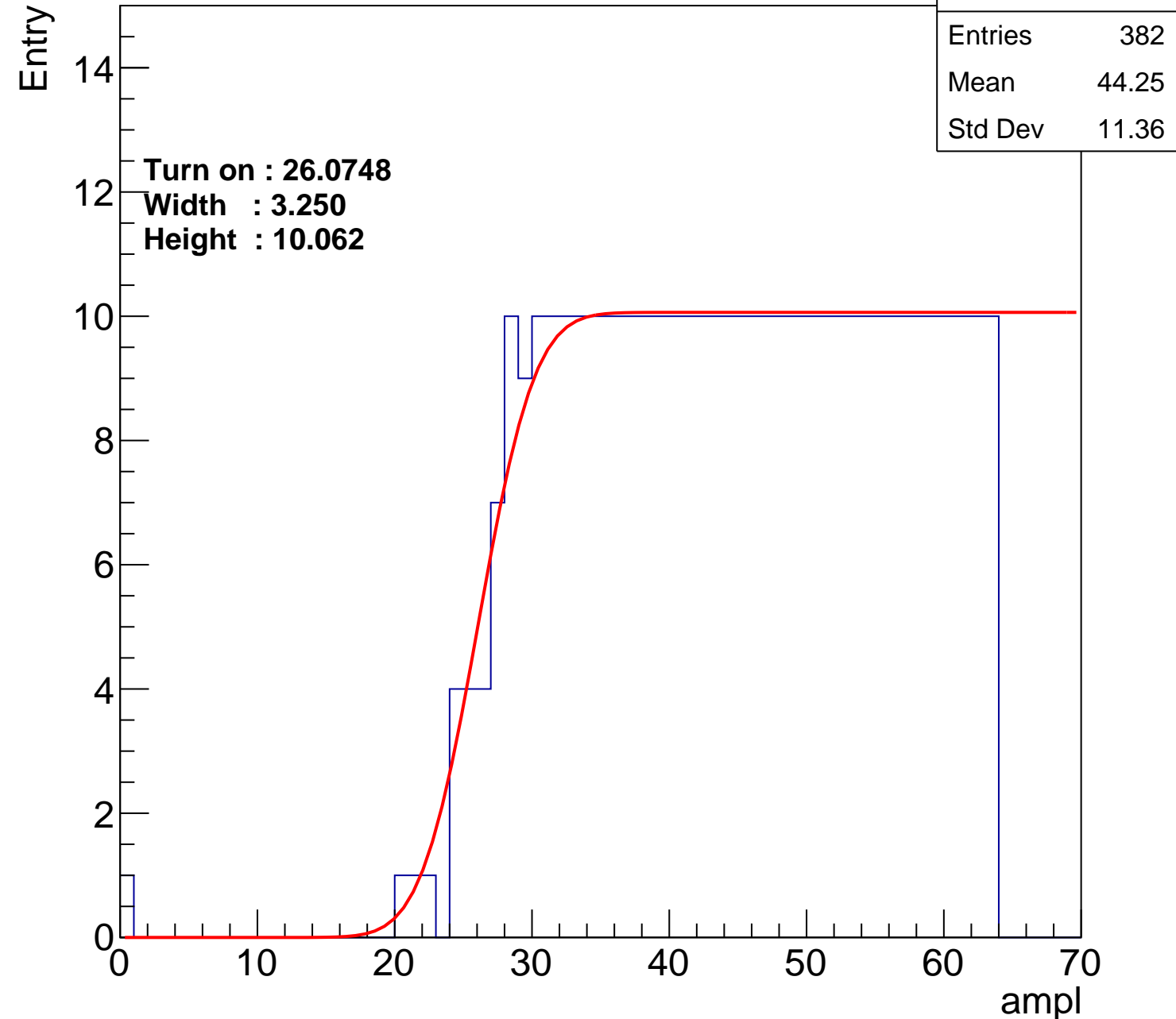
Width : 3.250

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch35

calib_packv5_042523_0143.root, FC#11, port A2

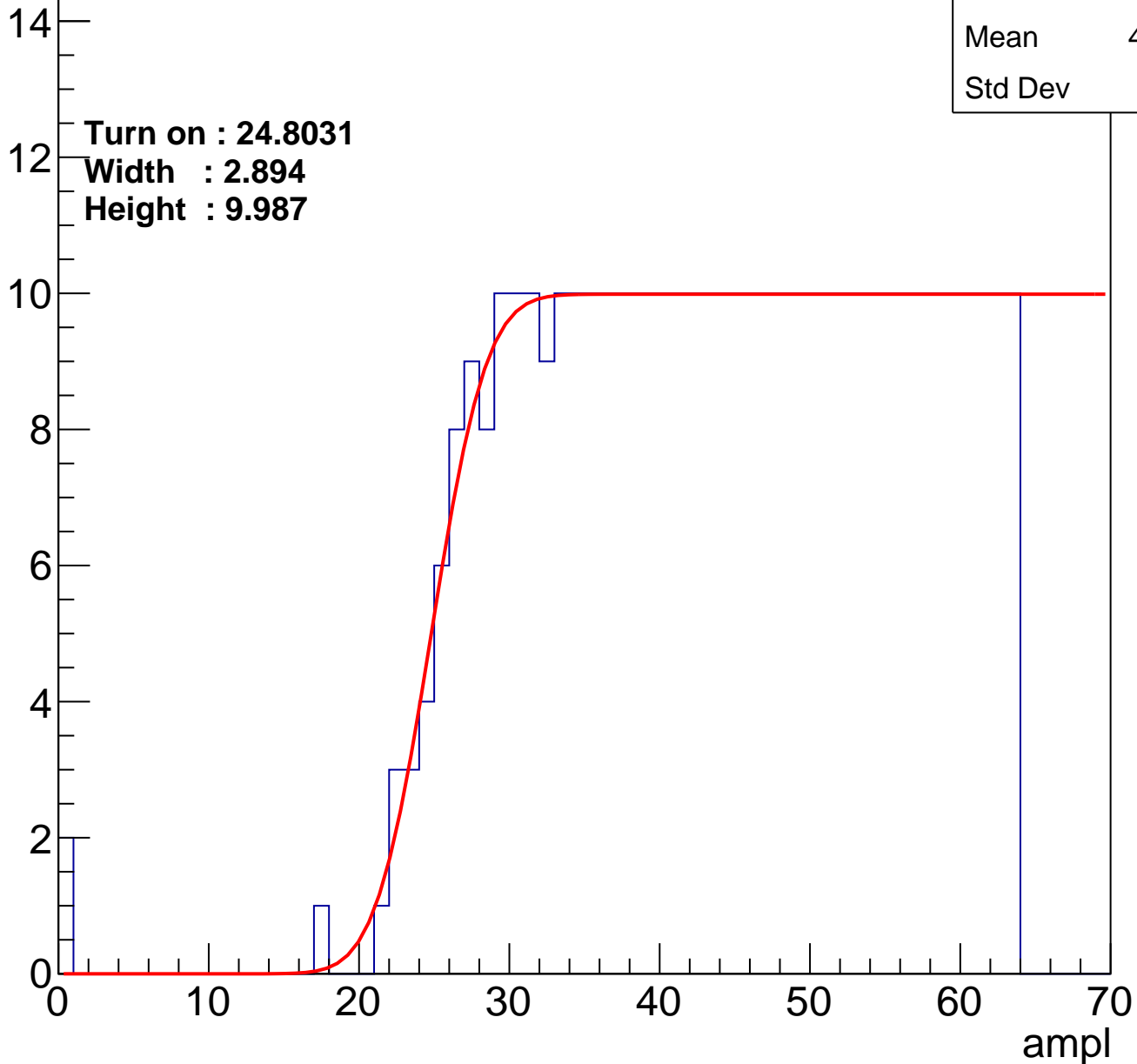
Entries	394
Mean	43.56
Std Dev	11.9

Turn on : 24.8031

Width : 2.894

Height : 9.987

Entry



B1L102S, U23-ch36

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.91
Std Dev	11.36

Turn on : 28.0527

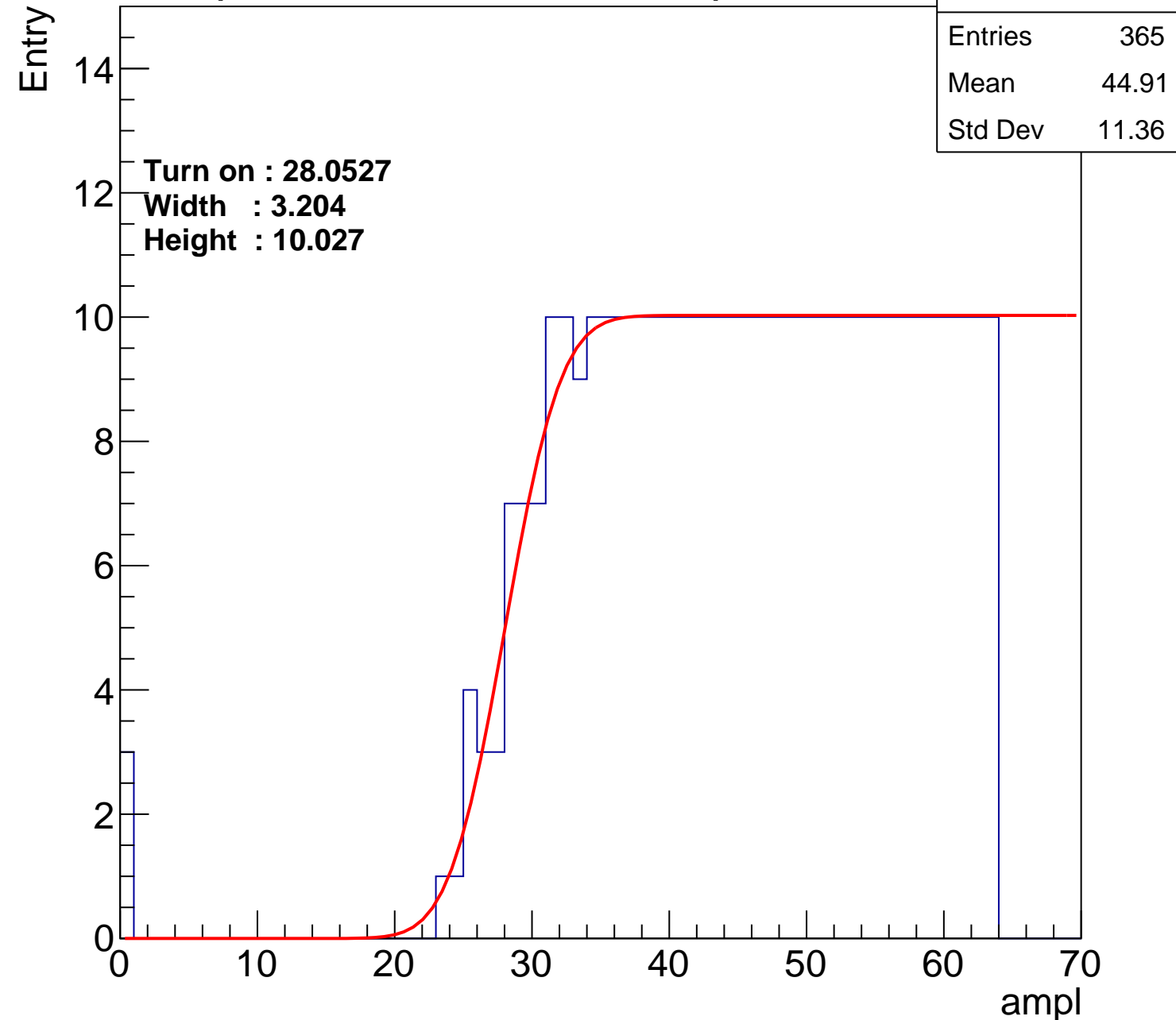
Width : 3.204

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch37

calib_packv5_042523_0143.root, FC#11, port A2

Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.4647

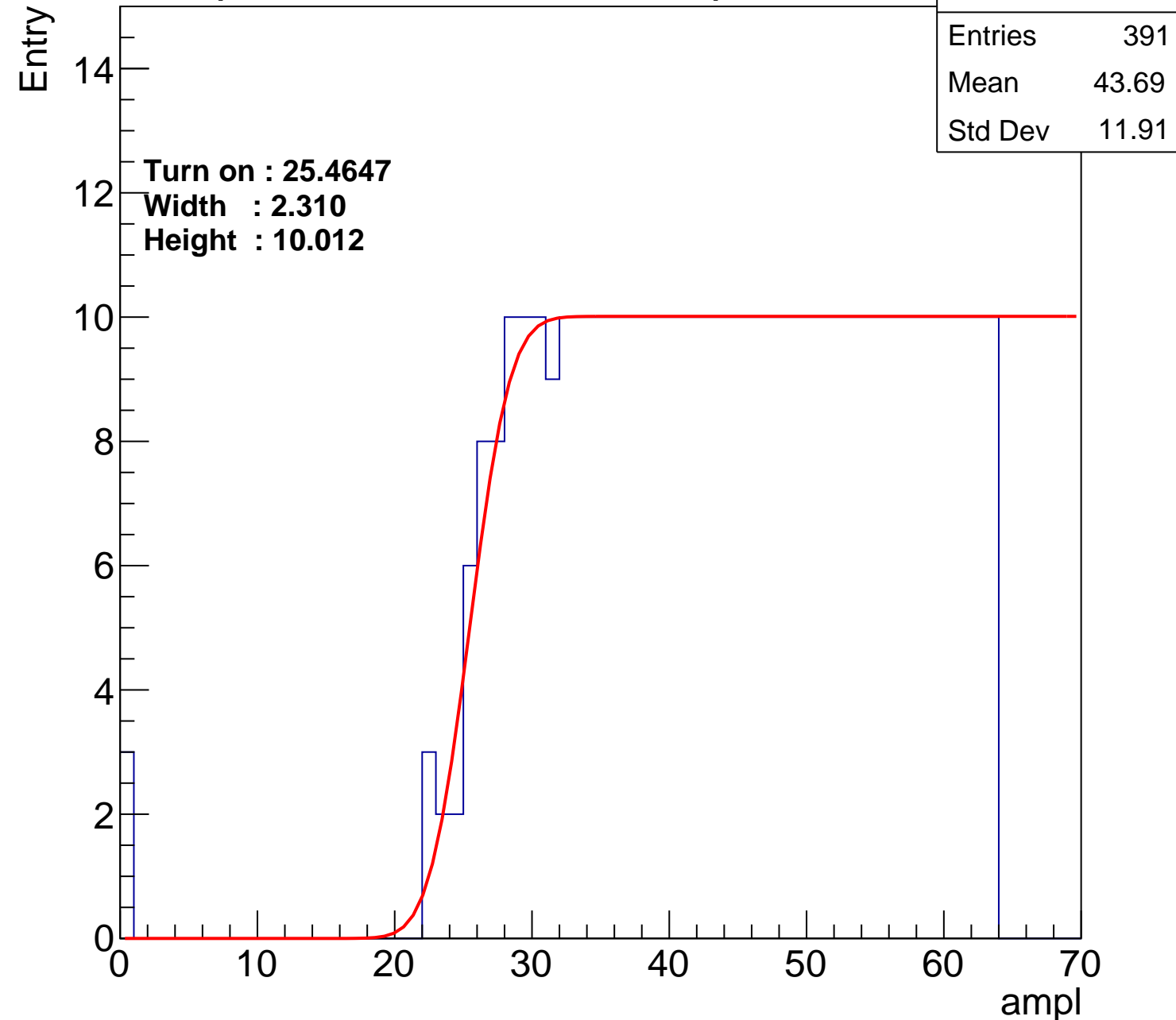
Width : 2.310

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch38

calib_packv5_042523_0143.root, FC#11, port A2

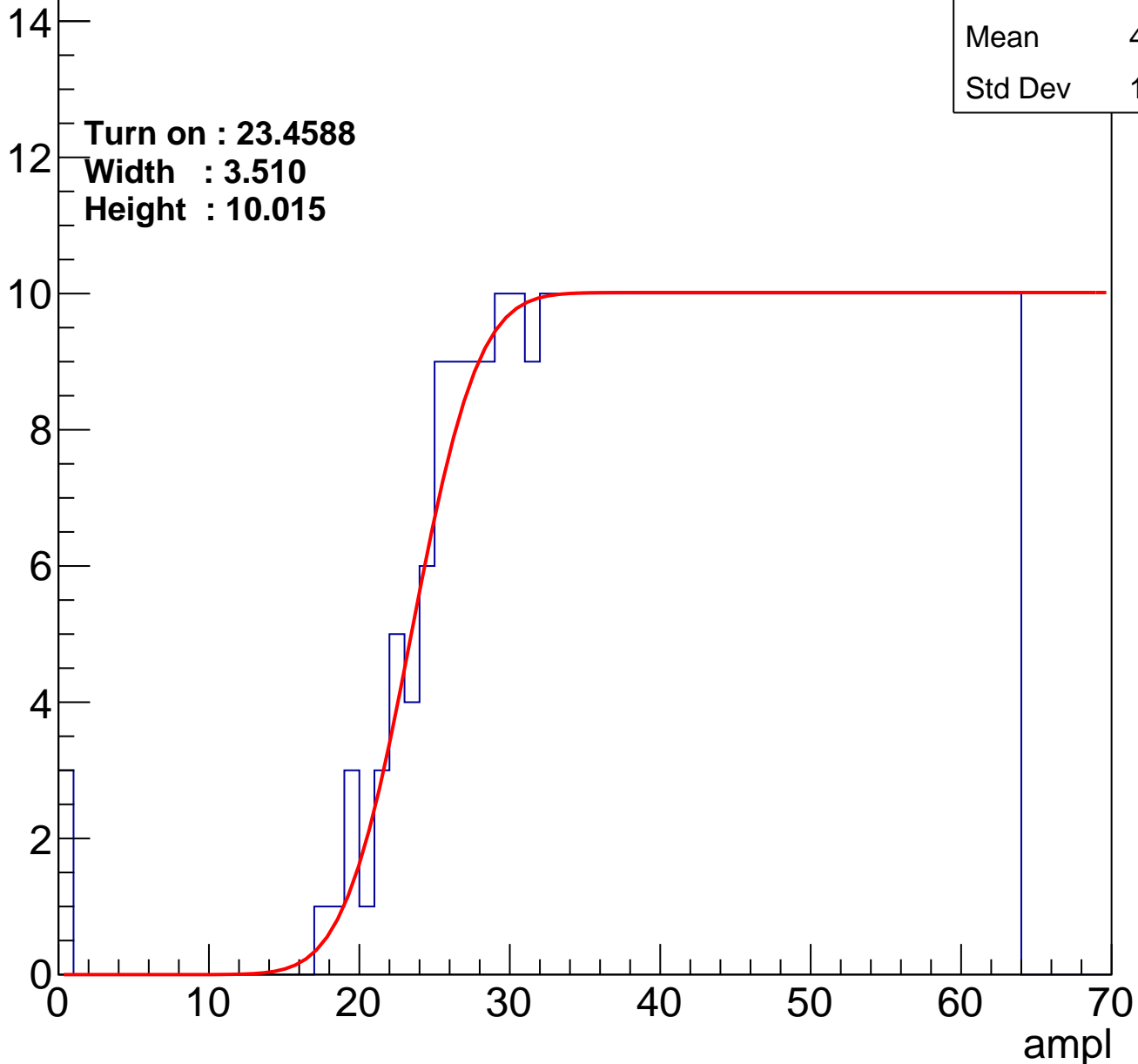
Entries	412
Mean	42.58
Std Dev	12.56

Turn on : 23.4588

Width : 3.510

Height : 10.015

Entry



B1L102S, U23-ch39

calib_packv5_042523_0143.root, FC#11, port A2

Entries	370
Mean	44.74
Std Dev	11.29

Turn on : 27.7635

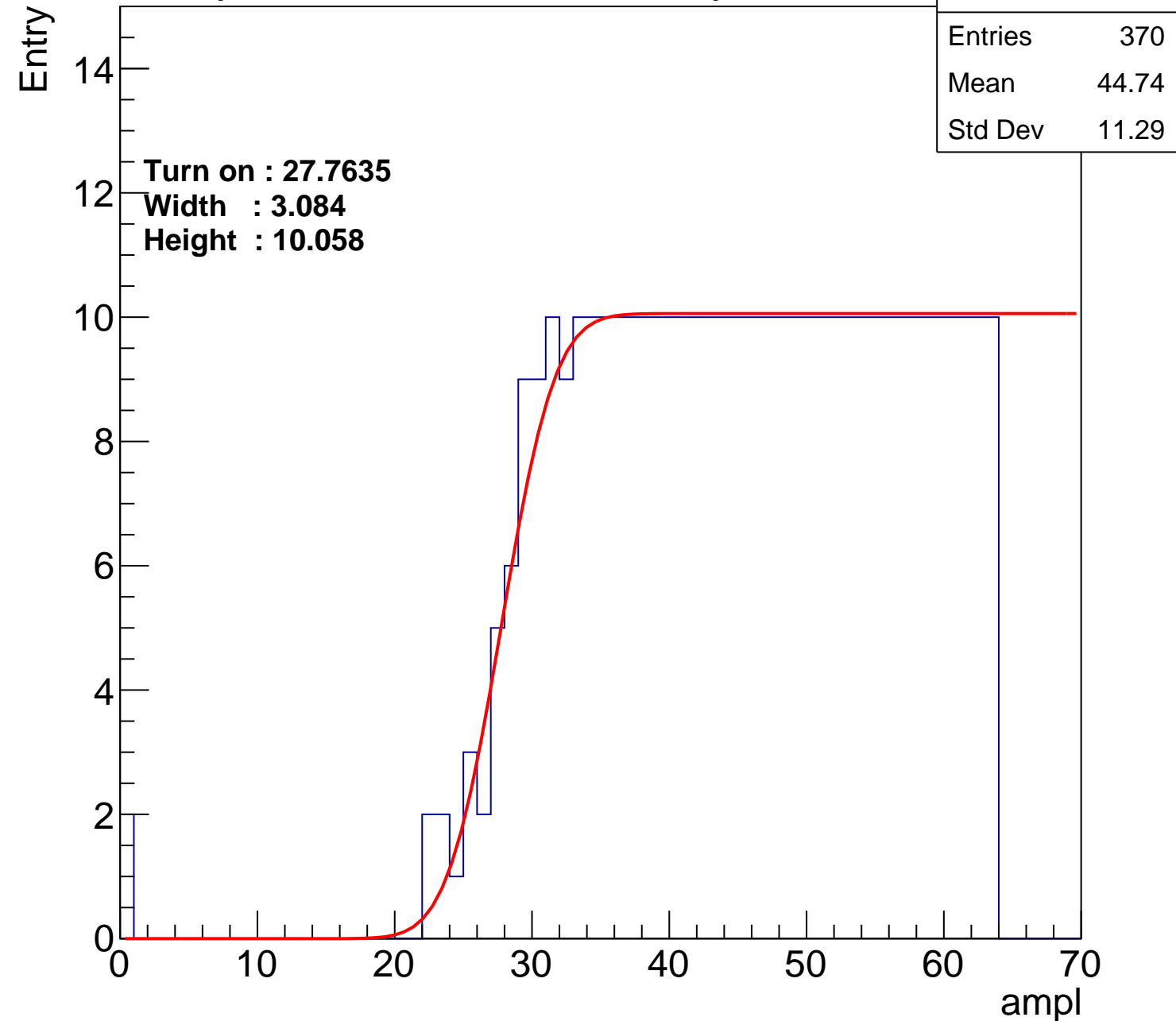
Width : 3.084

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch40

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.49
Std Dev	11.84

Turn on : 24.9845

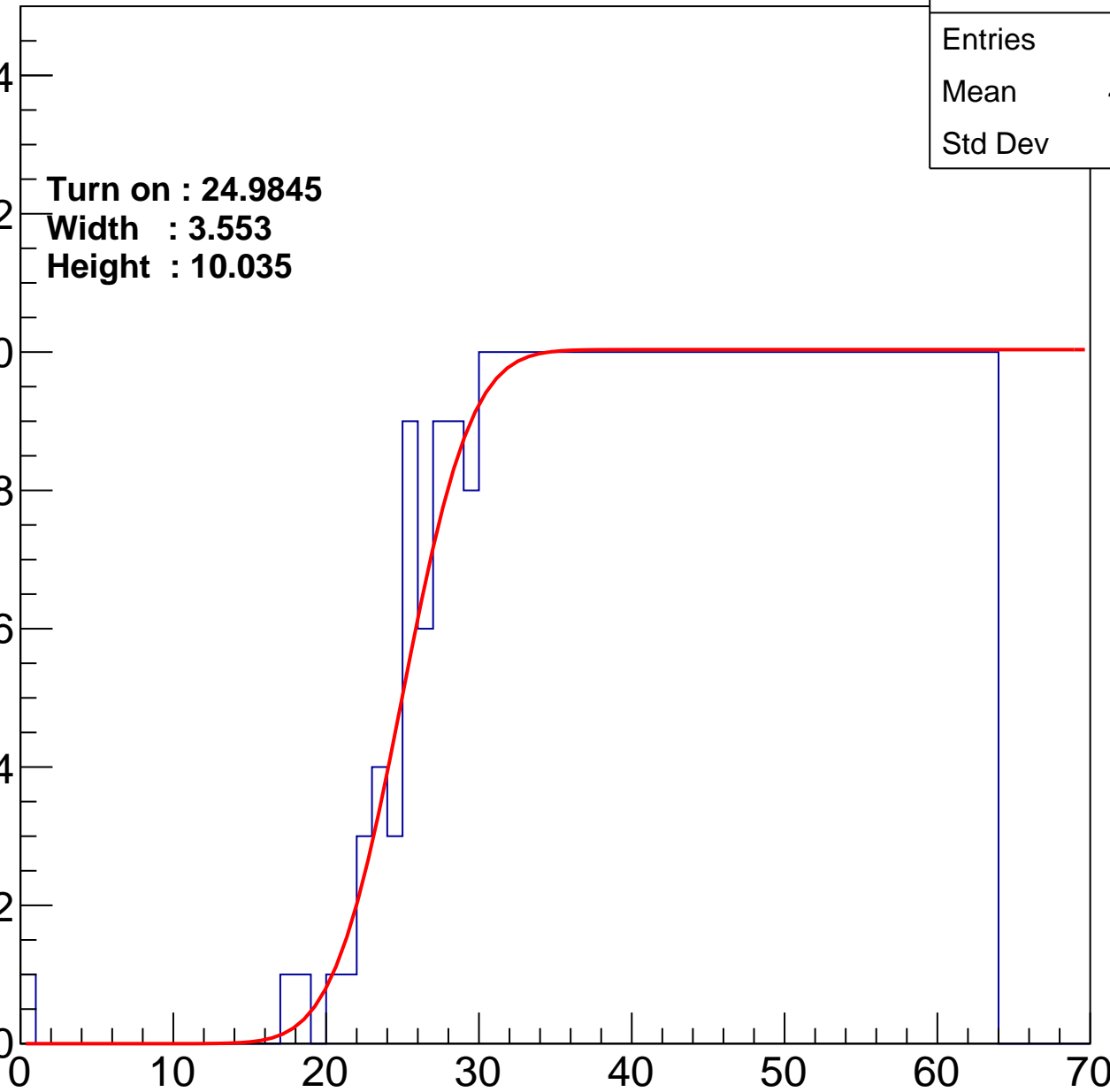
Width : 3.553

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch41

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.27
Std Dev	11.67

Turn on : 26.3628

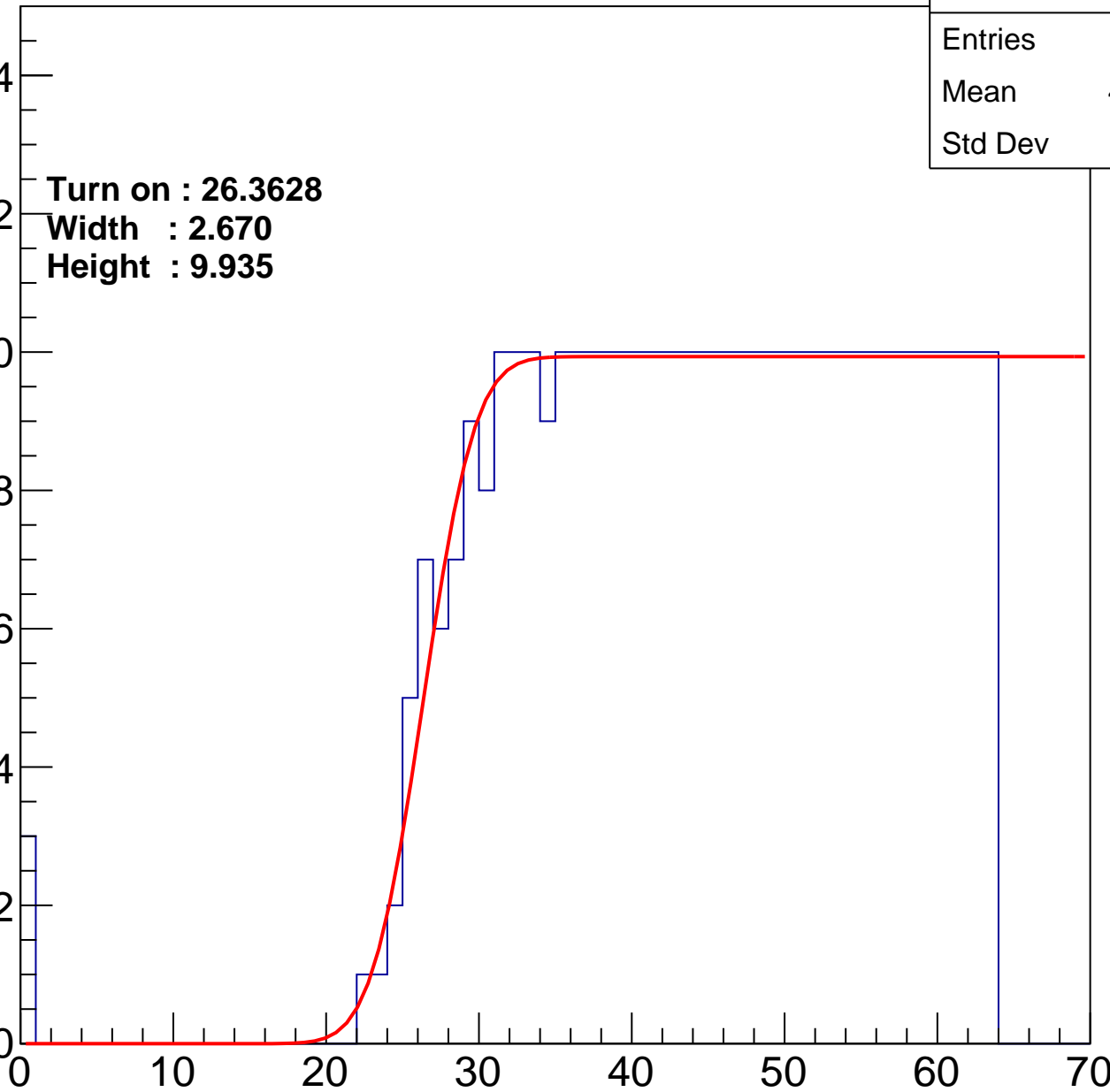
Width : 2.670

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch42

calib_packv5_042523_0143.root, FC#11, port A2

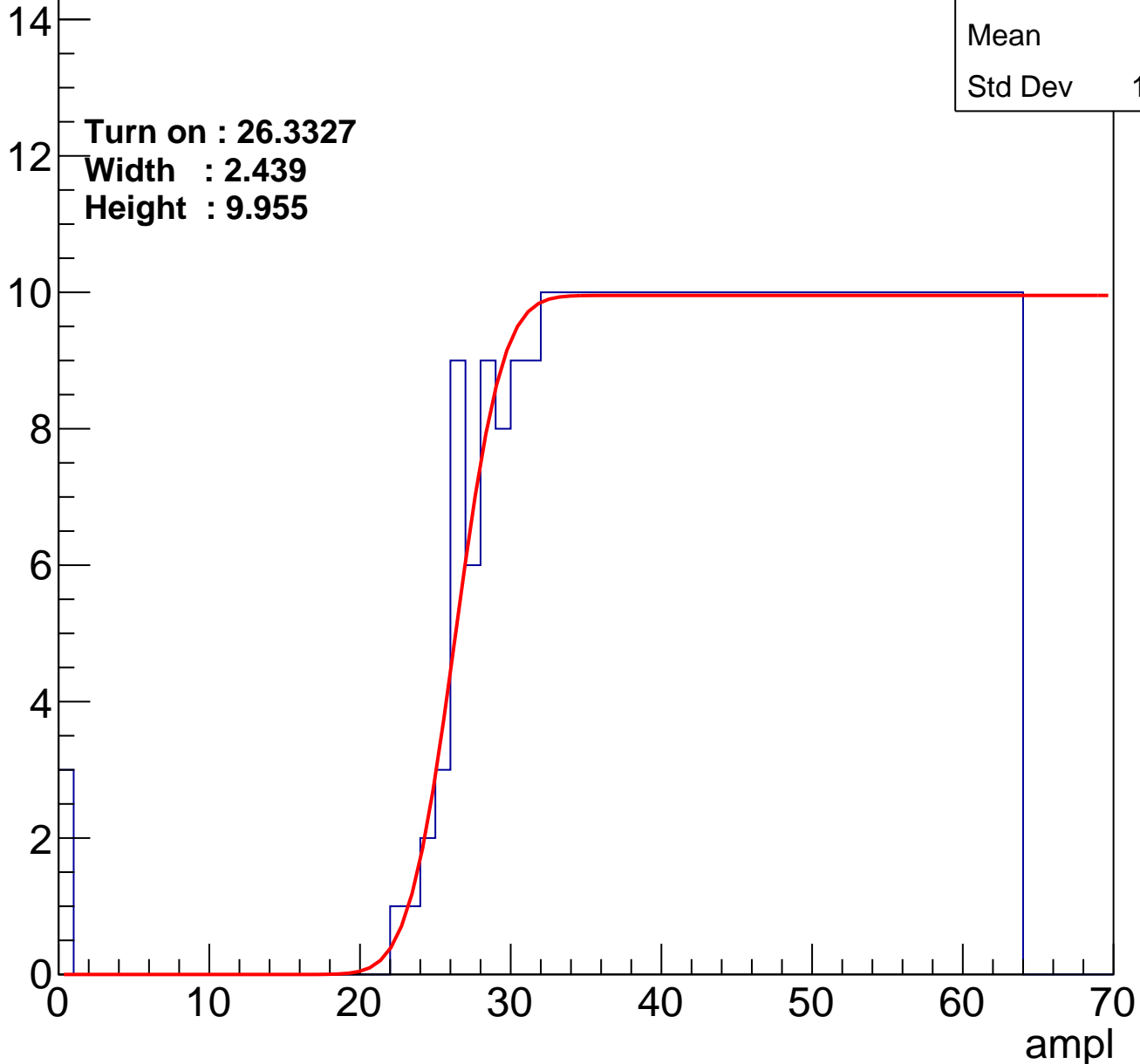
Entries	380
Mean	44.2
Std Dev	11.68

Turn on : 26.3327

Width : 2.439

Height : 9.955

Entry



B1L102S, U23-ch43

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.2
Std Dev	12.39

Turn on : 25.6174

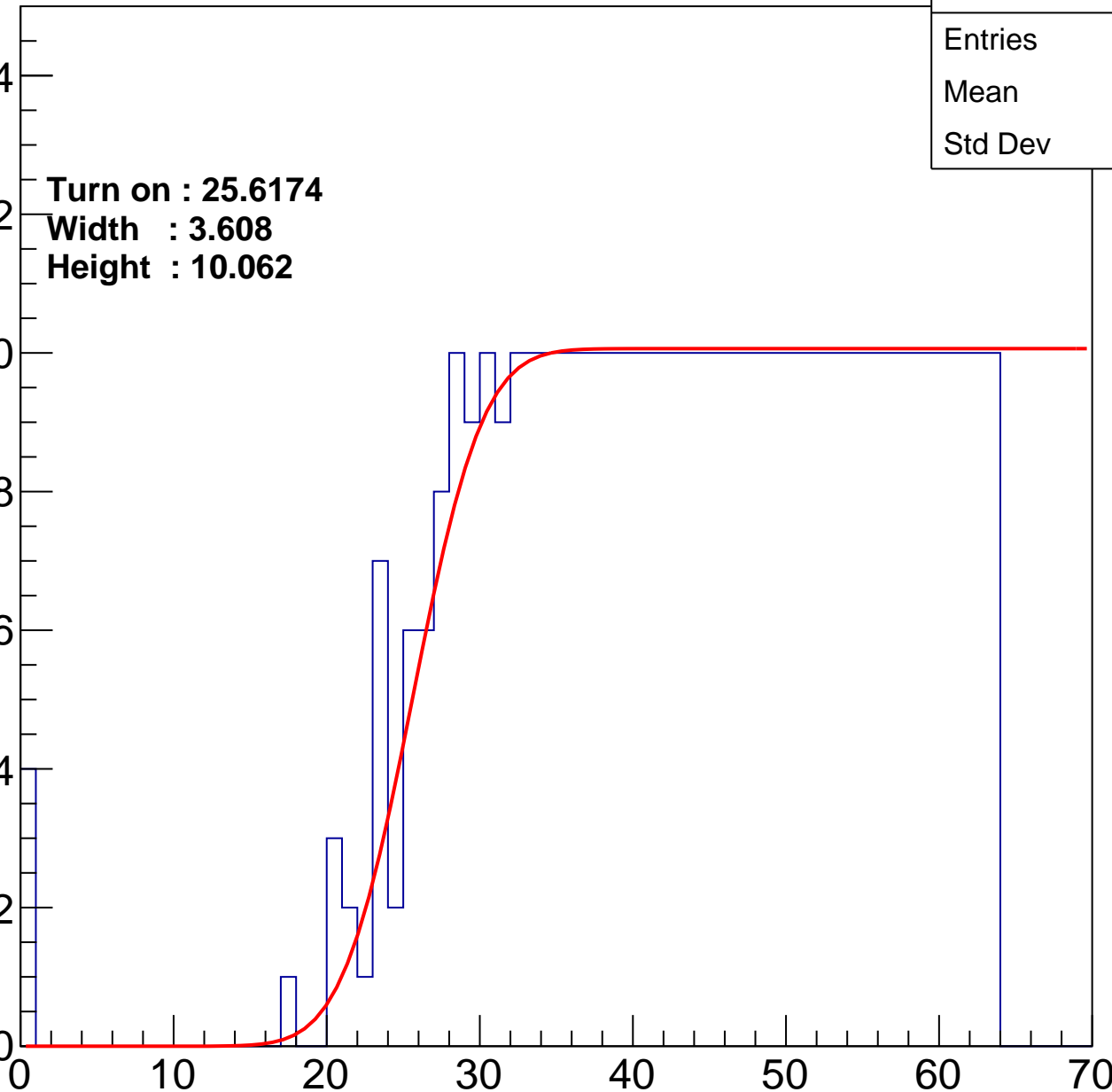
Width : 3.608

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch44

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.28
Std Dev	10.8

Turn on : 28.2377

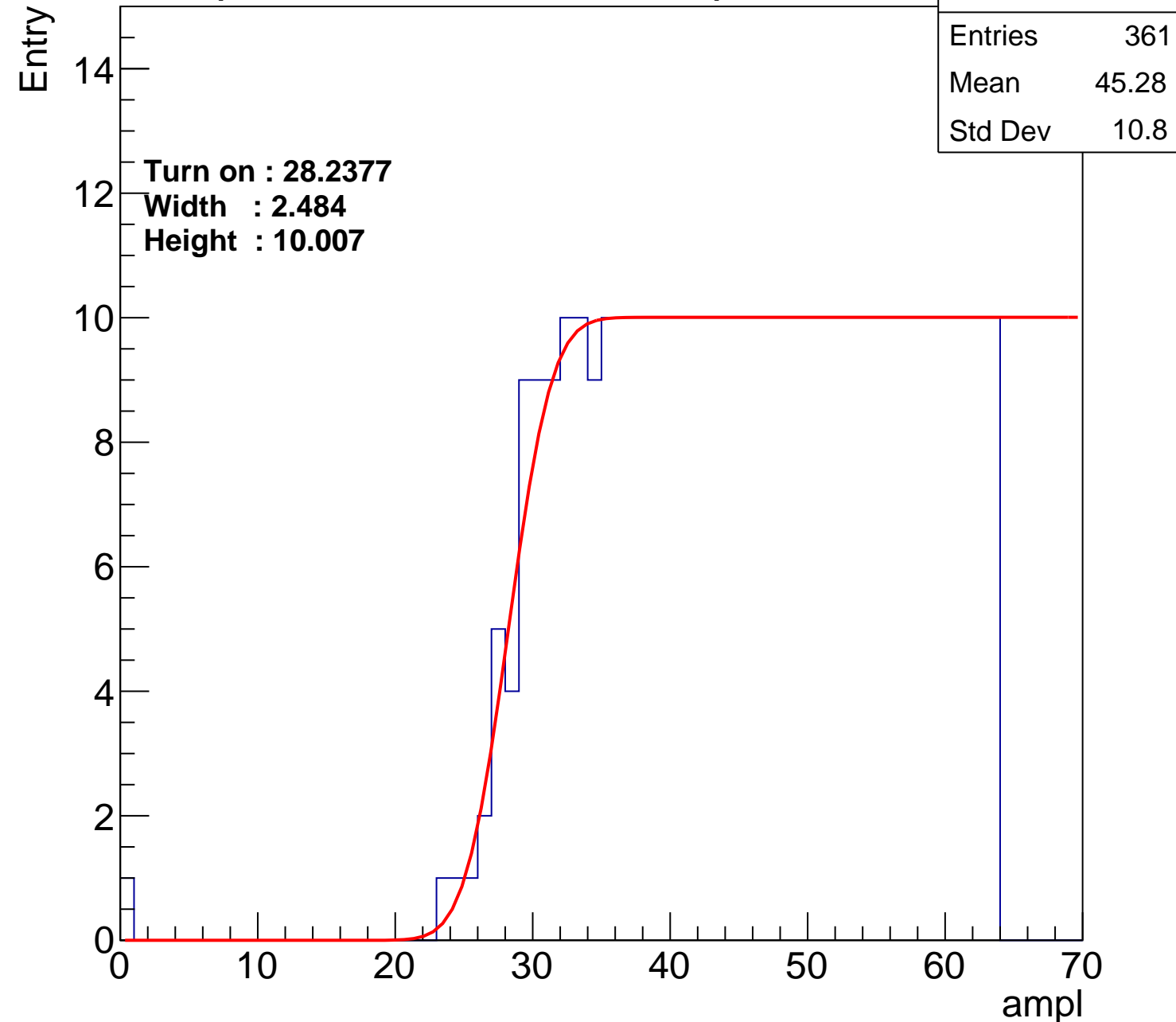
Width : 2.484

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch45

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.14
Std Dev	11.65

Turn on : 26.1213

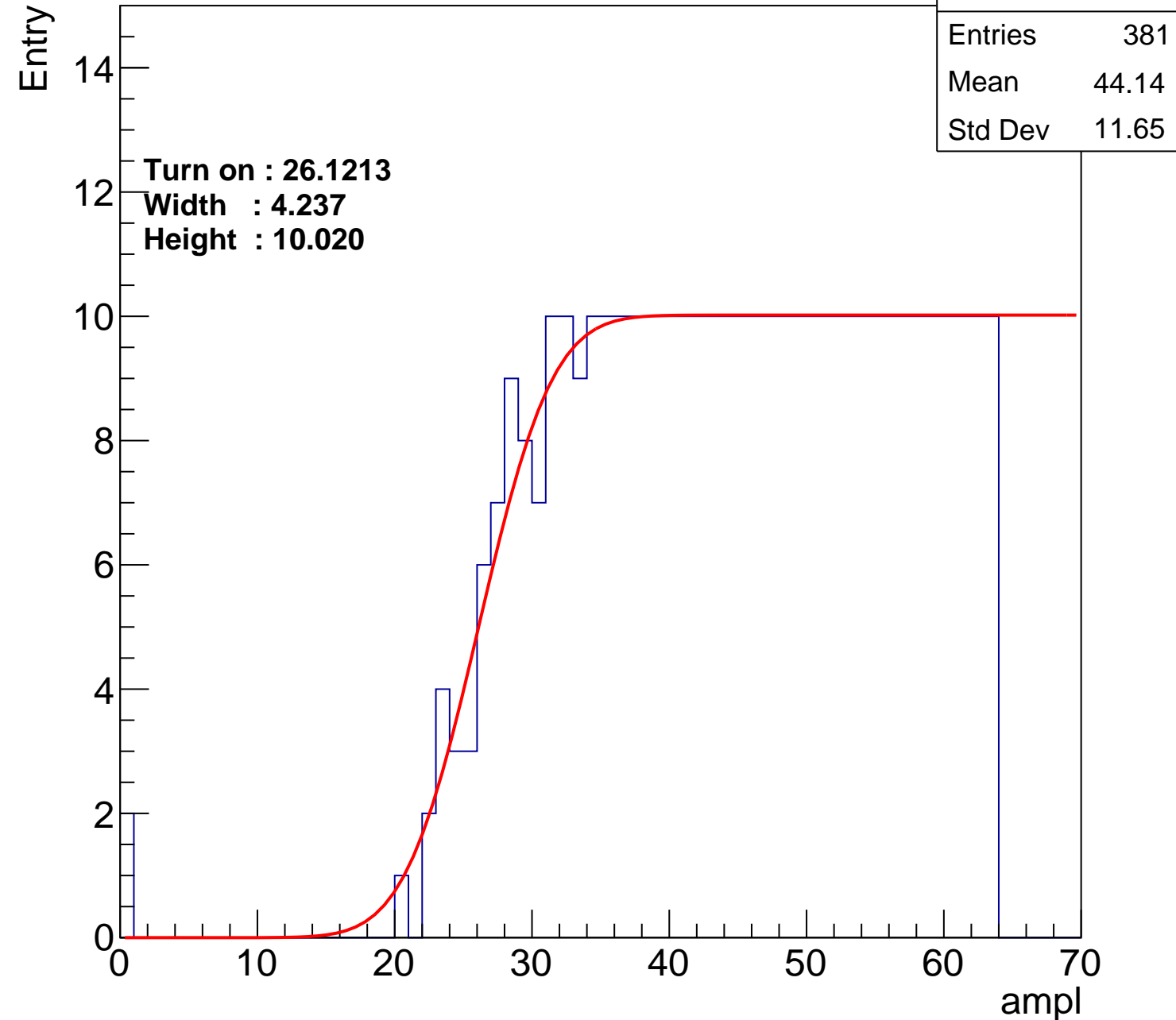
Width : 4.237

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch46

calib_packv5_042523_0143.root, FC#11, port A2

Entries	394
Mean	43.49
Std Dev	12.01

Turn on : 25.2120

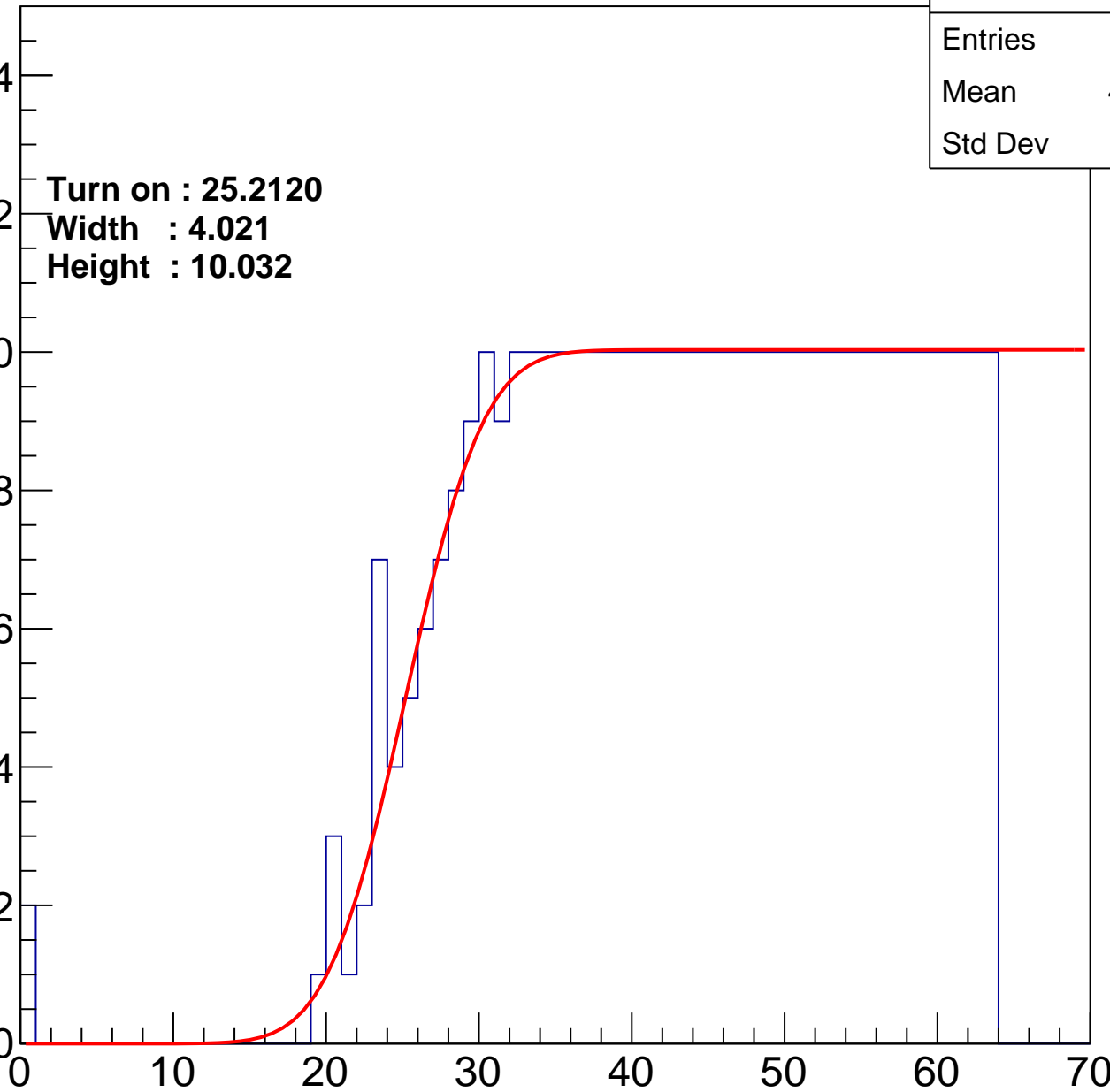
Width : 4.021

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch47

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	43.95
Std Dev	12.17

Turn on : 26.7701

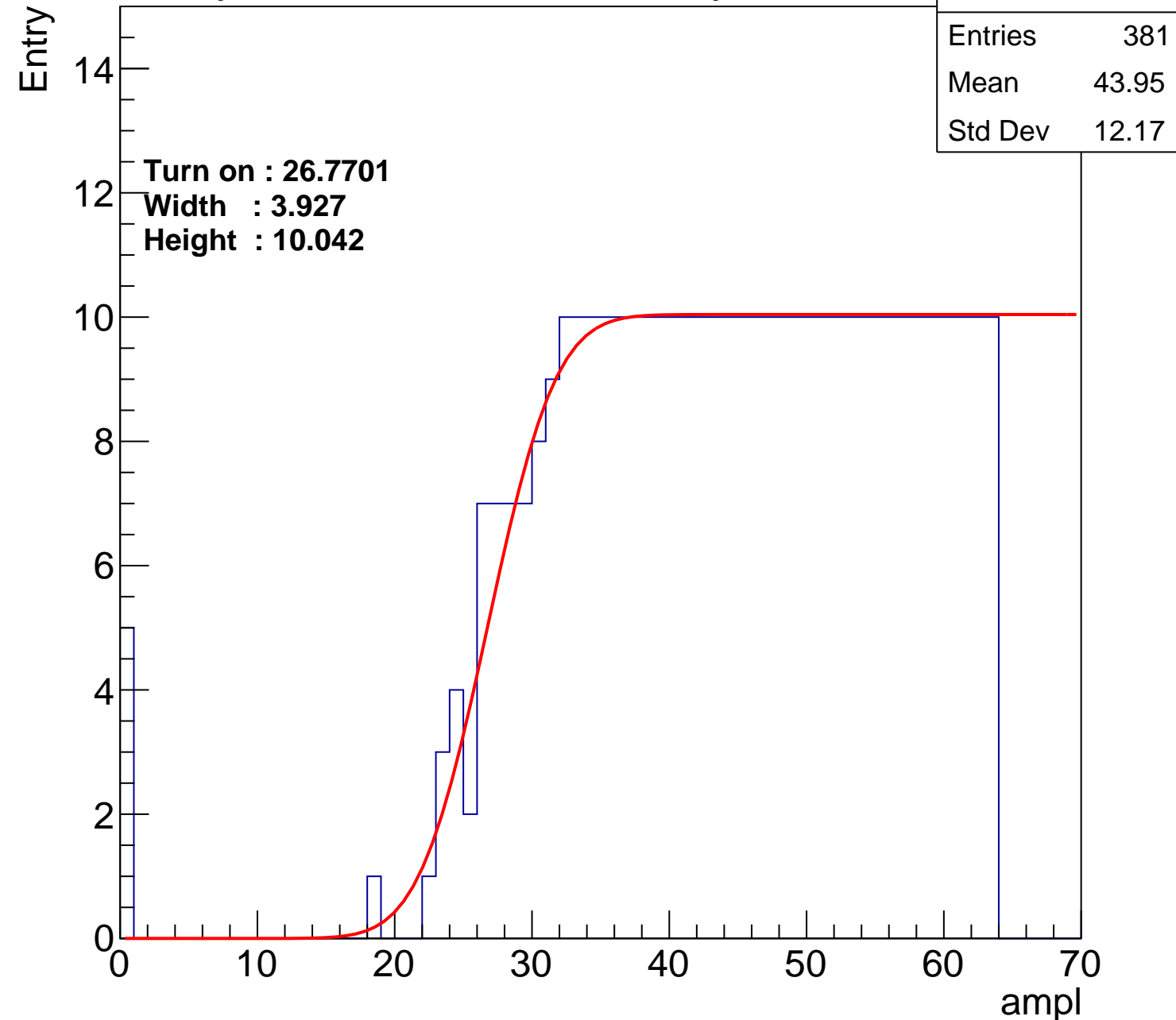
Width : 3.927

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch48

calib_packv5_042523_0143.root, FC#11, port A2

Entries	358
Mean	45.31
Std Dev	11.11

Turn on : 28.7081

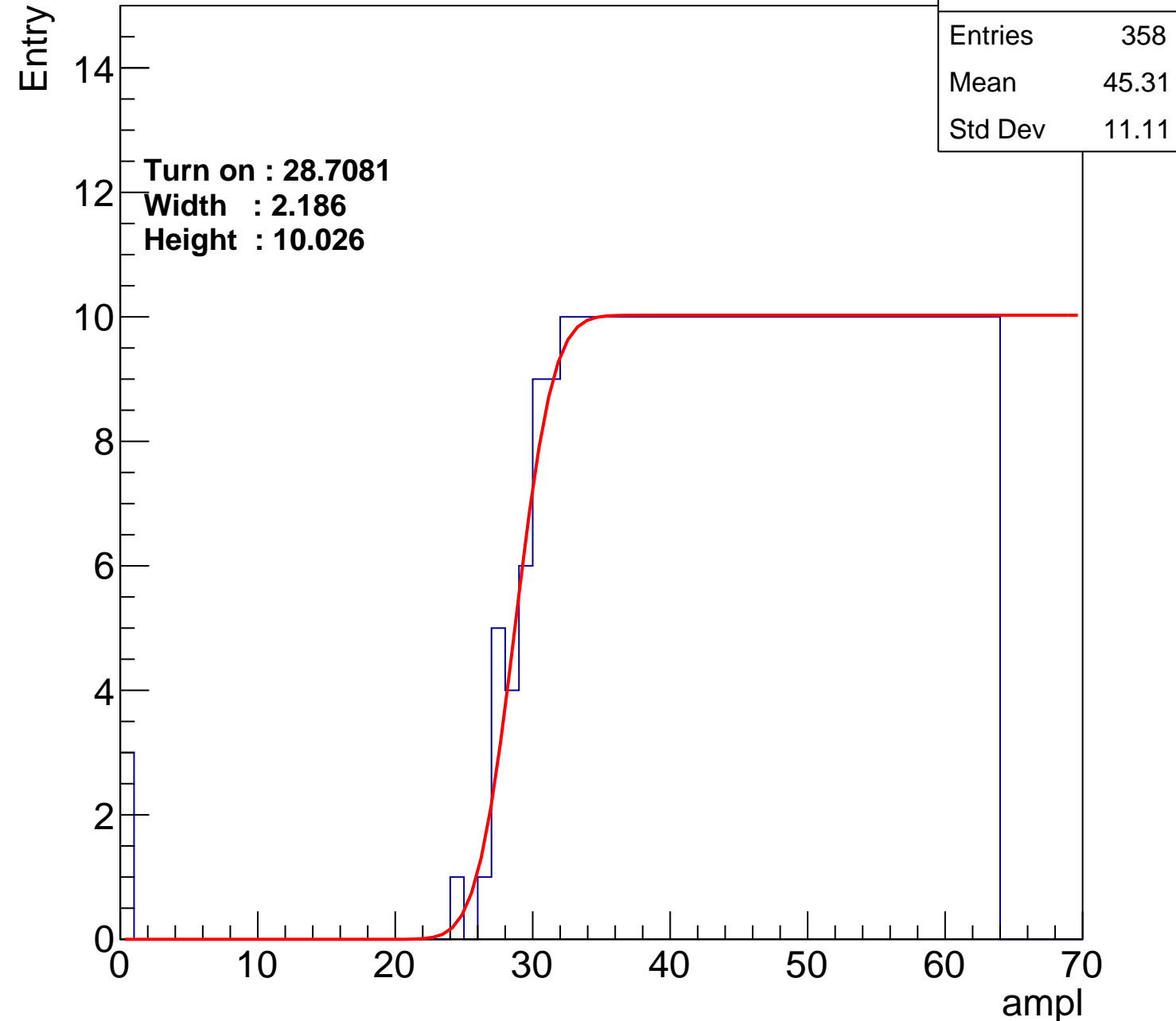
Width : 2.186

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch49

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.82
Std Dev	11.69

Turn on : 25.8825

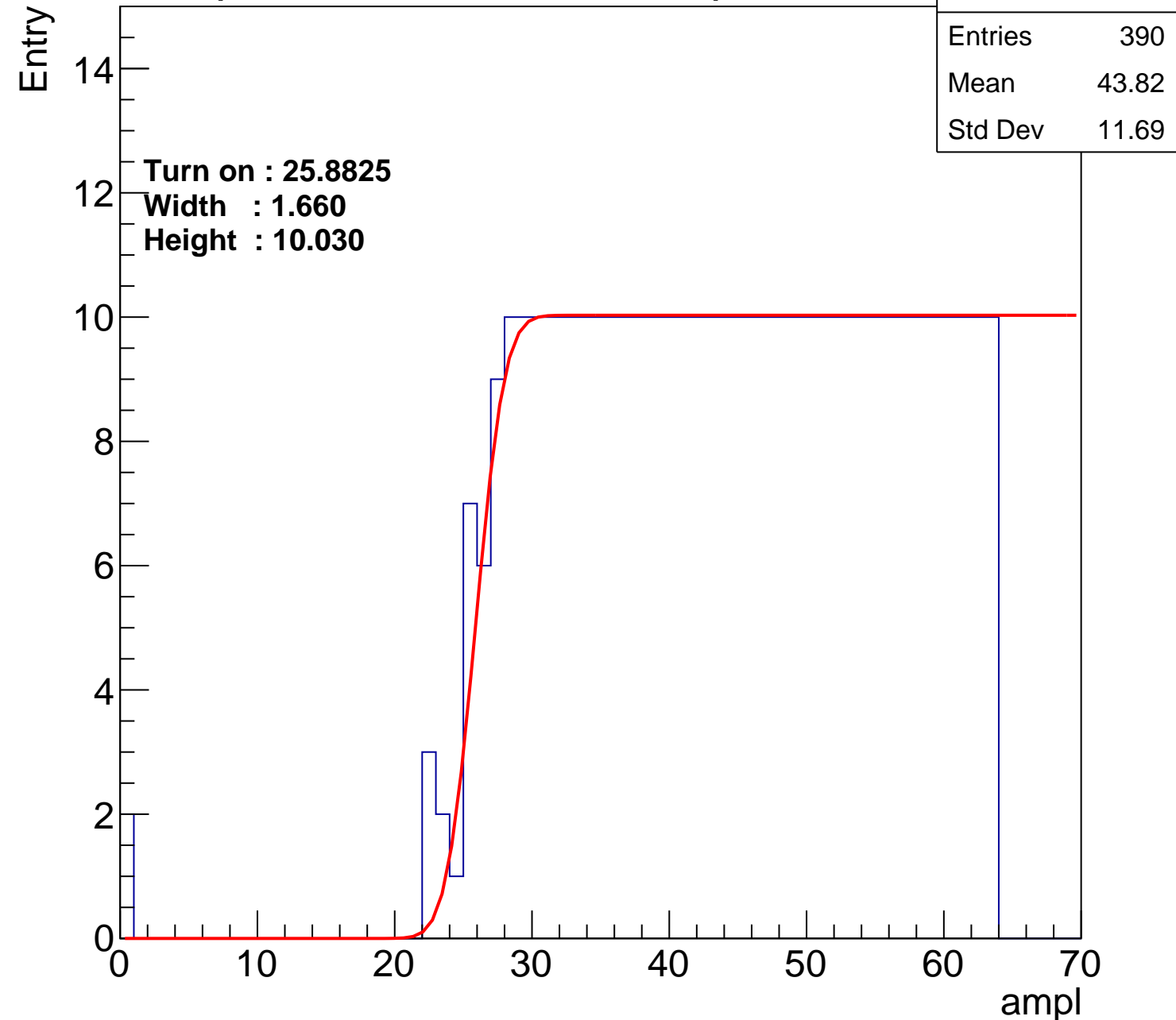
Width : 1.660

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch50

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.68
Std Dev	11.91

Turn on : 25.3656

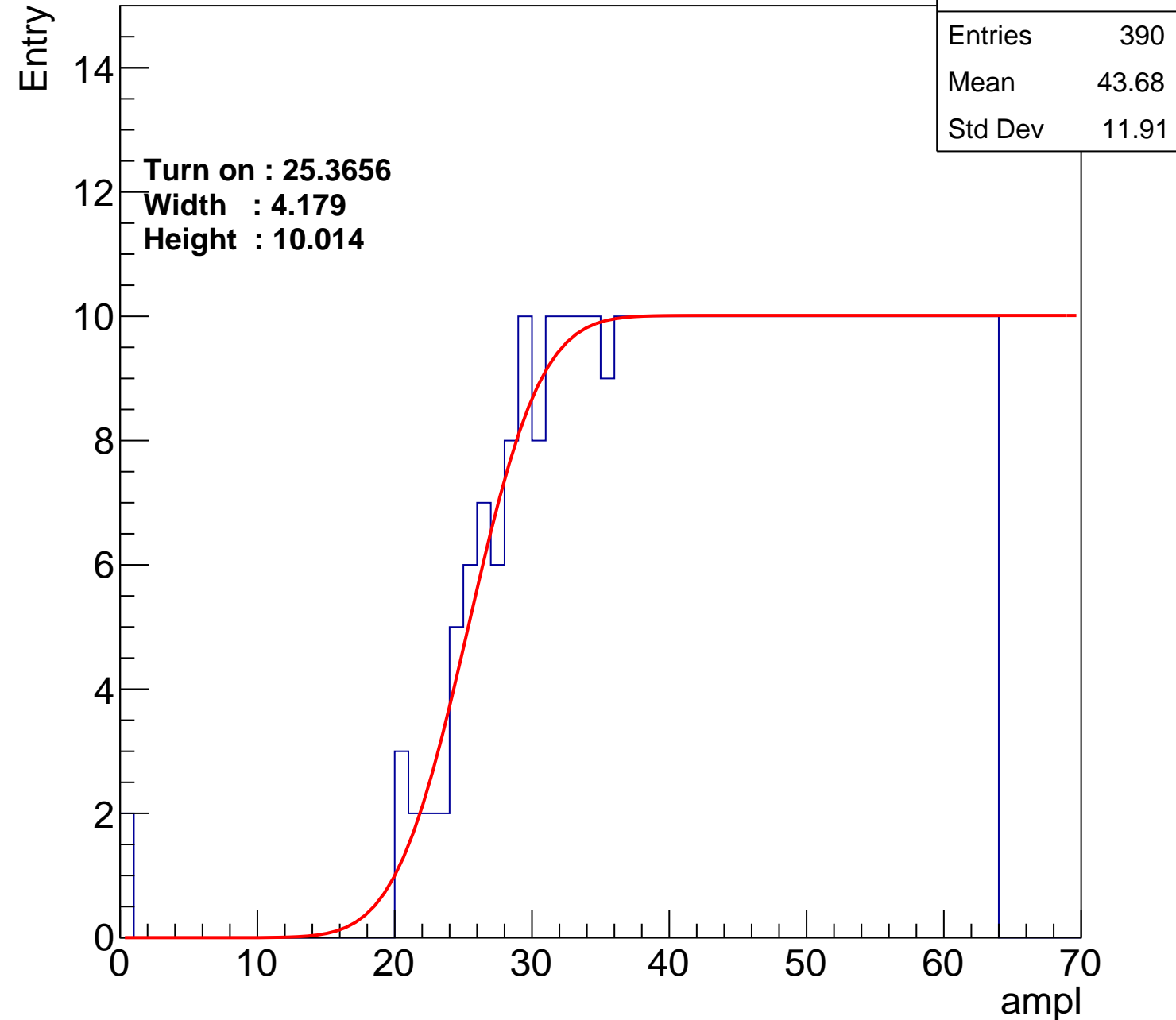
Width : 4.179

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch51

calib_packv5_042523_0143.root, FC#11, port A2

Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 27.2154

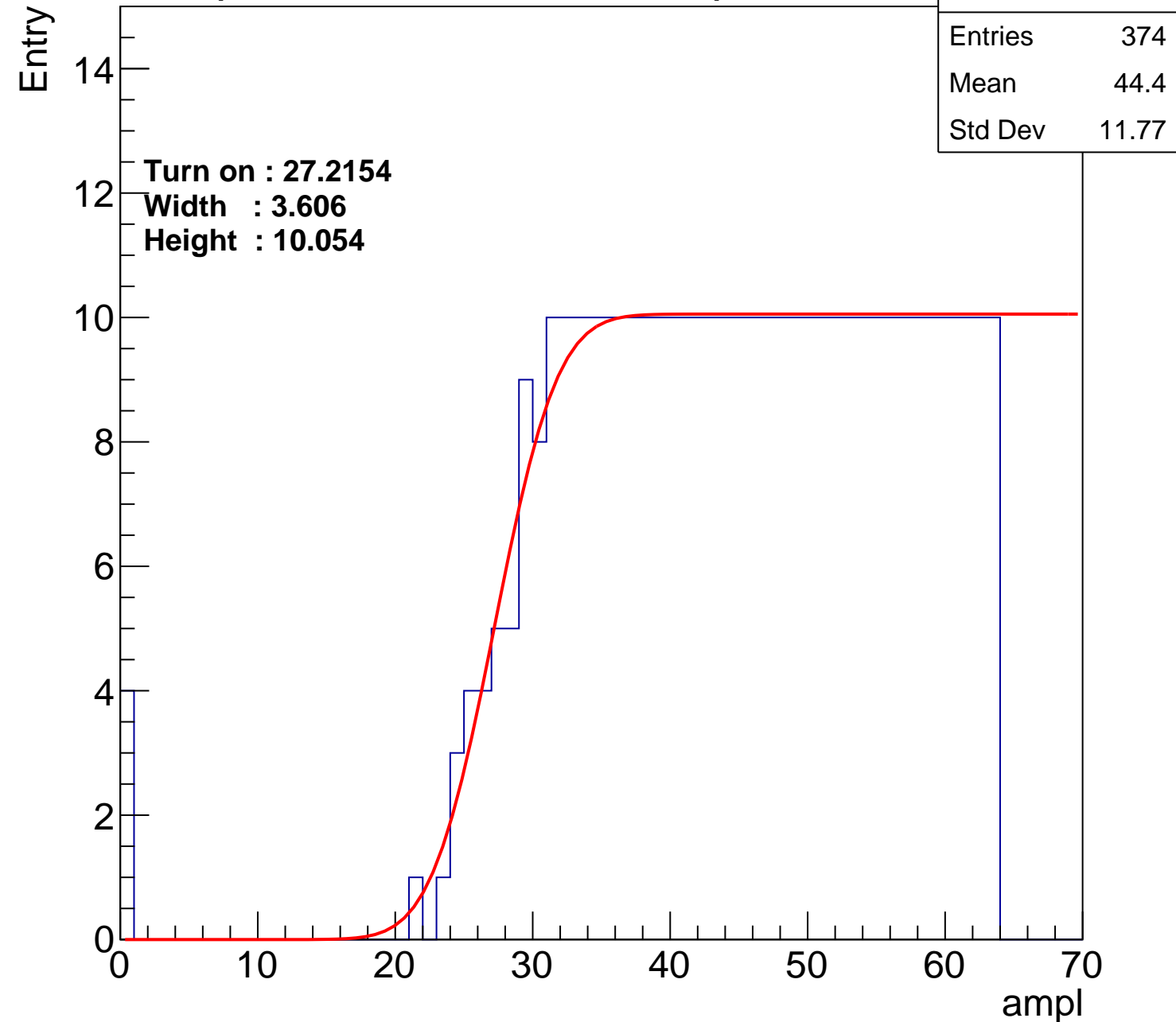
Width : 3.606

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch52

calib_packv5_042523_0143.root, FC#11, port A2

Entries	410
Mean	42.7
Std Dev	12.54

Turn on : 23.6670

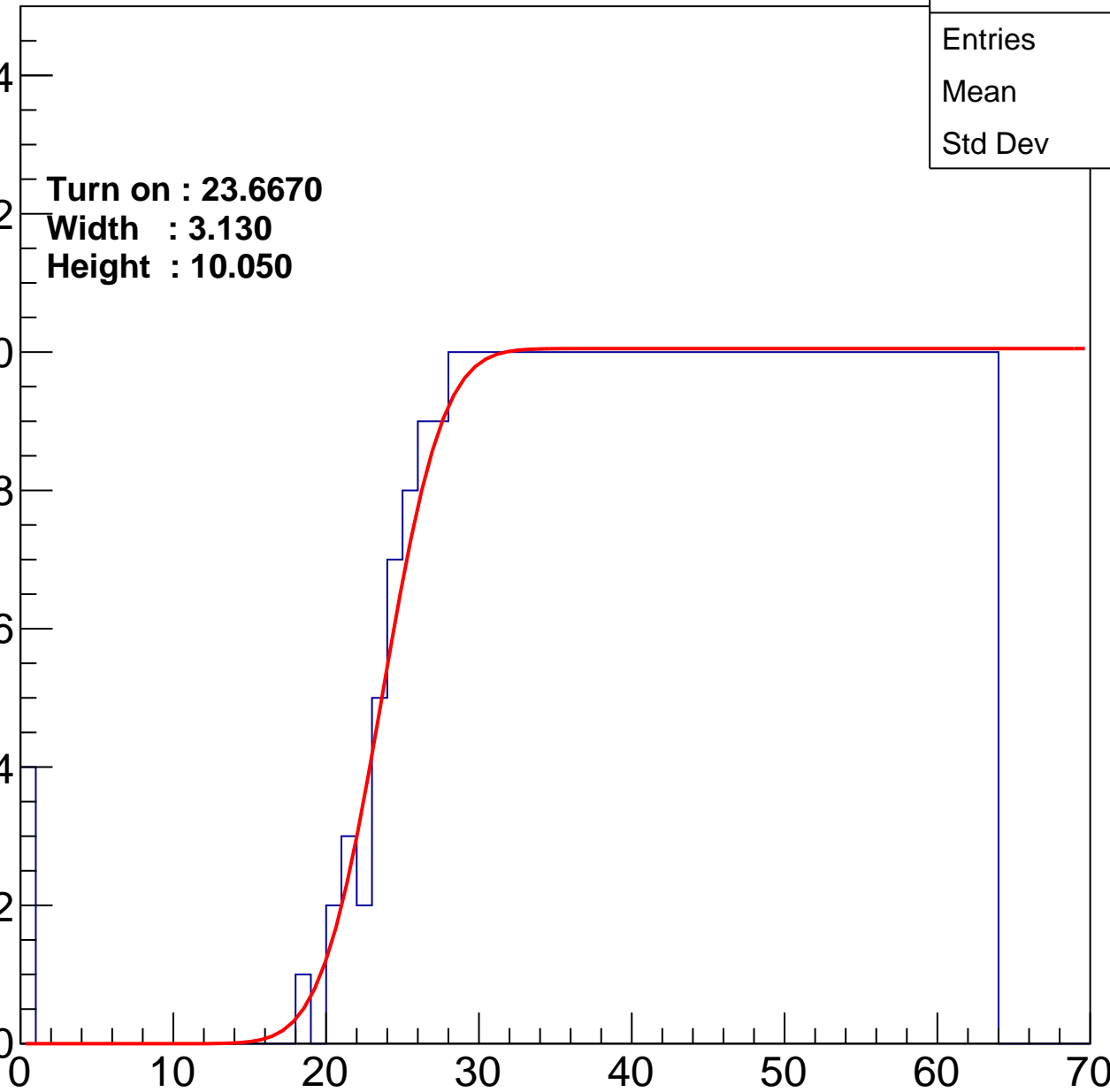
Width : 3.130

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch53

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.9043

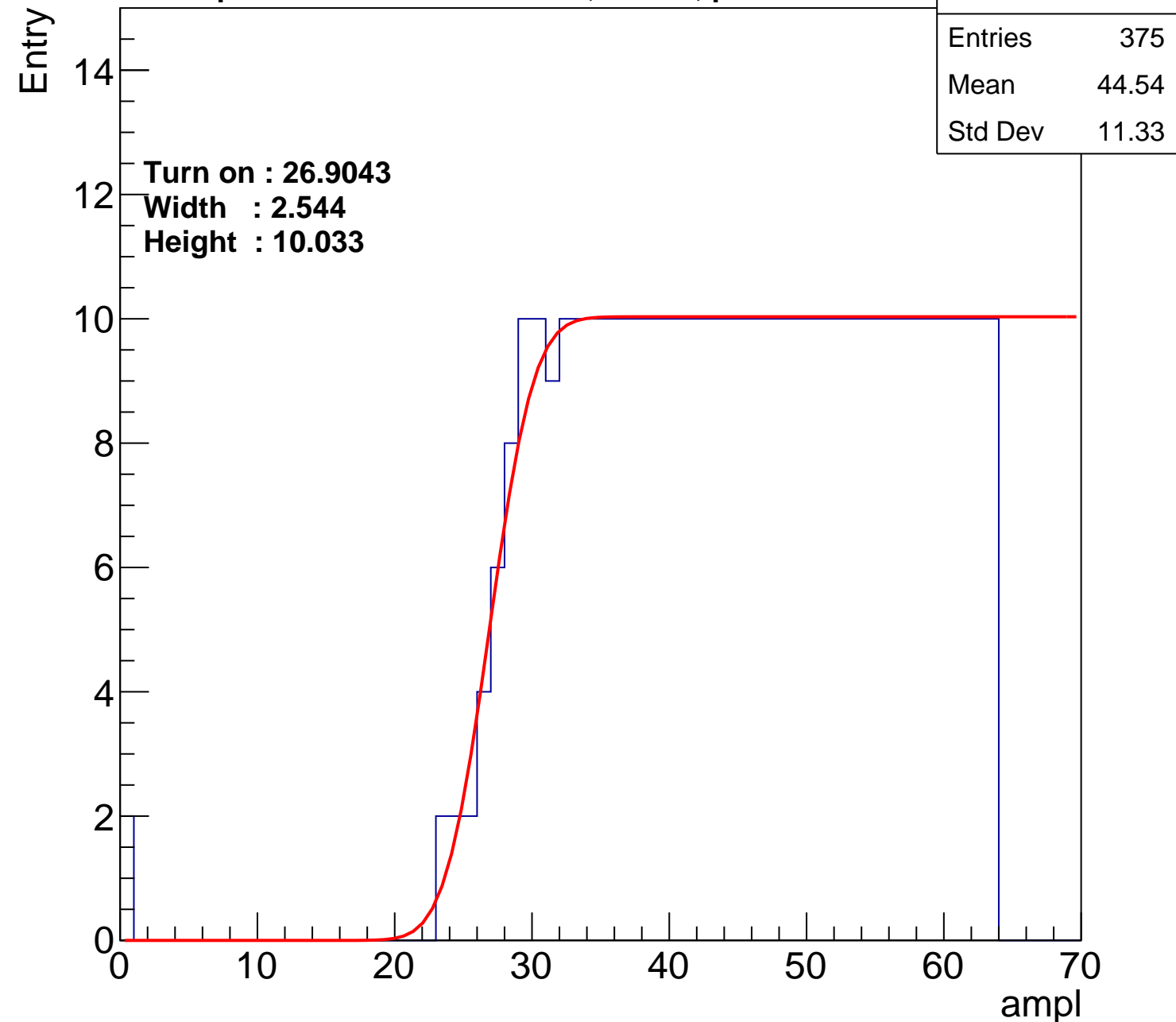
Width : 2.544

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch54

calib_packv5_042523_0143.root, FC#11, port A2

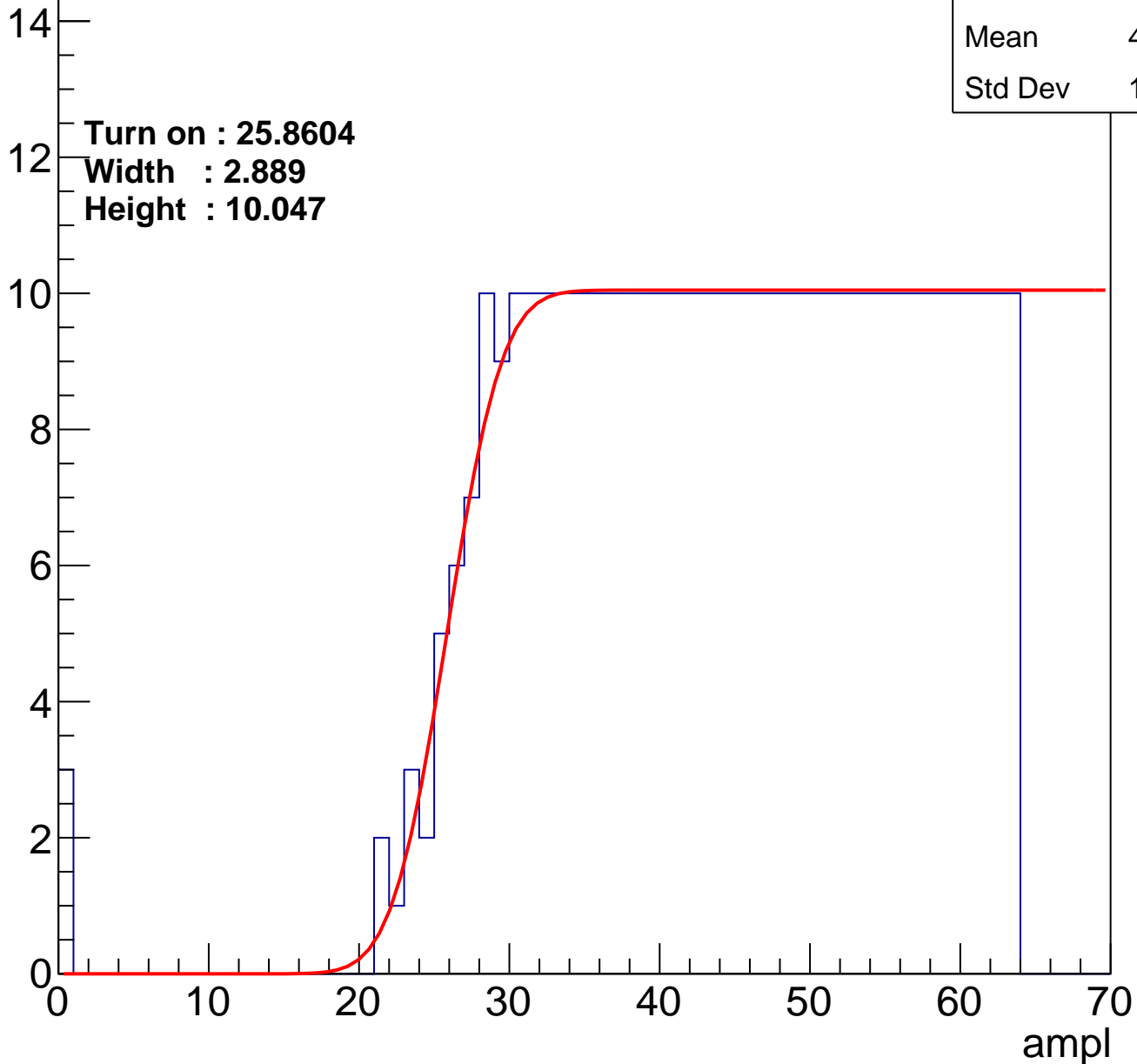
Entries	388
Mean	43.82
Std Dev	11.87

Turn on : 25.8604

Width : 2.889

Height : 10.047

Entry



B1L102S, U23-ch55

calib_packv5_042523_0143.root, FC#11, port A2

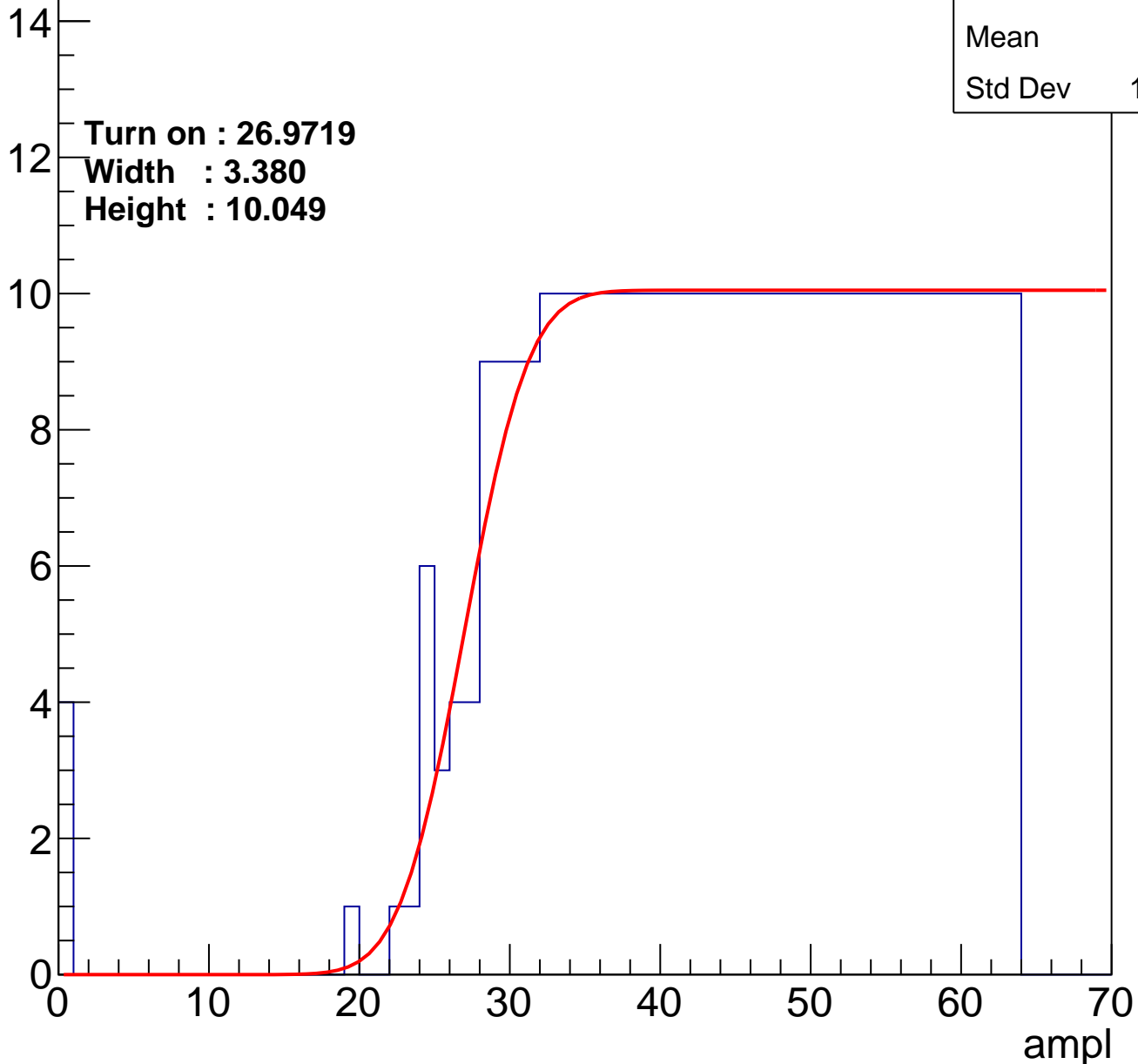
Entries	380
Mean	44.1
Std Dev	11.93

Turn on : 26.9719

Width : 3.380

Height : 10.049

Entry



B1L102S, U23-ch56

calib_packv5_042523_0143.root, FC#11, port A2

Entries	405
Mean	42.8
Std Dev	12.68

Turn on : 24.0877

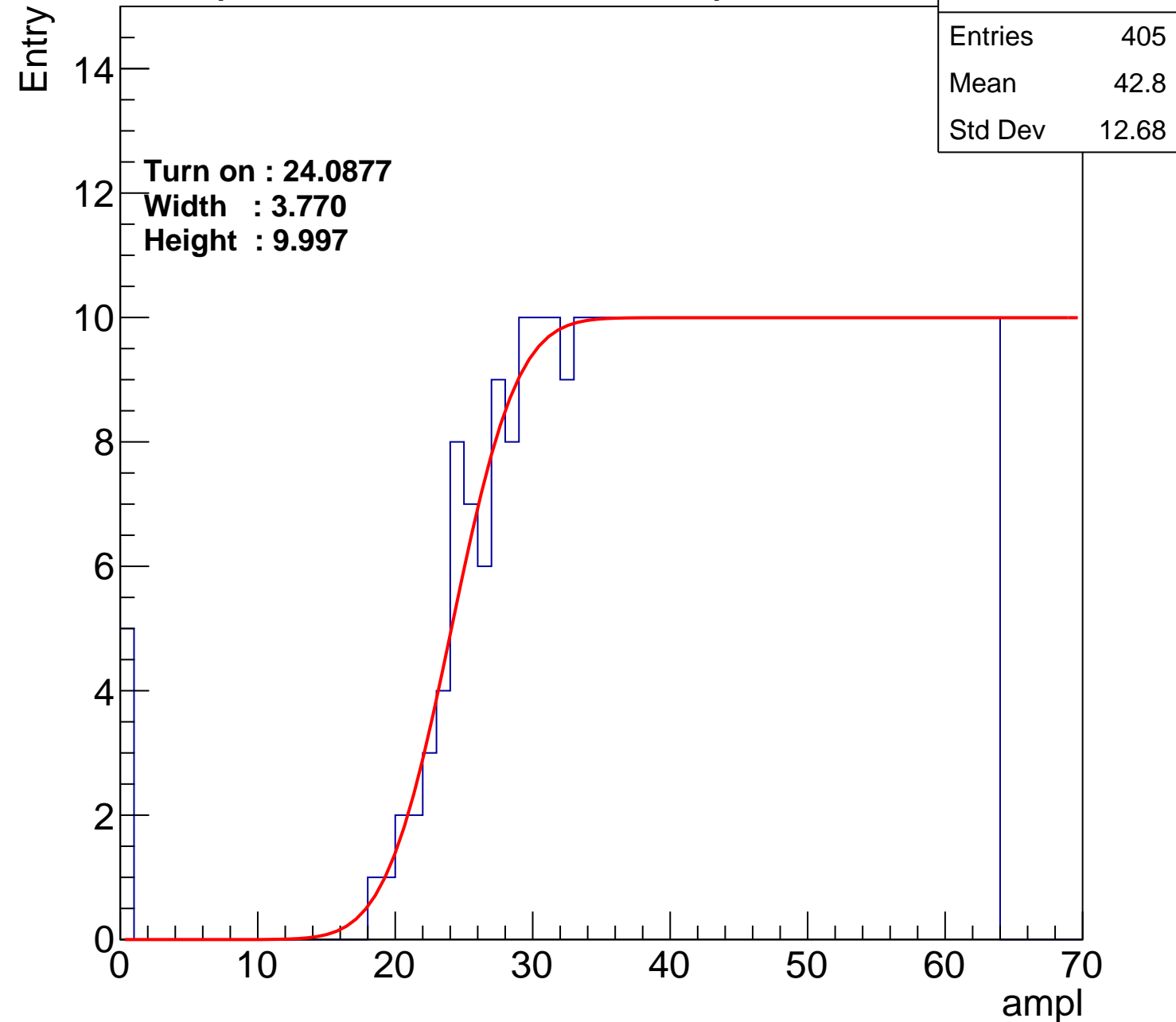
Width : 3.770

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch57

calib_packv5_042523_0143.root, FC#11, port A2

Entries	384
Mean	44.14
Std Dev	11.42

Turn on : 26.0865

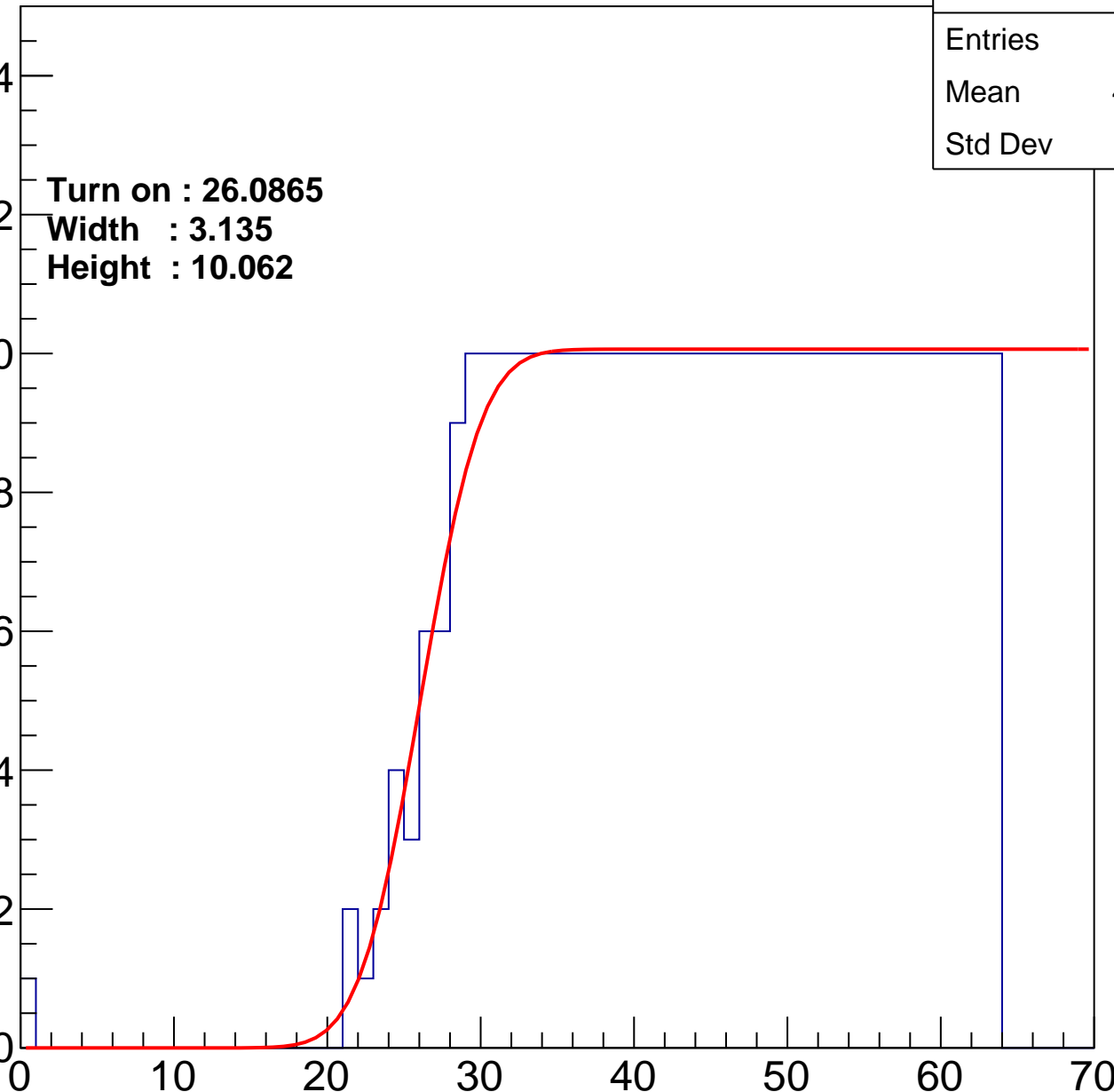
Width : 3.135

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch58

calib_packv5_042523_0143.root, FC#11, port A2

Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.7583

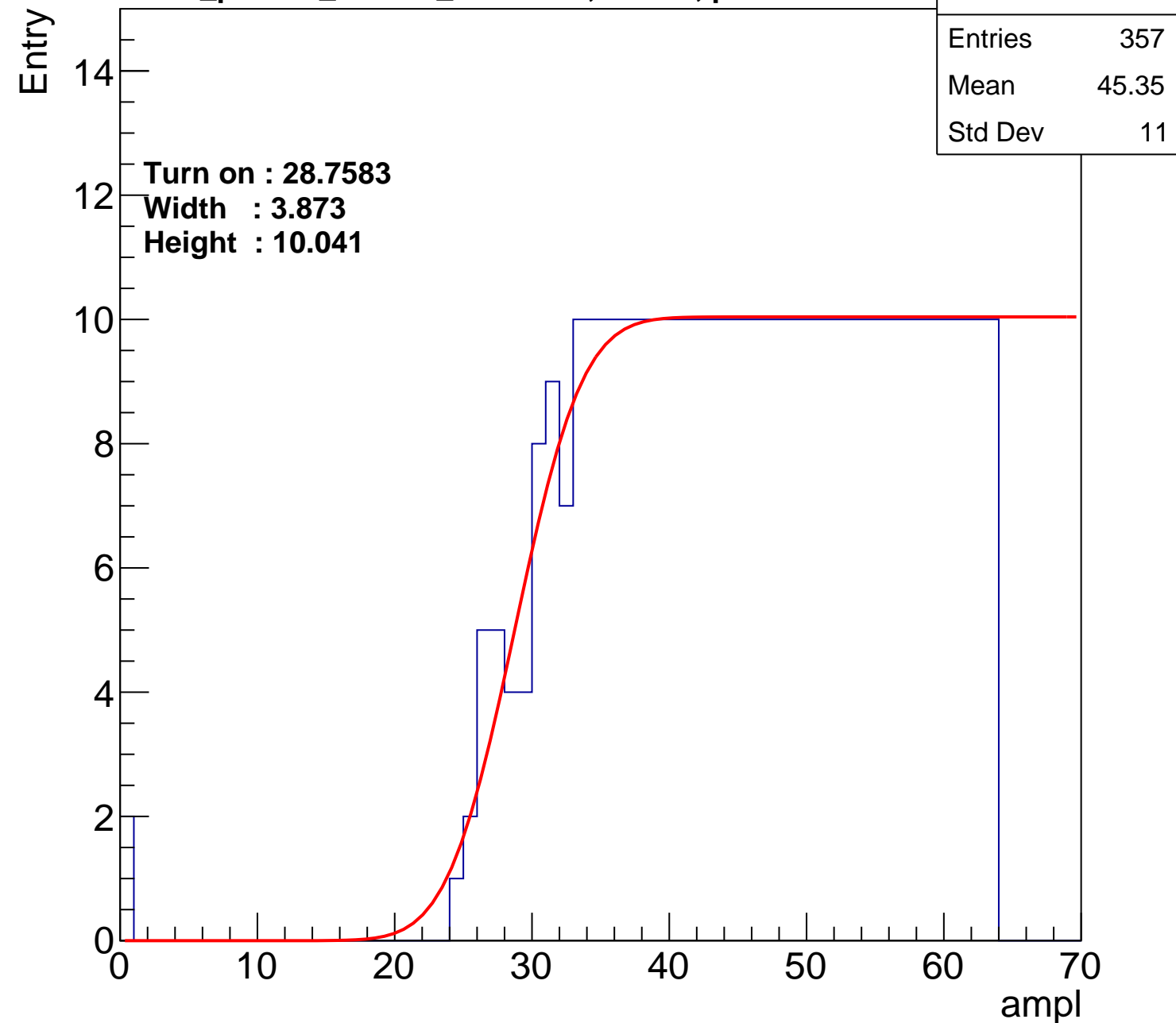
Width : 3.873

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch59

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	44.89
Std Dev	11.39

Turn on : 26.6533

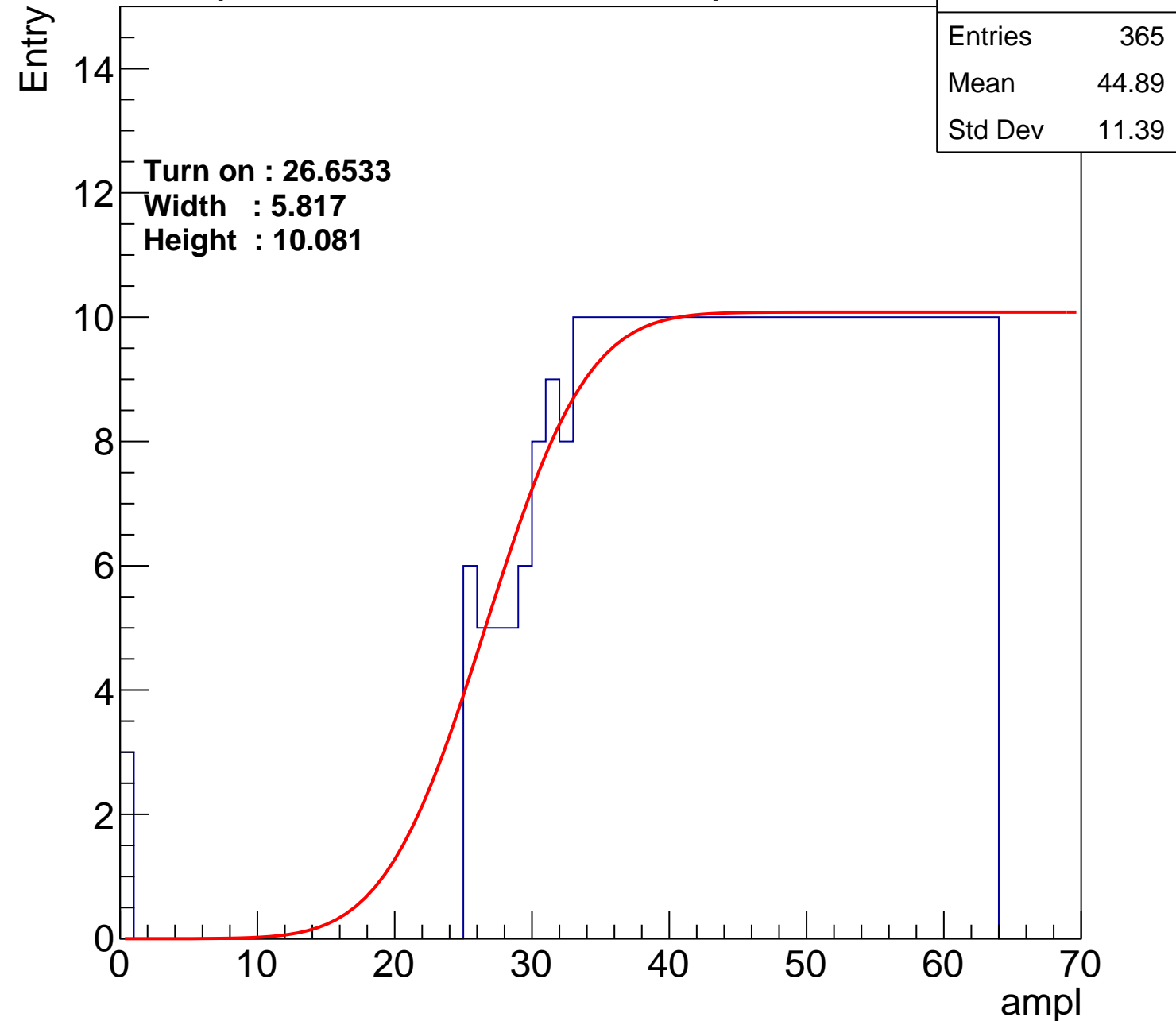
Width : 5.817

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch60

calib_packv5_042523_0143.root, FC#11, port A2

Entries	393
Mean	43.69
Std Dev	11.67

Turn on : 25.1208

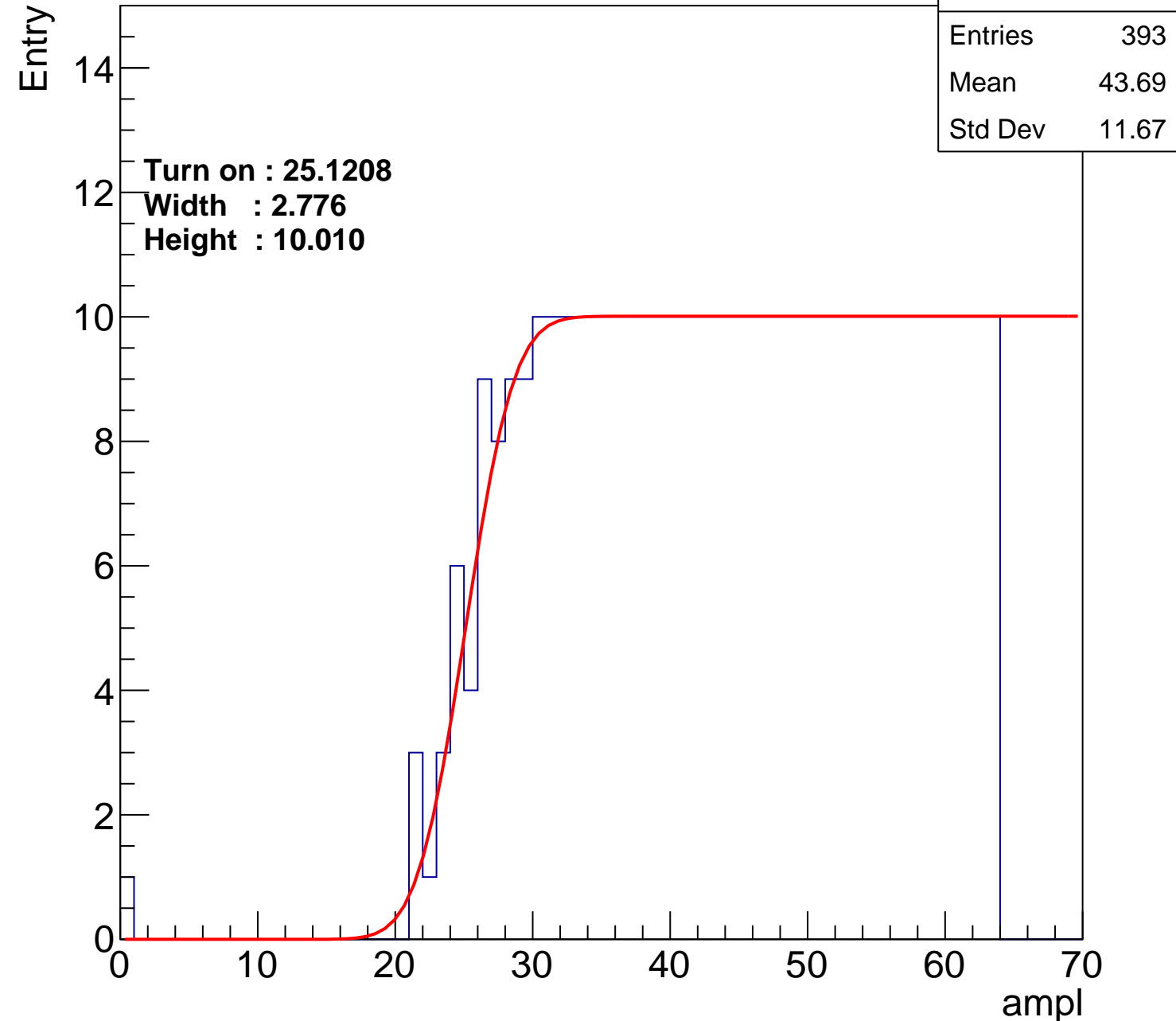
Width : 2.776

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch61

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 26.5575

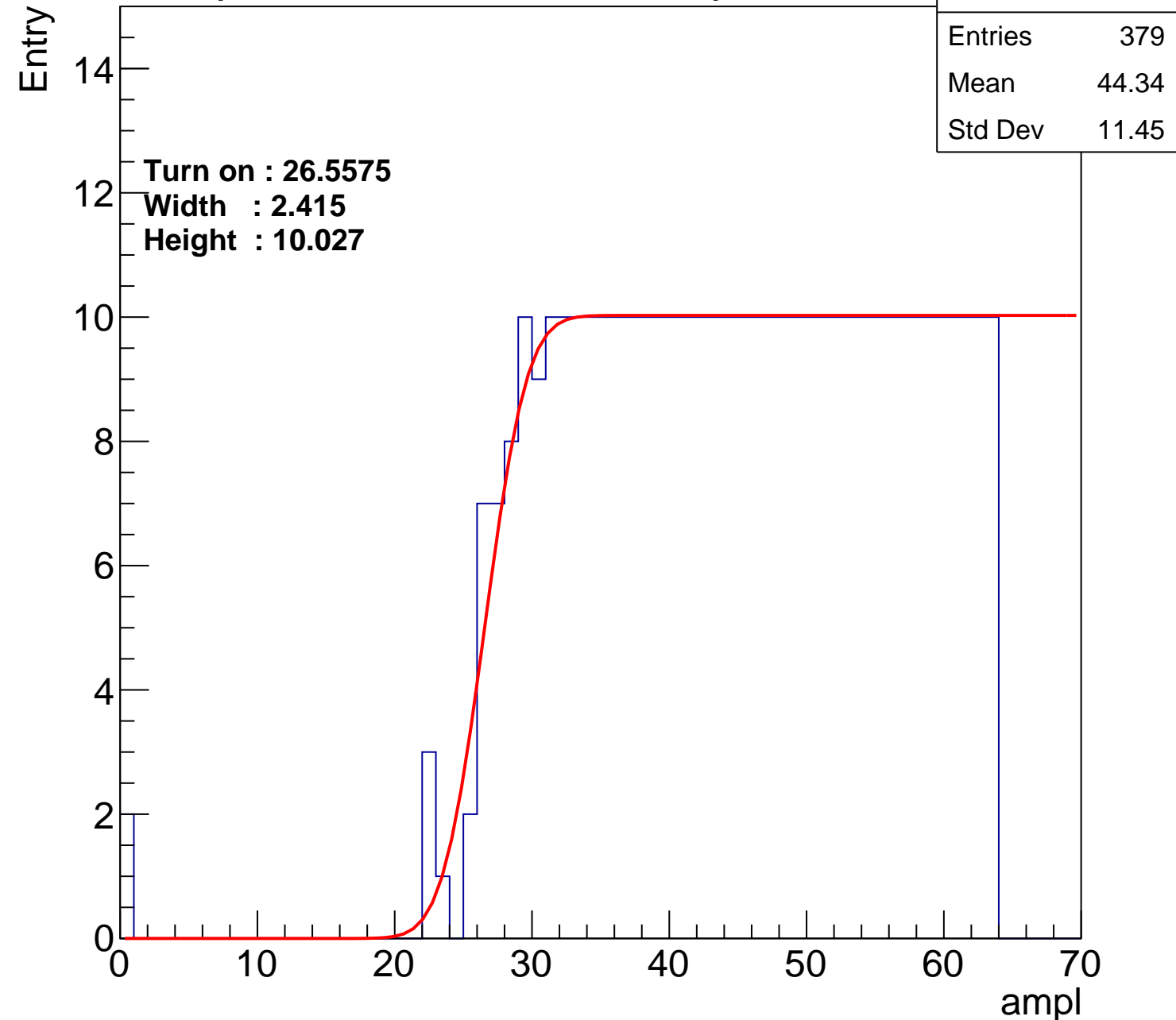
Width : 2.415

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch62

calib_packv5_042523_0143.root, FC#11, port A2

Entries	410
Mean	42.74
Std Dev	12.43

Turn on : 23.7721

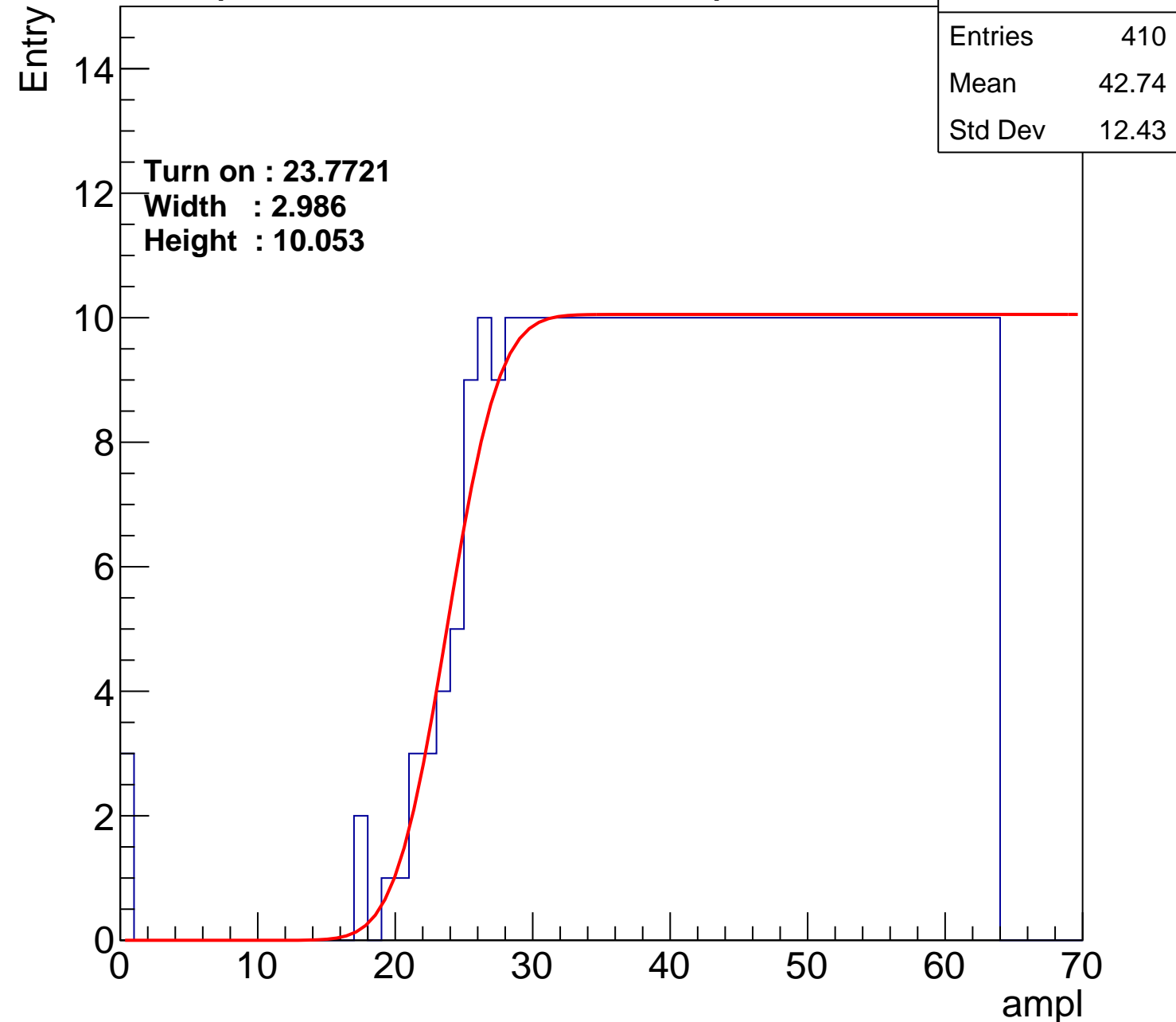
Width : 2.986

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch63

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.15
Std Dev	11.05

Turn on : 28.2213

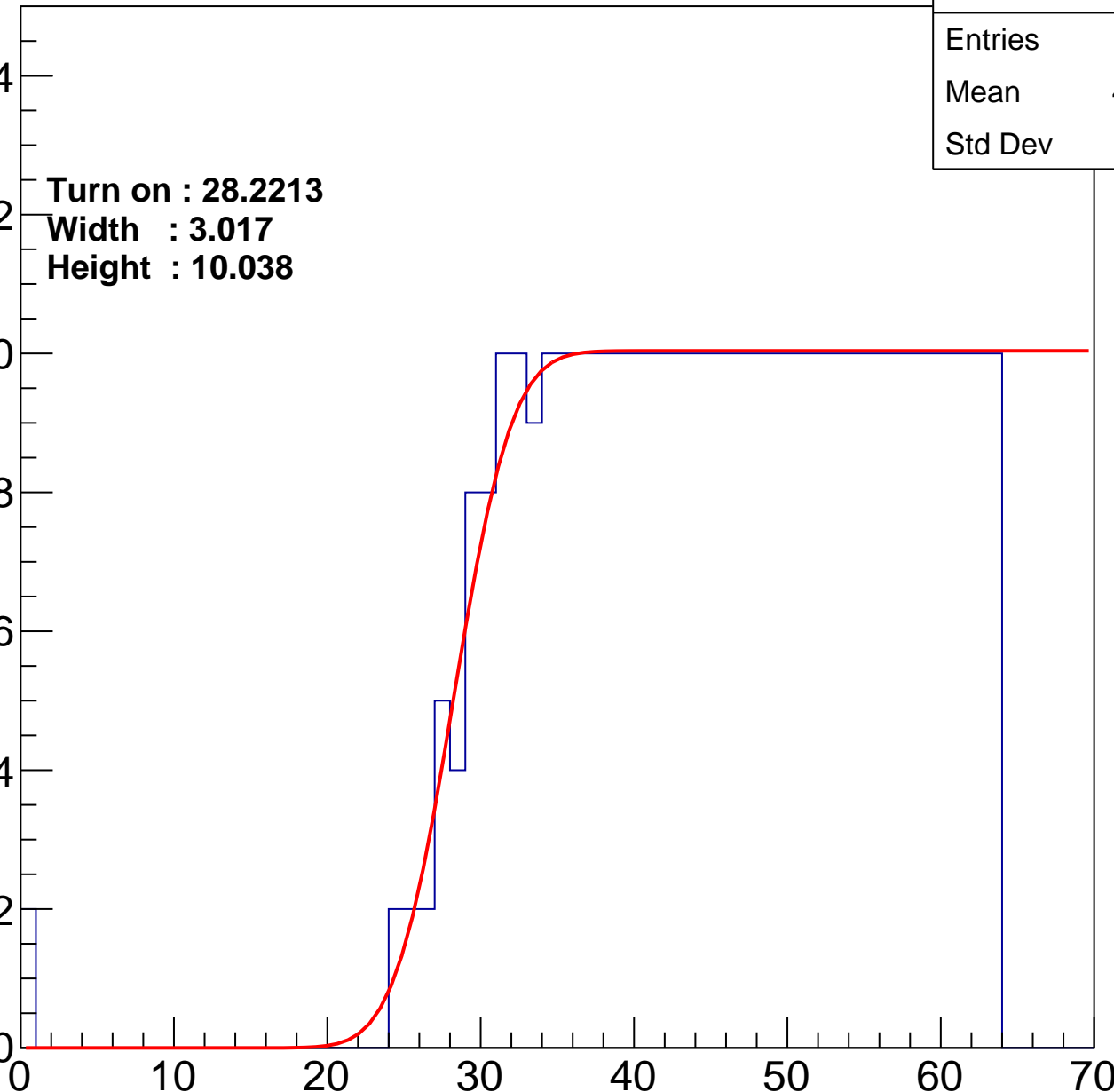
Width : 3.017

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch64

calib_packv5_042523_0143.root, FC#11, port A2

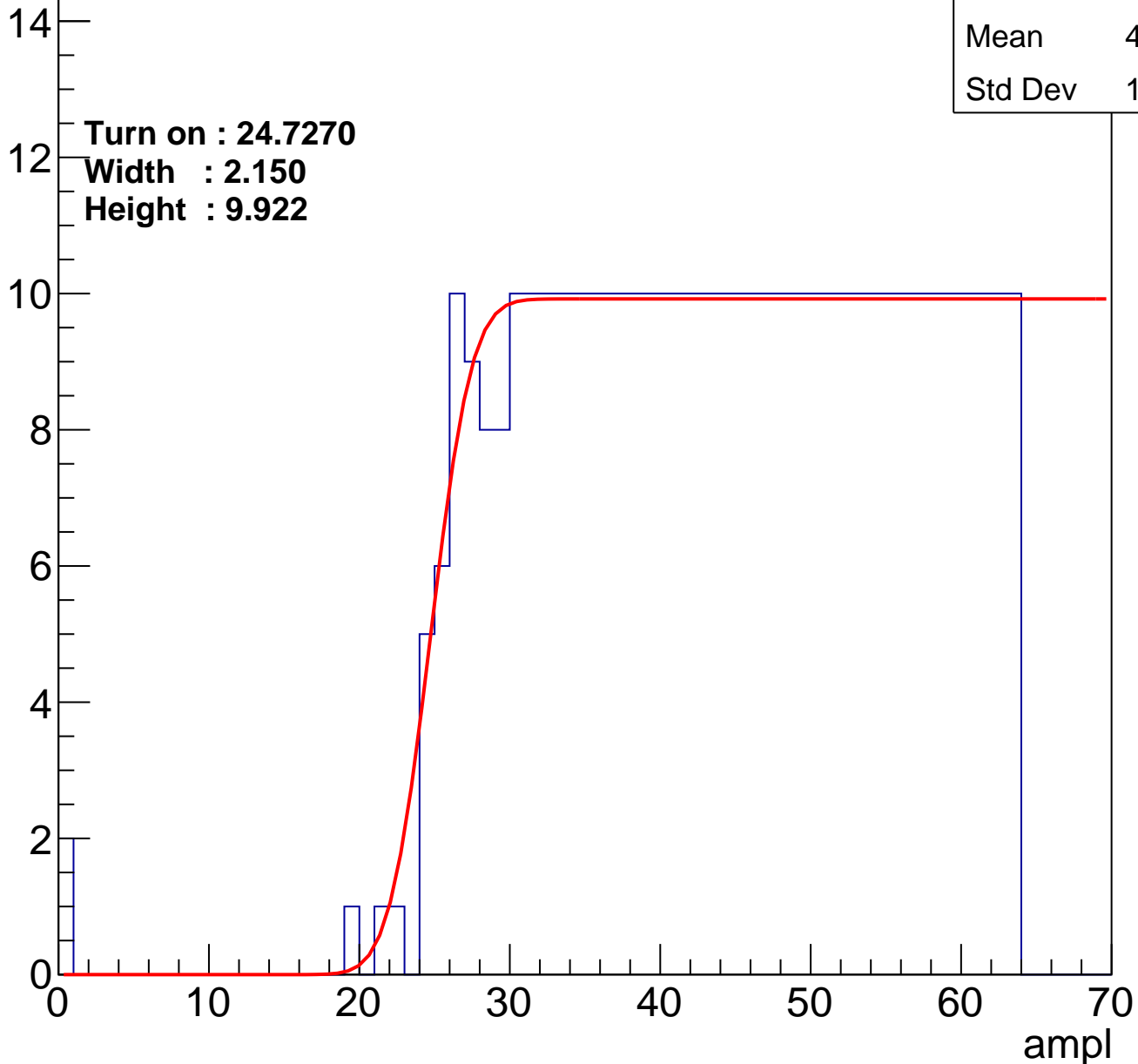
Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 24.7270

Width : 2.150

Height : 9.922

Entry



B1L102S, U23-ch65

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 27.3345

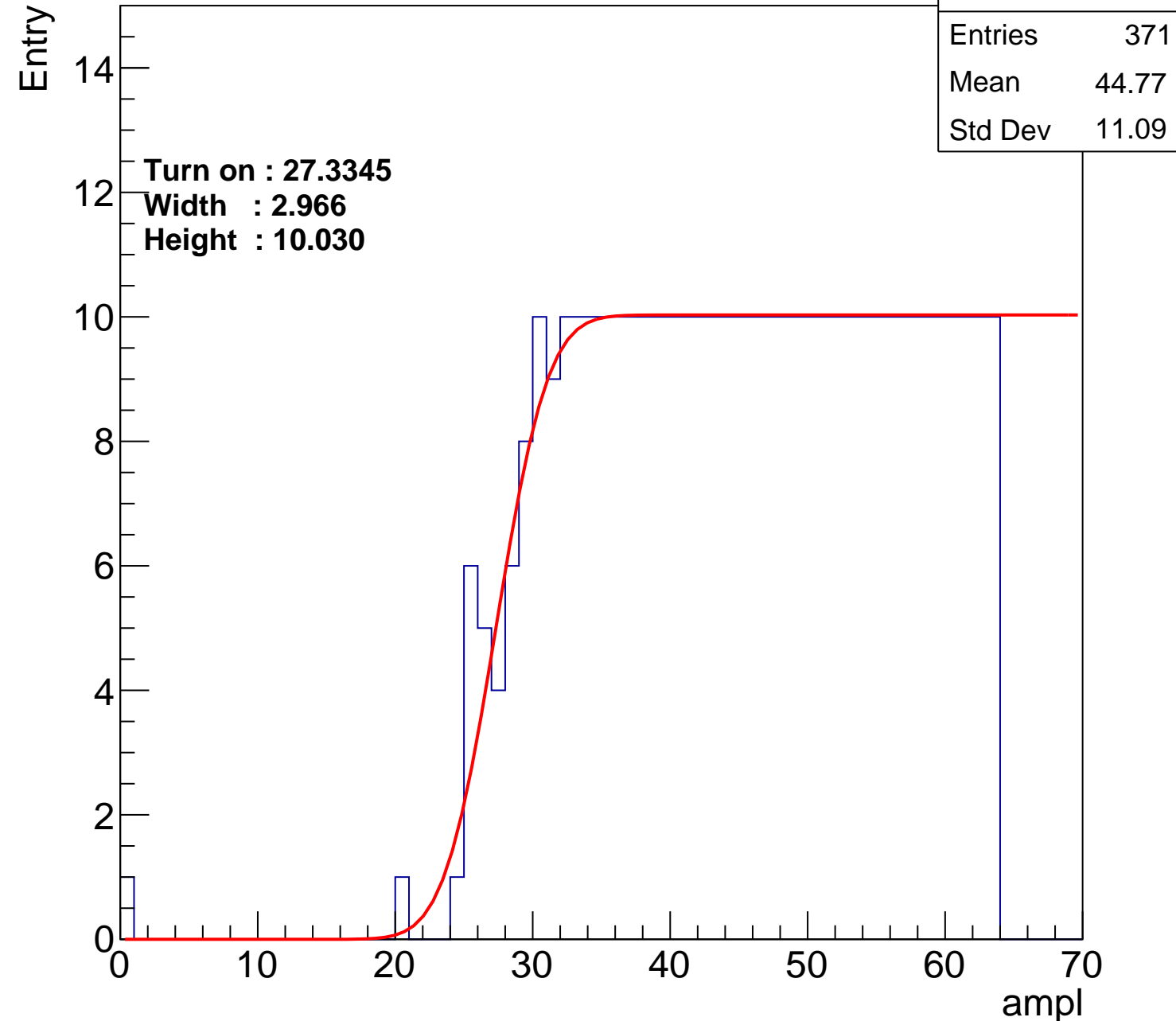
Width : 2.966

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch66

calib_packv5_042523_0143.root, FC#11, port A2

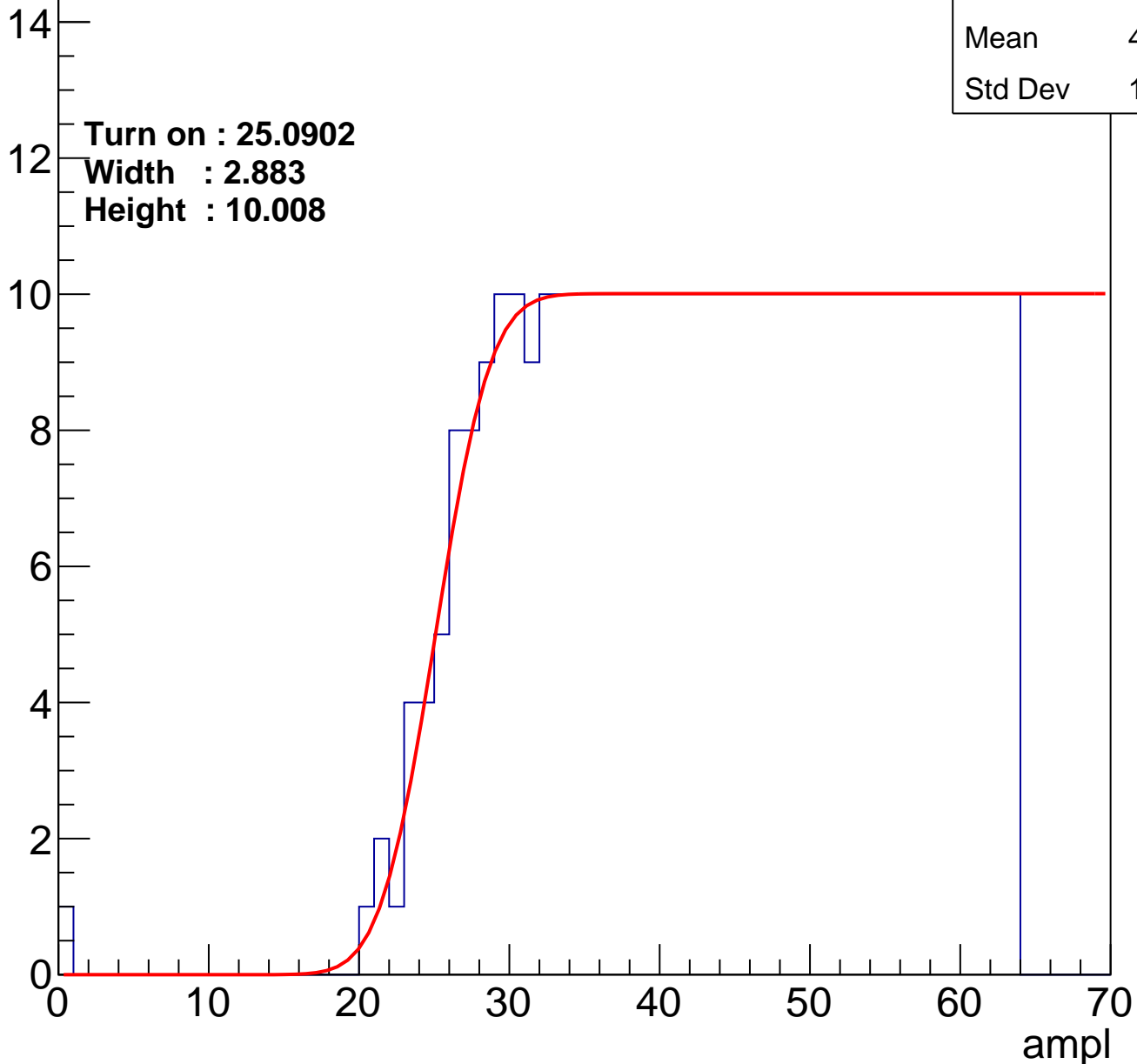
Entries	392
Mean	43.73
Std Dev	11.66

Turn on : 25.0902

Width : 2.883

Height : 10.008

Entry



B1L102S, U23-ch67

calib_packv5_042523_0143.root, FC#11, port A2

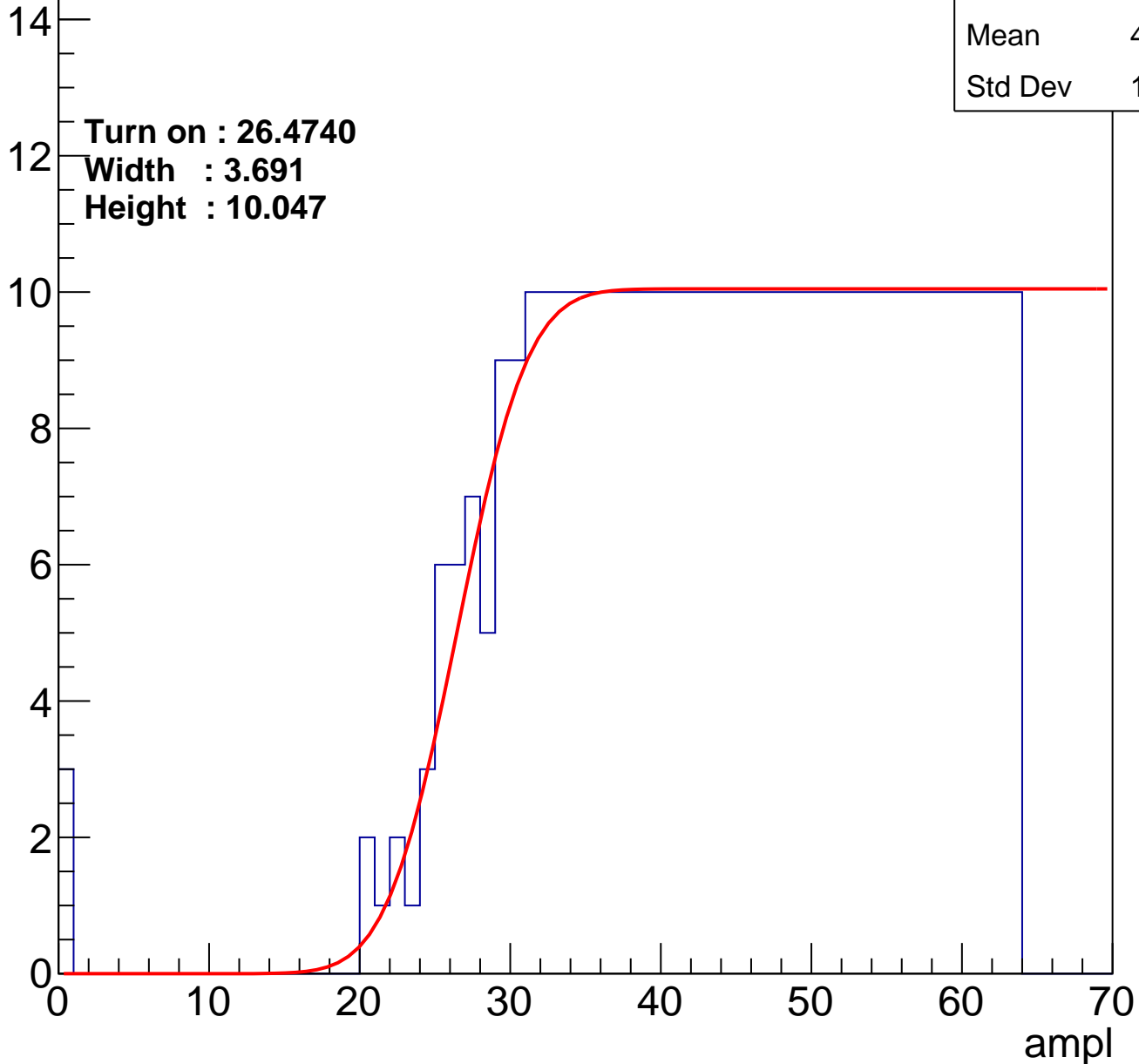
Entries	384
Mean	43.95
Std Dev	11.88

Turn on : 26.4740

Width : 3.691

Height : 10.047

Entry



B1L102S, U23-ch68

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.45
Std Dev	11.44

Turn on : 27.2828

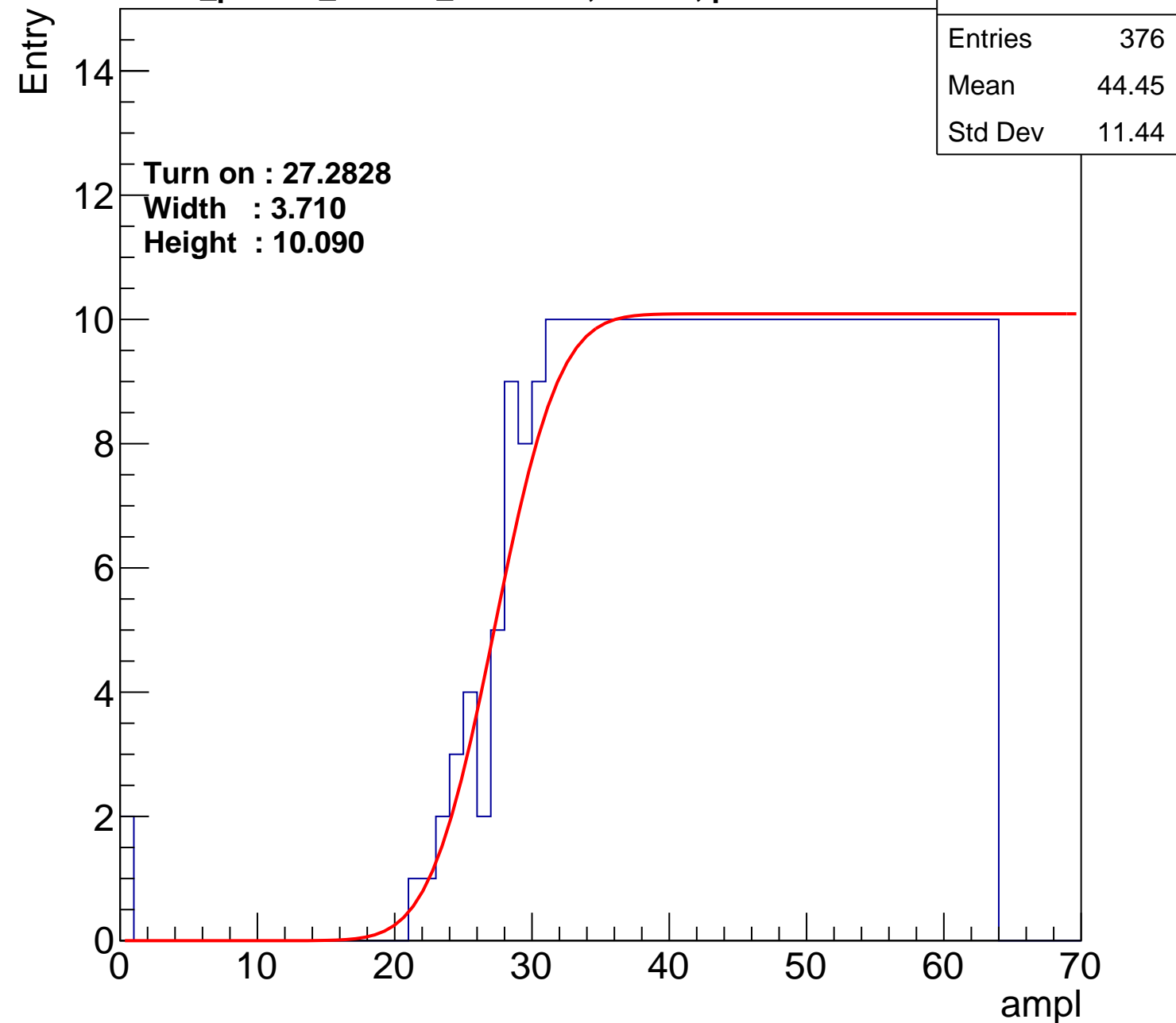
Width : 3.710

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl

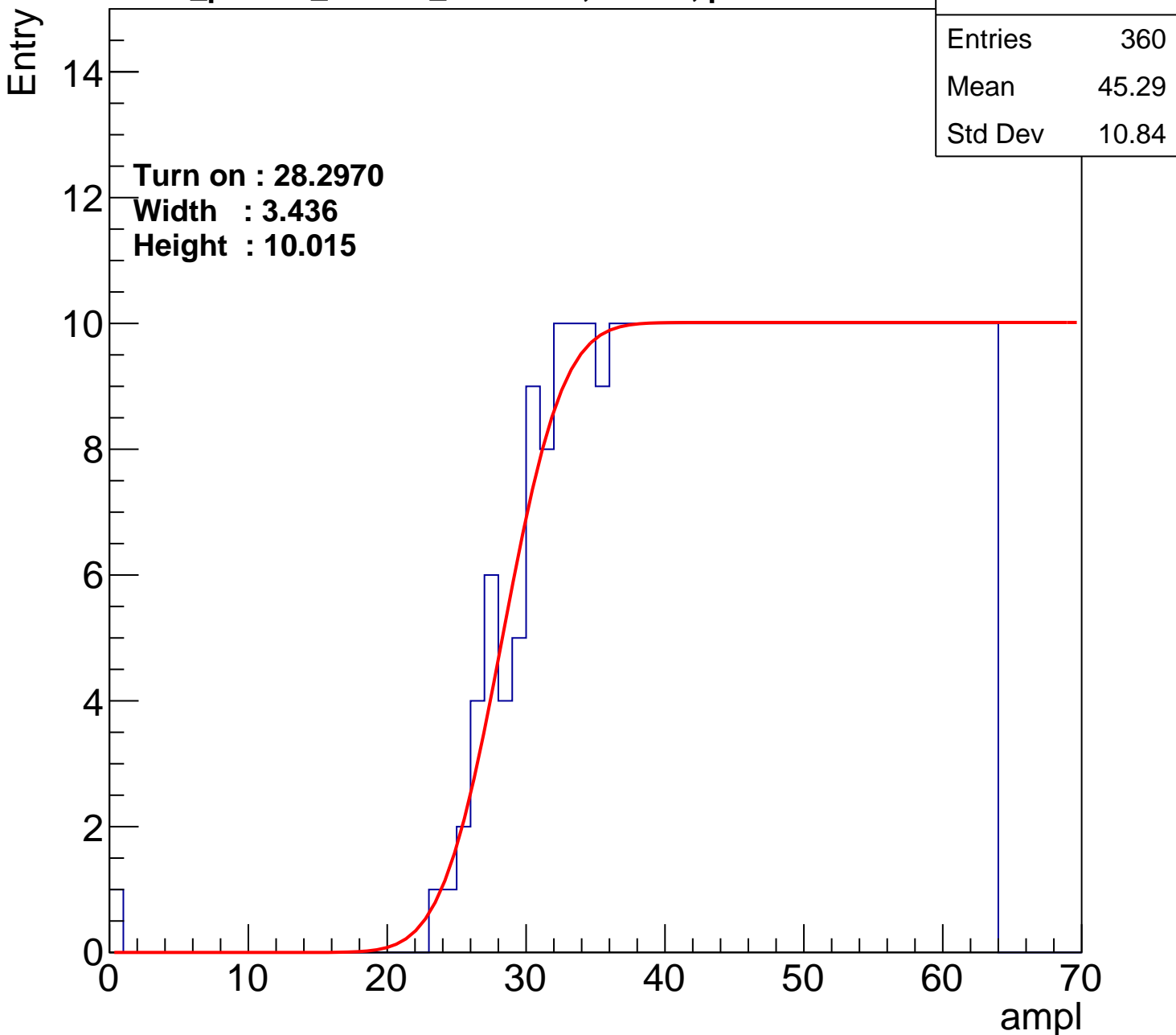


calib_packv5_042523_0143.root, FC#11, port A2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	360
Mean	45.29
Std Dev	10.84

Height : 10.015



B1L102S, U23-ch70

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.36
Std Dev	11.55

Turn on : 26.6622

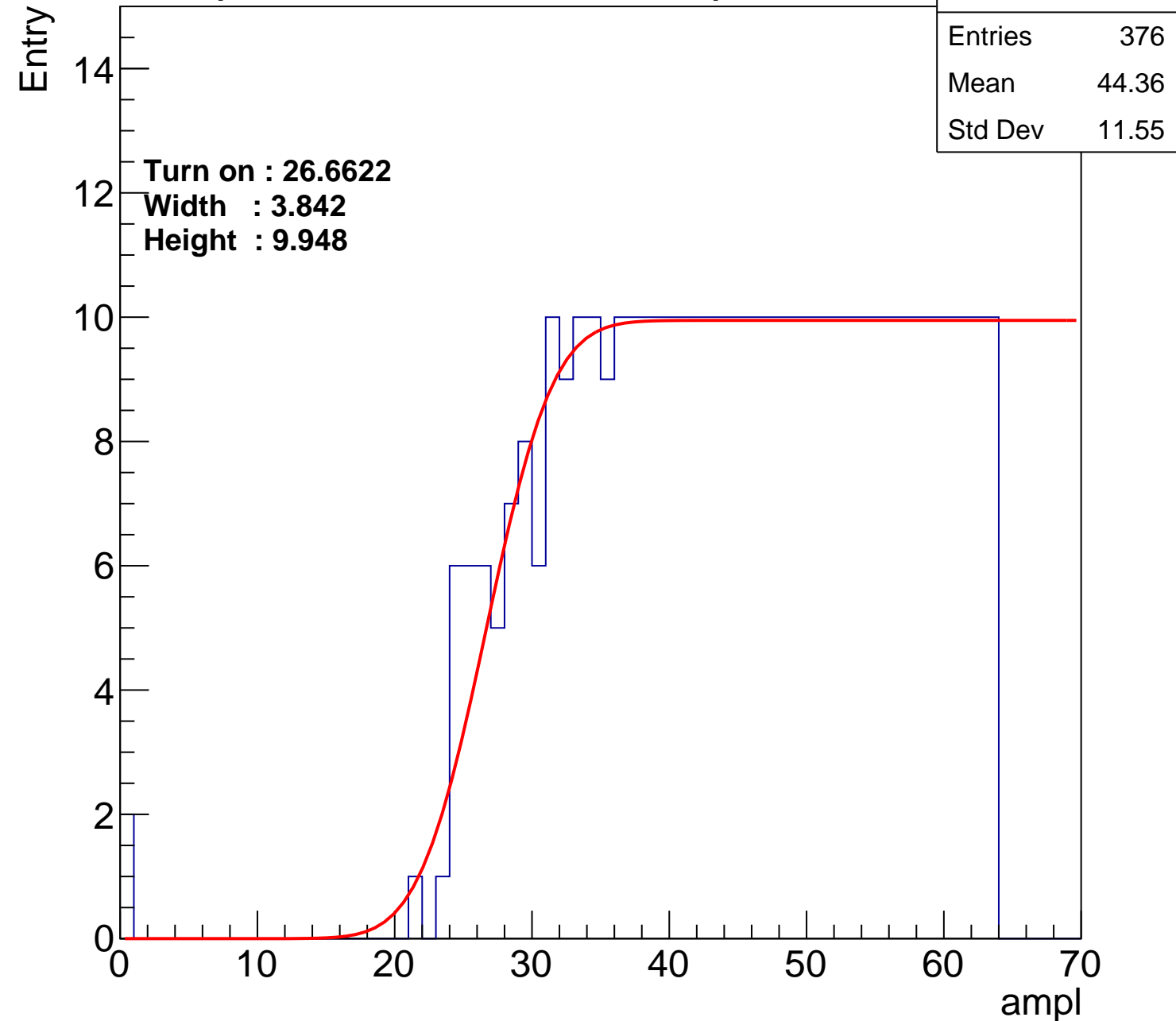
Width : 3.842

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch71

calib_packv5_042523_0143.root, FC#11, port A2

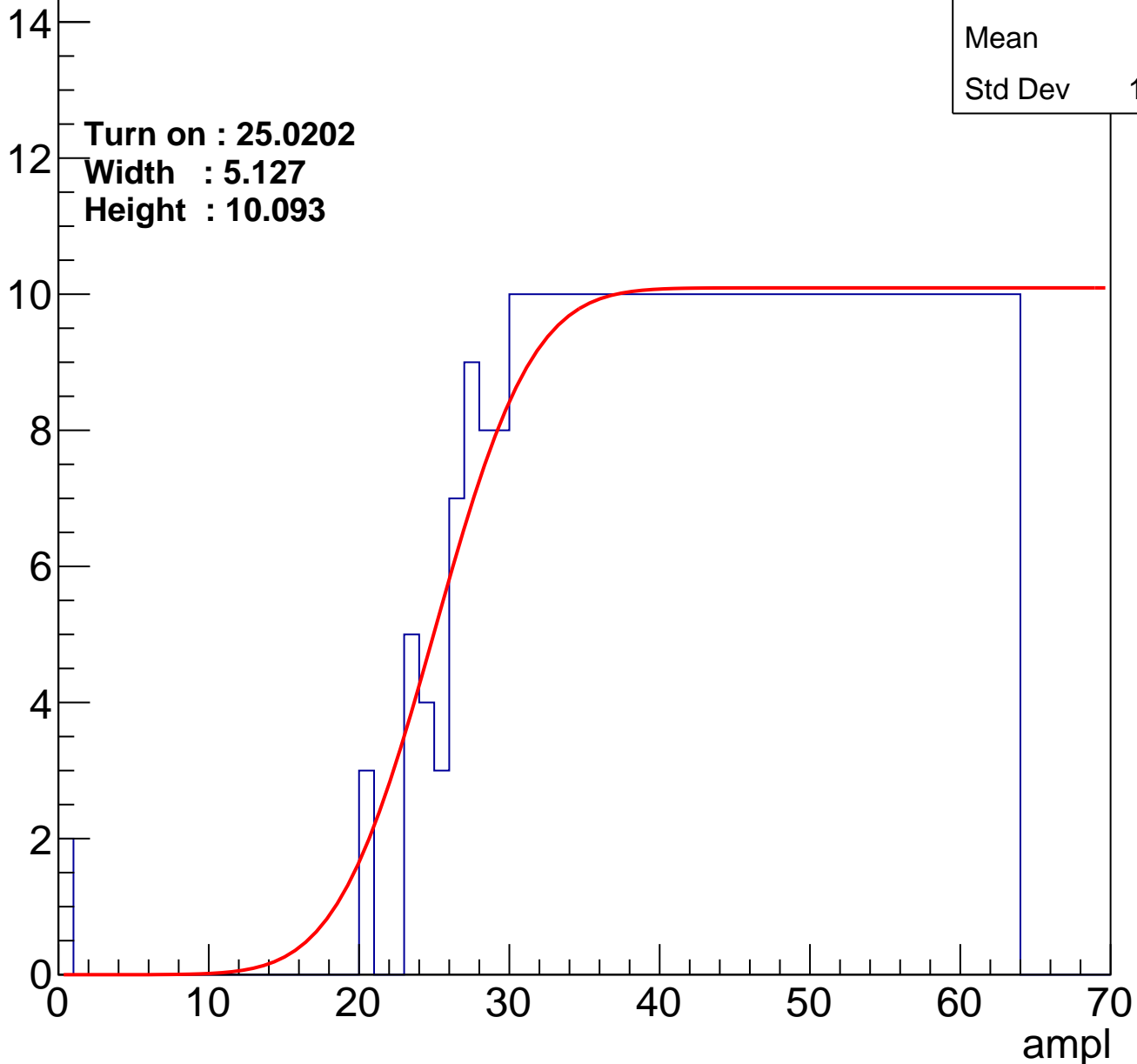
Entries	389
Mean	43.8
Std Dev	11.79

Turn on : 25.0202

Width : 5.127

Height : 10.093

Entry



B1L102S, U23-ch72

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.33
Std Dev	12.06

Turn on : 24.5481

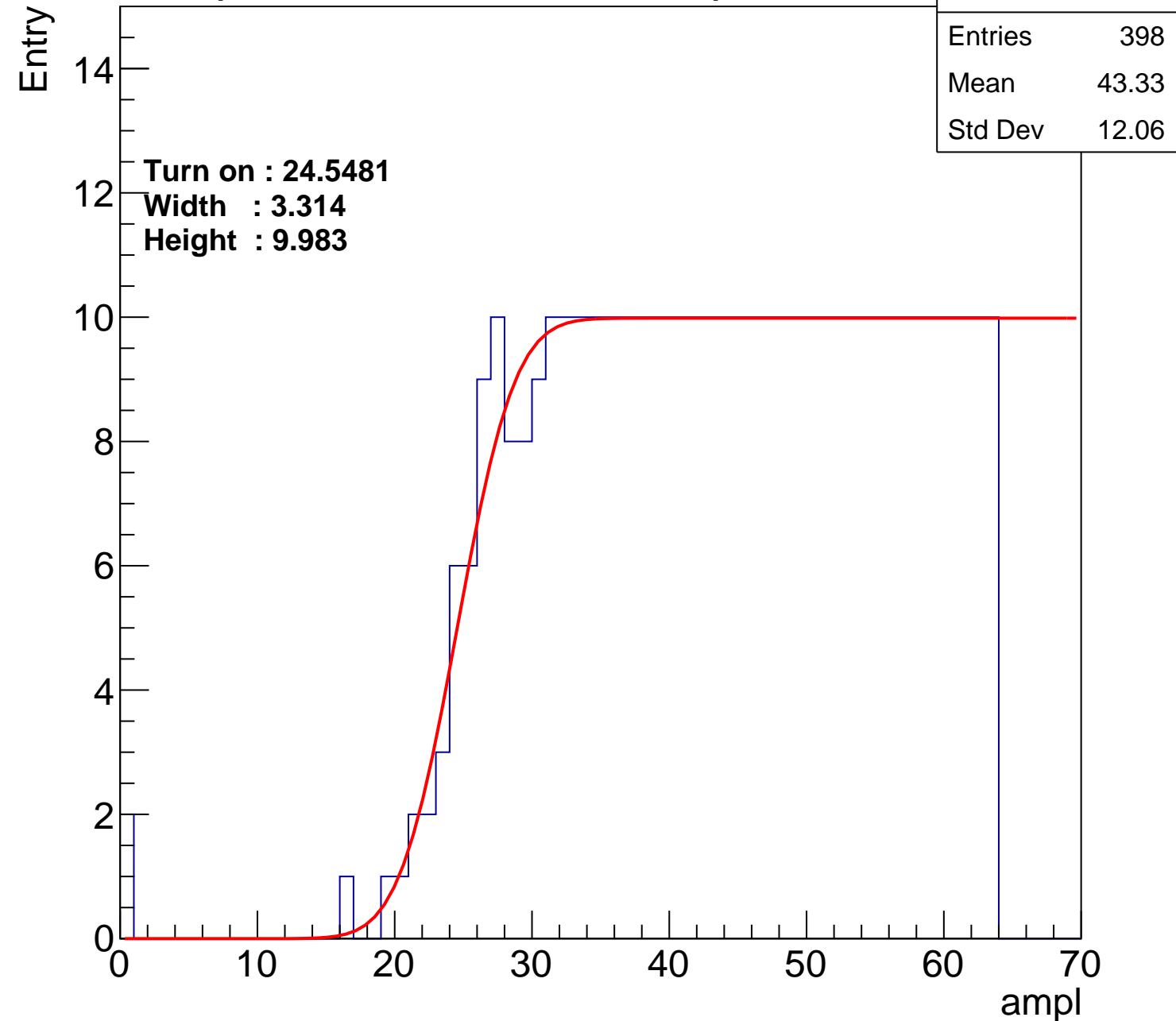
Width : 3.314

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch73

calib_packv5_042523_0143.root, FC#11, port A2

Entries	378
Mean	44.22
Std Dev	11.83

Turn on : 26.5110

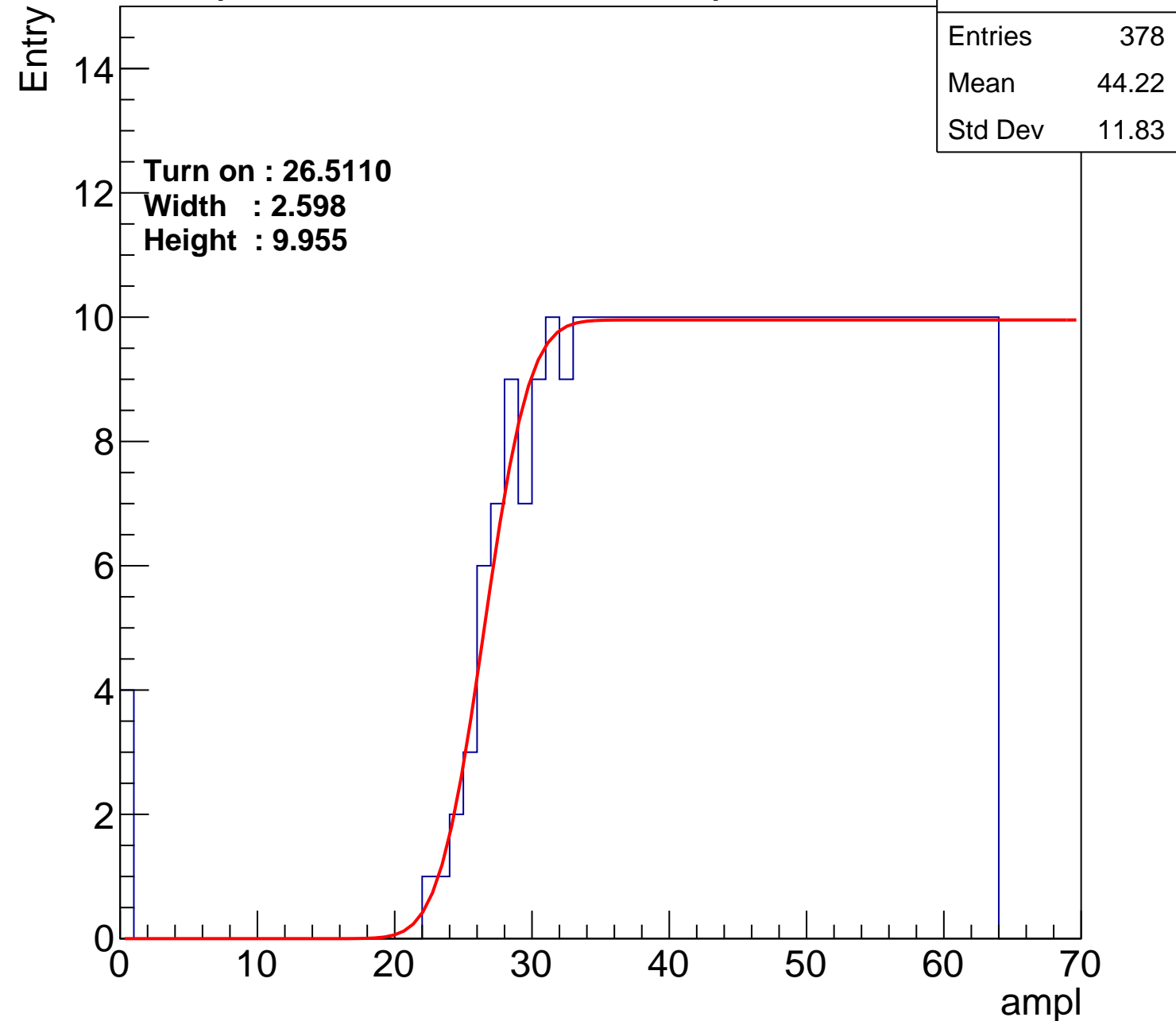
Width : 2.598

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch74

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.7681

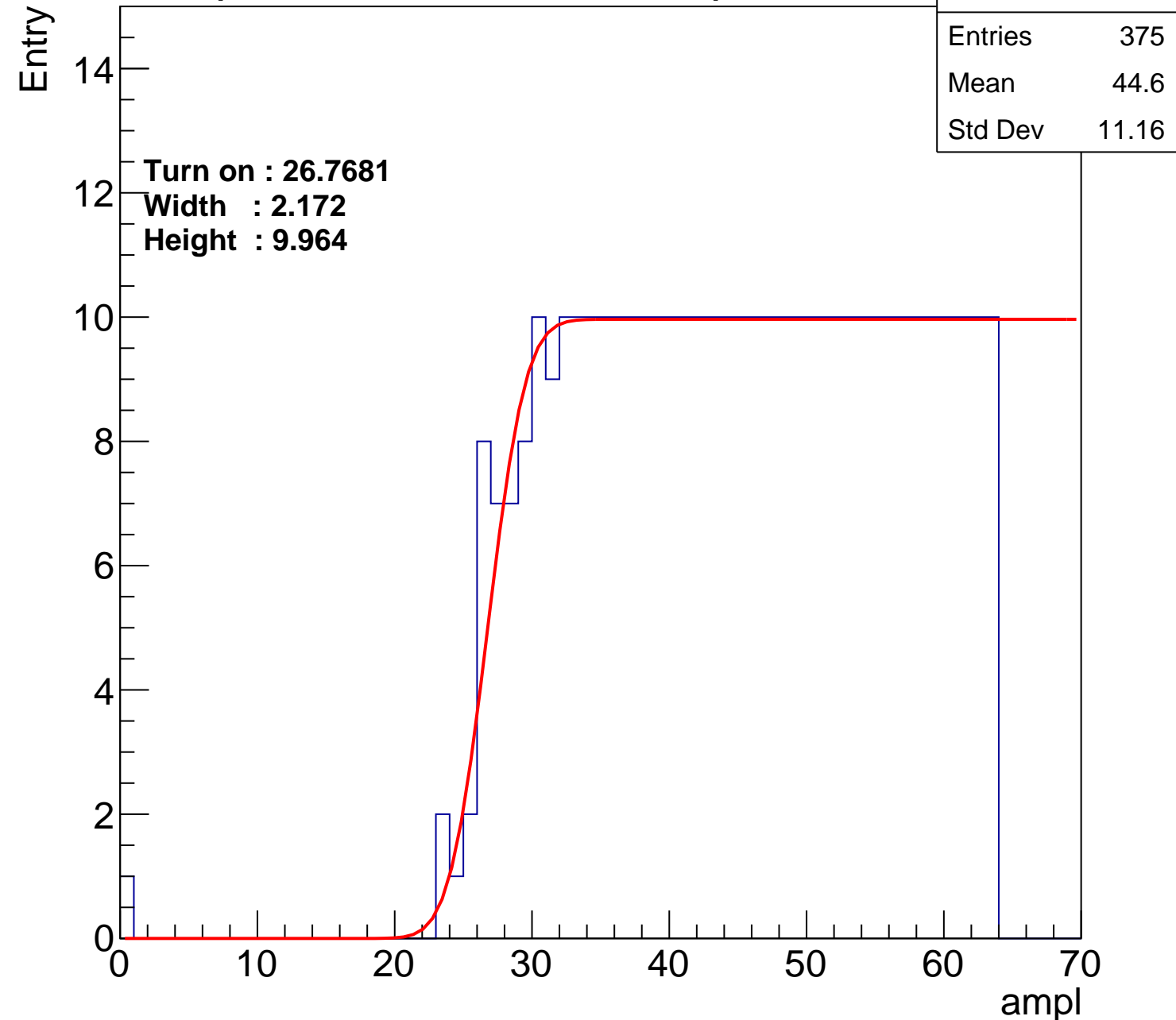
Width : 2.172

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch75

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.96
Std Dev	11.56

Turn on : 25.3151

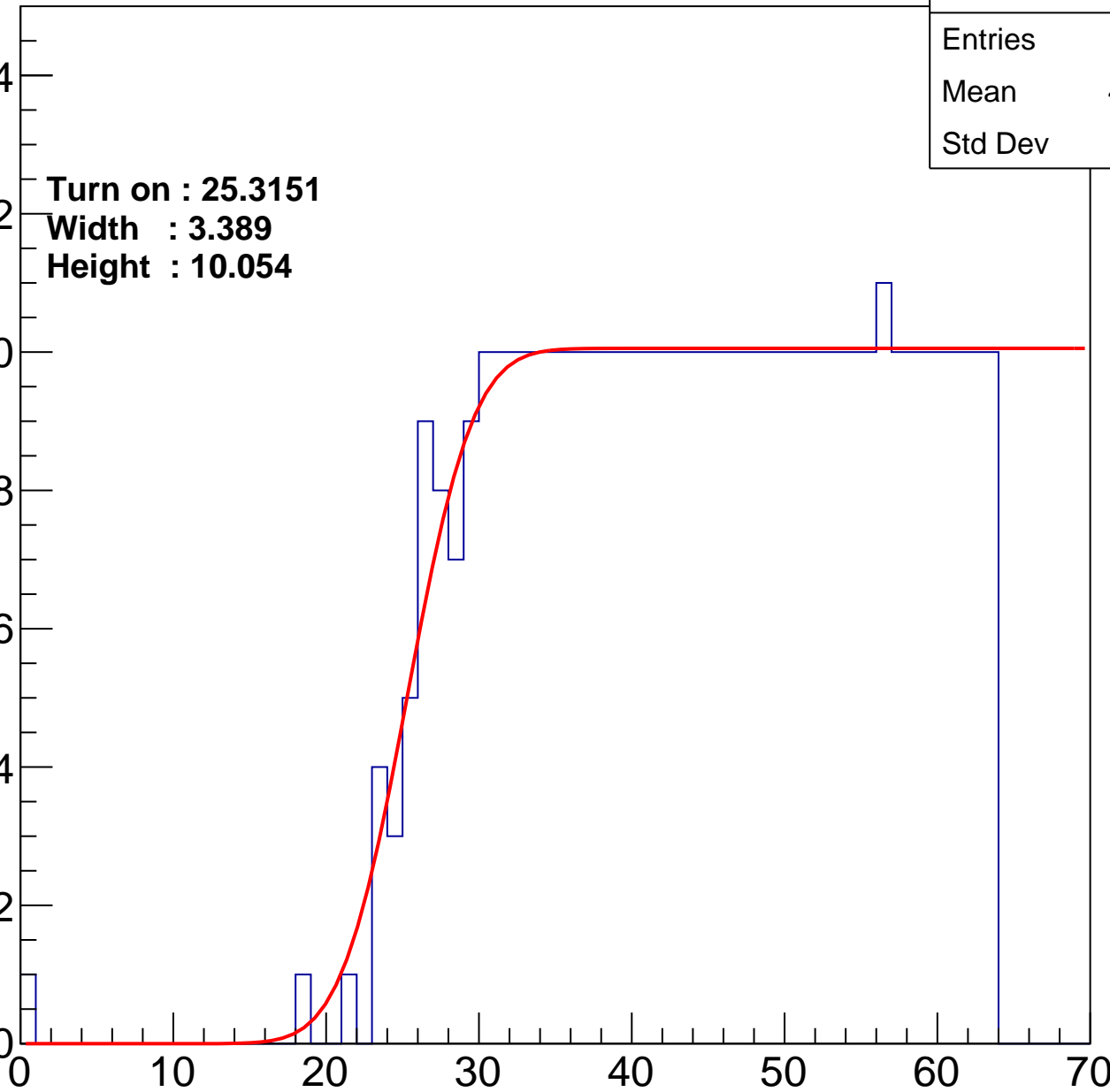
Width : 3.389

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch76

calib_packv5_042523_0143.root, FC#11, port A2

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.9561

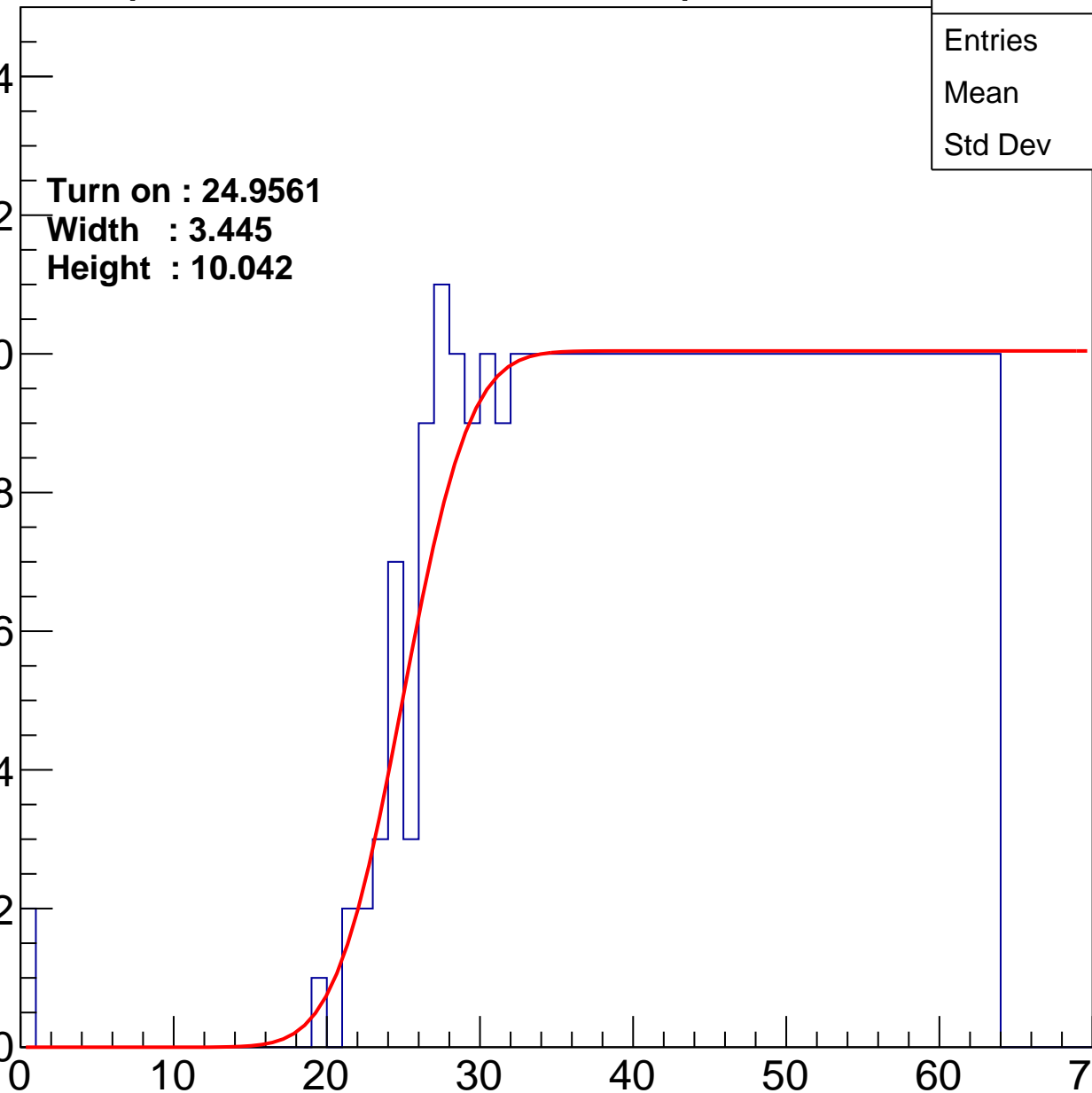
Width : 3.445

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch77

calib_packv5_042523_0143.root, FC#11, port A2

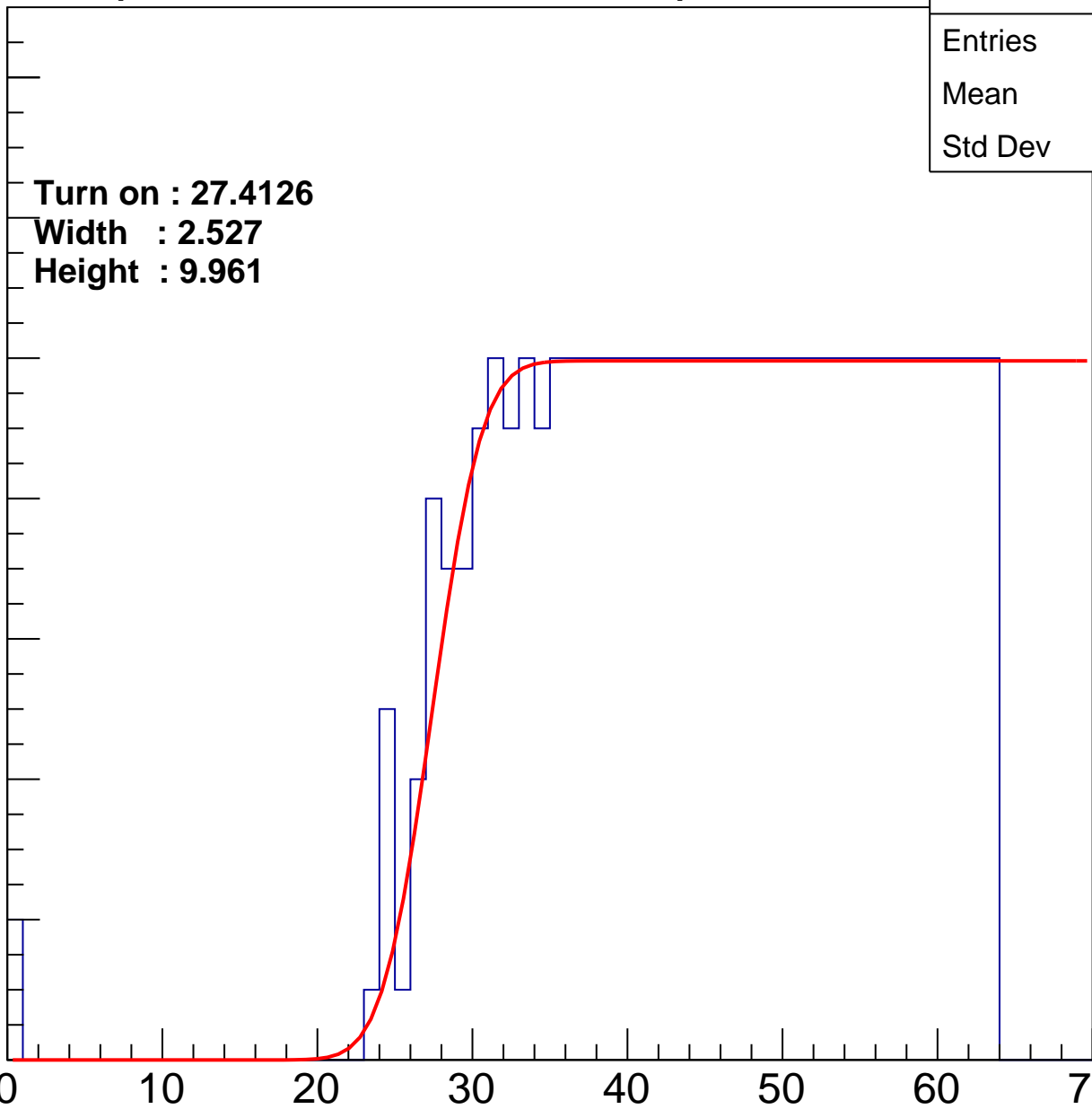
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4126
Width : 2.527
Height : 9.961

Entries	372
Mean	44.63
Std Dev	11.35

ampl



B1L102S, U23-ch78

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.81
Std Dev	12.06

Turn on : 26.0619

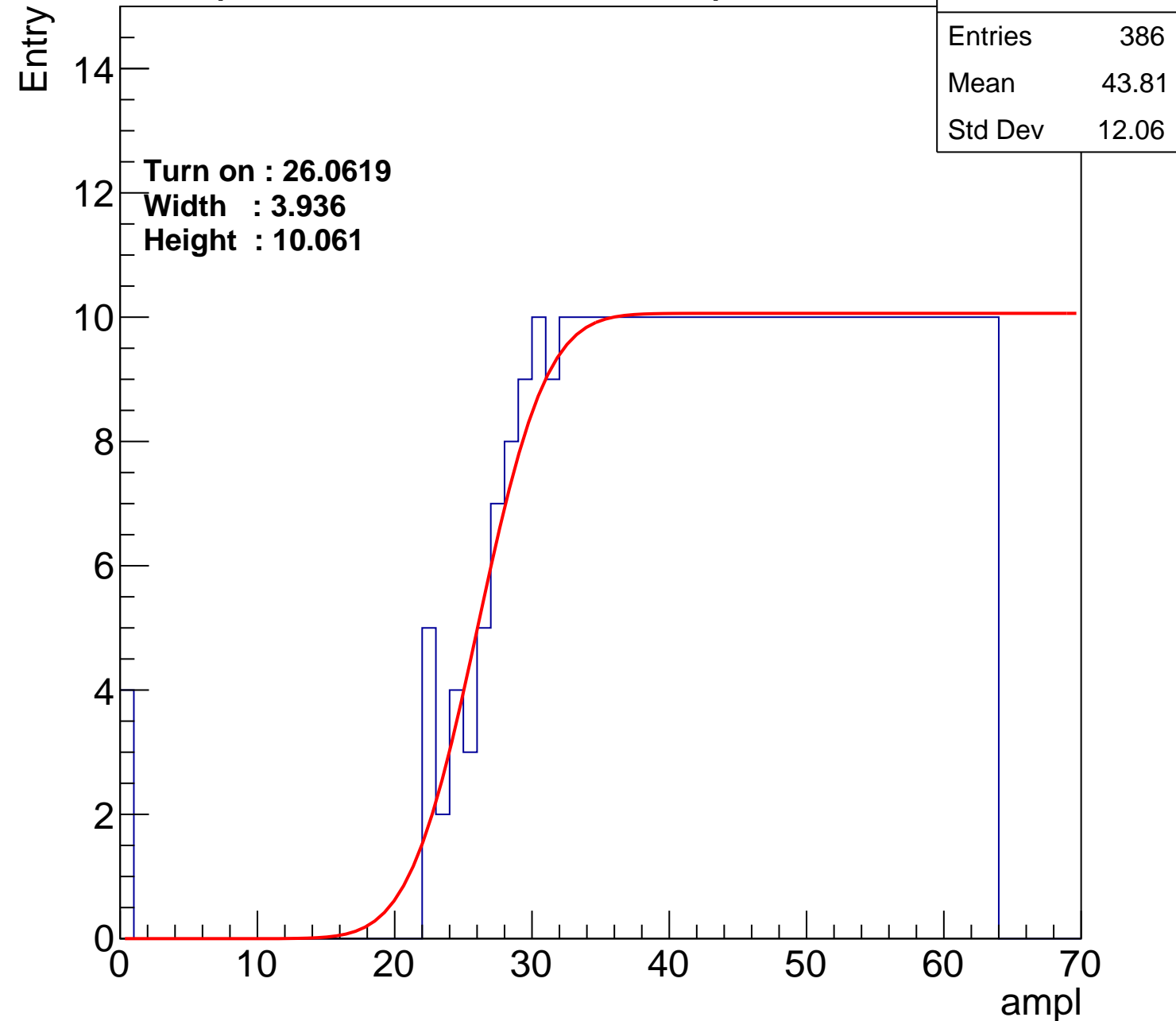
Width : 3.936

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch79

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.6173

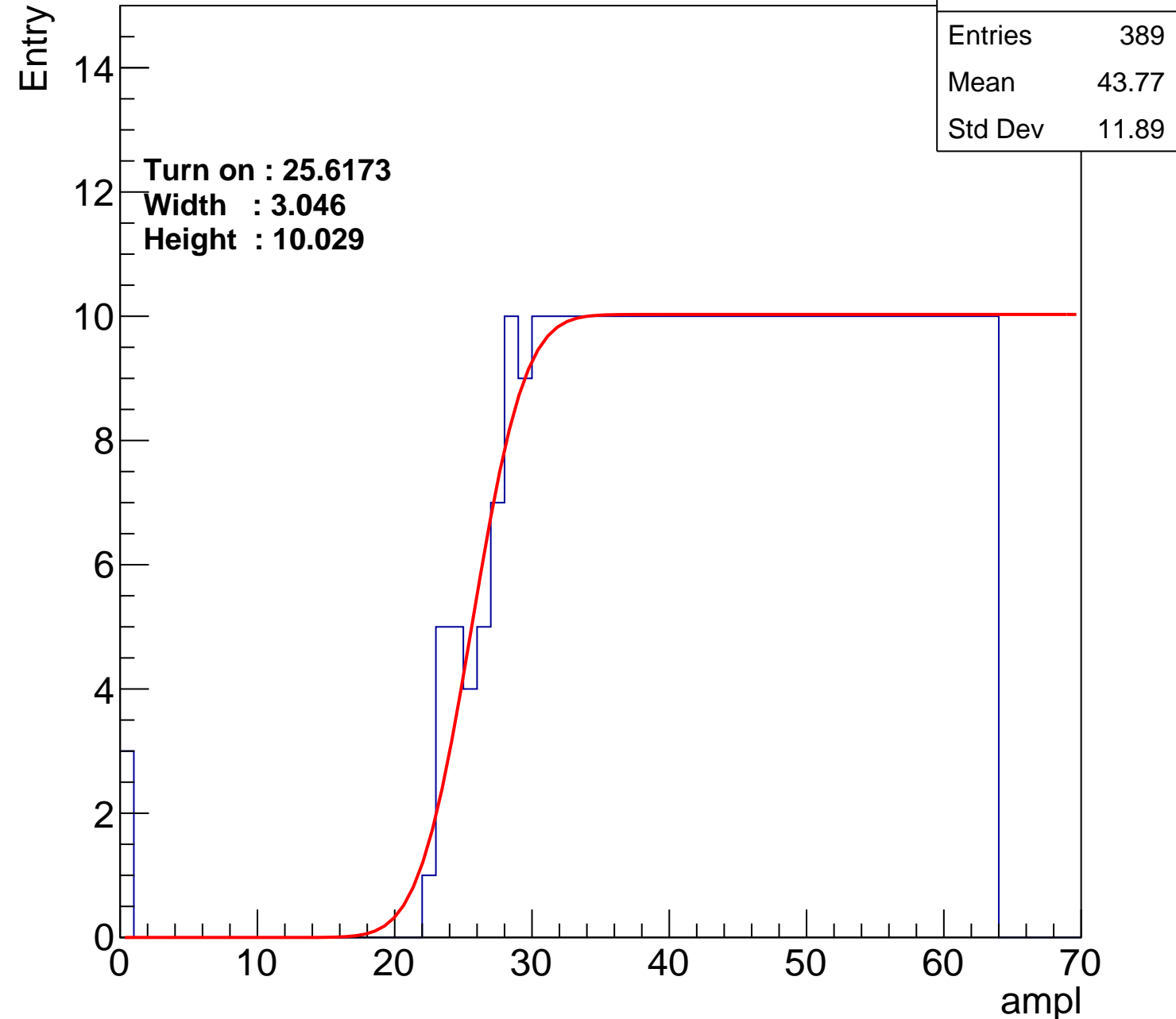
Width : 3.046

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch80

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.93
Std Dev	11.69

Turn on : 25.8911

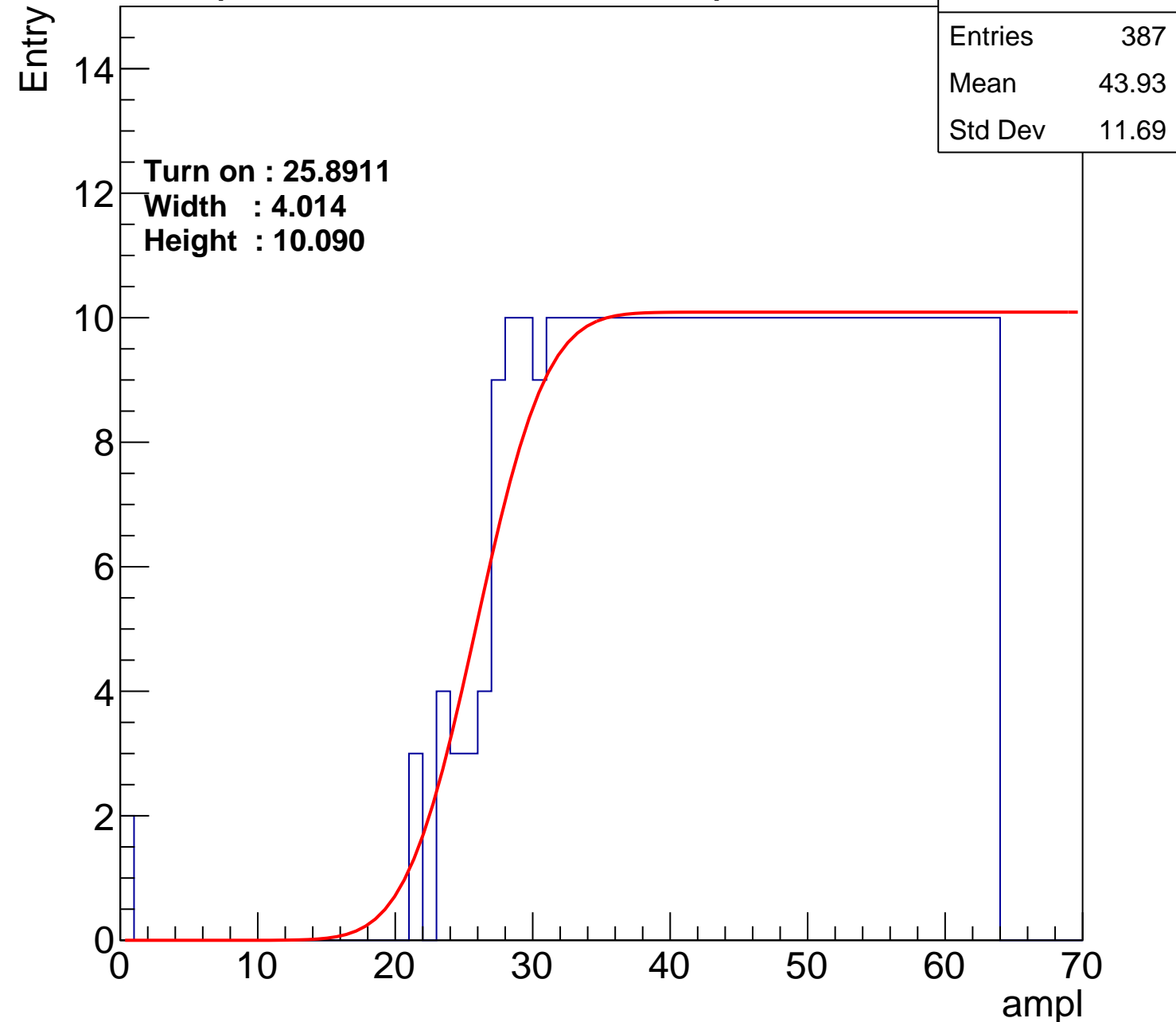
Width : 4.014

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch81

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.58
Std Dev	11.64

Turn on : 27.6197

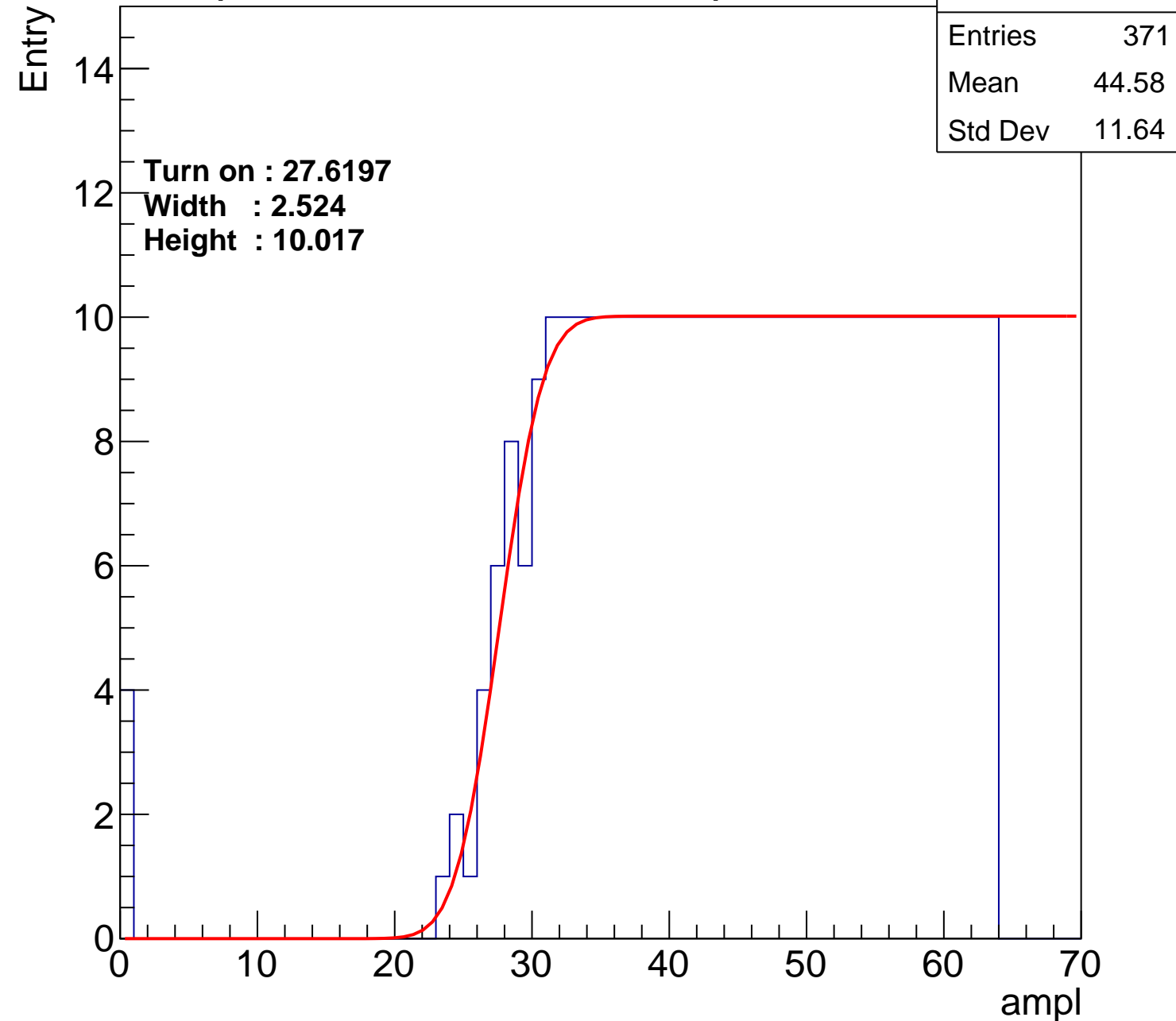
Width : 2.524

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch82

calib_packv5_042523_0143.root, FC#11, port A2

Entries	401
Mean	42.98
Std Dev	12.67

Turn on : 24.5605

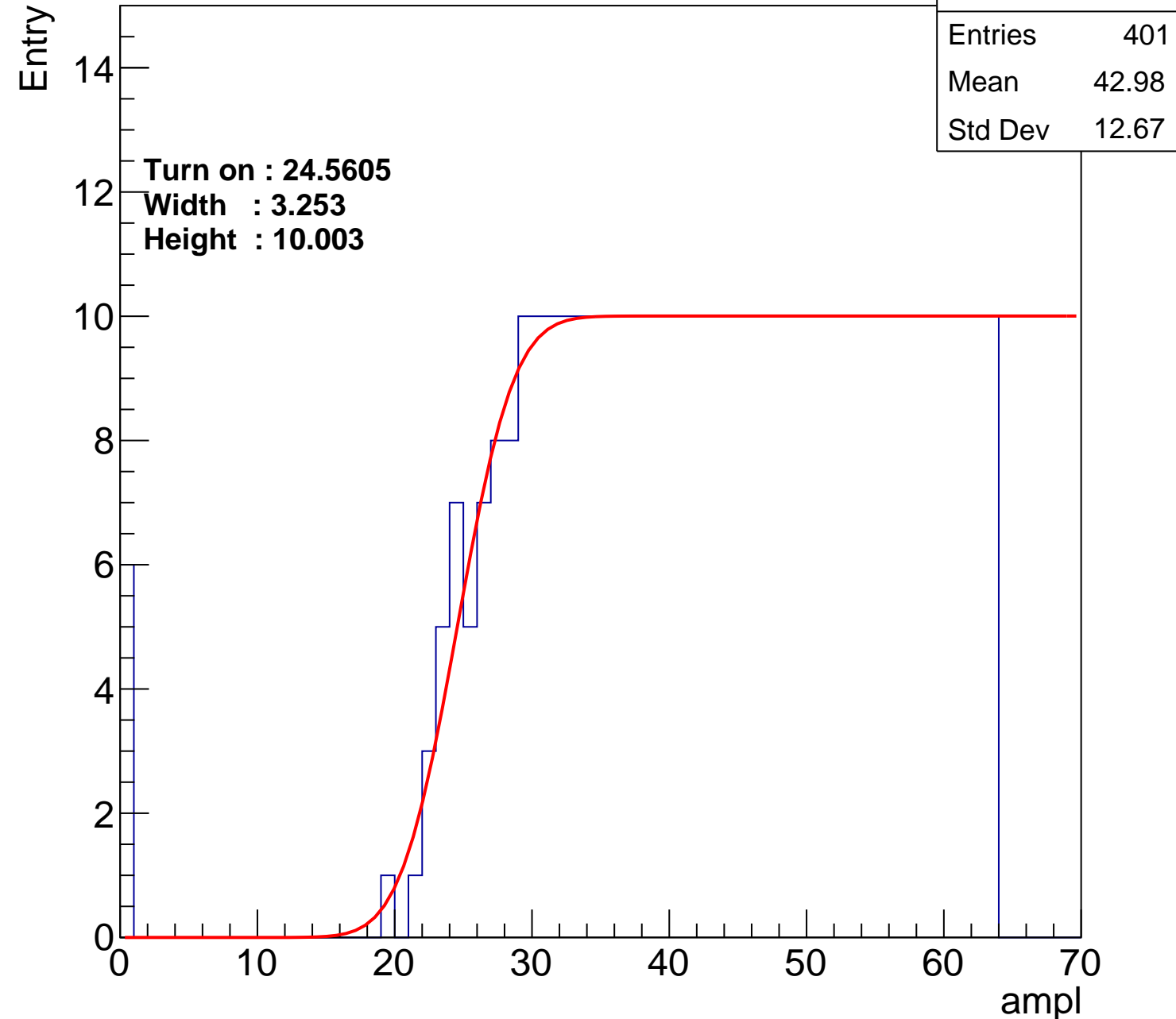
Width : 3.253

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch83

calib_packv5_042523_0143.root, FC#11, port A2

Entries	361
Mean	45.16
Std Dev	11.09

Turn on : 28.2834

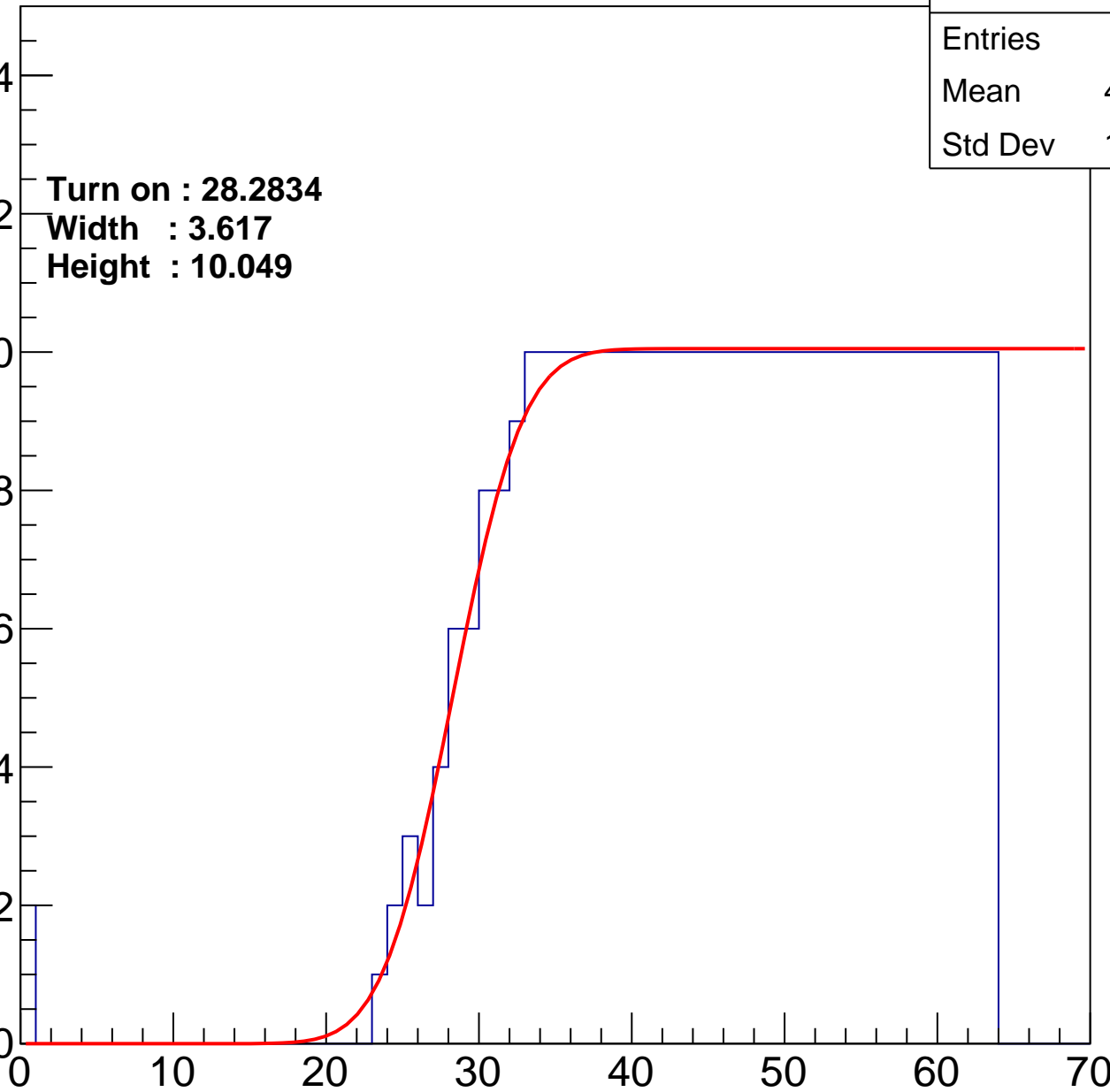
Width : 3.617

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch84

calib_packv5_042523_0143.root, FC#11, port A2

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.6811

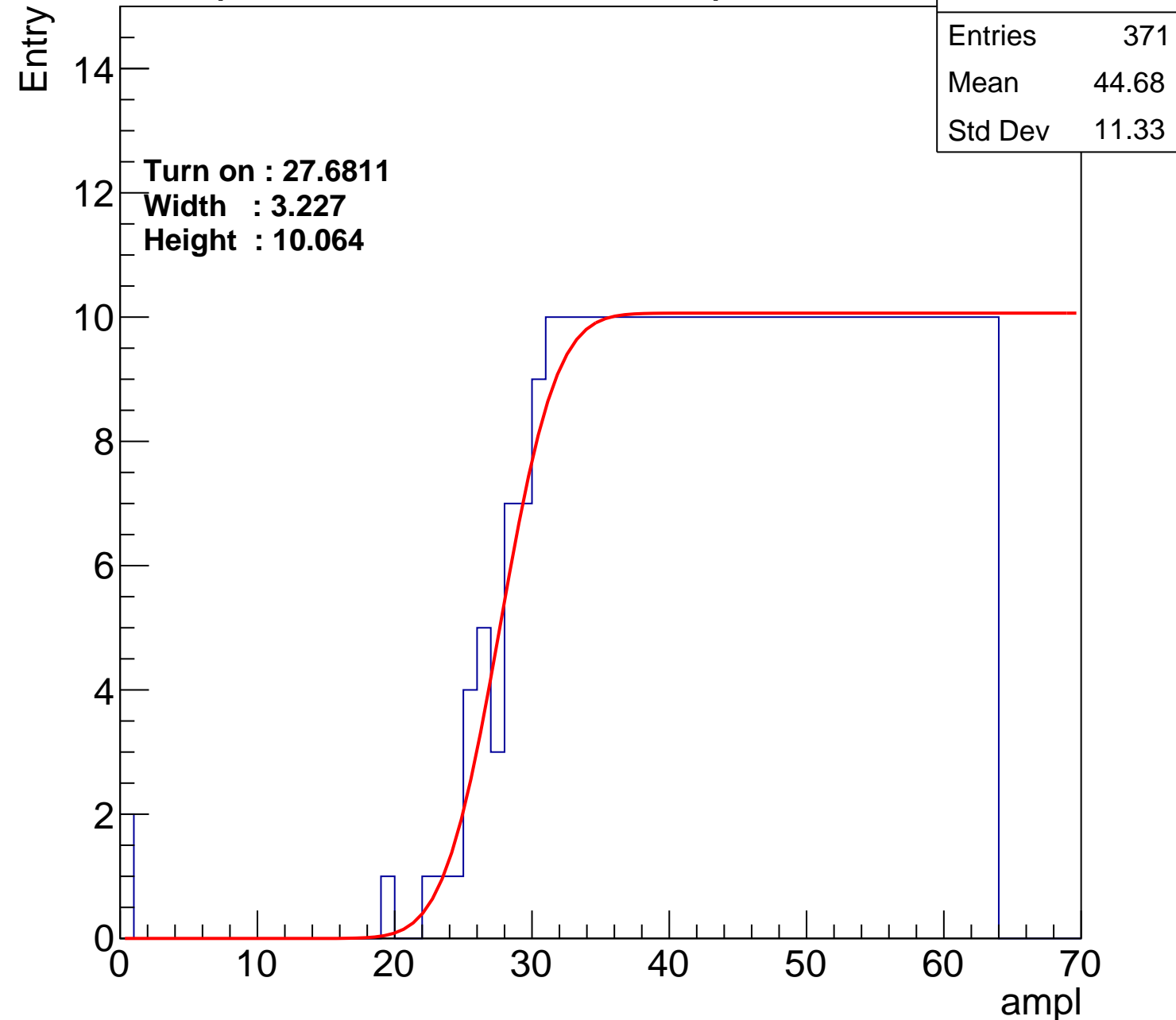
Width : 3.227

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch85

calib_packv5_042523_0143.root, FC#11, port A2

Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.6744

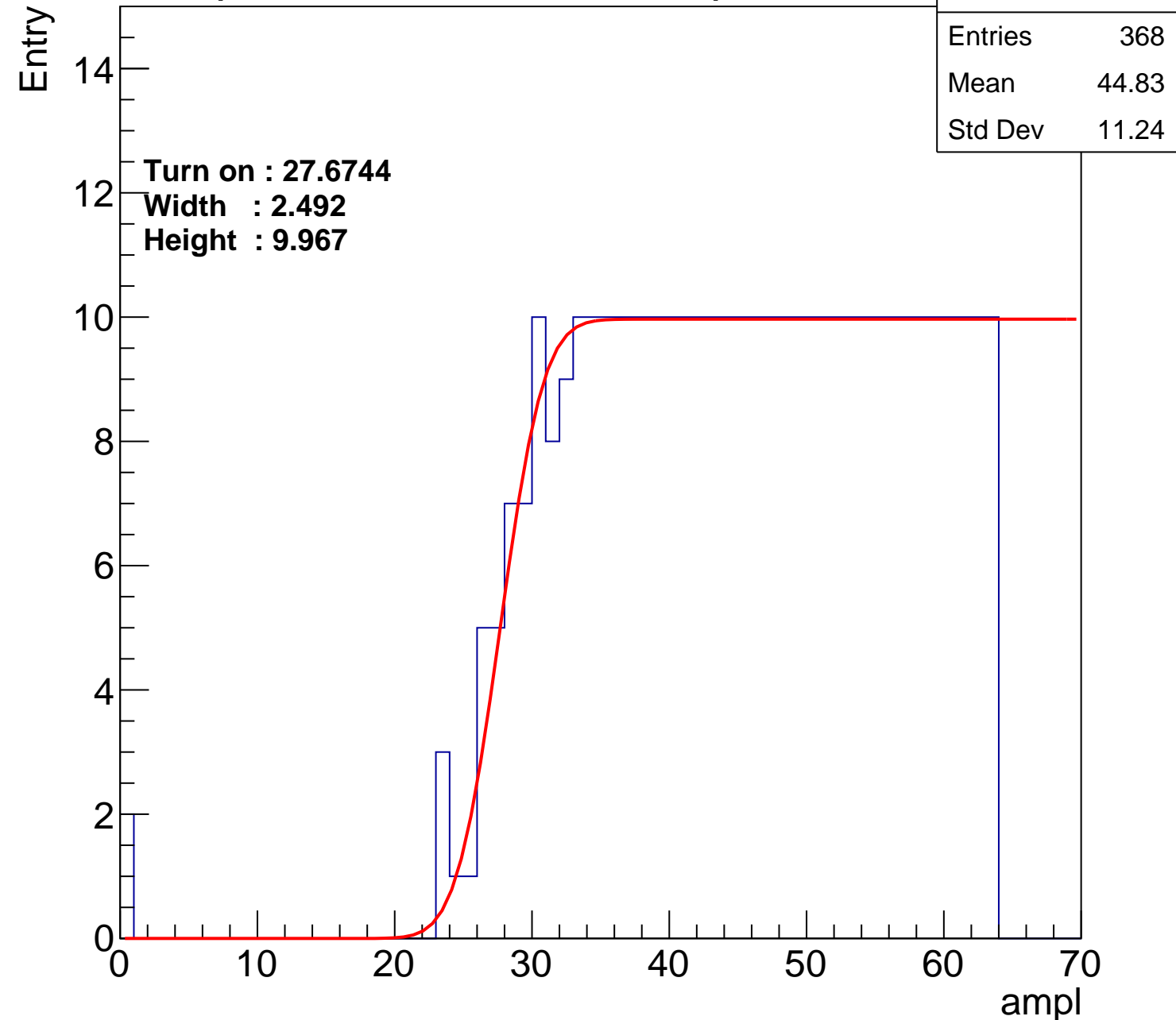
Width : 2.492

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch86

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.88
Std Dev	11.2

Turn on : 27.2263

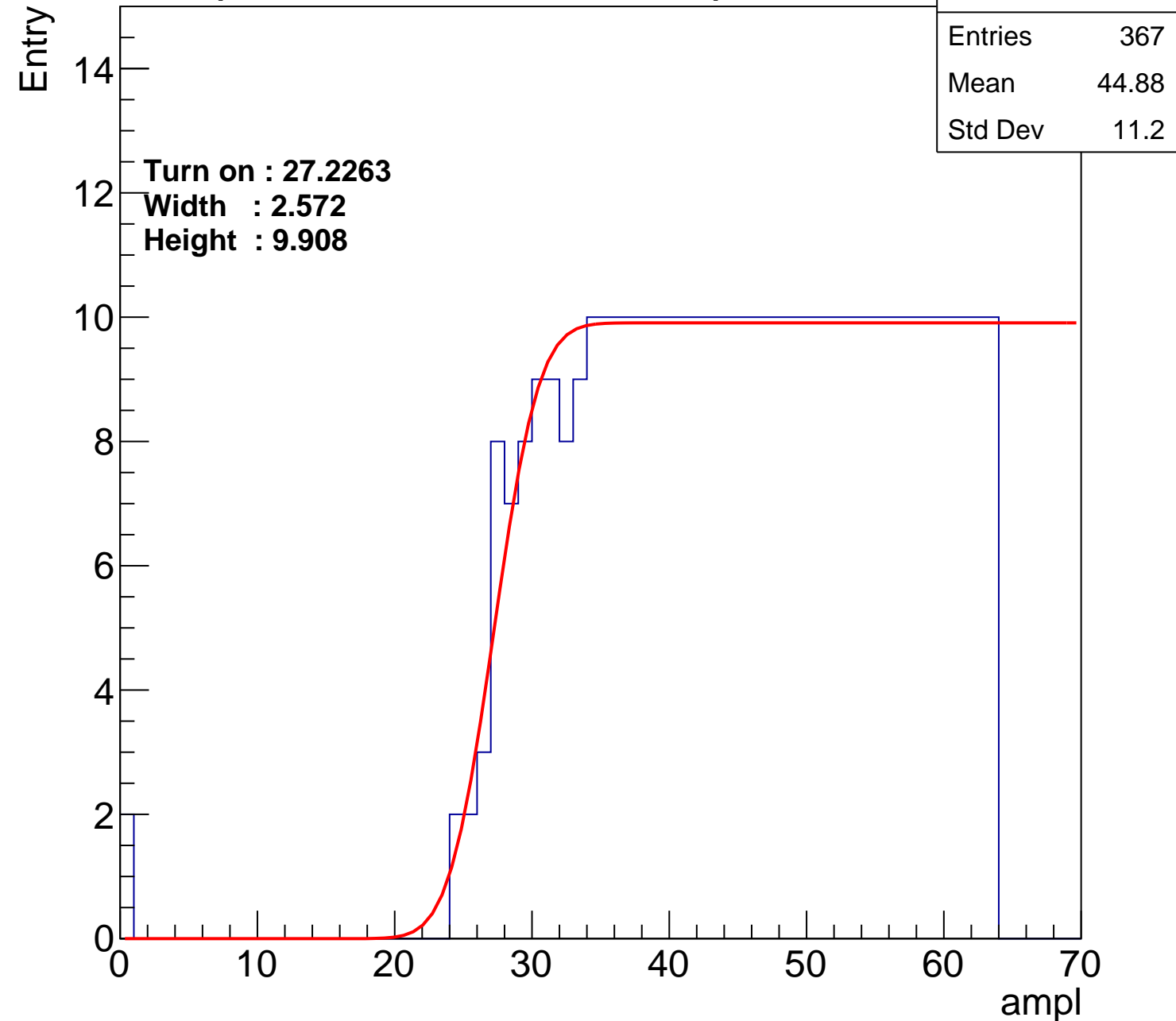
Width : 2.572

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch87

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.69
Std Dev	11.39

Turn on : 26.8704

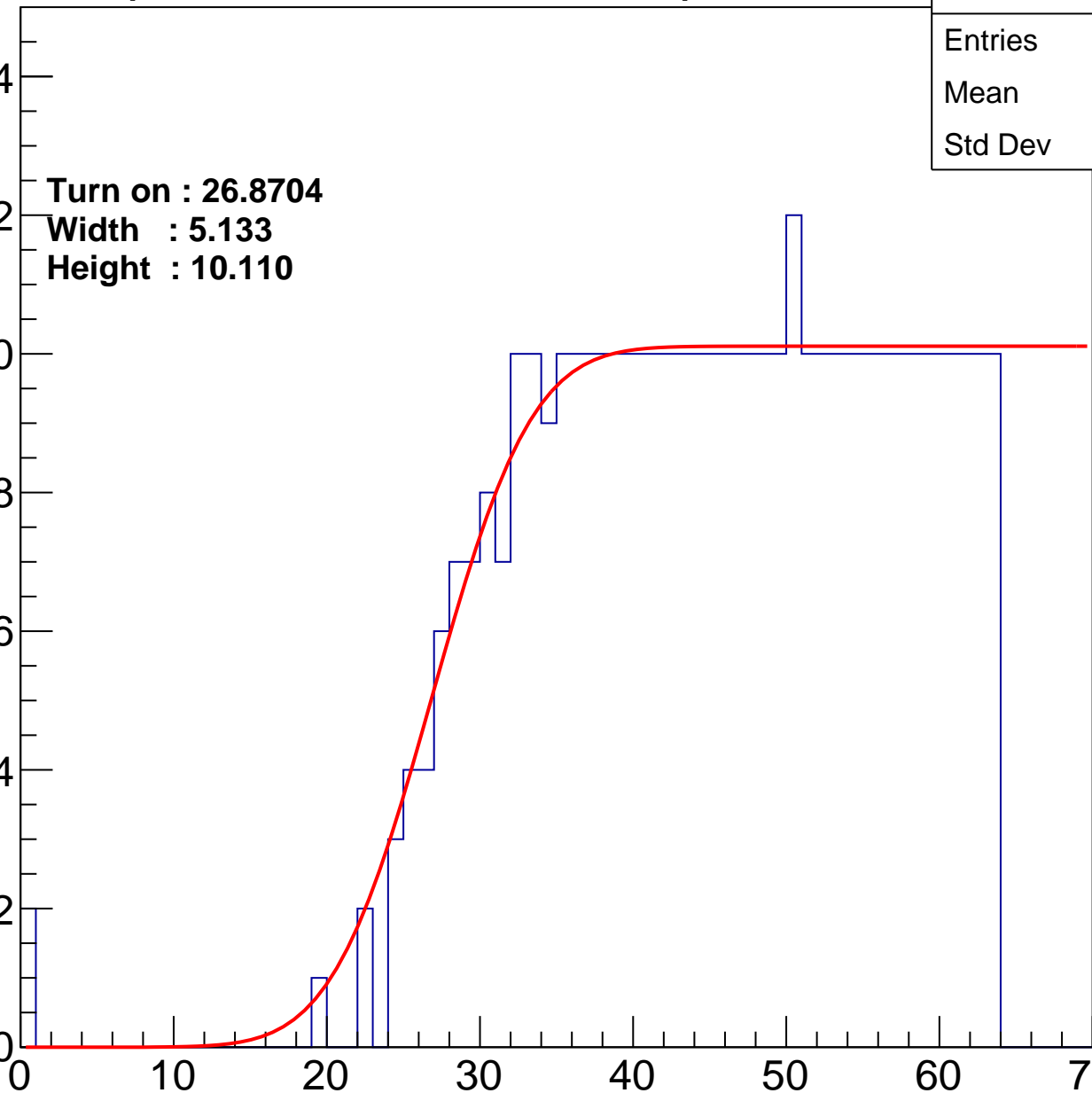
Width : 5.133

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch88

calib_packv5_042523_0143.root, FC#11, port A2

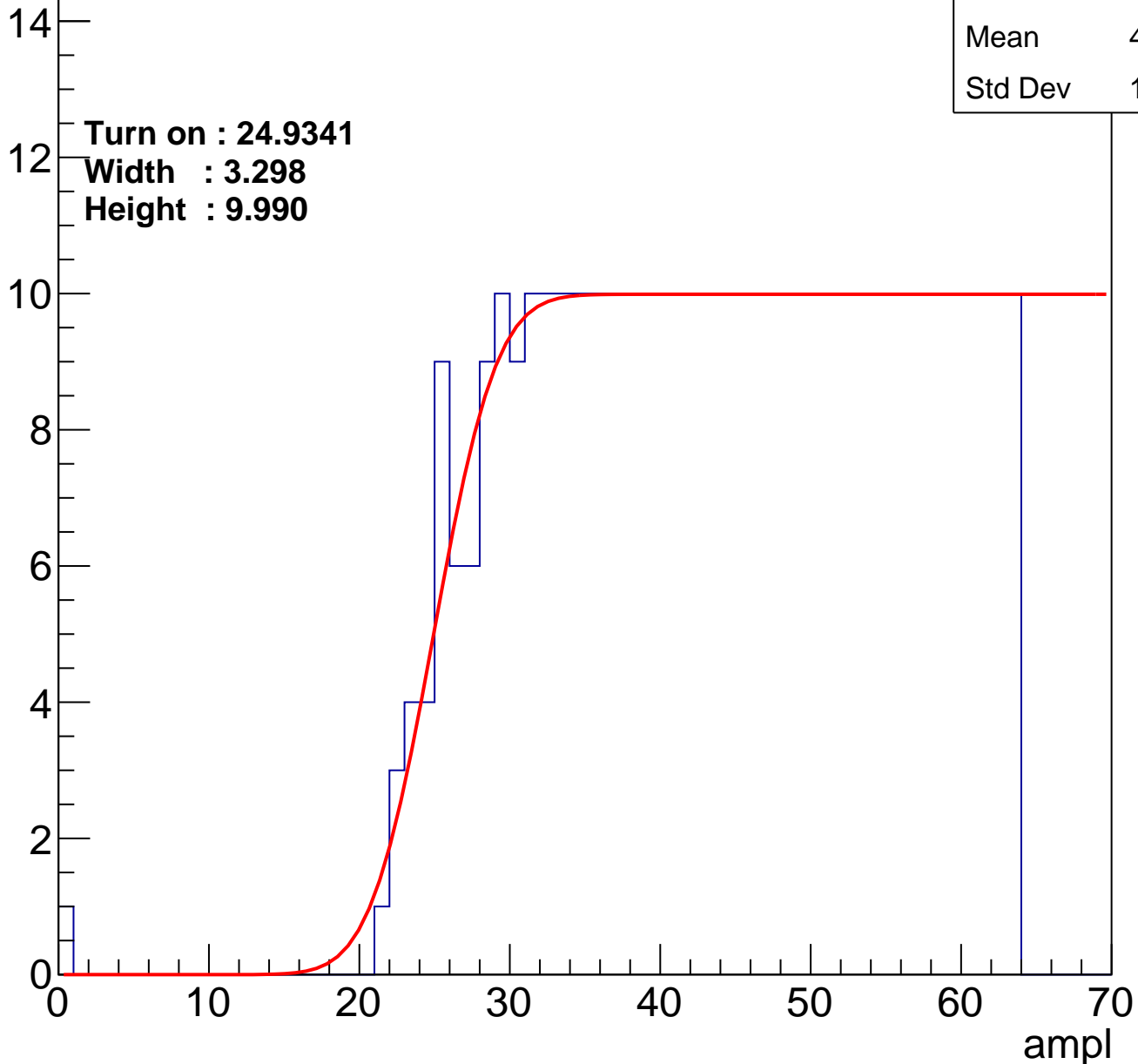
Entries	392
Mean	43.72
Std Dev	11.67

Turn on : 24.9341

Width : 3.298

Height : 9.990

Entry



B1L102S, U23-ch89

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.8867

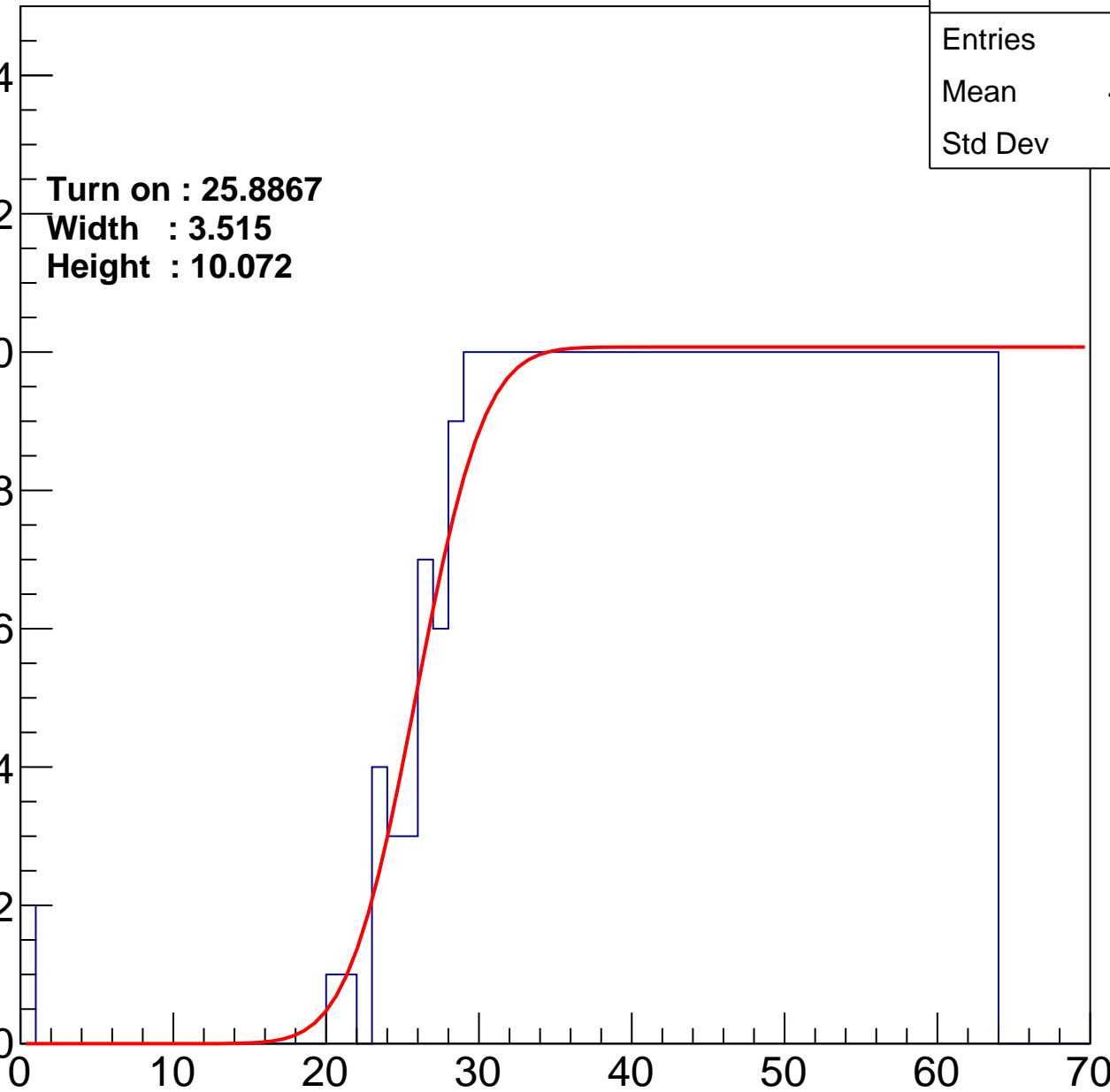
Width : 3.515

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch90

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.31
Std Dev	11.68

Turn on : 26.9006

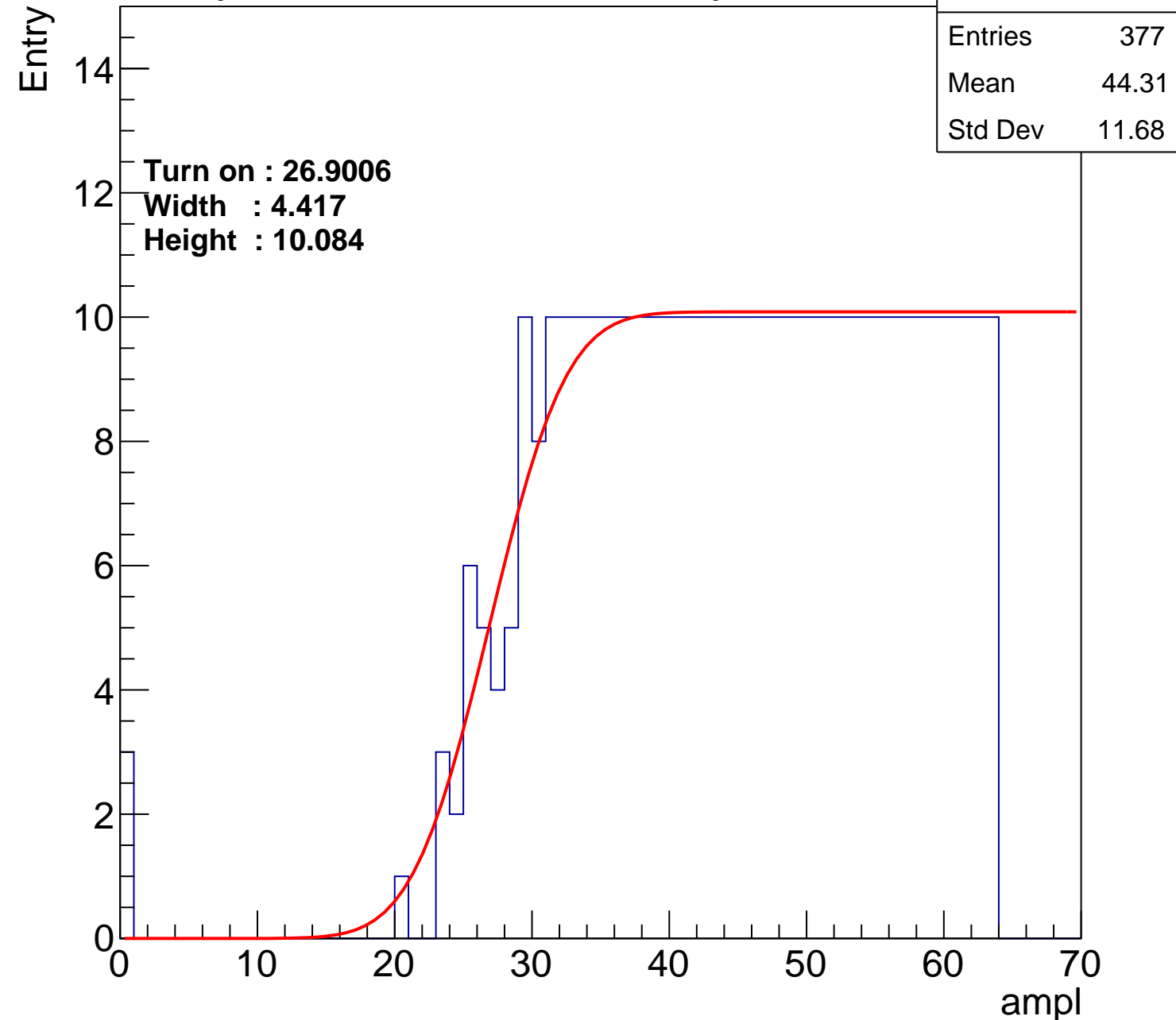
Width : 4.417

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch91

calib_packv5_042523_0143.root, FC#11, port A2

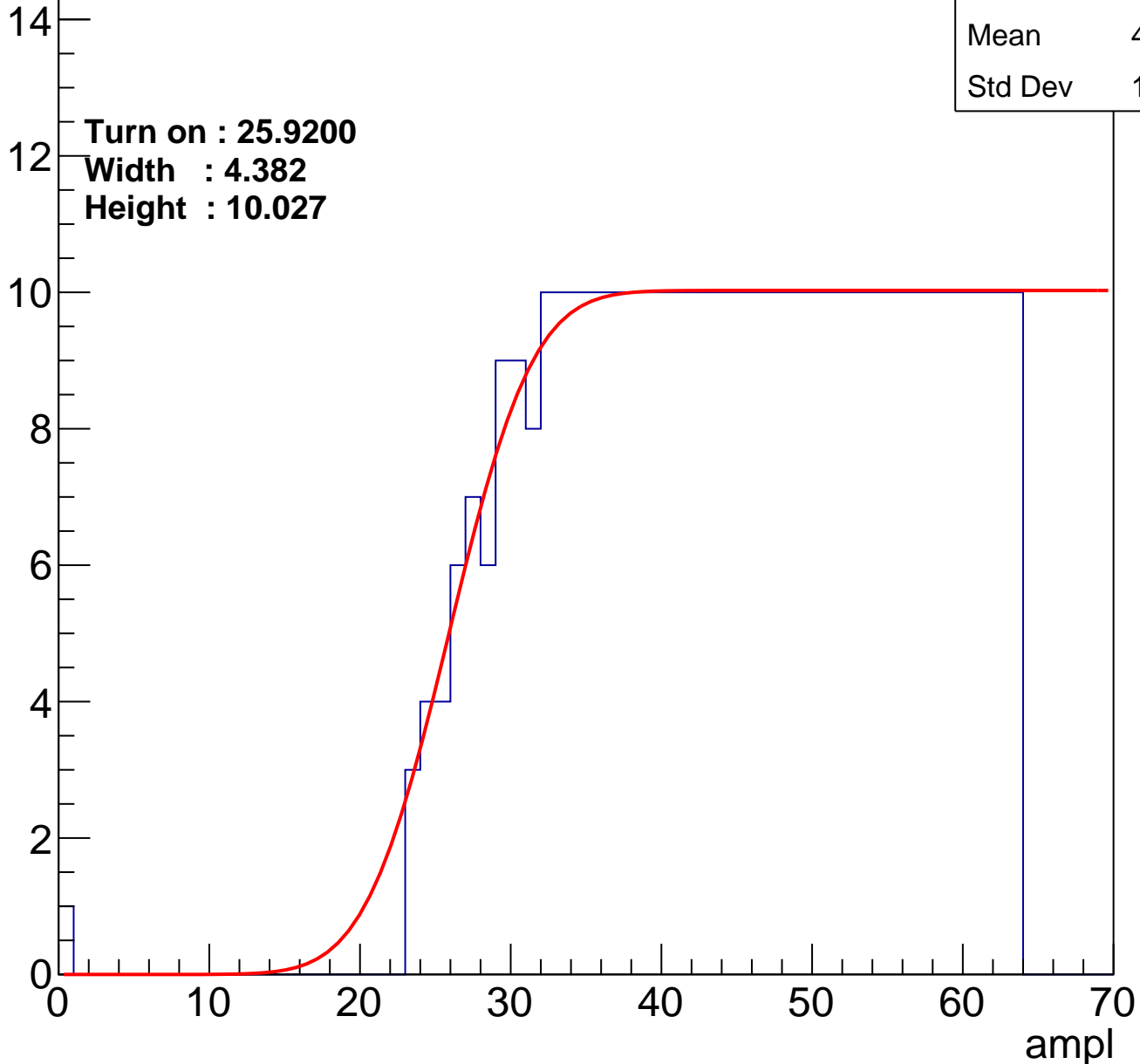
Entries	377
Mean	44.45
Std Dev	11.29

Turn on : 25.9200

Width : 4.382

Height : 10.027

Entry



B1L102S, U23-ch92

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.93
Std Dev	11.6

Turn on : 25.2693

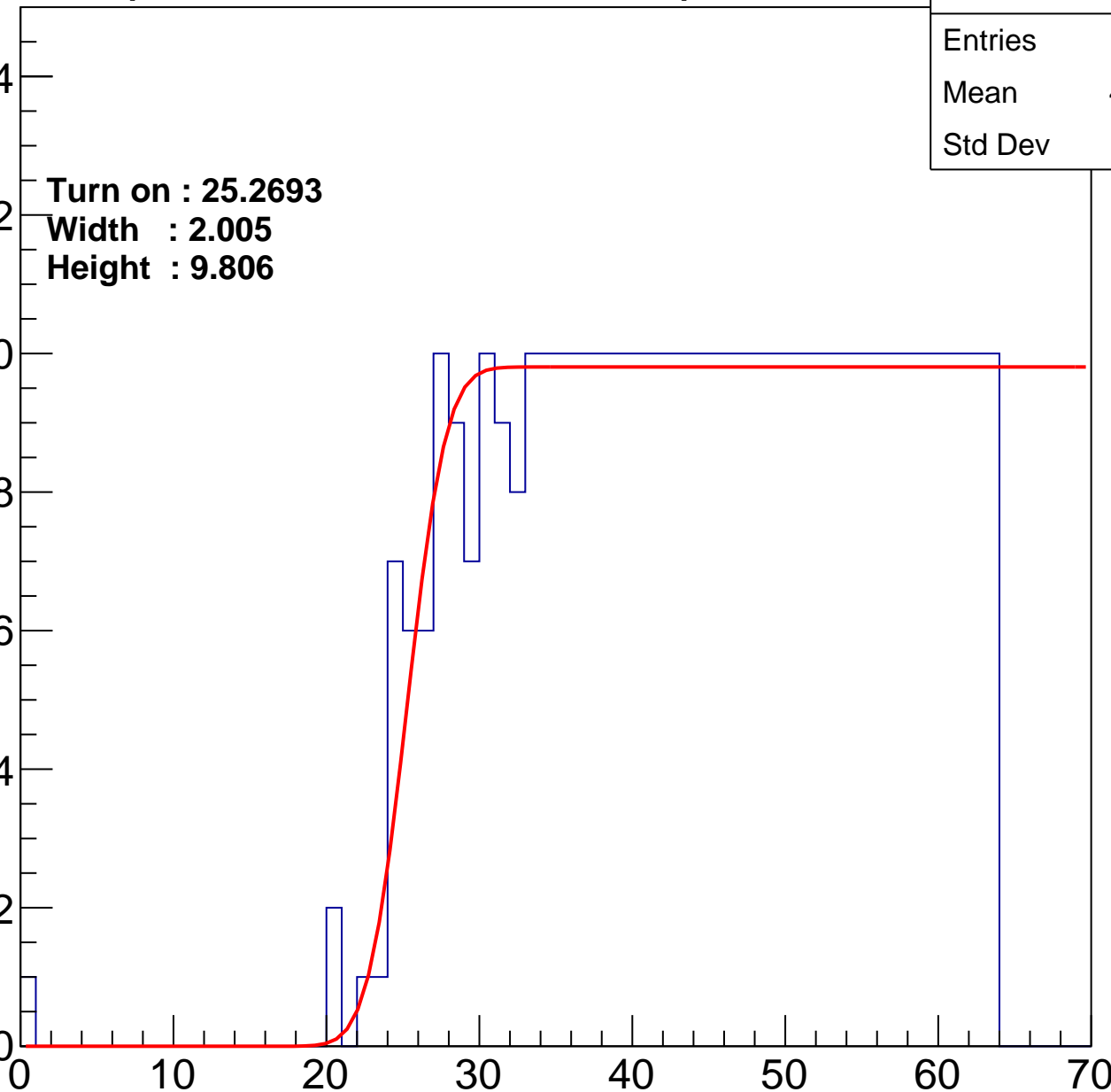
Width : 2.005

Height : 9.806

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch93

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	44.17
Std Dev	11.72

Turn on : 26.3409

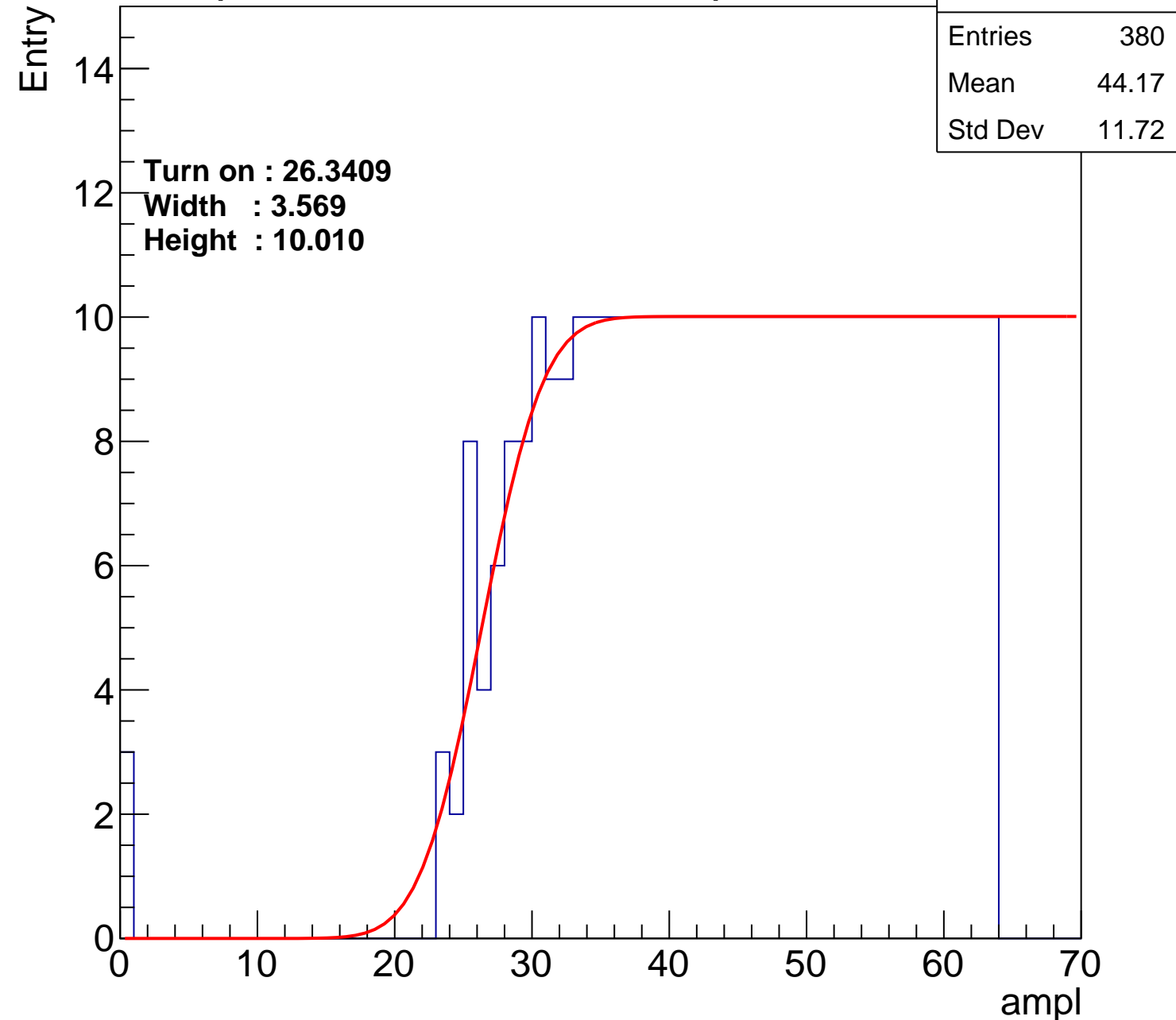
Width : 3.569

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch94

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.5061

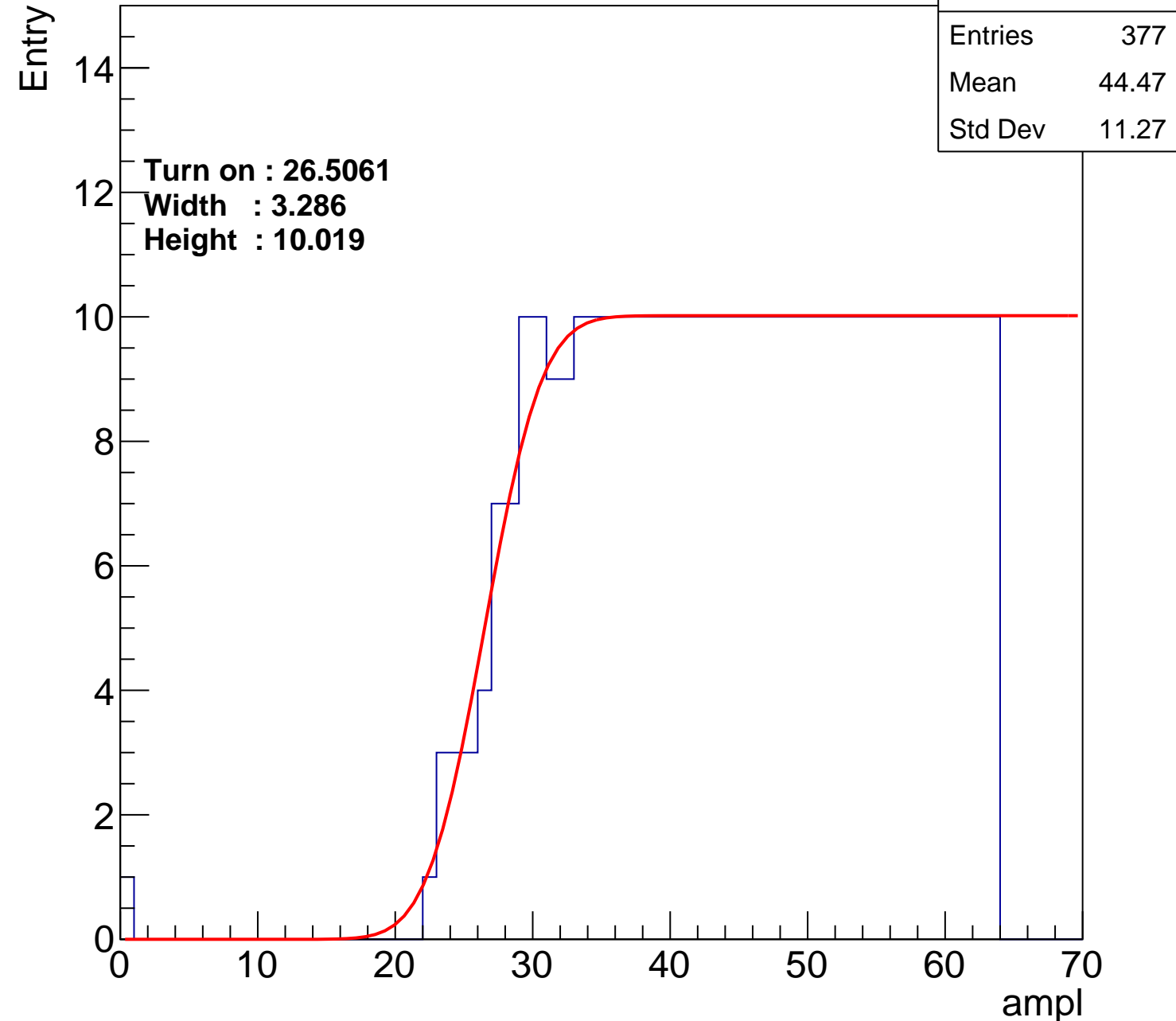
Width : 3.286

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch95

calib_packv5_042523_0143.root, FC#11, port A2

Entries	362
Mean	45.15
Std Dev	11.06

Turn on : 28.2368

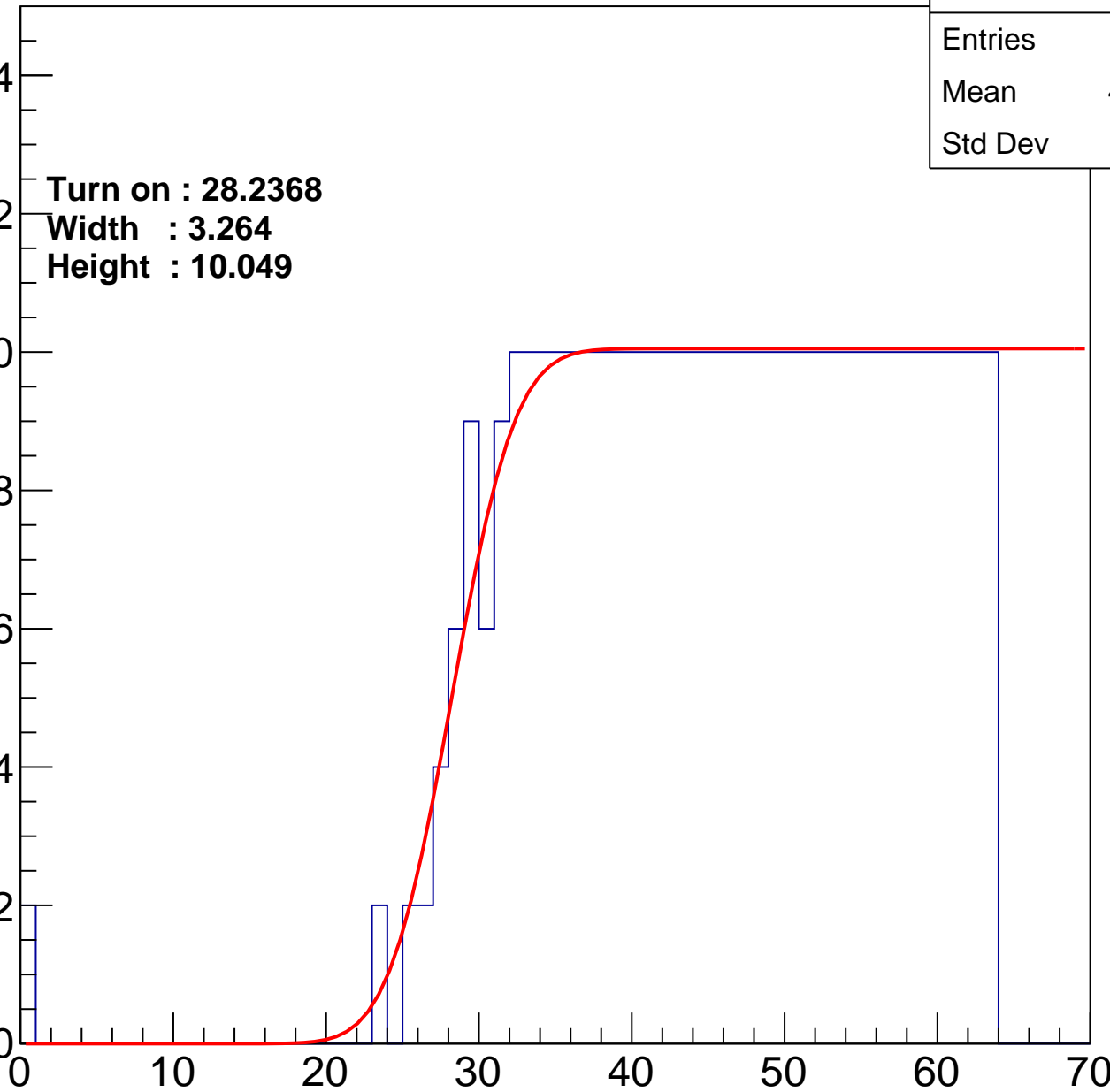
Width : 3.264

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch96

calib_packv5_042523_0143.root, FC#11, port A2

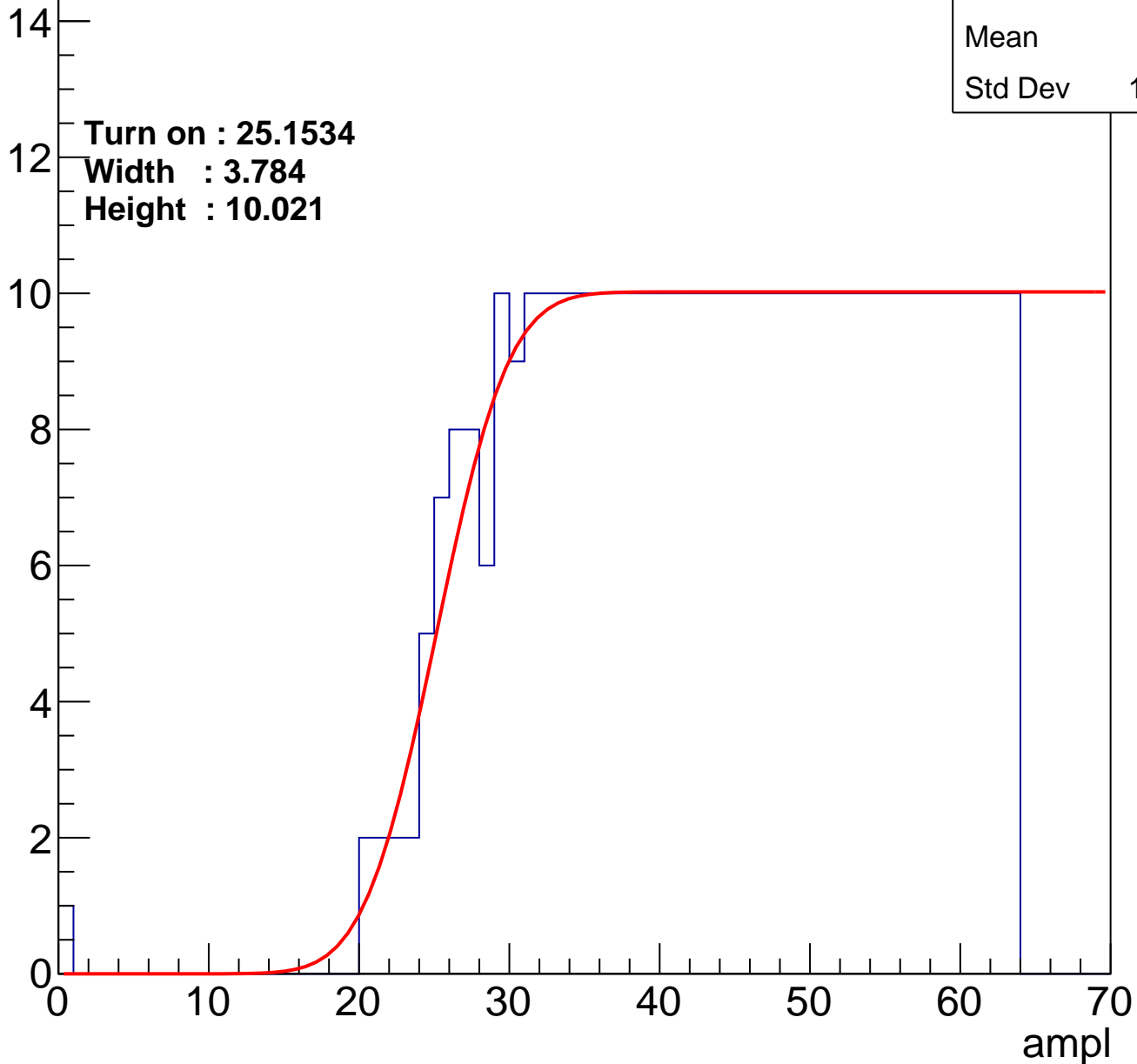
Entries	392
Mean	43.7
Std Dev	11.72

Turn on : 25.1534

Width : 3.784

Height : 10.021

Entry



B1L102S, U23-ch97

calib_packv5_042523_0143.root, FC#11, port A2

Entries	365
Mean	45.02
Std Dev	11.01

Turn on : 28.1787

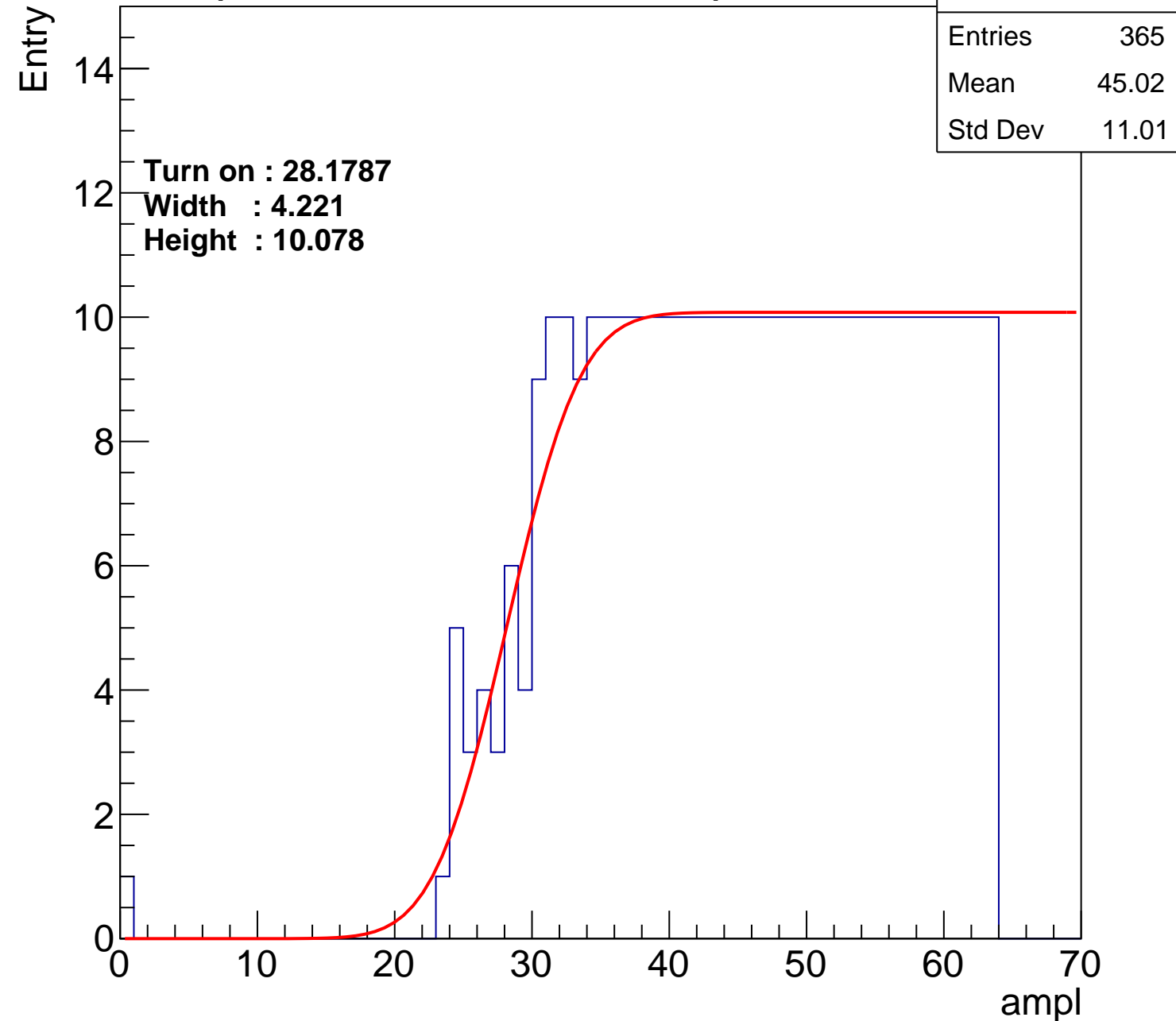
Width : 4.221

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch98

calib_packv5_042523_0143.root, FC#11, port A2

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.6769

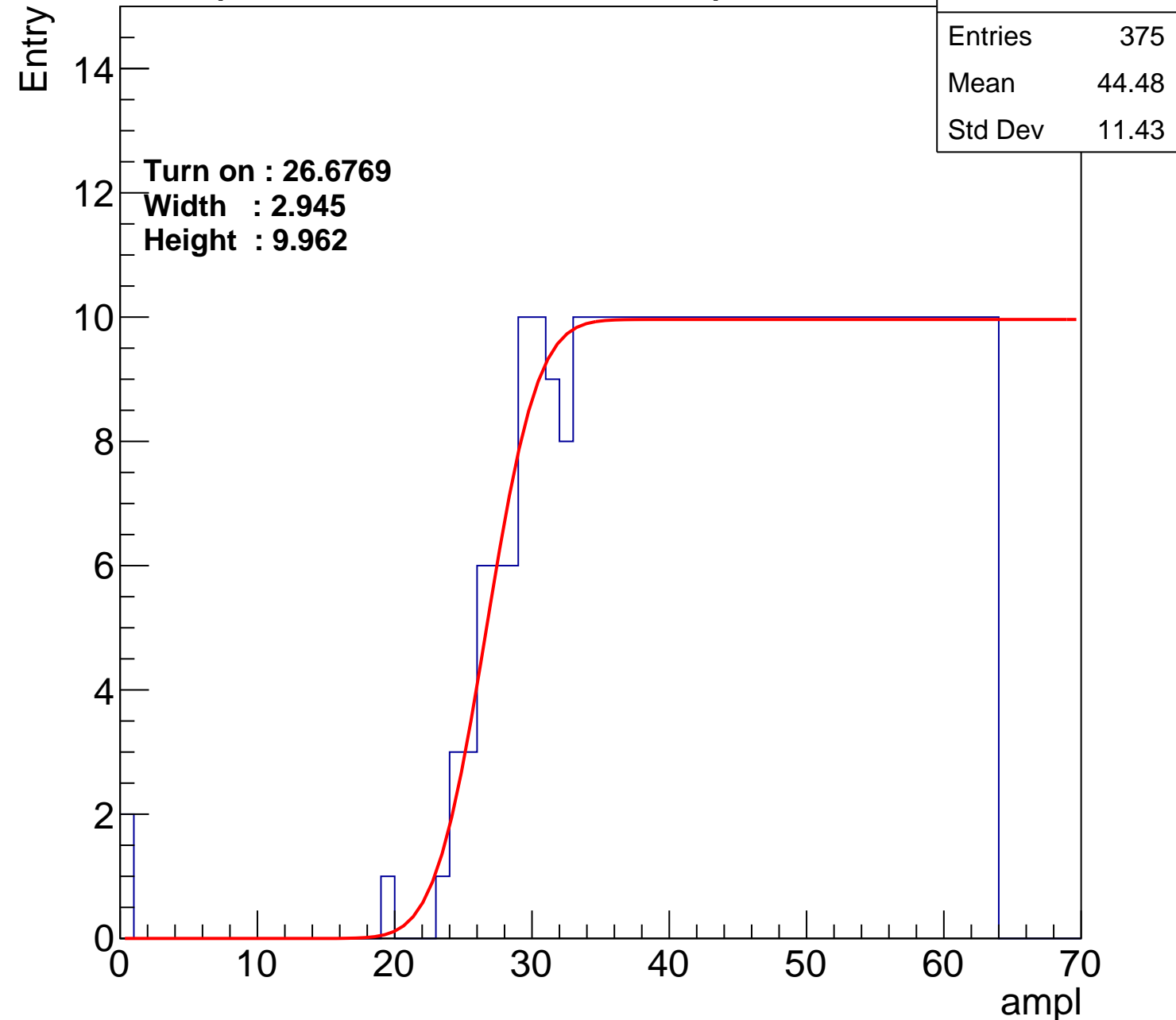
Width : 2.945

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch99

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	43.89
Std Dev	12.16

Turn on : 26.4871

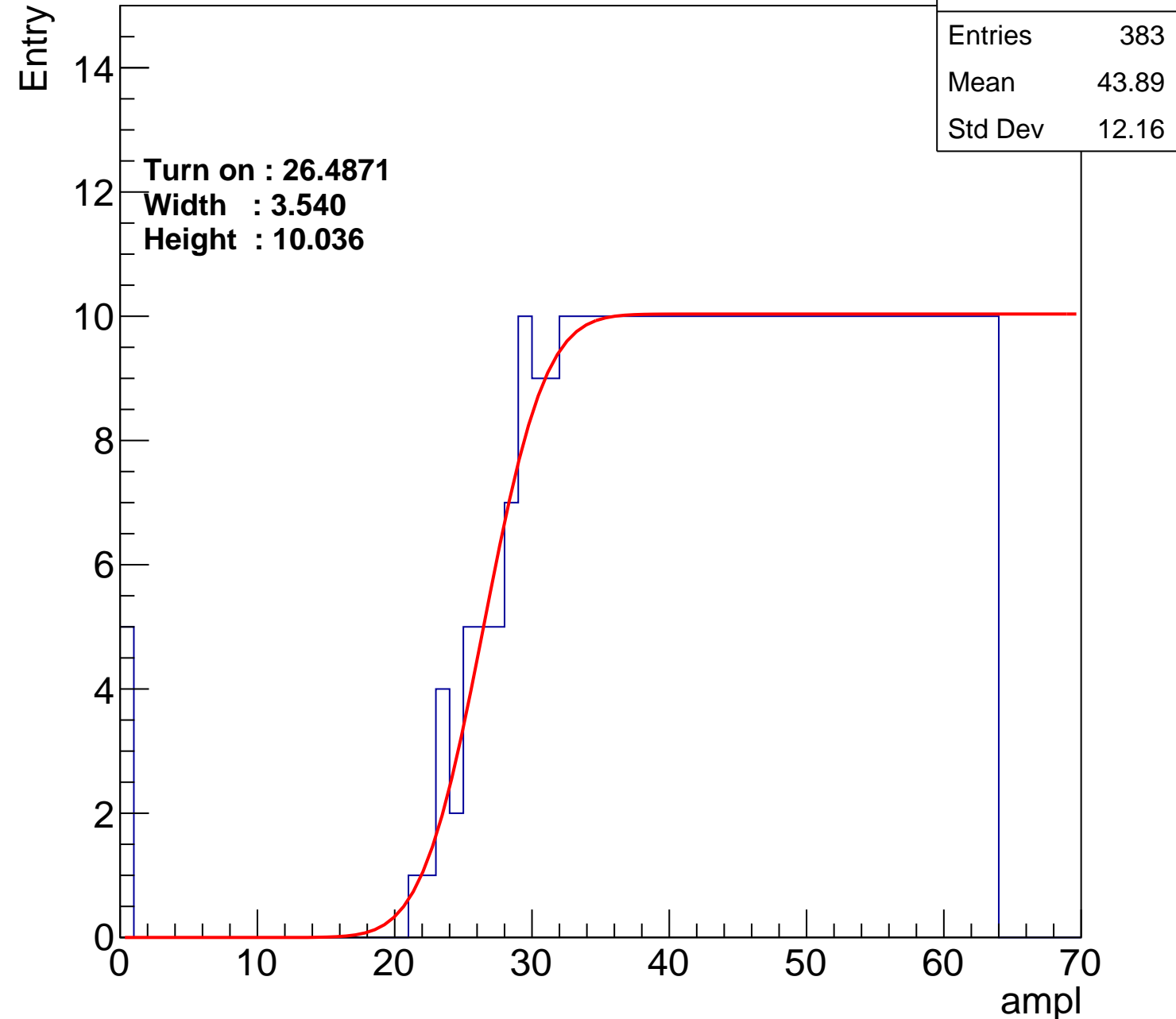
Width : 3.540

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch100

calib_packv5_042523_0143.root, FC#11, port A2

Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 25.1673

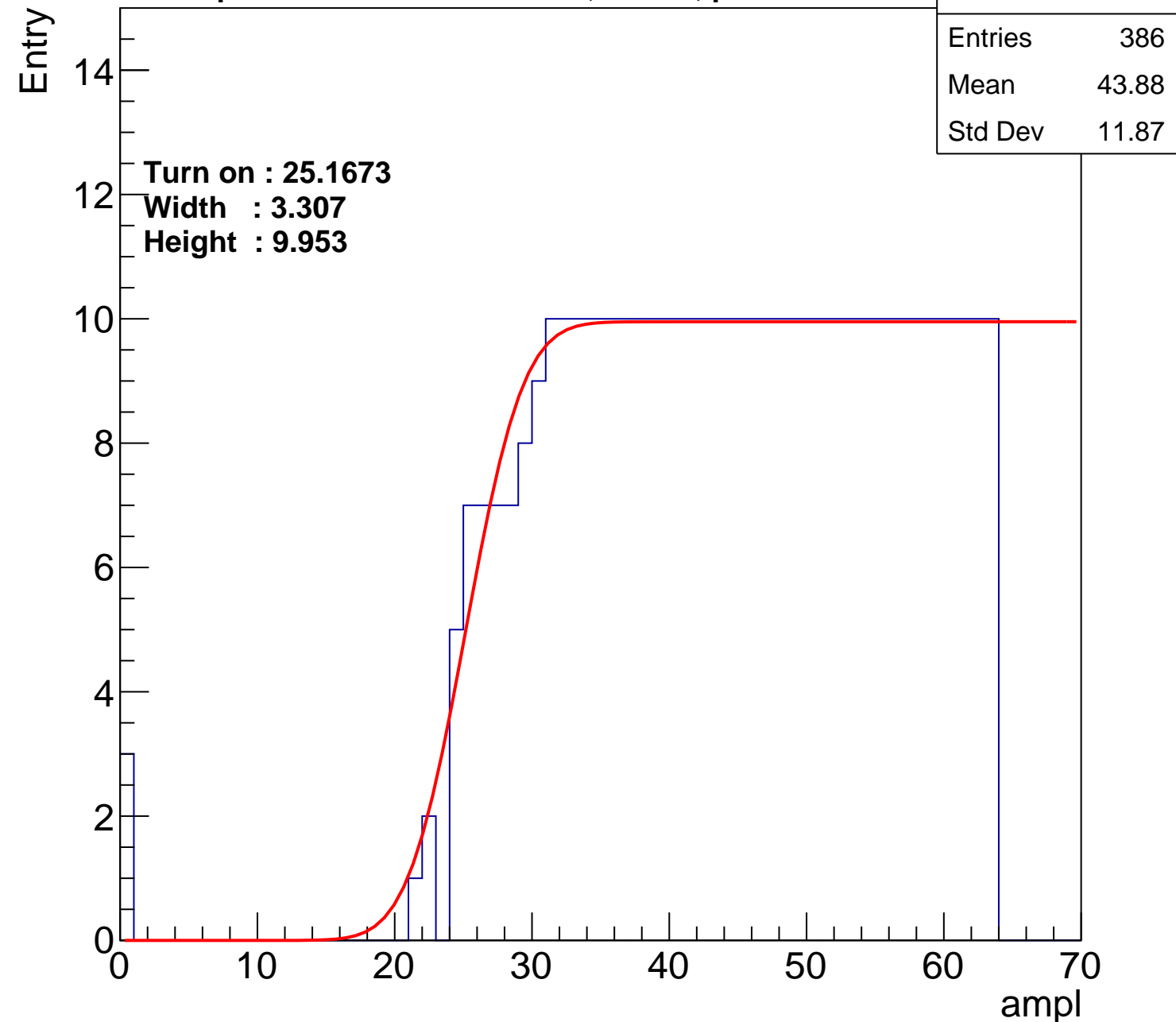
Width : 3.307

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch101

calib_packv5_042523_0143.root, FC#11, port A2

Entries	387
Mean	43.78
Std Dev	12.06

Turn on : 25.9943

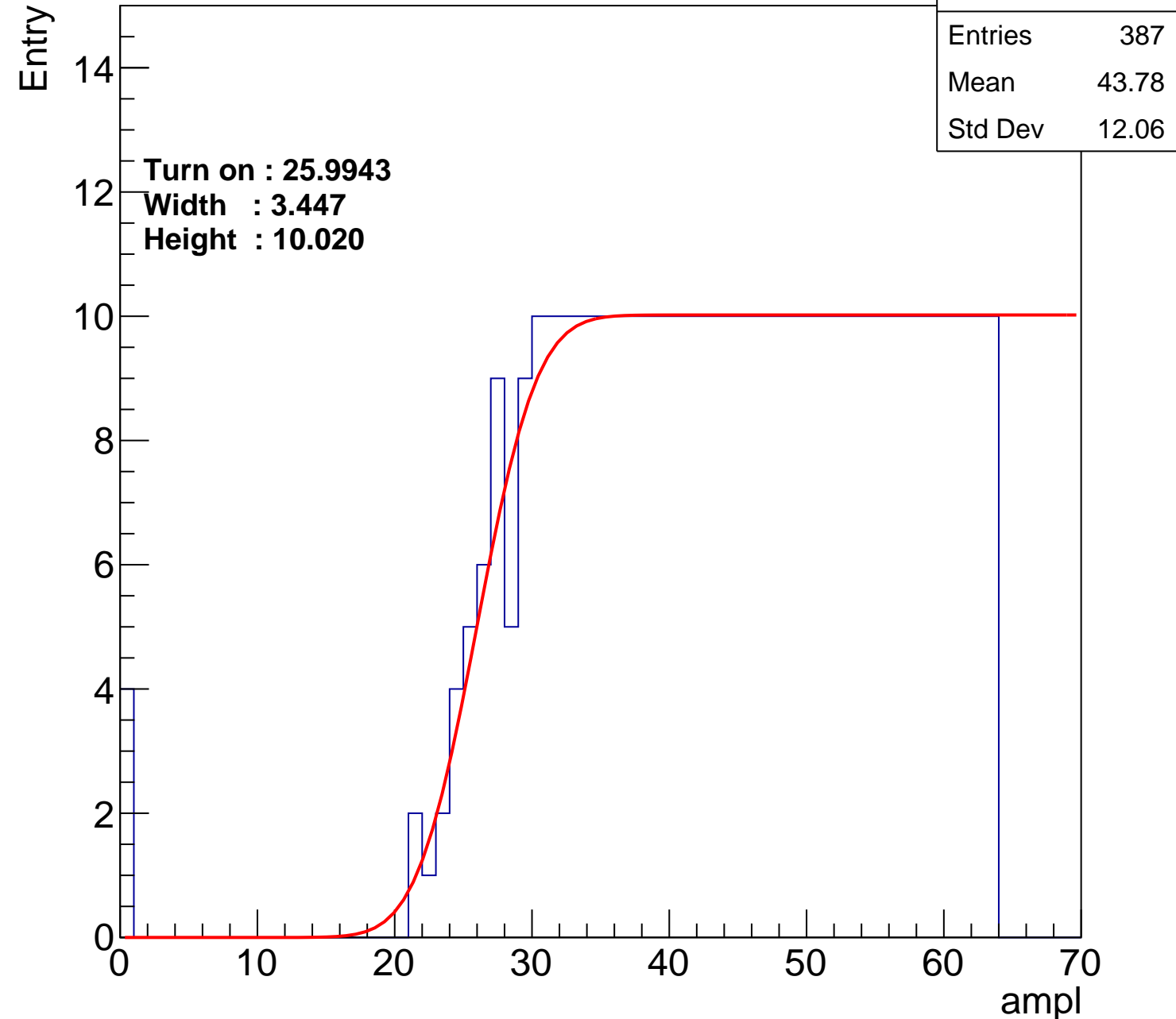
Width : 3.447

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch102

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
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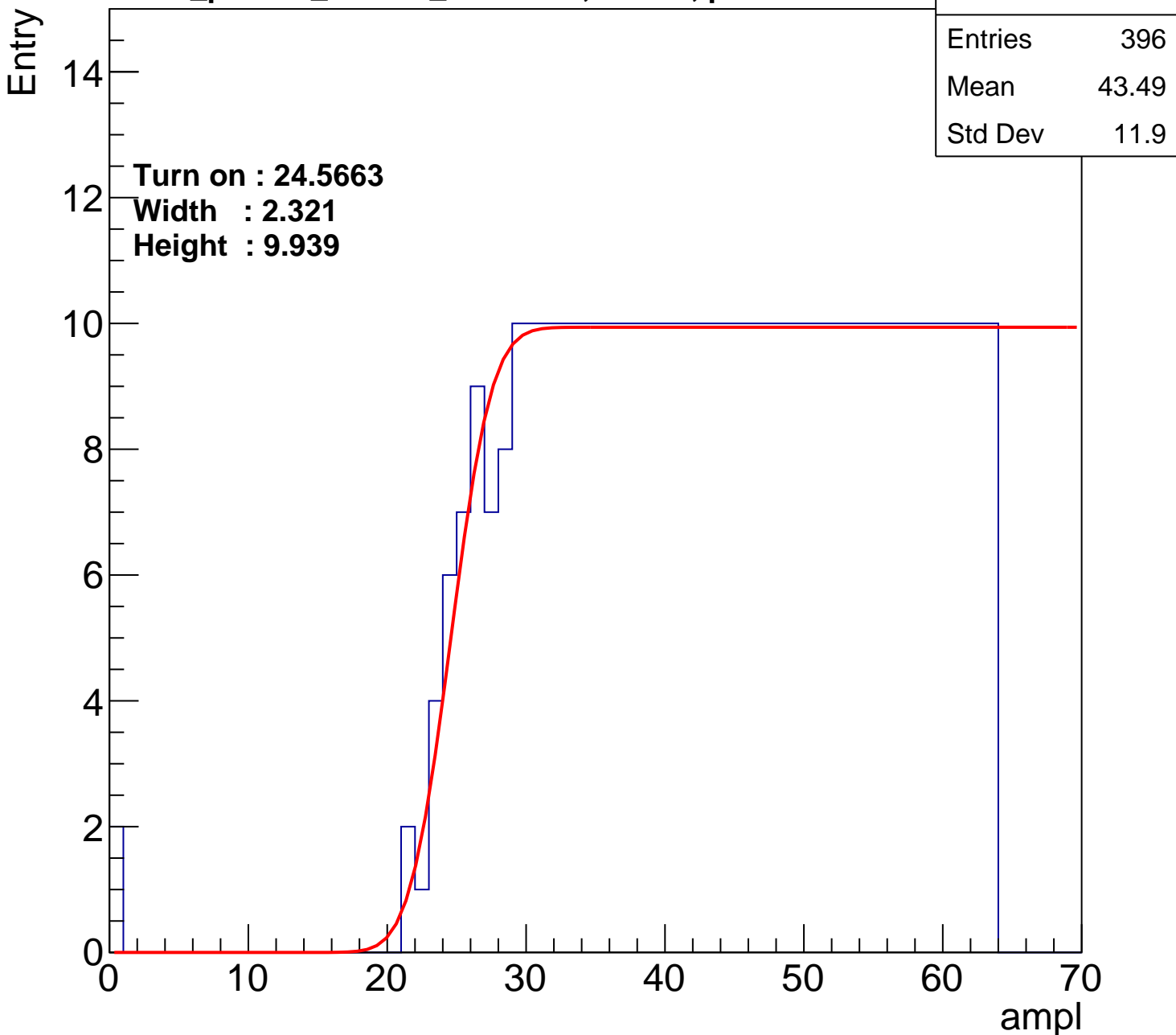
Mean	43.49
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Std Dev	11.9
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Turn on : 24.5663

Width : 2.321

Height : 9.939



B1L102S, U23-ch103

calib_packv5_042523_0143.root, FC#11, port A2

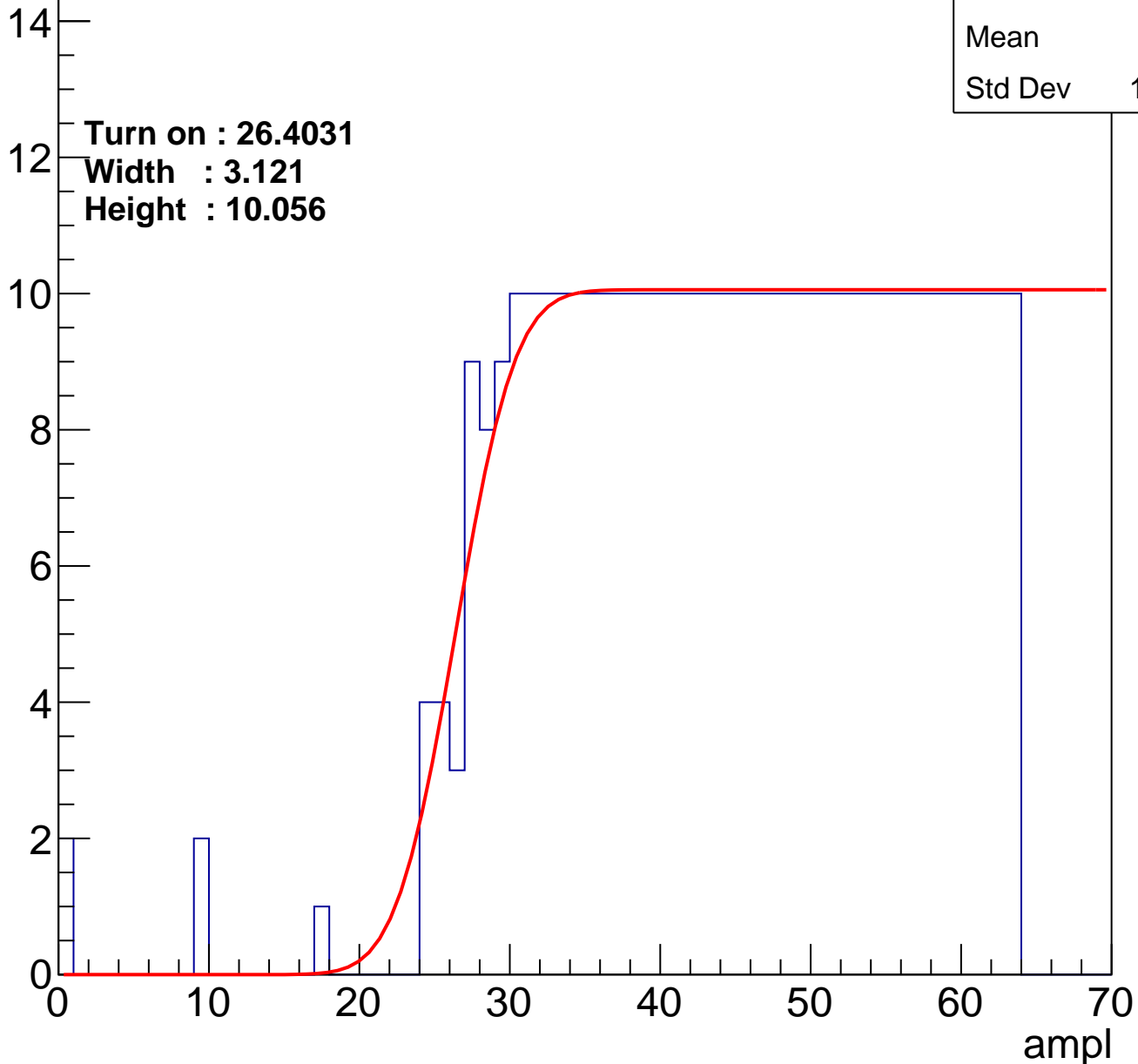
Entries	382
Mean	44.1
Std Dev	11.73

Turn on : 26.4031

Width : 3.121

Height : 10.056

Entry



B1L102S, U23-ch104

calib_packv5_042523_0143.root, FC#11, port A2

Entries	380
Mean	43.97
Std Dev	12.26

Turn on : 26.9863

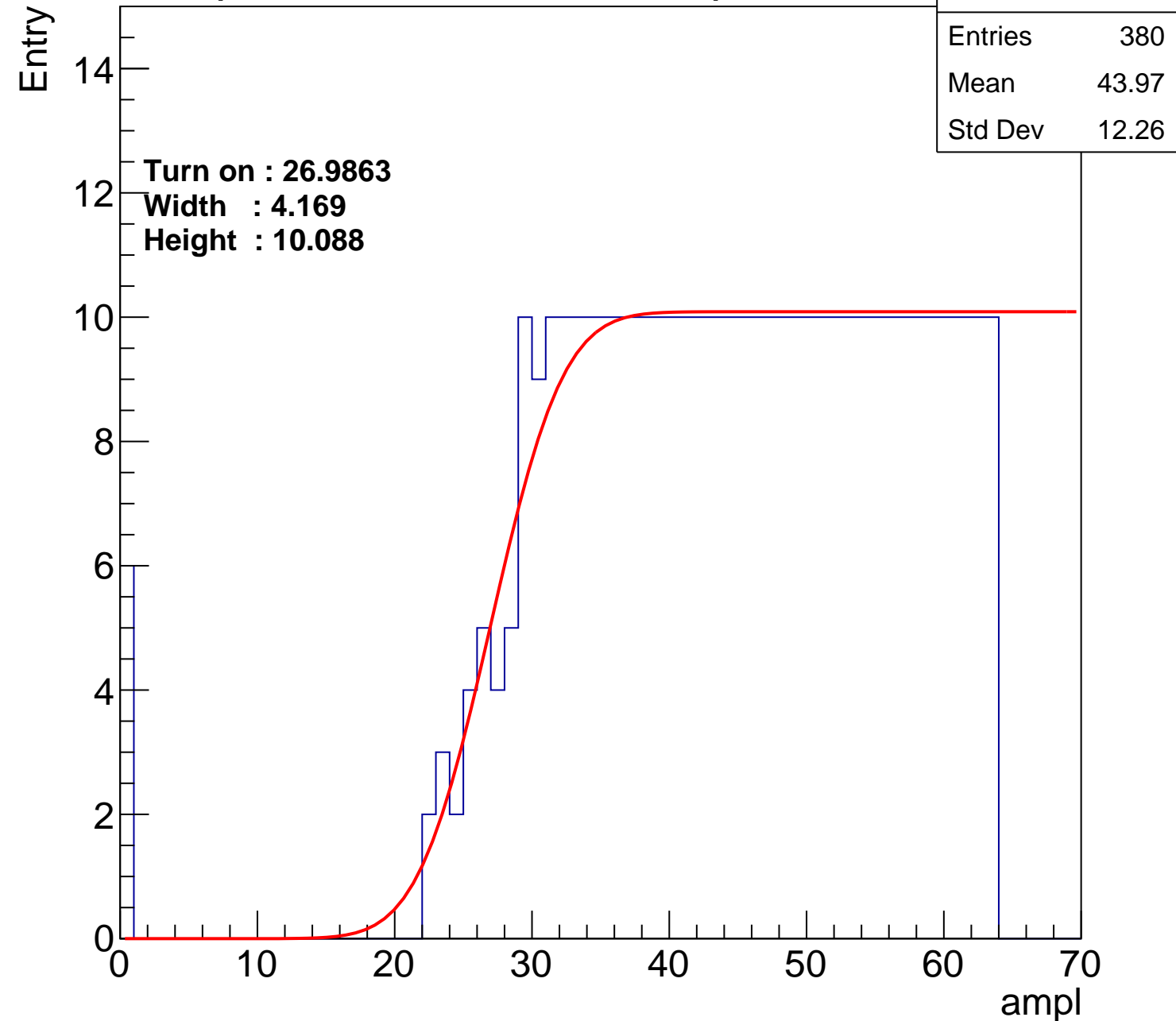
Width : 4.169

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch105

calib_packv5_042523_0143.root, FC#11, port A2

Entries	376
Mean	44.28
Std Dev	11.84

Turn on : 26.5899

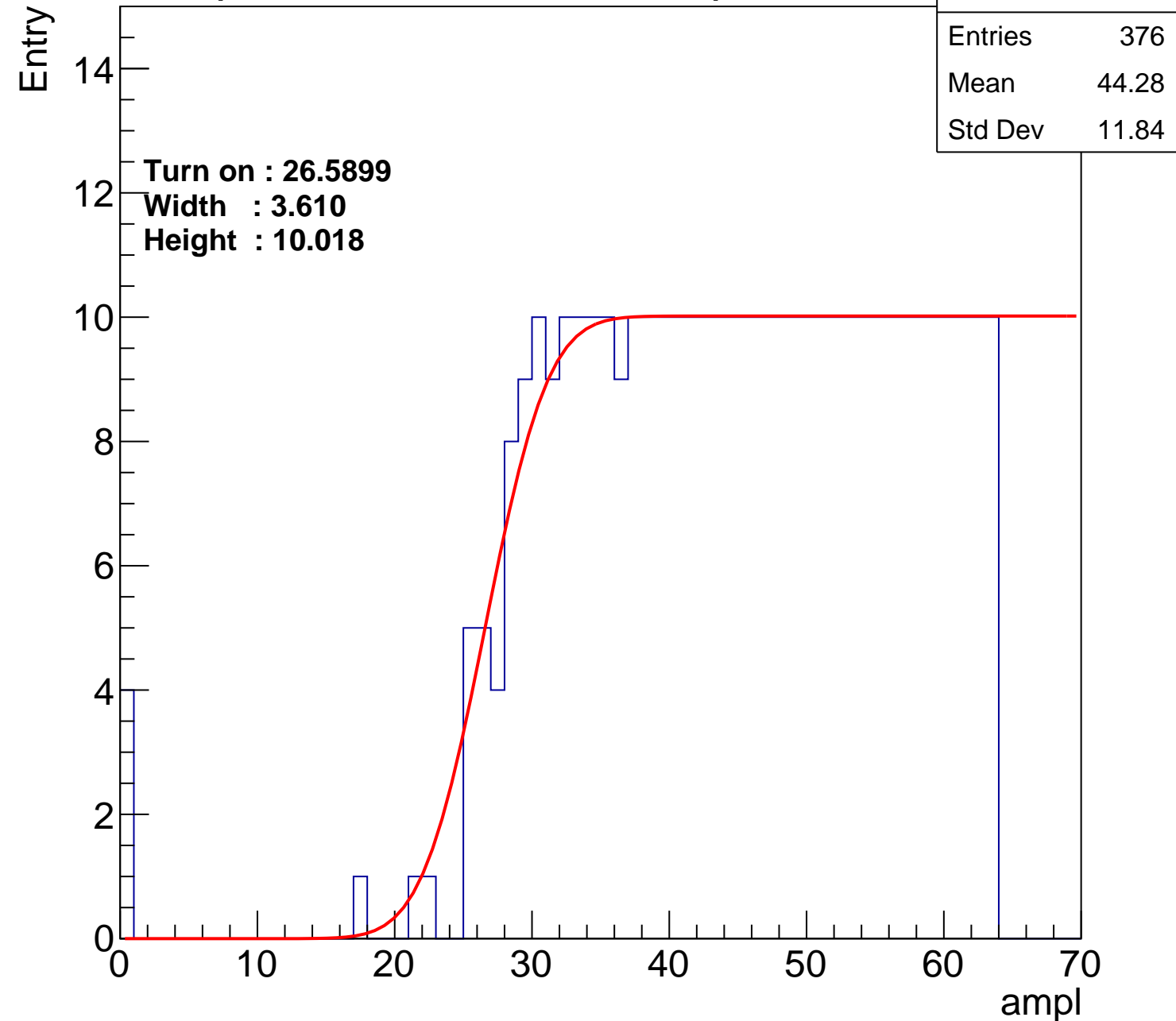
Width : 3.610

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch106

calib_packv5_042523_0143.root, FC#11, port A2

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.9919

Width : 3.877

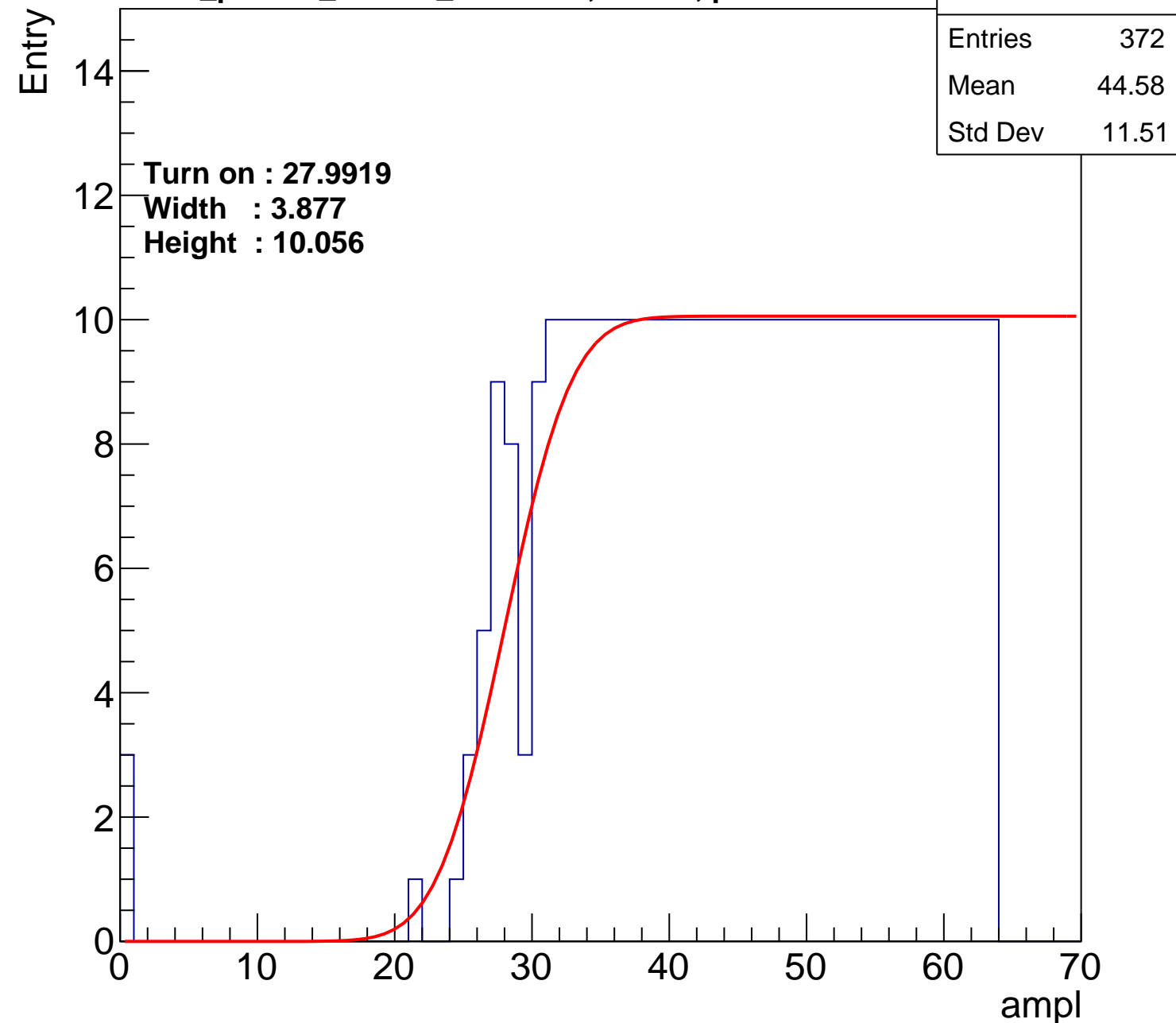
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U23-ch107

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 25.9190

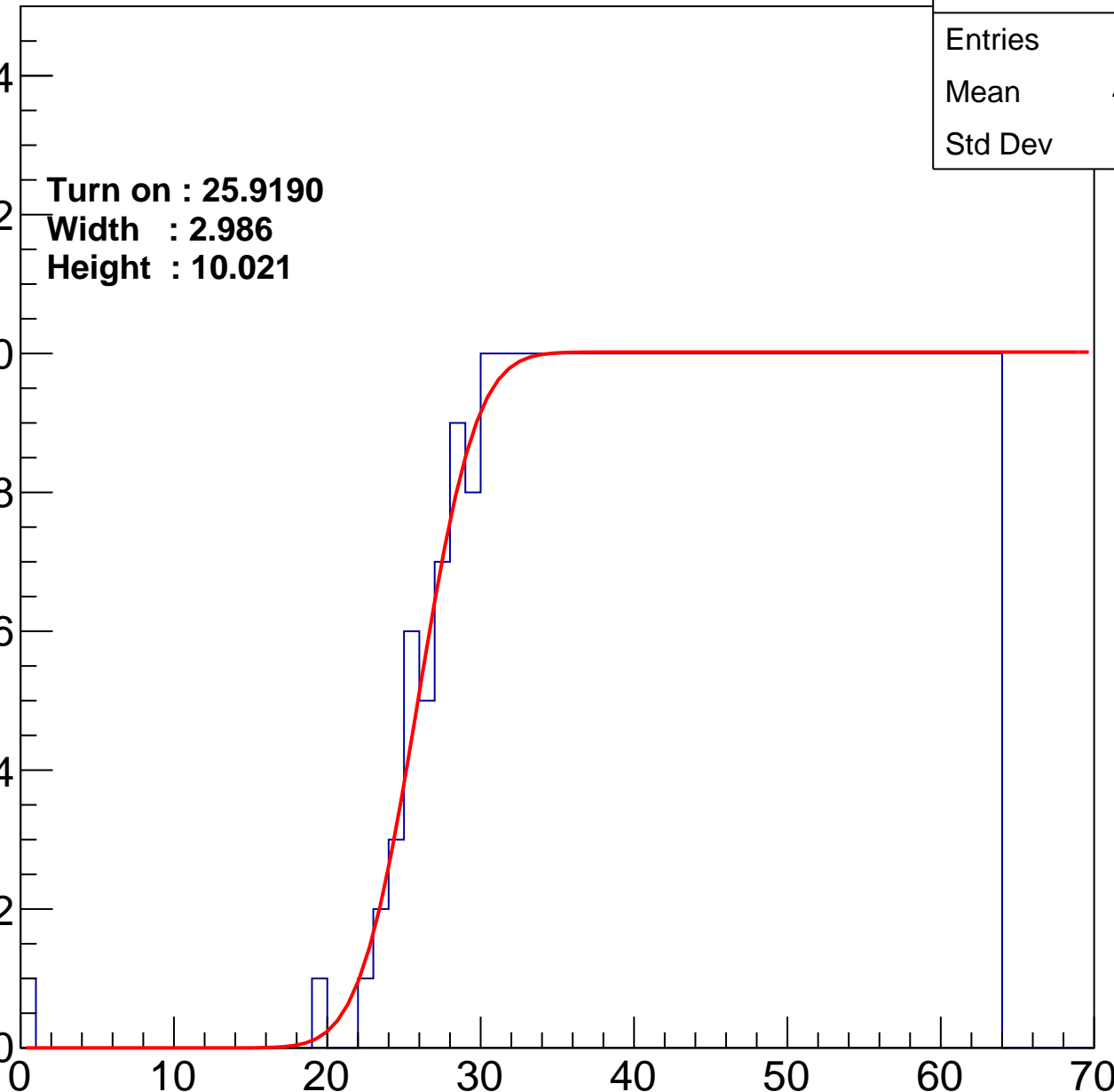
Width : 2.986

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch108

calib_packv5_042523_0143.root, FC#11, port A2

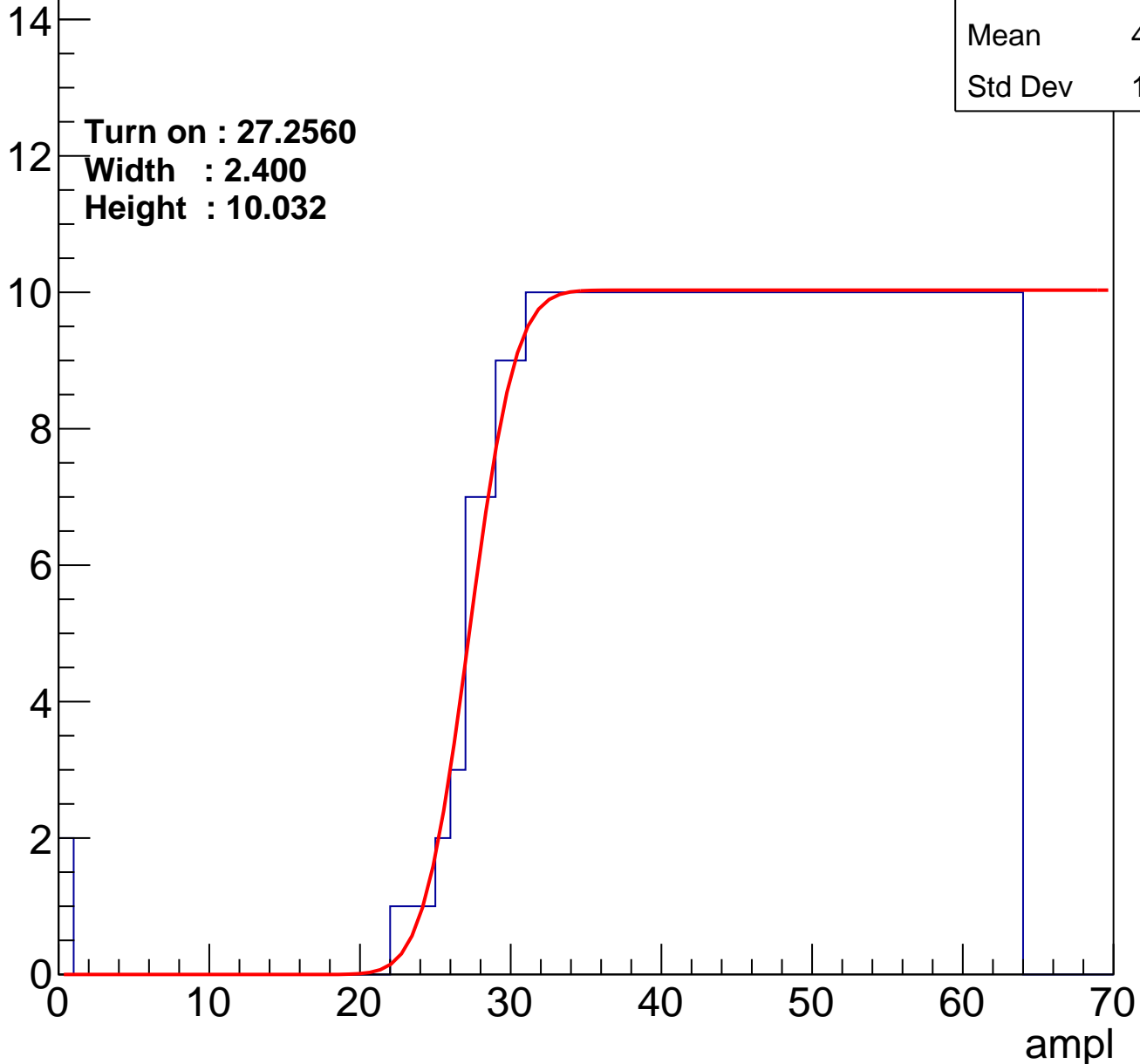
Entries	372
Mean	44.69
Std Dev	11.27

Turn on : 27.2560

Width : 2.400

Height : 10.032

Entry



B1L102S, U23-ch109

calib_packv5_042523_0143.root, FC#11, port A2

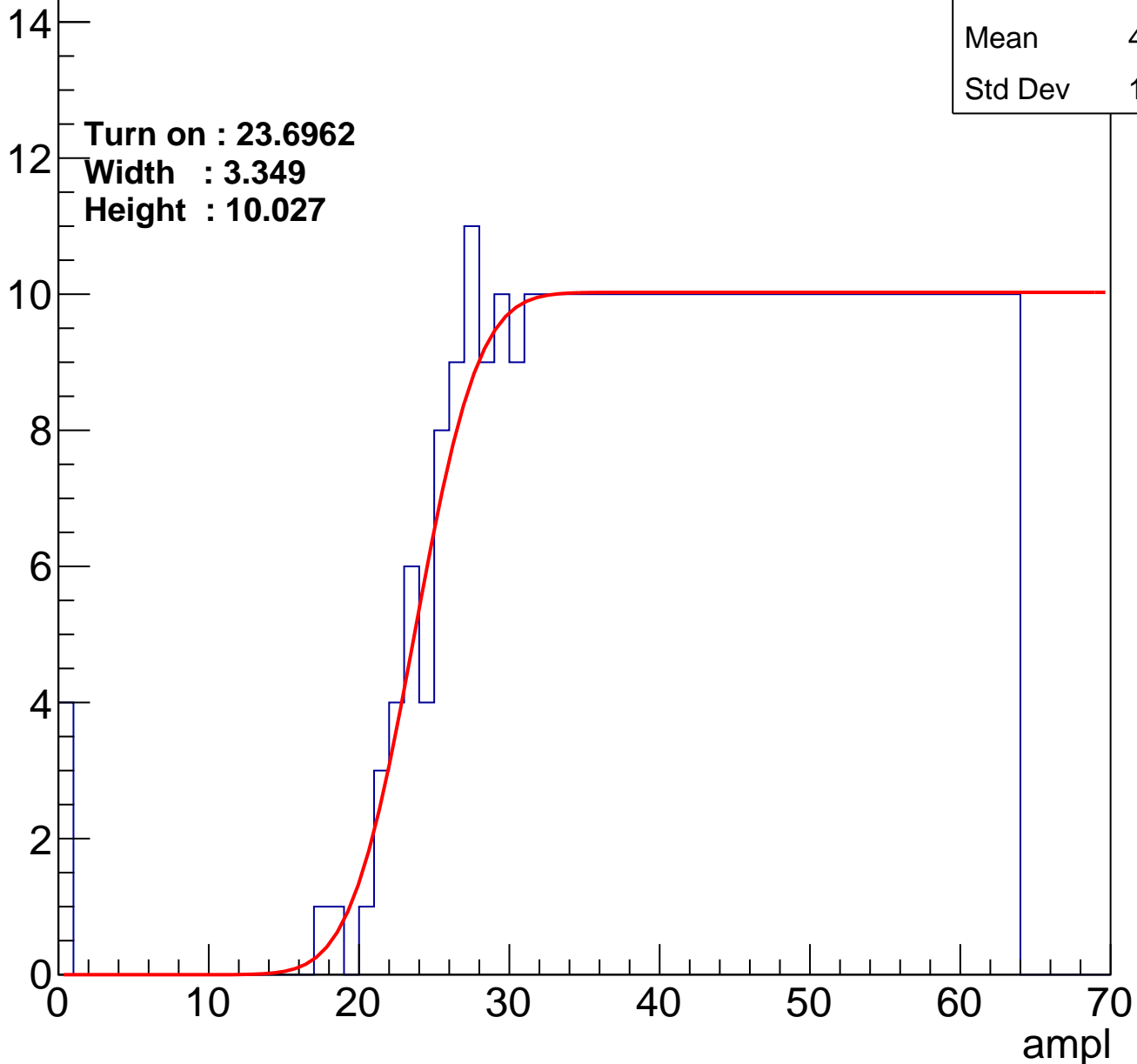
Entries	410
Mean	42.67
Std Dev	12.59

Turn on : 23.6962

Width : 3.349

Height : 10.027

Entry



B1L102S, U23-ch110

calib_packv5_042523_0143.root, FC#11, port A2

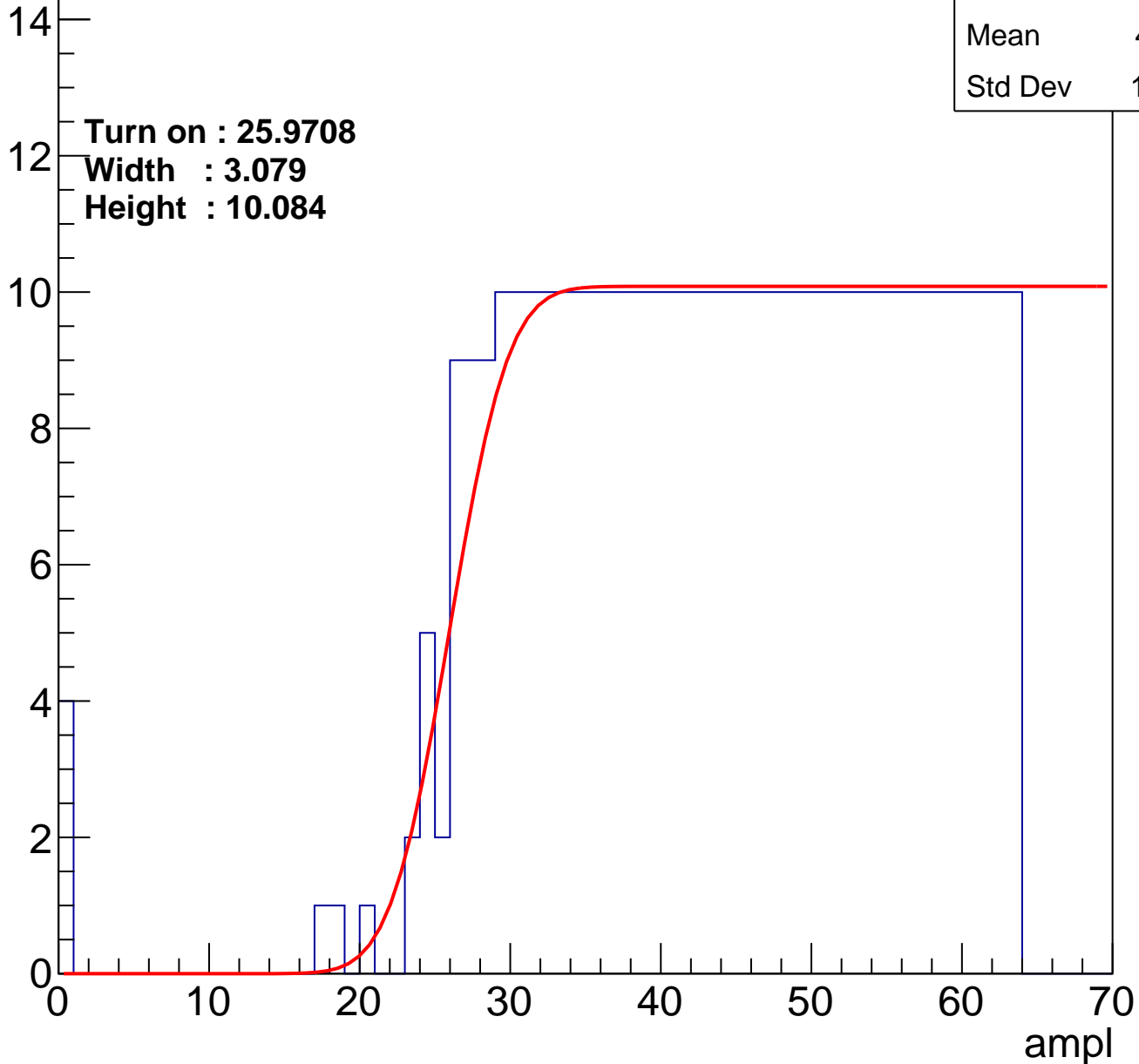
Entries	393
Mean	43.51
Std Dev	12.16

Turn on : 25.9708

Width : 3.079

Height : 10.084

Entry



B1L102S, U23-ch111

calib_packv5_042523_0143.root, FC#11, port A2

Entries	390
Mean	43.7
Std Dev	11.95

Turn on : 25.7077

Width : 2.844

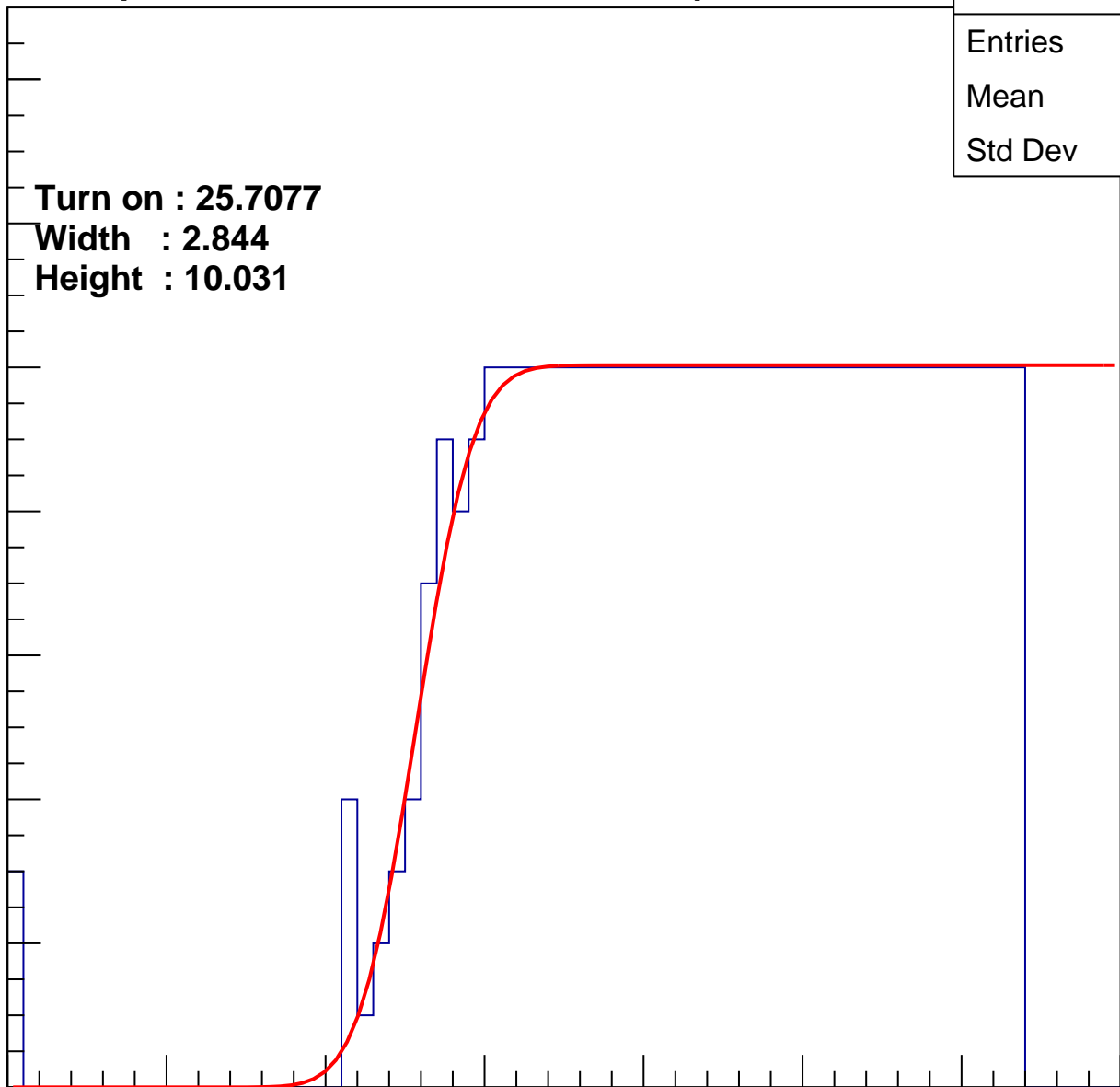
Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U23-ch112

calib_packv5_042523_0143.root, FC#11, port A2

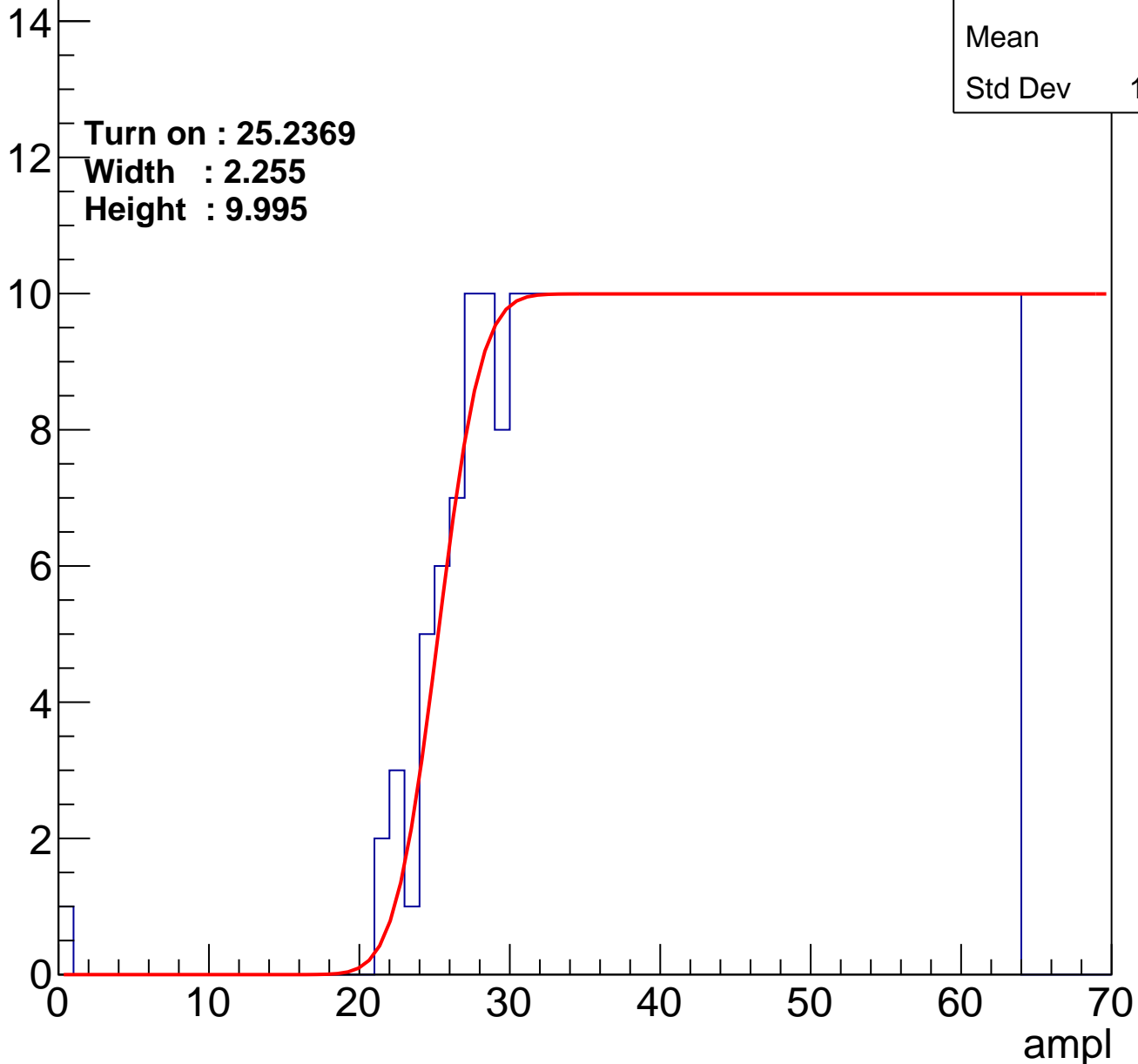
Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 25.2369

Width : 2.255

Height : 9.995

Entry



B1L102S, U23-ch113

calib_packv5_042523_0143.root, FC#11, port A2

Entries	385
Mean	43.96
Std Dev	11.8

Turn on : 25.9942

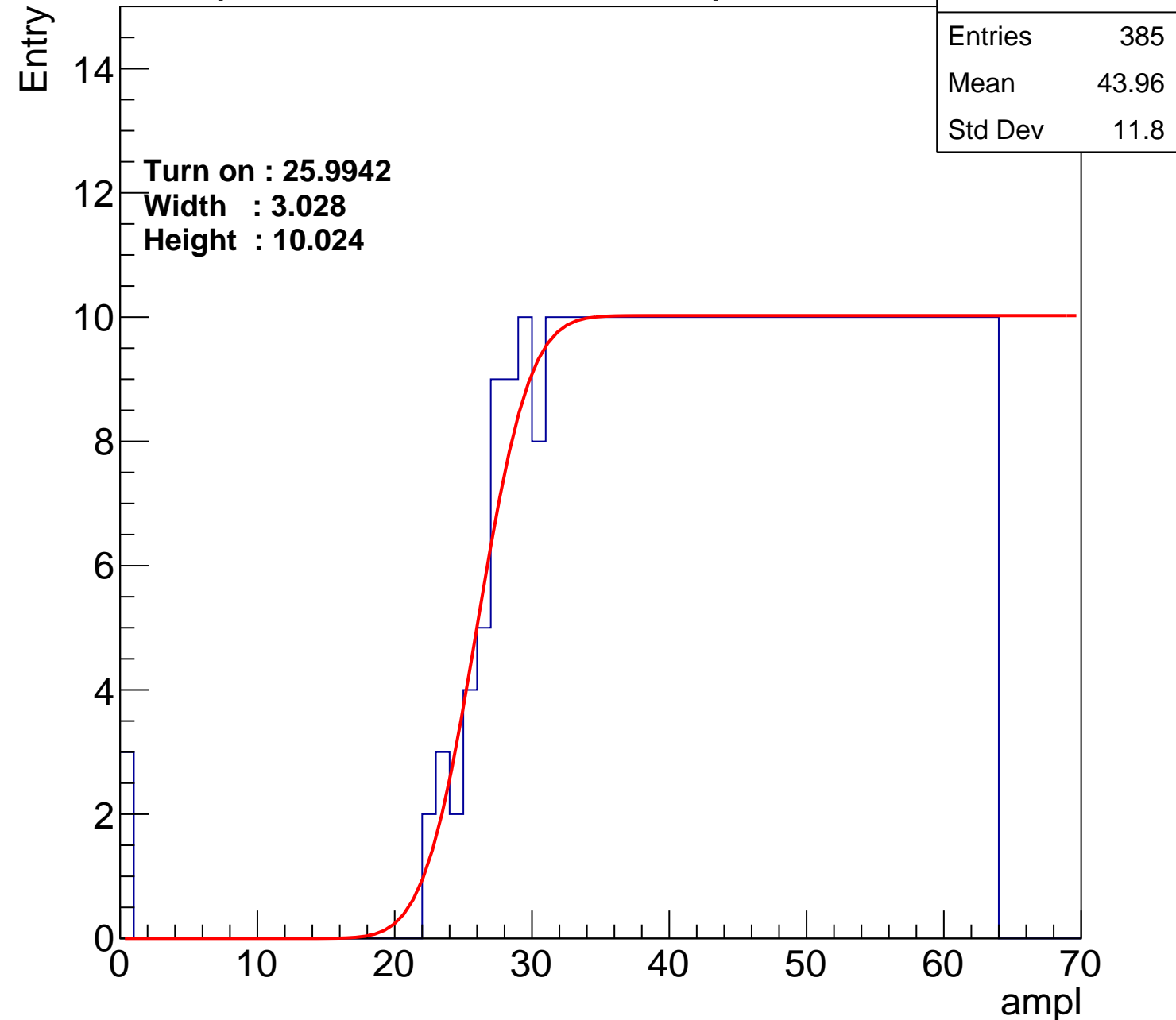
Width : 3.028

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch114

calib_packv5_042523_0143.root, FC#11, port A2

Entries	396
Mean	43.43
Std Dev	12

Turn on : 24.9255

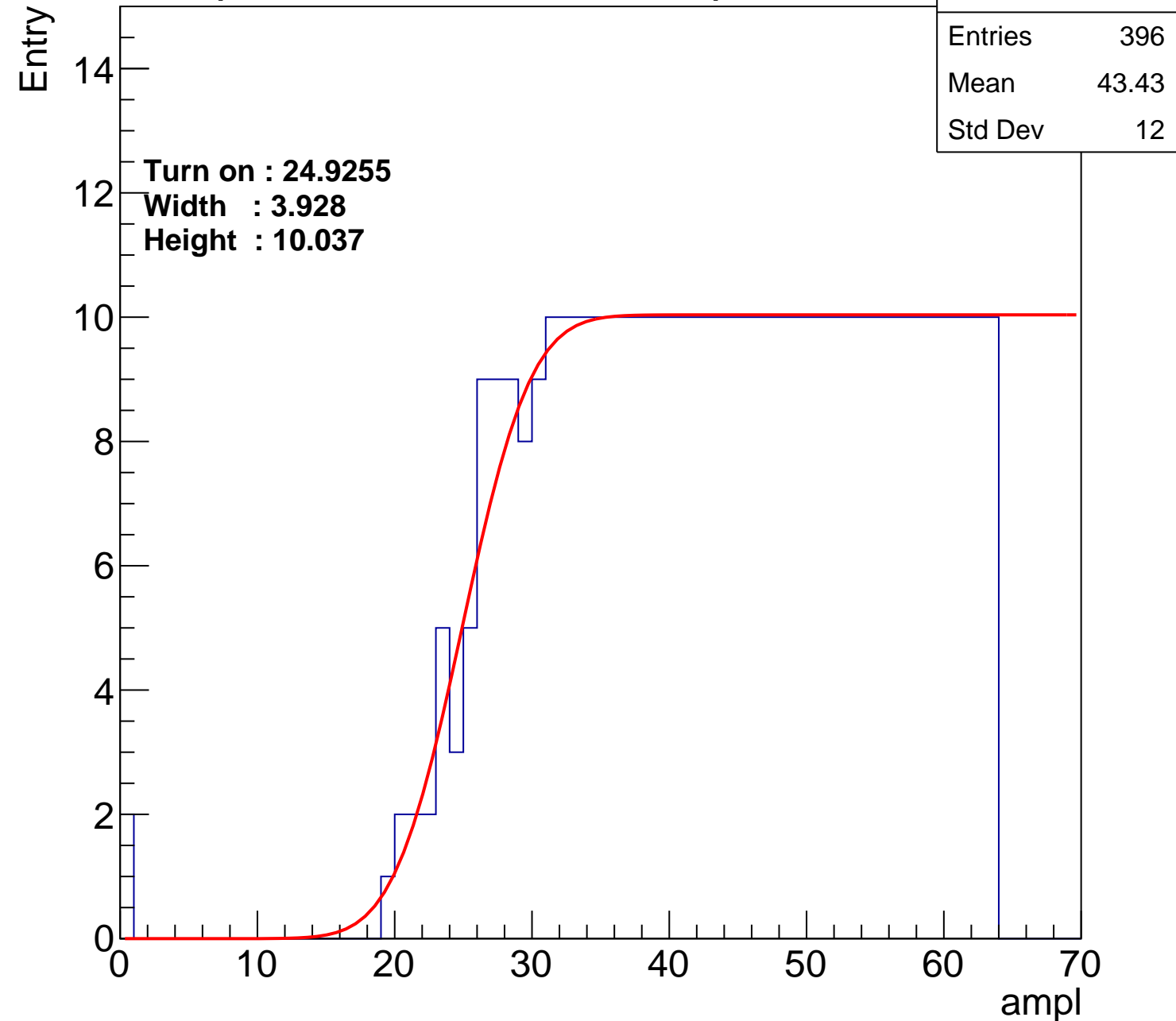
Width : 3.928

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch115

calib_packv5_042523_0143.root, FC#11, port A2

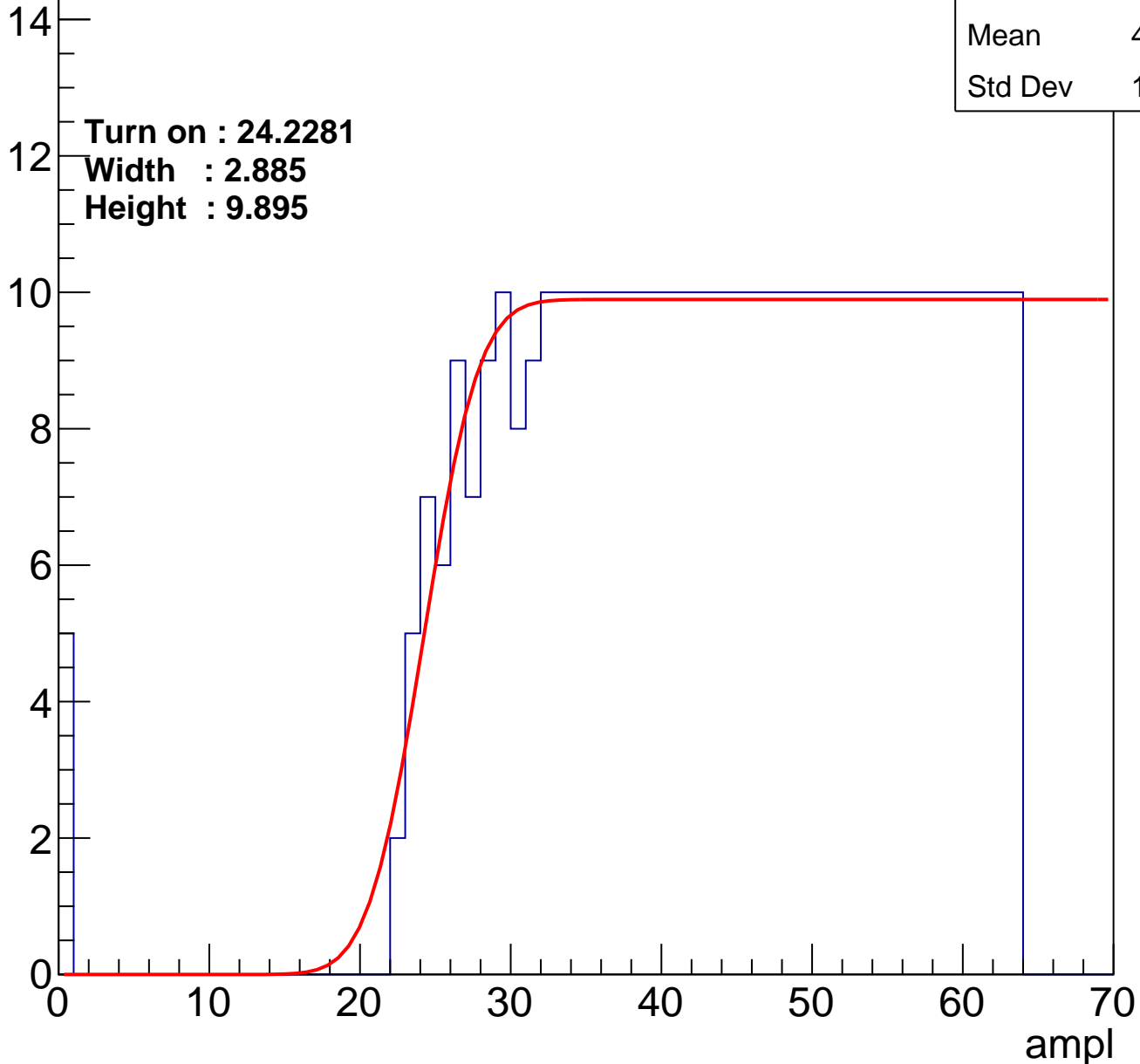
Entries	397
Mean	43.23
Std Dev	12.43

Turn on : 24.2281

Width : 2.885

Height : 9.895

Entry



B1L102S, U23-ch116

calib_packv5_042523_0143.root, FC#11, port A2

Entries	383
Mean	43.96
Std Dev	11.98

Turn on : 26.6158

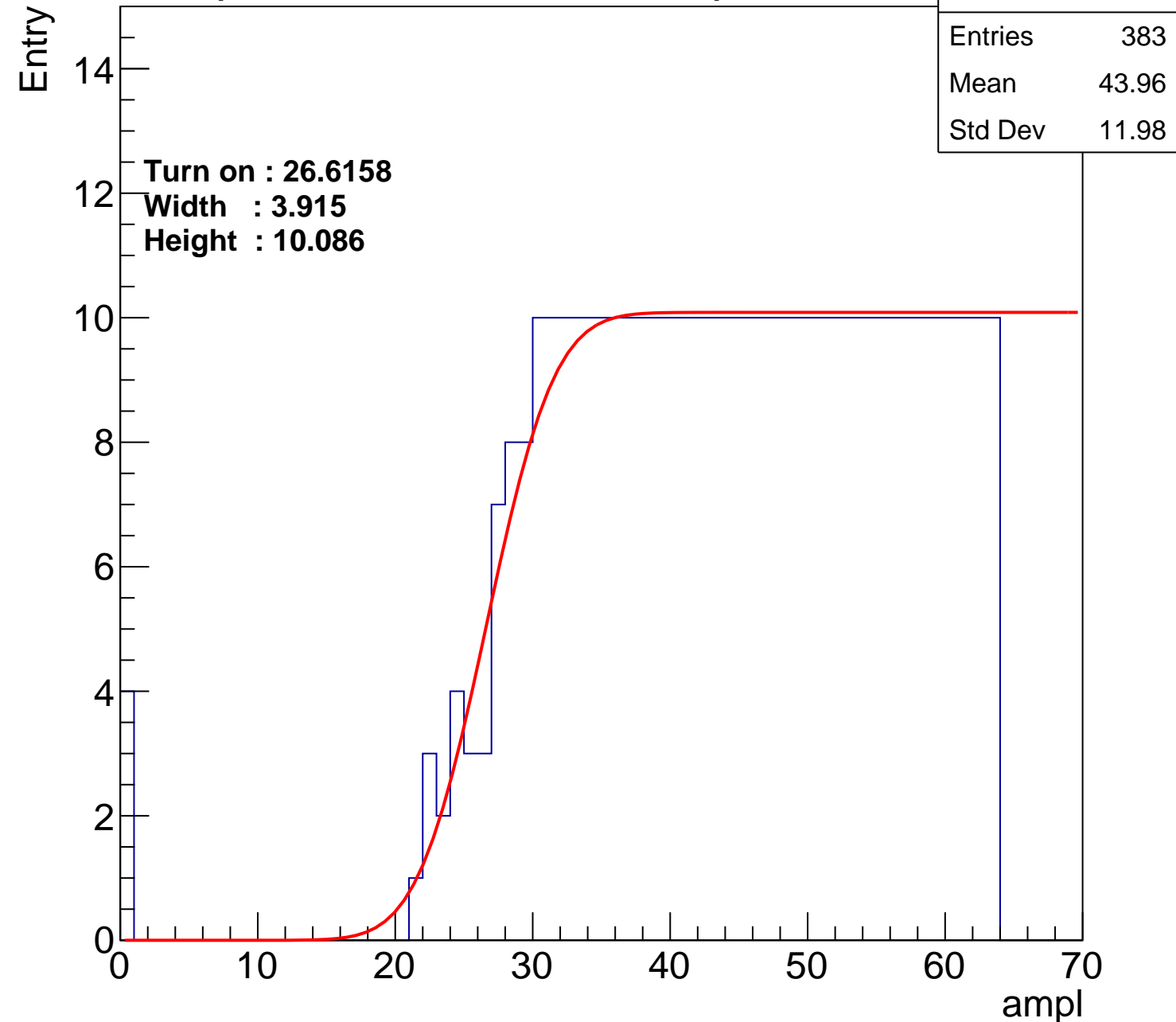
Width : 3.915

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch117

calib_packv5_042523_0143.root, FC#11, port A2

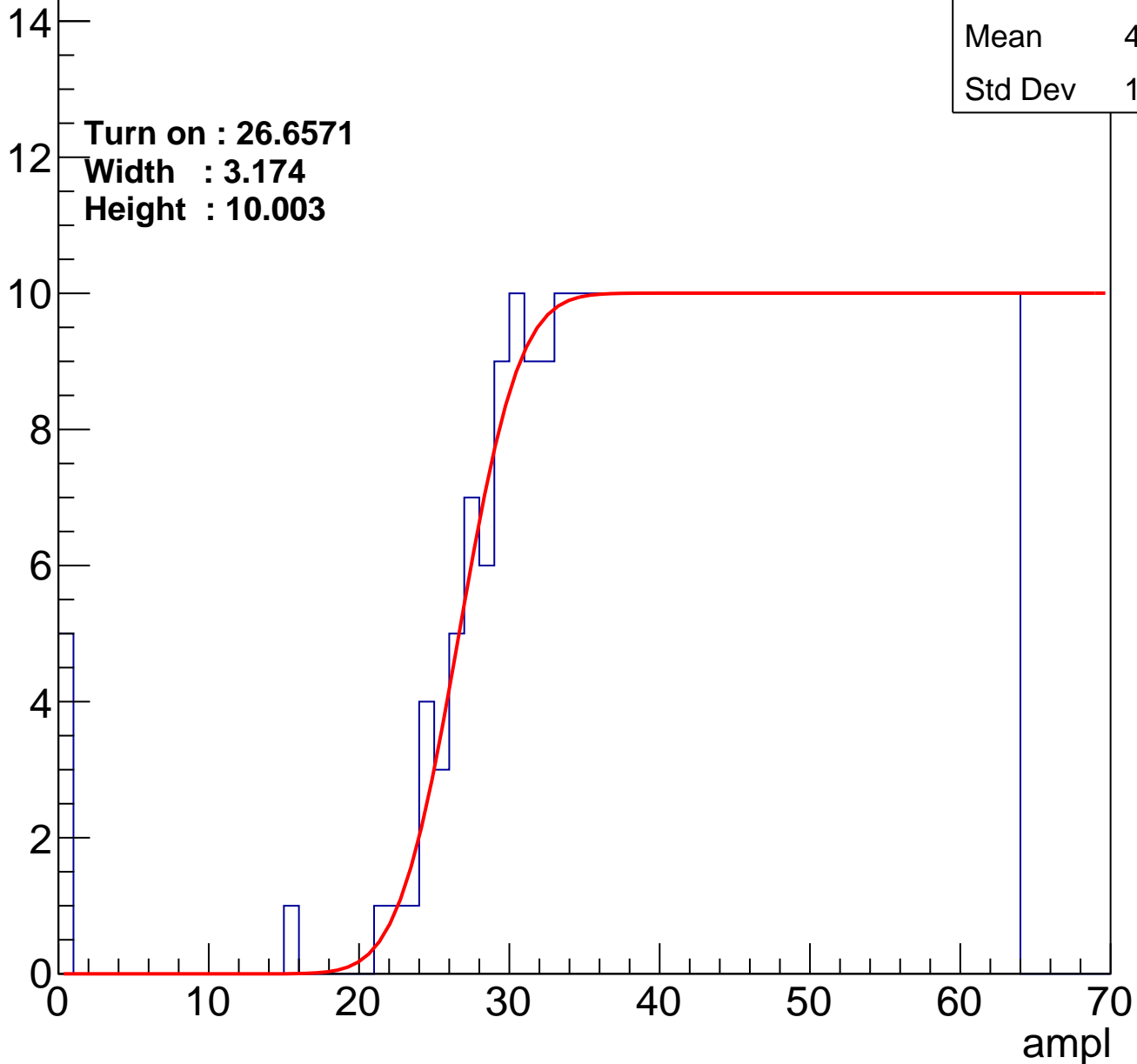
Entries	381
Mean	43.96
Std Dev	12.17

Turn on : 26.6571

Width : 3.174

Height : 10.003

Entry



B1L102S, U23-ch118

calib_packv5_042523_0143.root, FC#11, port A2

Entries	373
Mean	44.49
Std Dev	11.59

Turn on : 27.3146

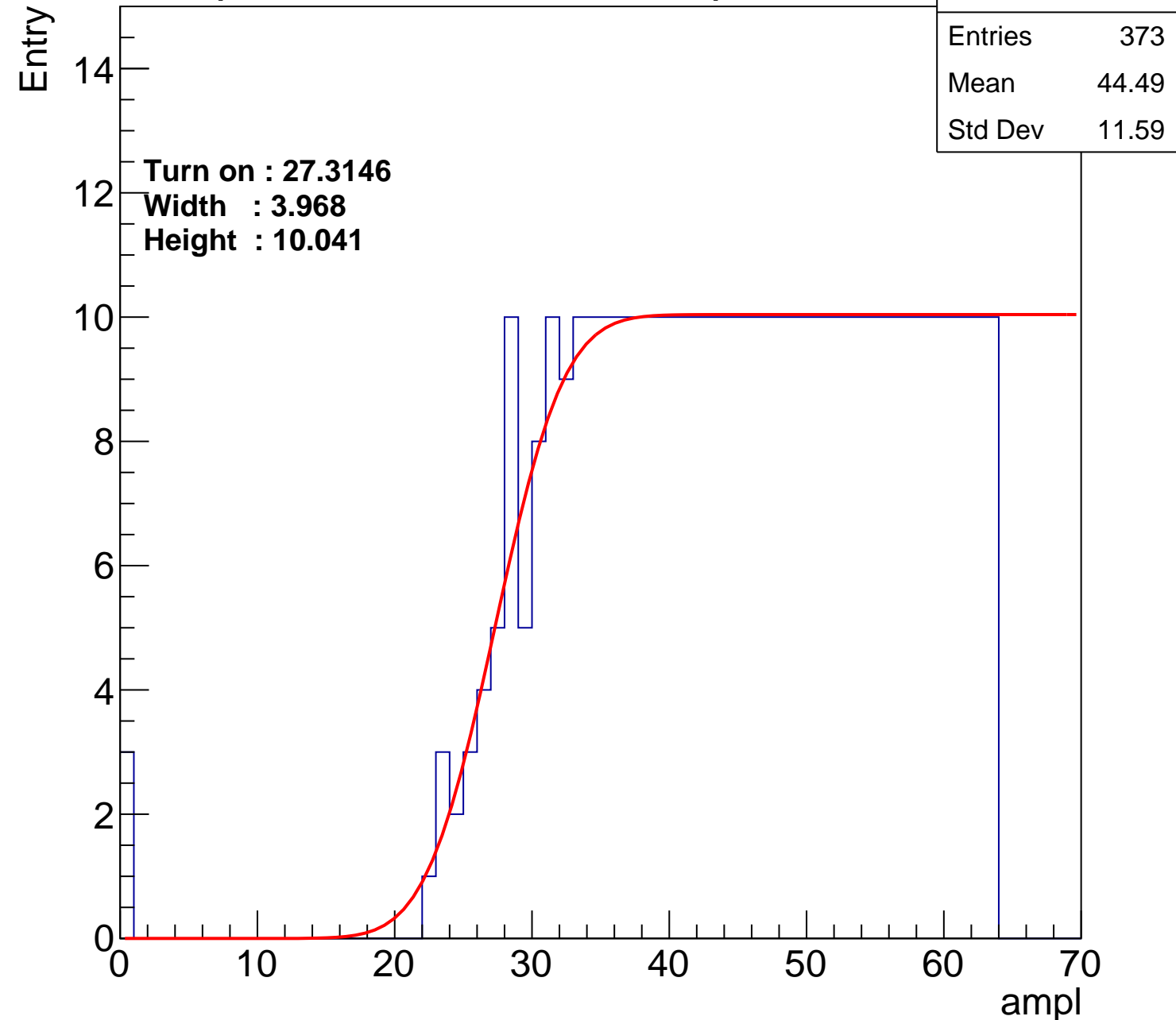
Width : 3.968

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch119

calib_packv5_042523_0143.root, FC#11, port A2

Entries	389
Mean	43.81
Std Dev	11.77

Turn on : 25.4695

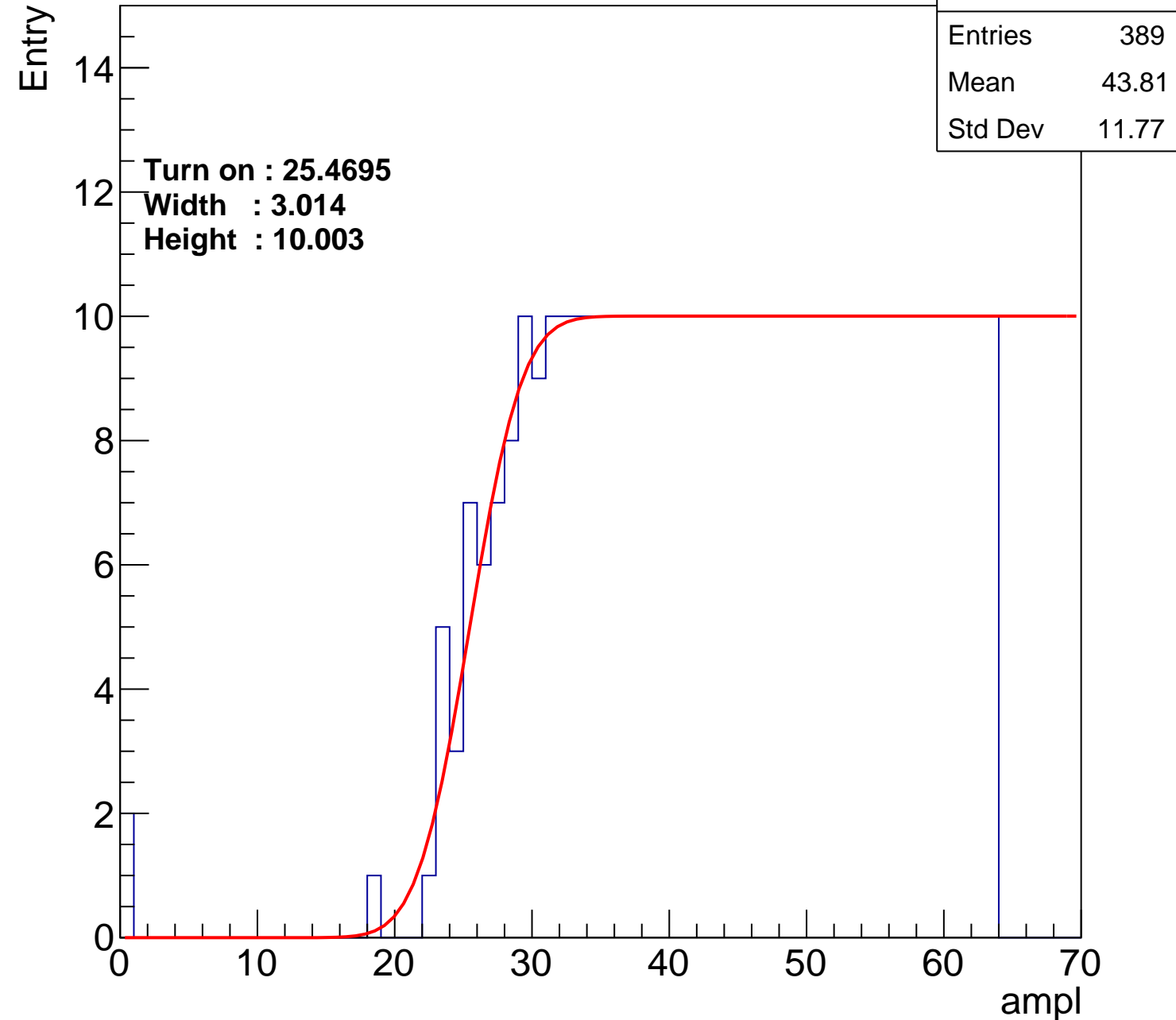
Width : 3.014

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch120

calib_packv5_042523_0143.root, FC#11, port A2

Entries	404
Mean	42.98
Std Dev	12.35

Turn on : 24.2756

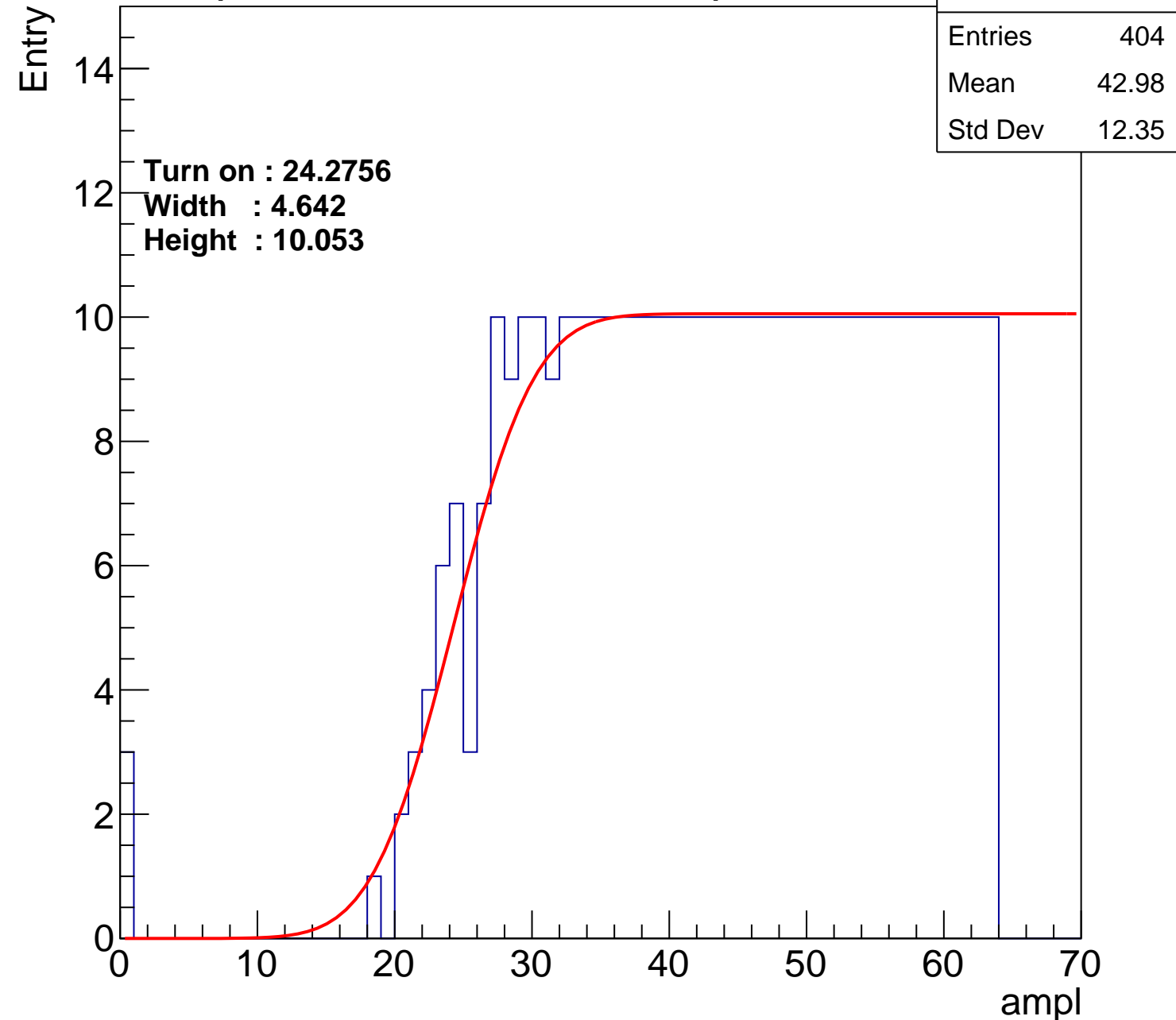
Width : 4.642

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch121

calib_packv5_042523_0143.root, FC#11, port A2

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.0292

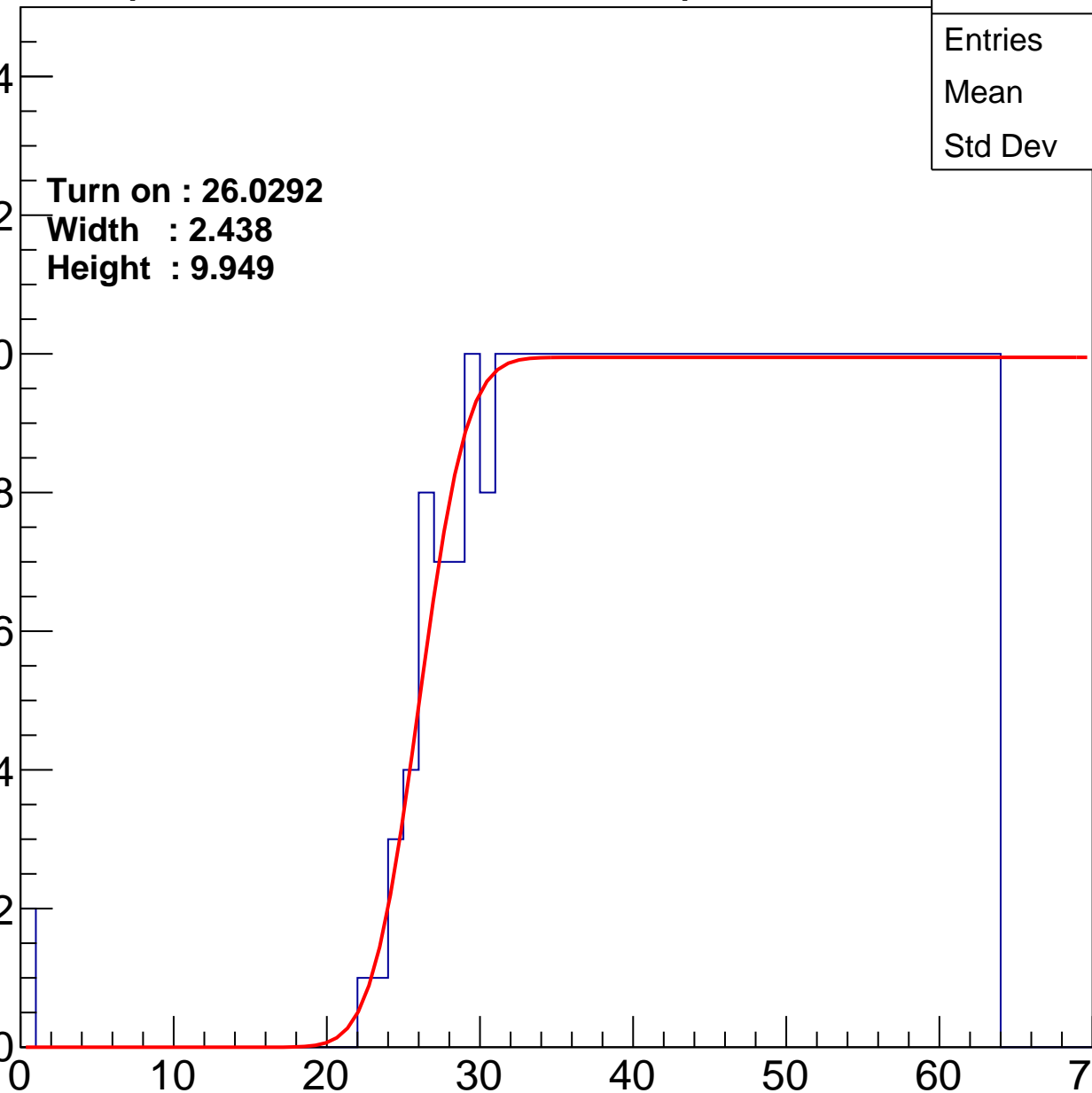
Width : 2.438

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch122

calib_packv5_042523_0143.root, FC#11, port A2

Entries	379
Mean	44.31
Std Dev	11.5

Turn on : 26.5907

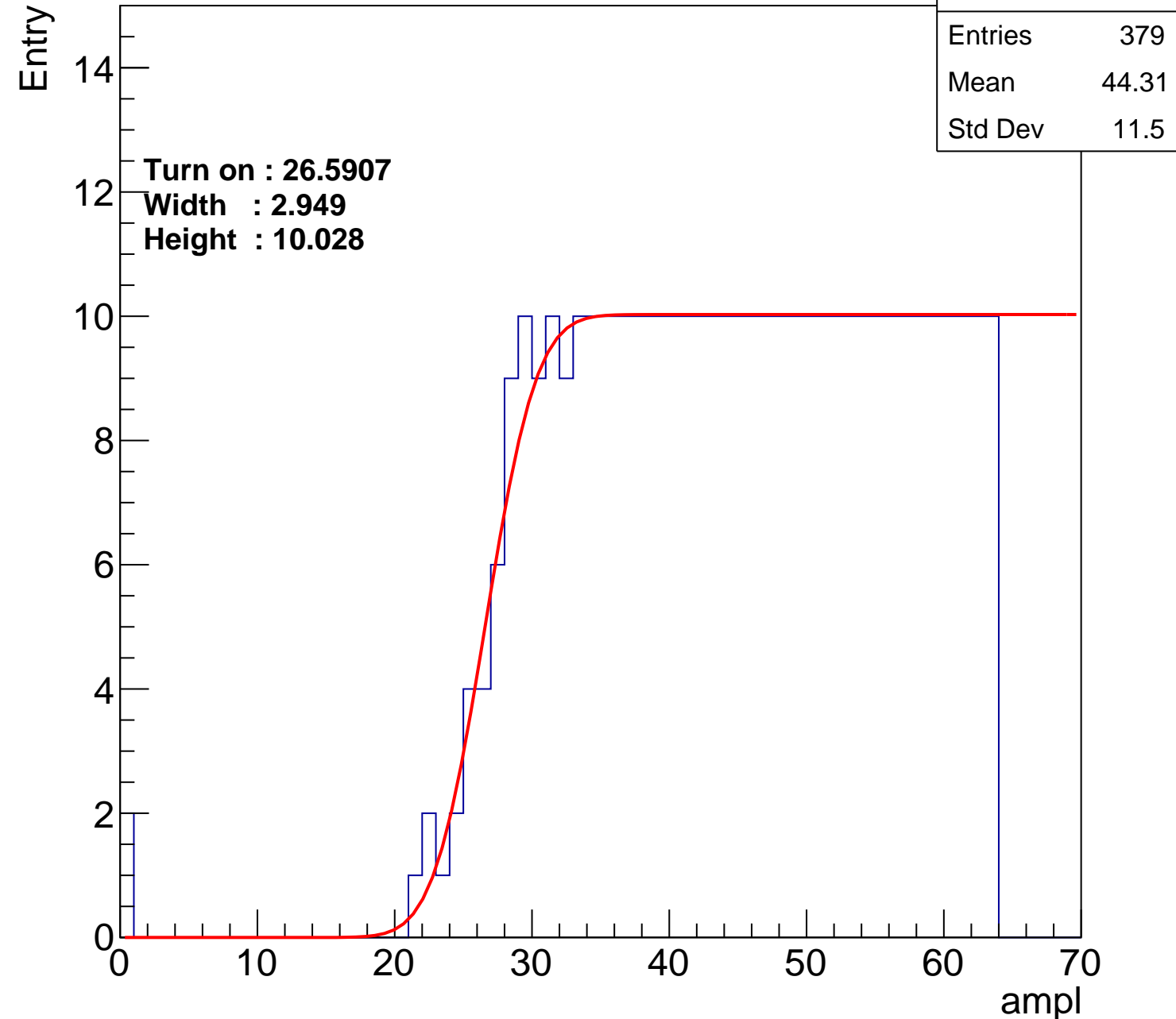
Width : 2.949

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch123

calib_packv5_042523_0143.root, FC#11, port A2

Entries	377
Mean	44.29
Std Dev	11.79

Turn on : 26.8764

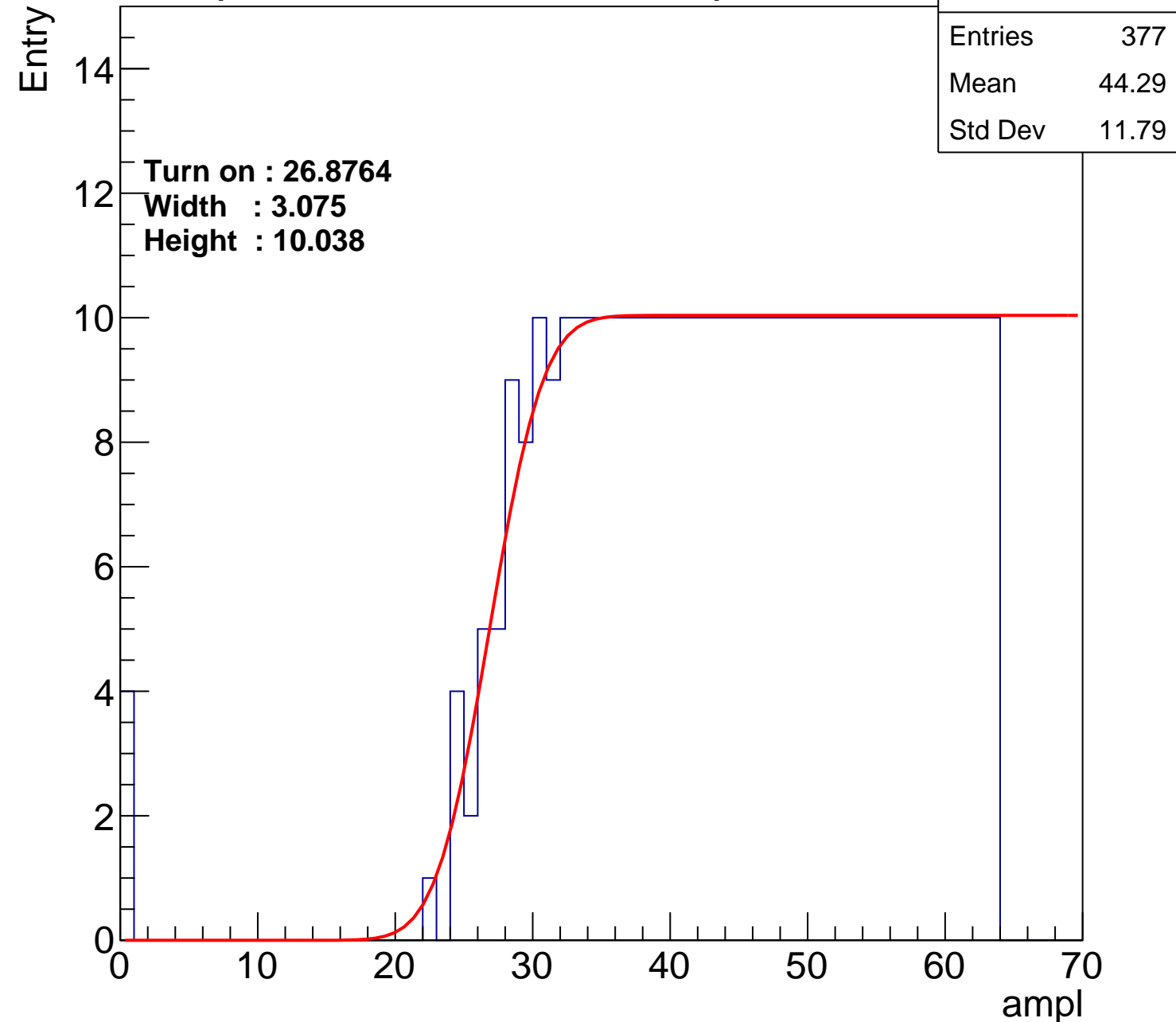
Width : 3.075

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch124

calib_packv5_042523_0143.root, FC#11, port A2

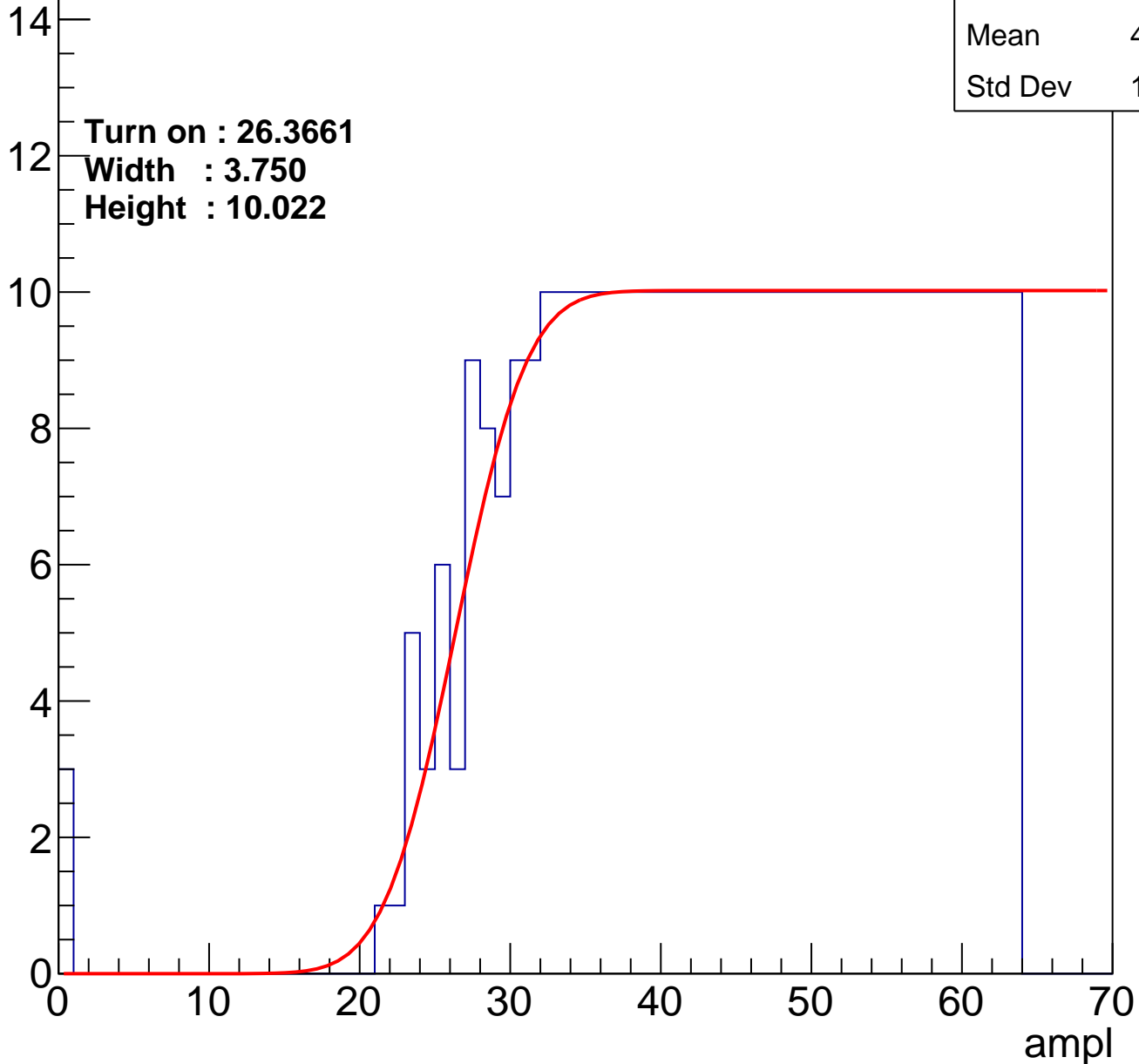
Entries	384
Mean	43.95
Std Dev	11.87

Turn on : 26.3661

Width : 3.750

Height : 10.022

Entry



B1L102S, U23-ch125

calib_packv5_042523_0143.root, FC#11, port A2

Entries	349
Mean	45.57
Std Dev	11.28

Turn on : 30.1211

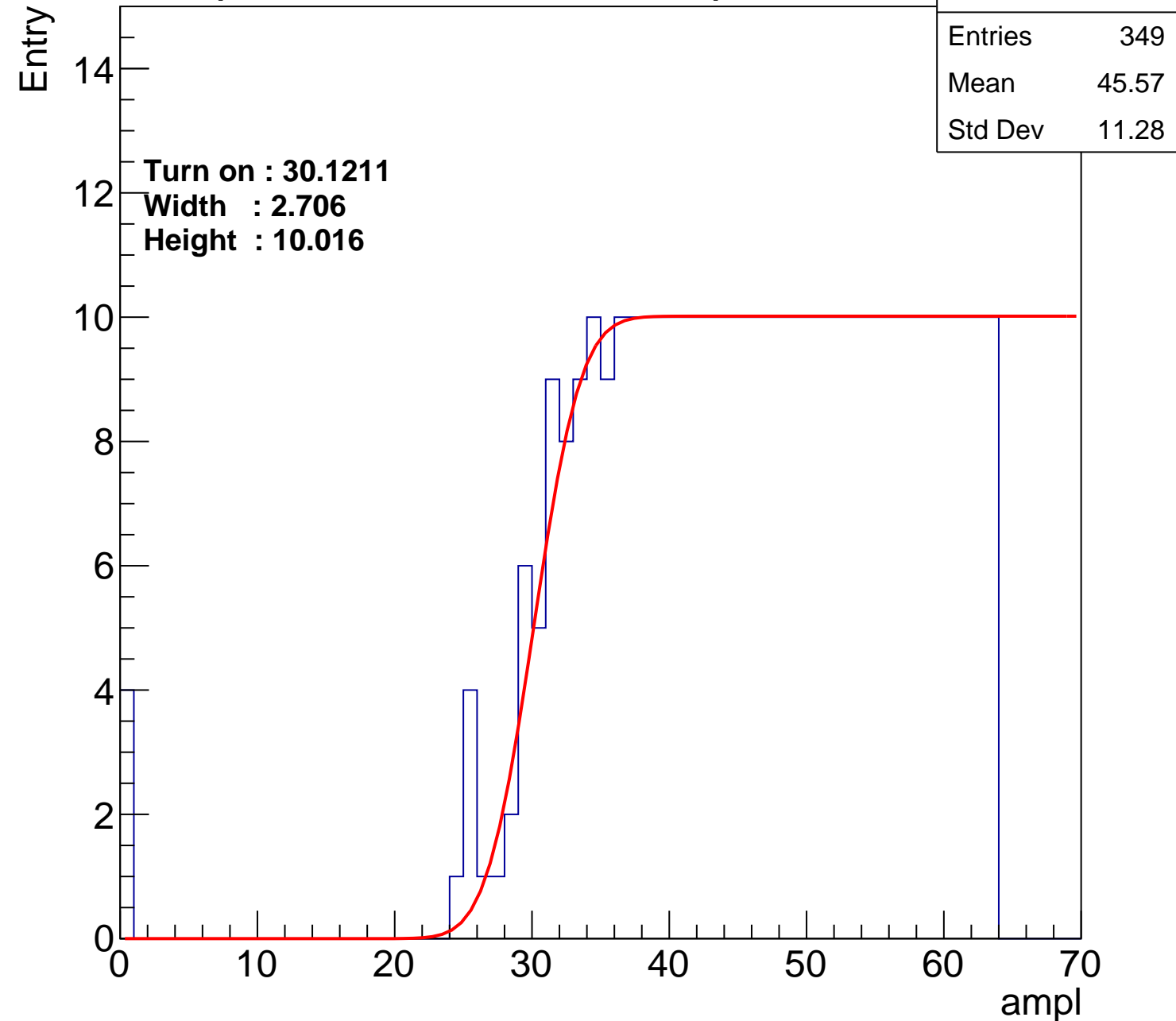
Width : 2.706

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch126

calib_packv5_042523_0143.root, FC#11, port A2

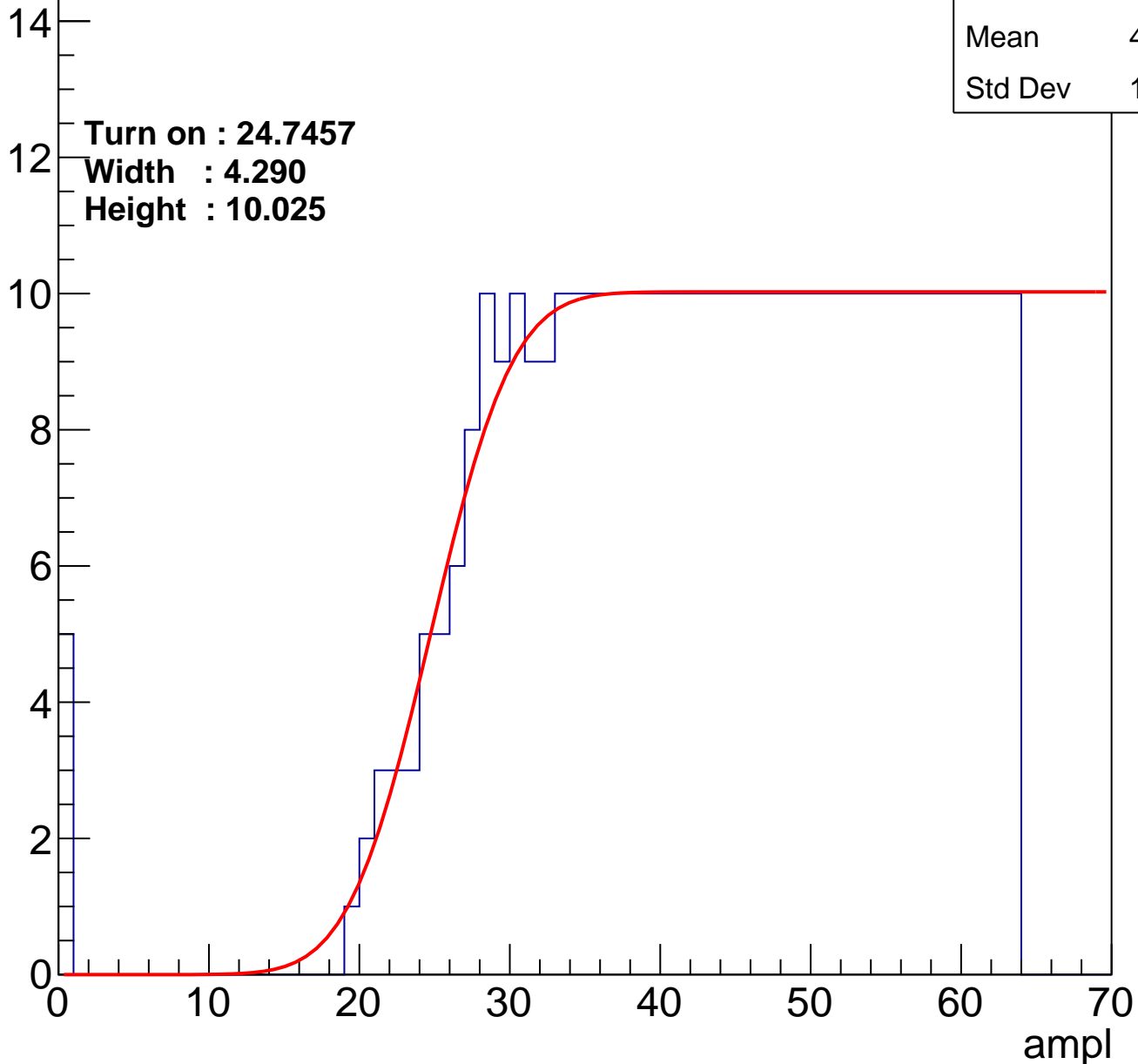
Entries	398
Mean	43.12
Std Dev	12.55

Turn on : 24.7457

Width : 4.290

Height : 10.025

Entry



B1L102S, U23-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.8
Std Dev	11.44

Turn on : 28.6461

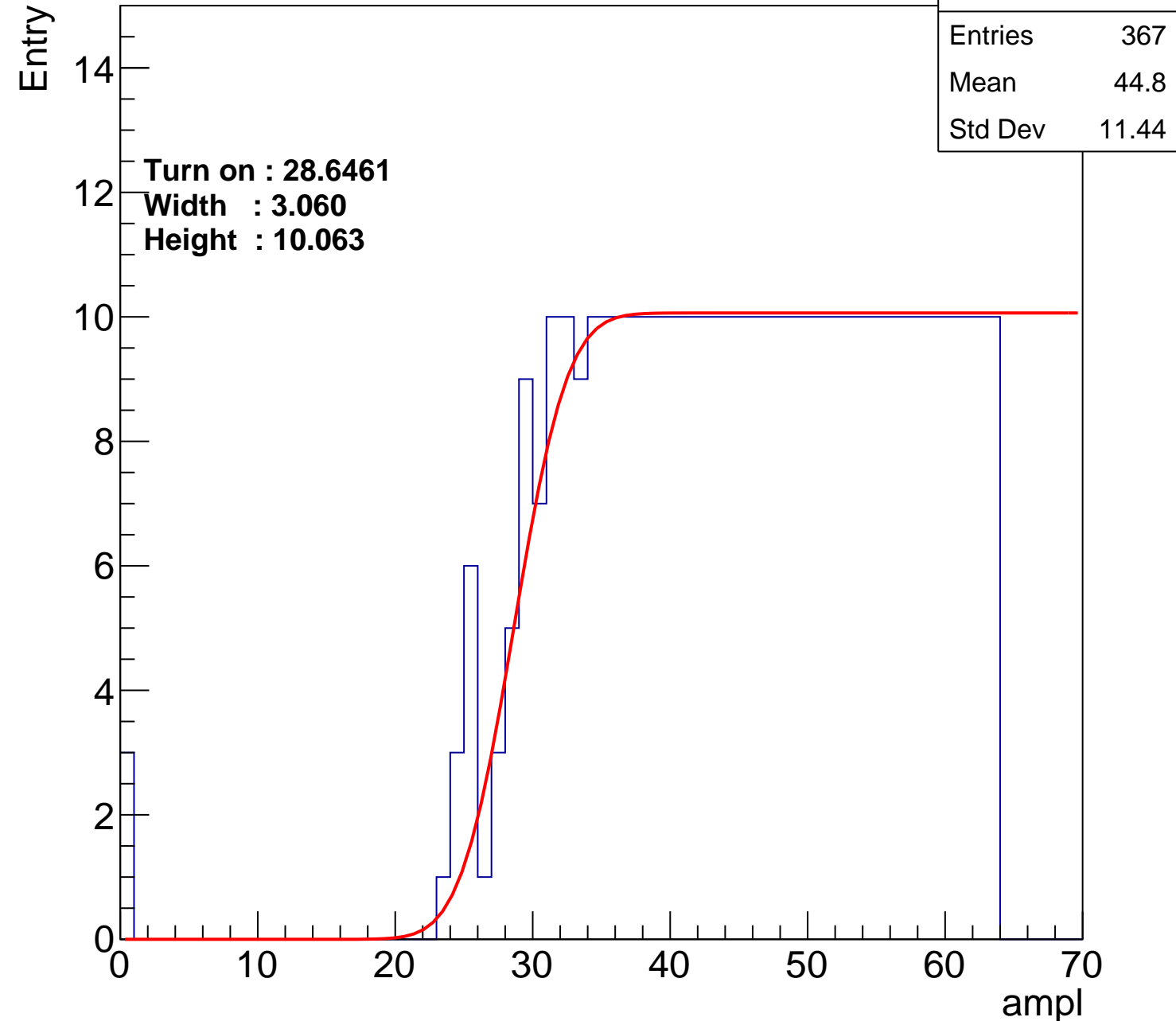
Width : 3.060

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L102S, U23-ch127

calib_packv5_042523_0143.root, FC#11, port A2

Entries	367
Mean	44.8
Std Dev	11.44

Turn on : 28.6461

Width : 3.060

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl

