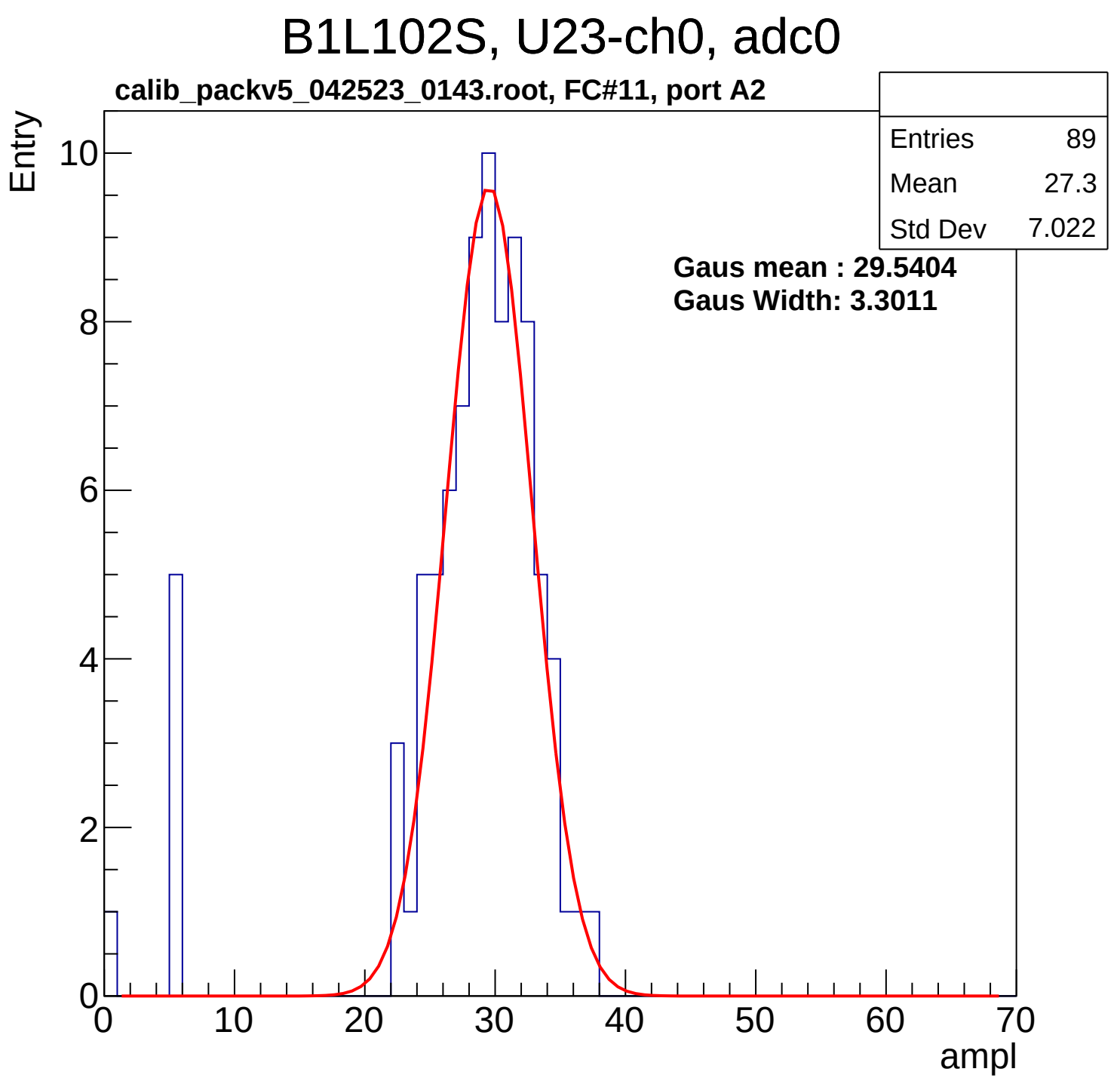
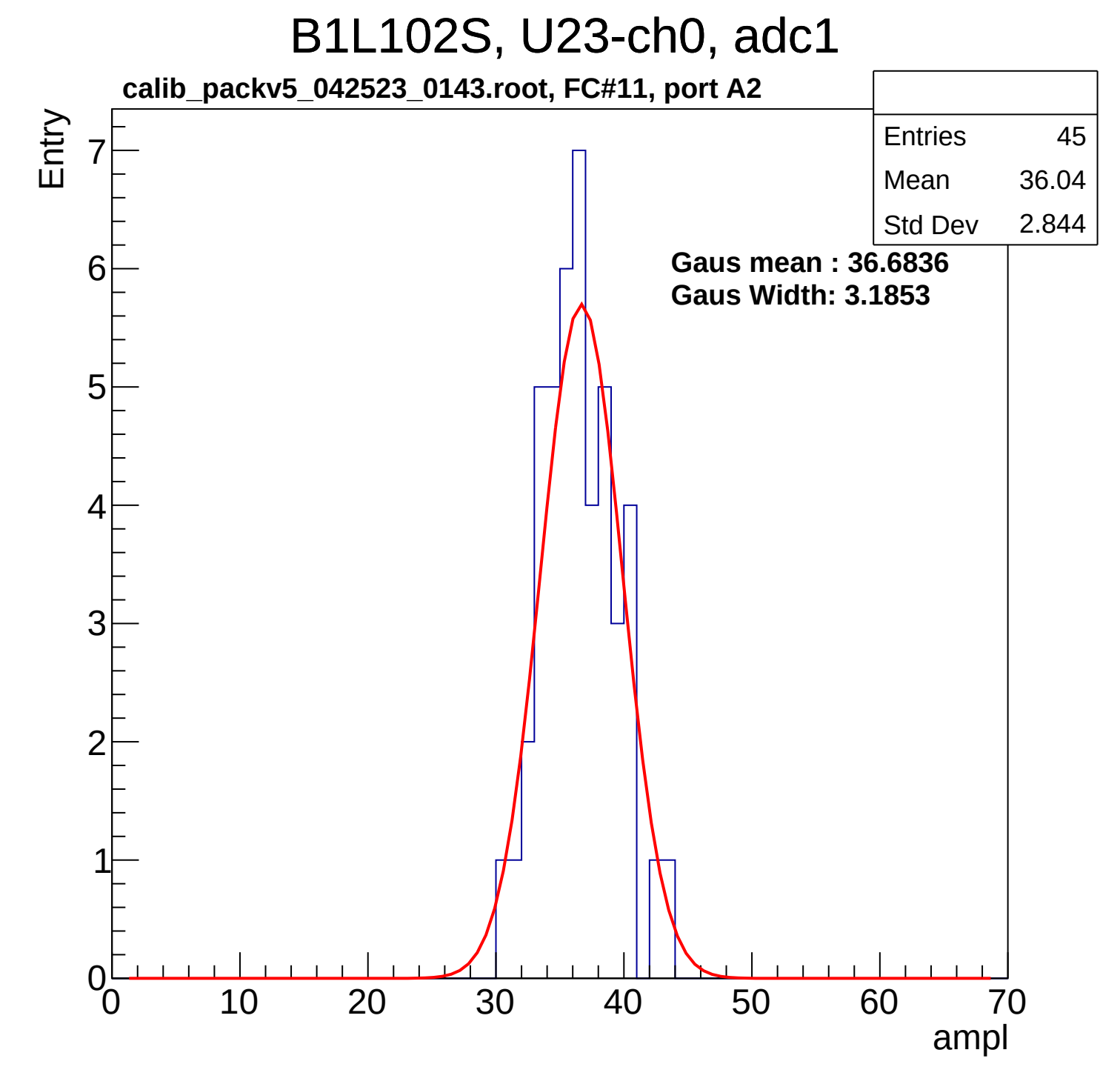
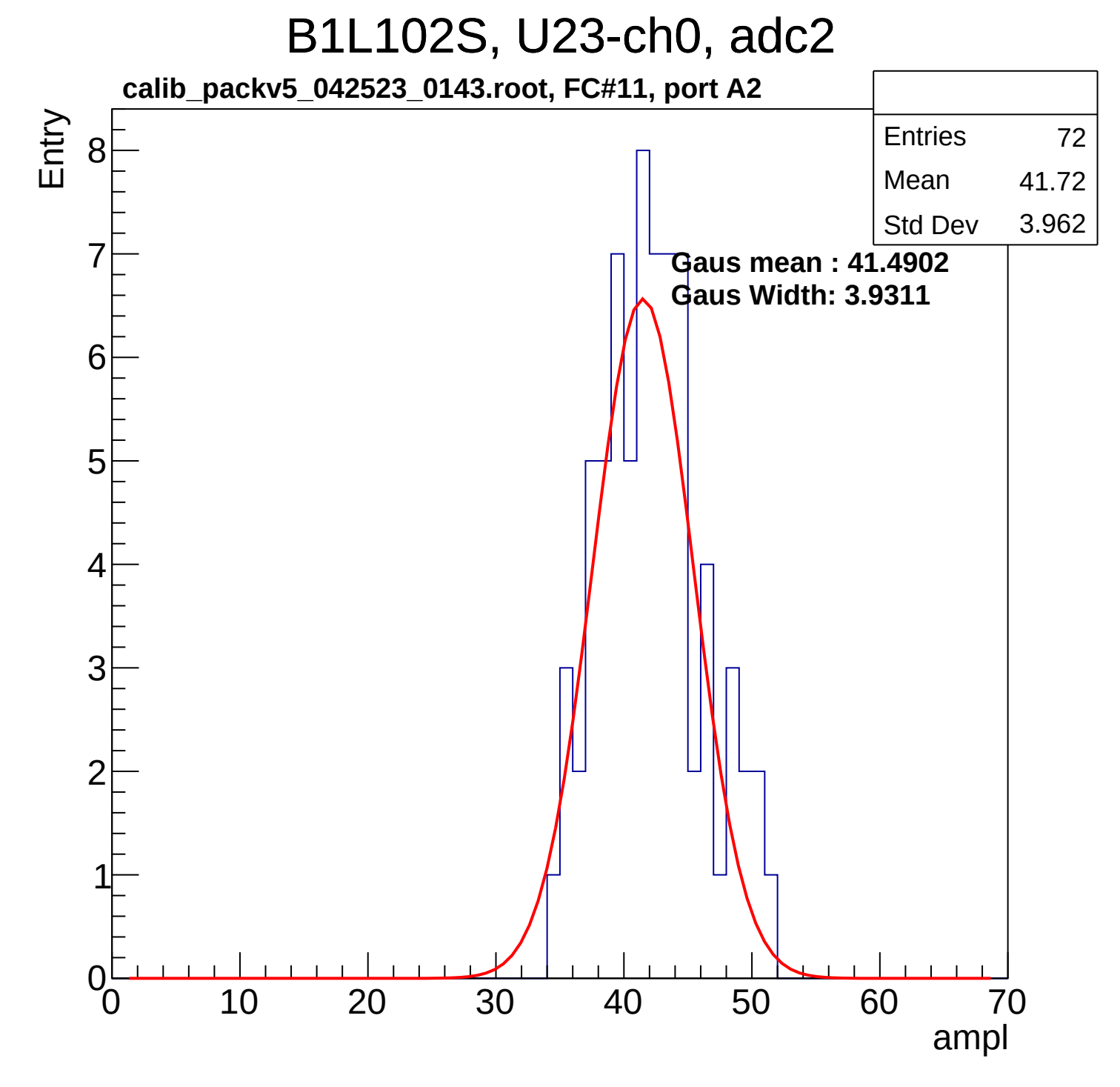
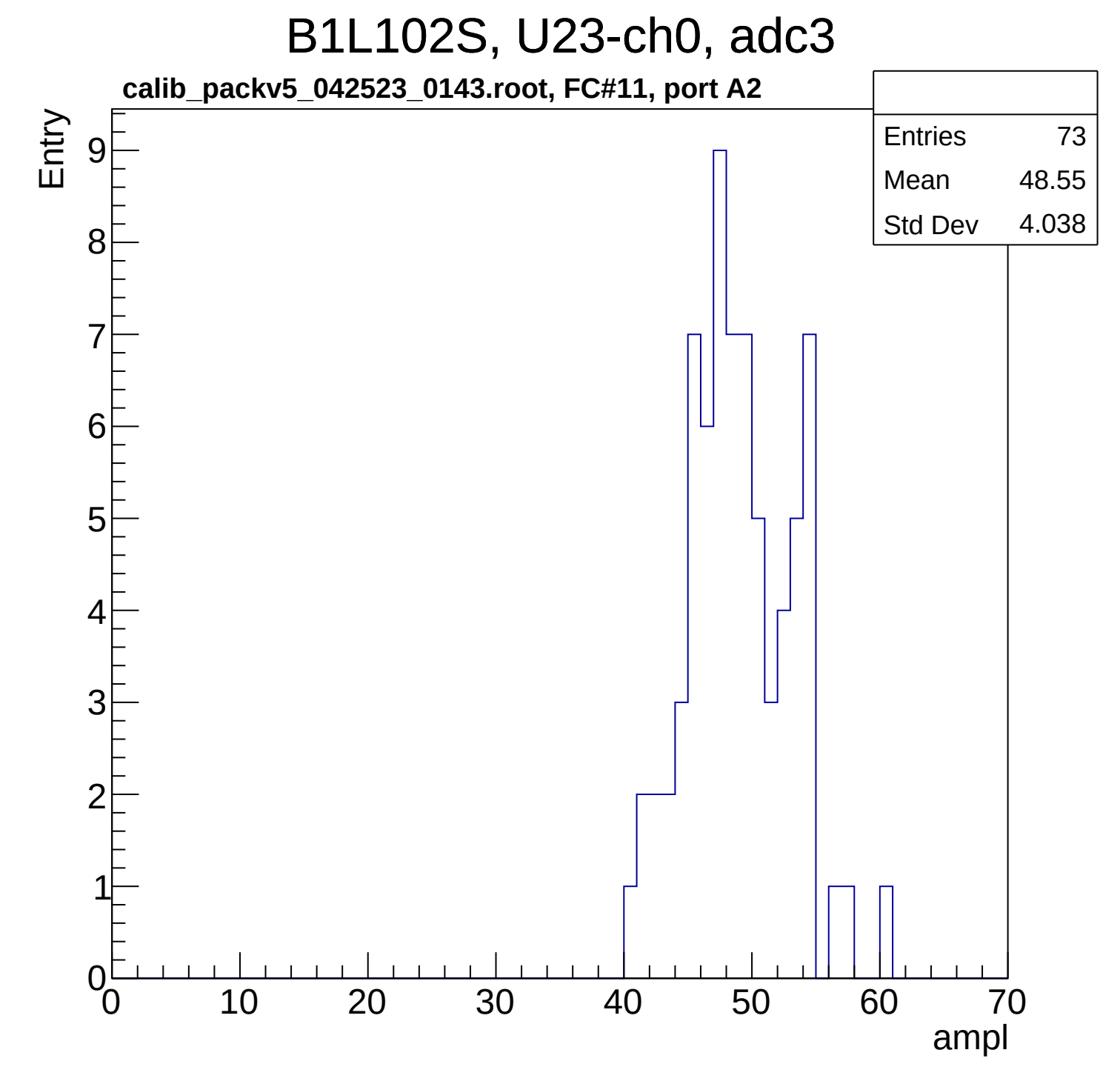
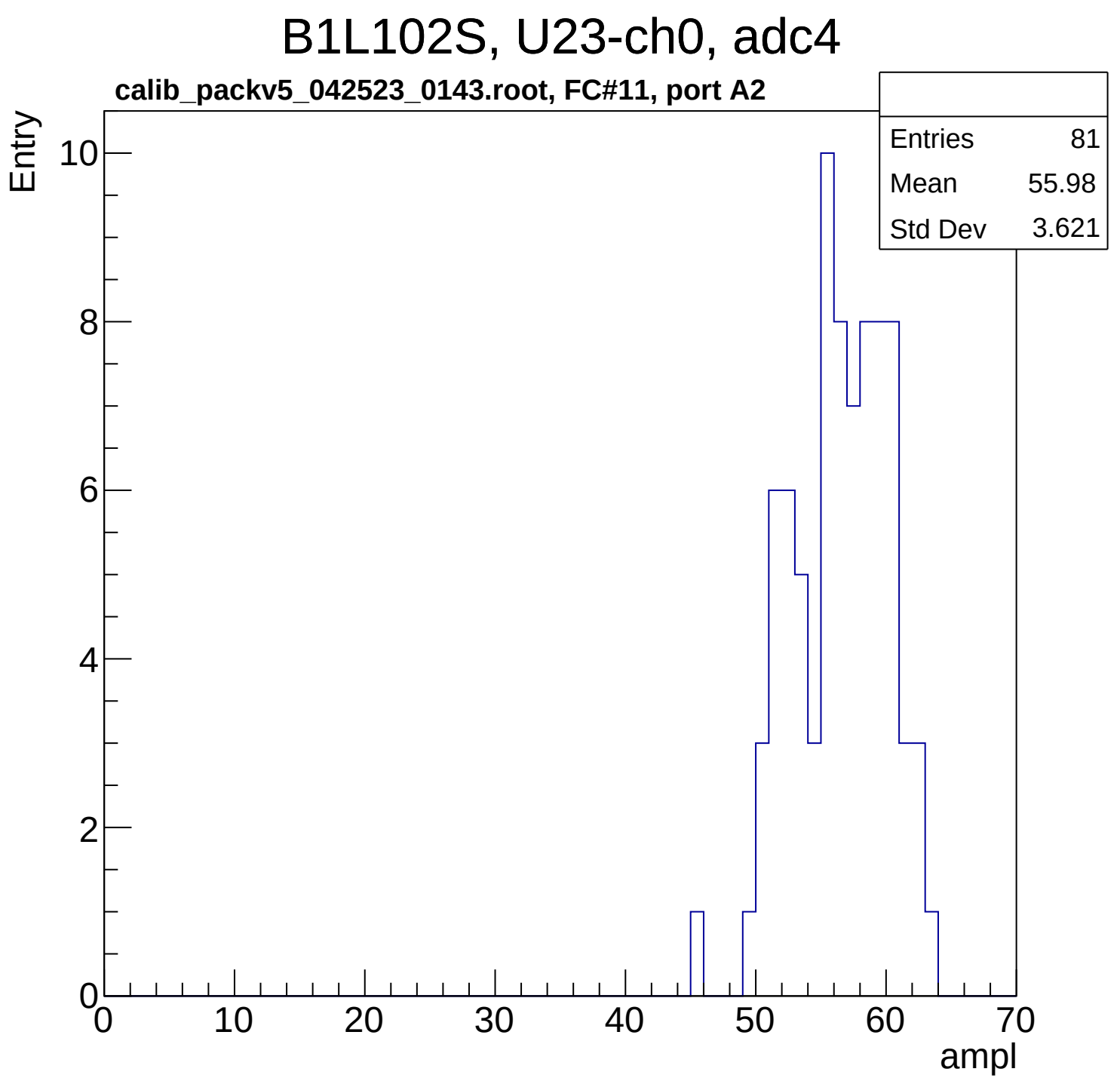
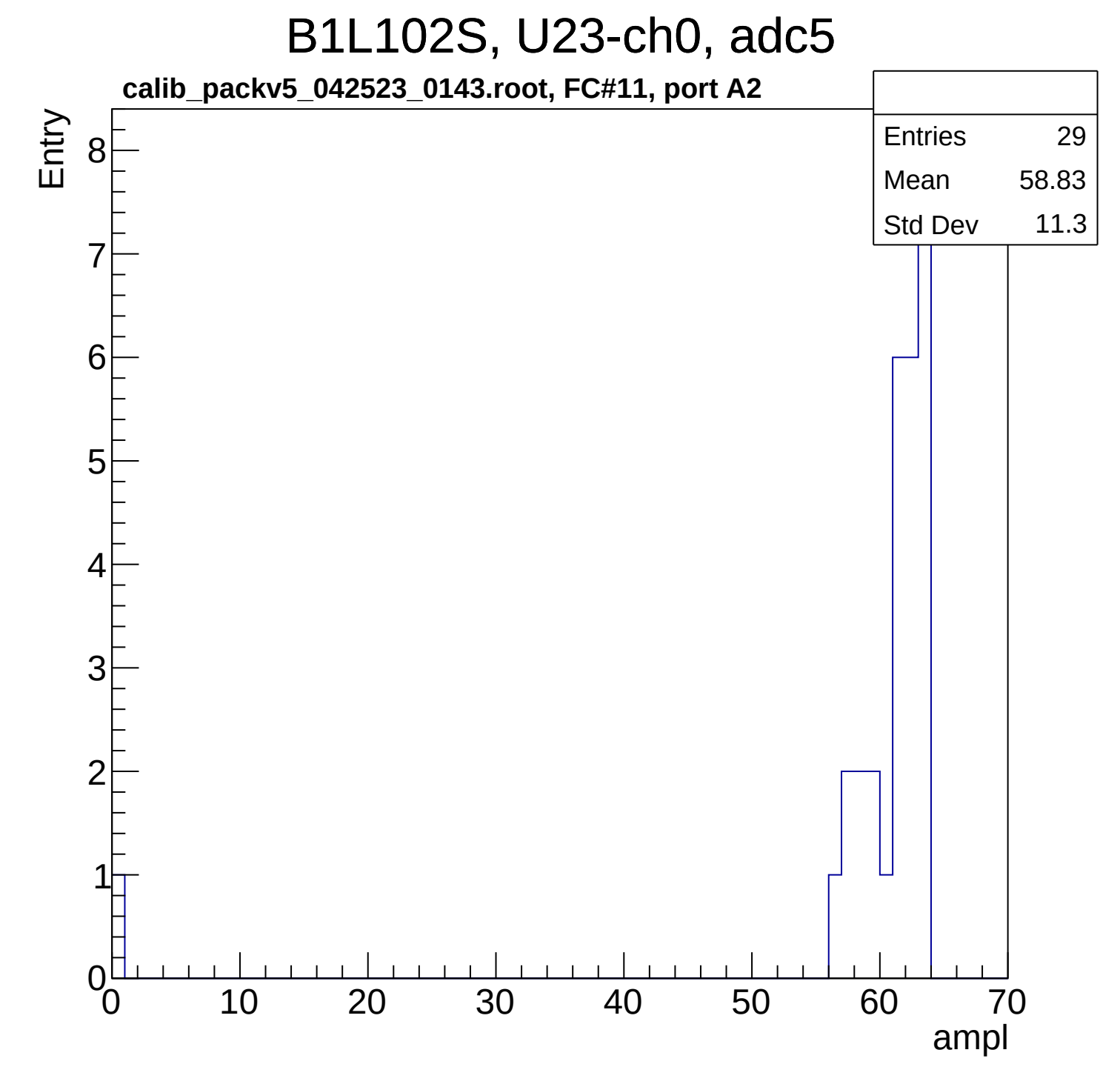

B1L102S, U23-ch0, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 27.3
Std Dev 7.022
Gaus mean : 29.5404
Gaus Width: 3.3011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

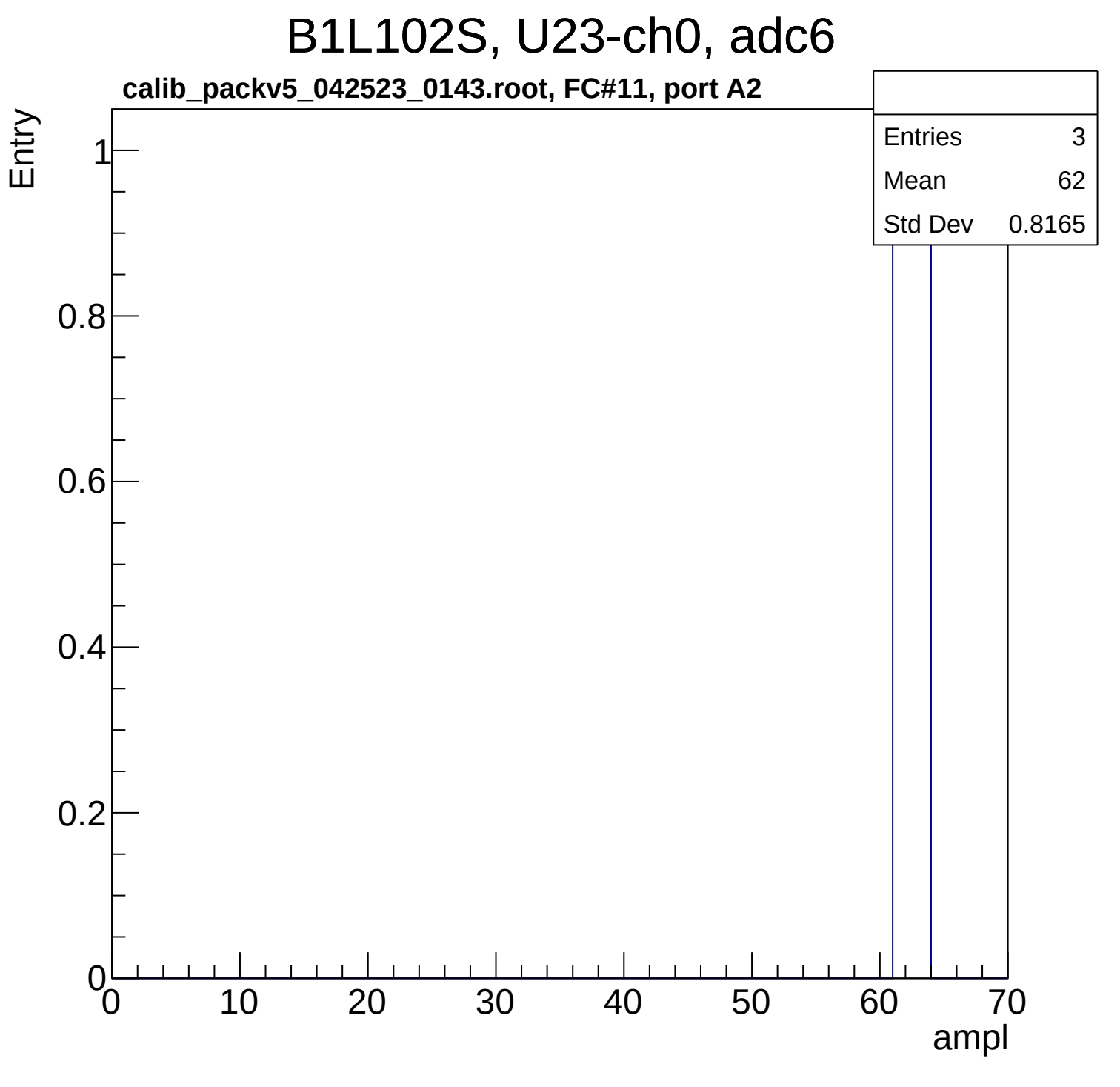

B1L102S, U23-ch0, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 36.04
Std Dev 2.844
Gaus mean : 36.6836
Gaus Width: 3.1853
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

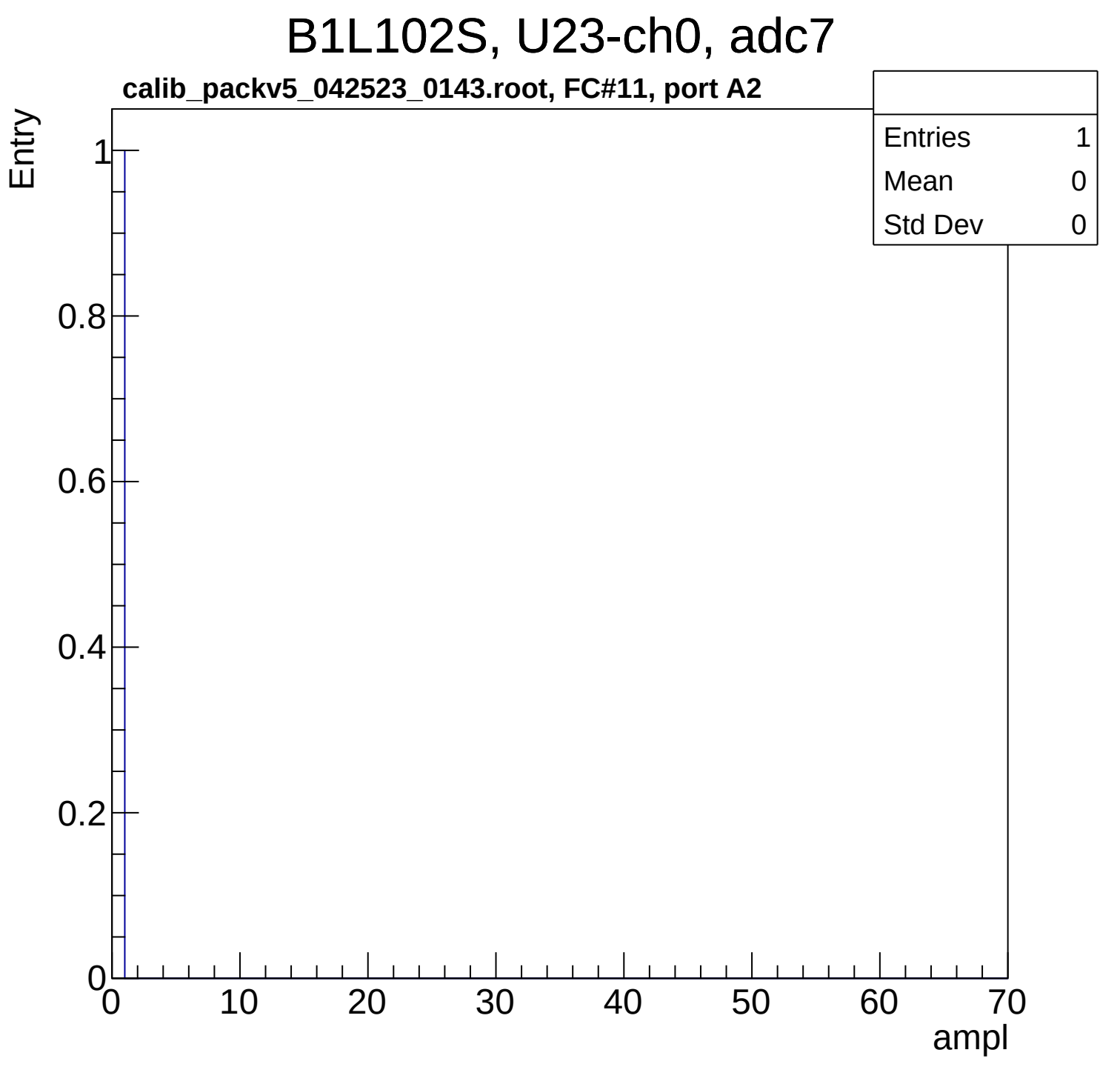

B1L102S, U23-ch0, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 41.72
Std Dev 3.962
Gaus mean : 41.4902
Gaus Width: 3.9311
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

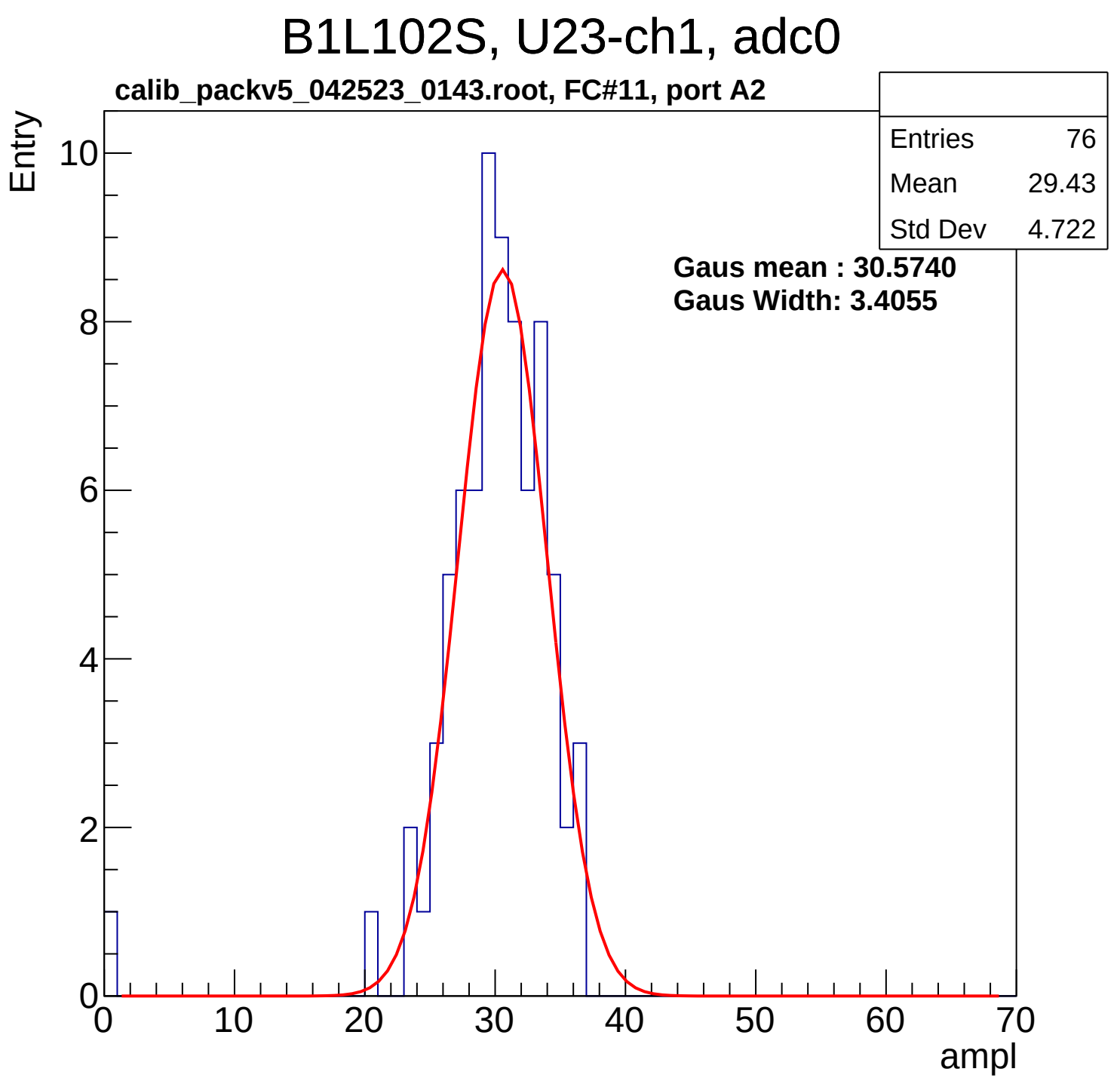

B1L102S, U23-ch0, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 48.55
Std Dev 4.038
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

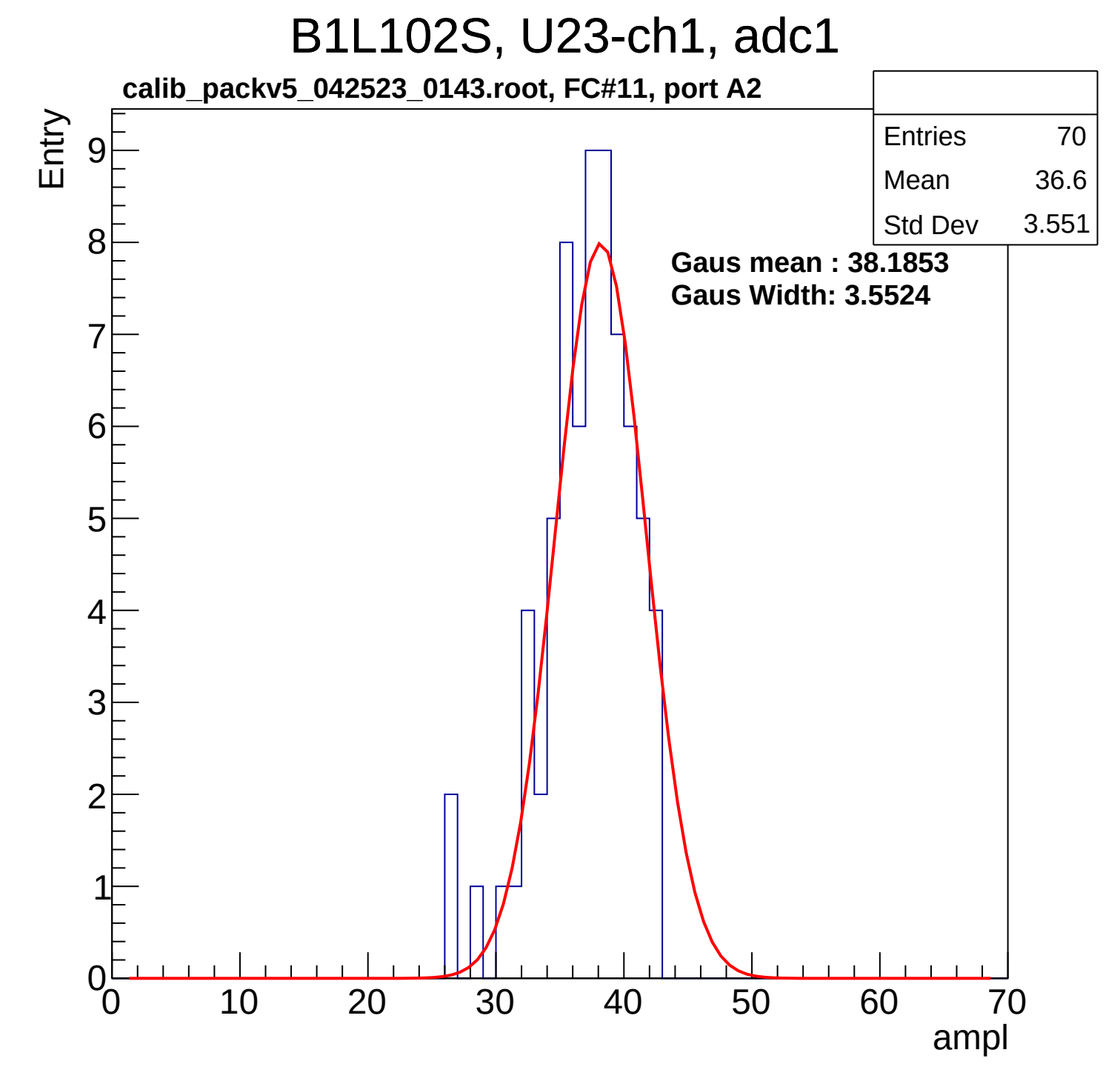

B1L102S, U23-ch0, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 55.98
Std Dev 3.621
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

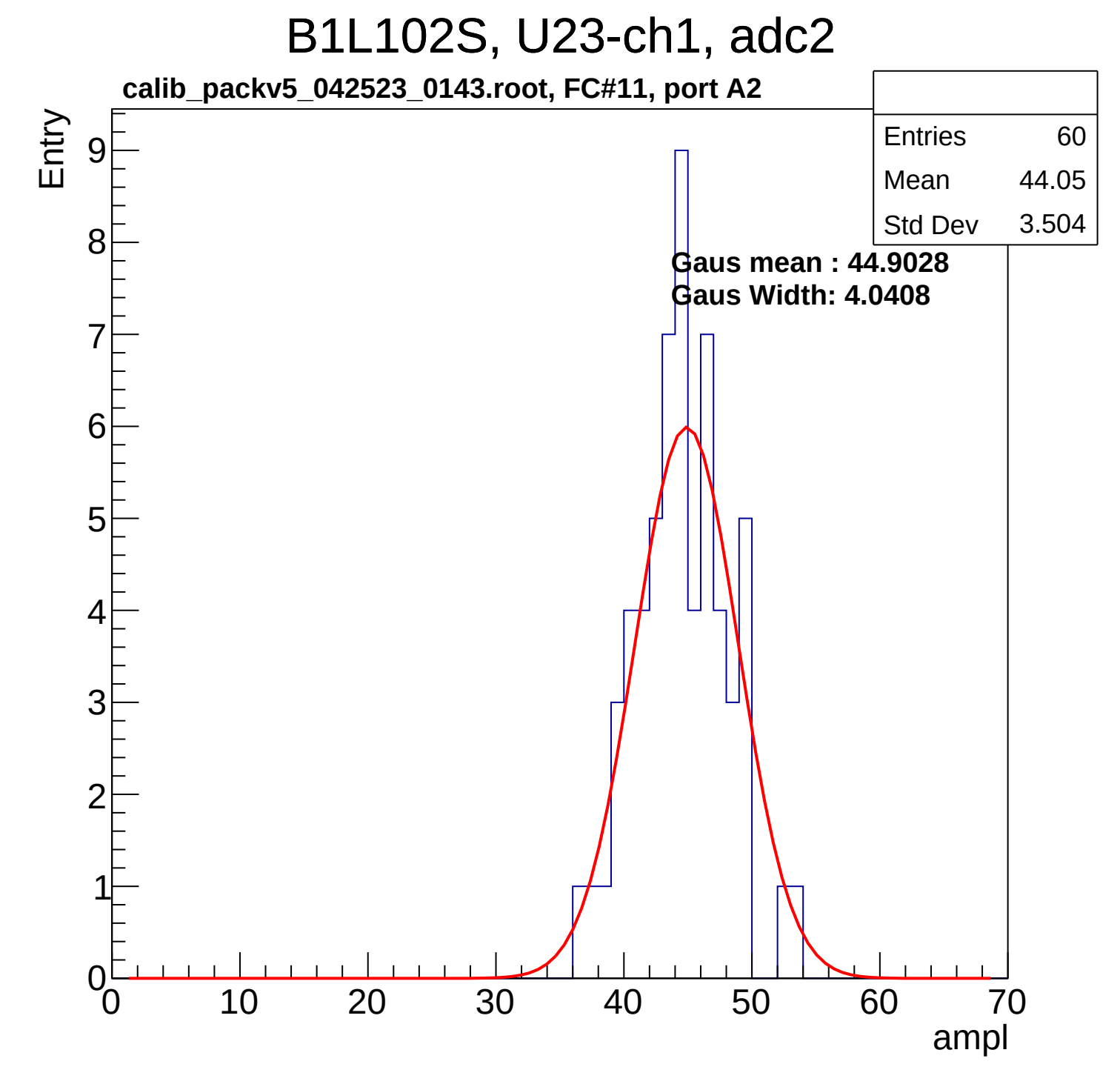

B1L102S, U23-ch0, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 29
Mean 58.83
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

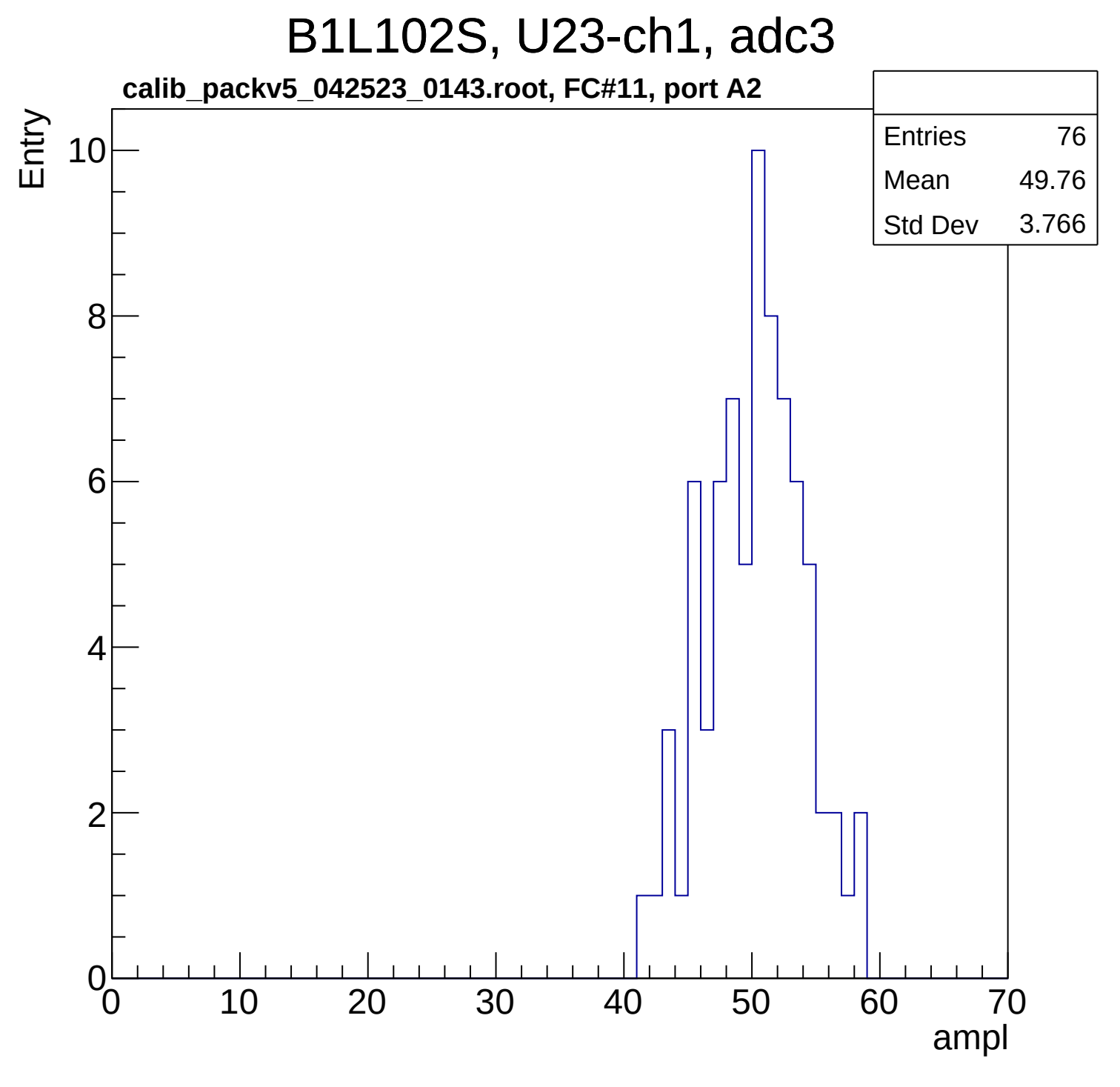

B1L102S, U23-ch0, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

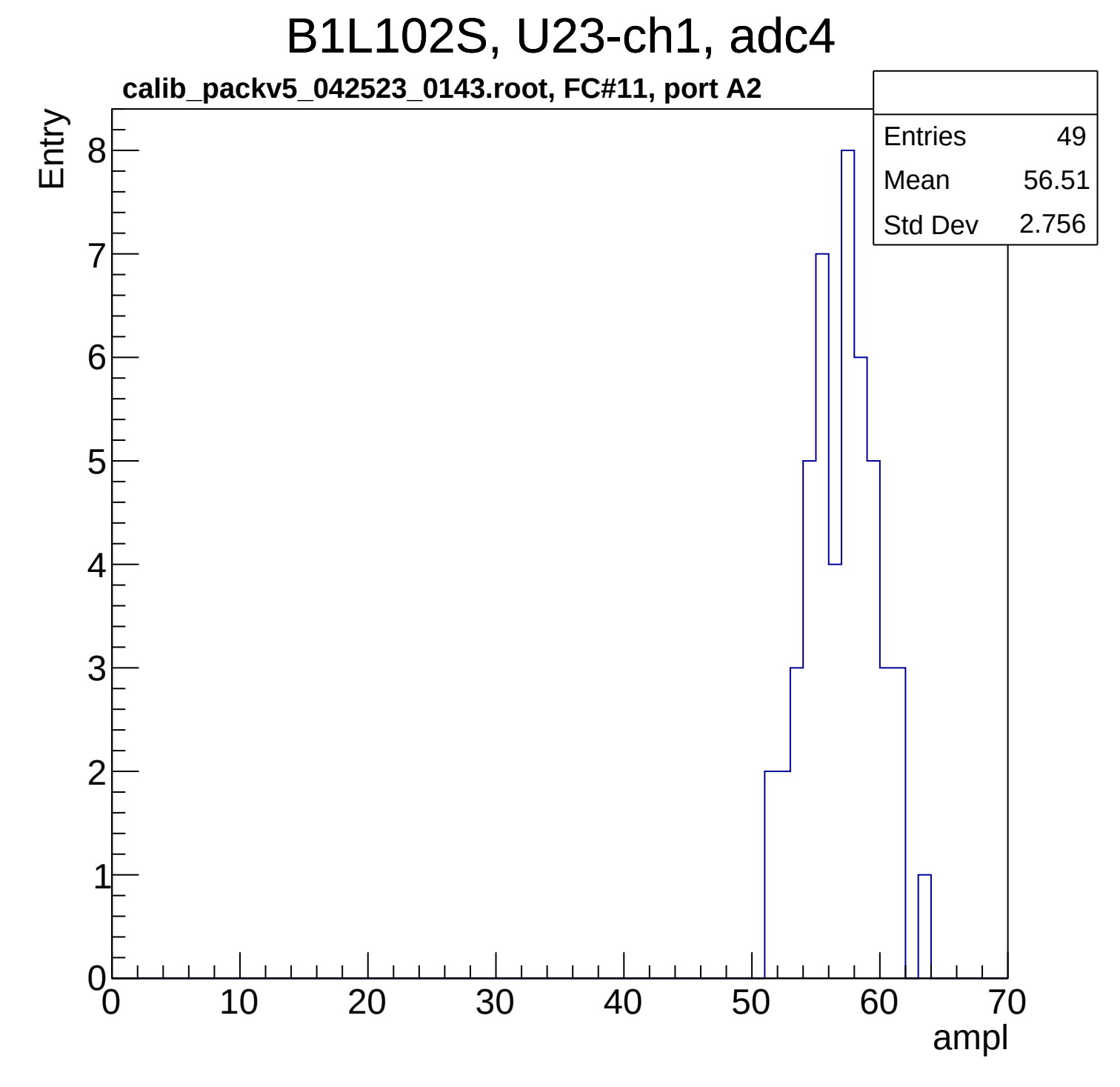

B1L102S, U23-ch0, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

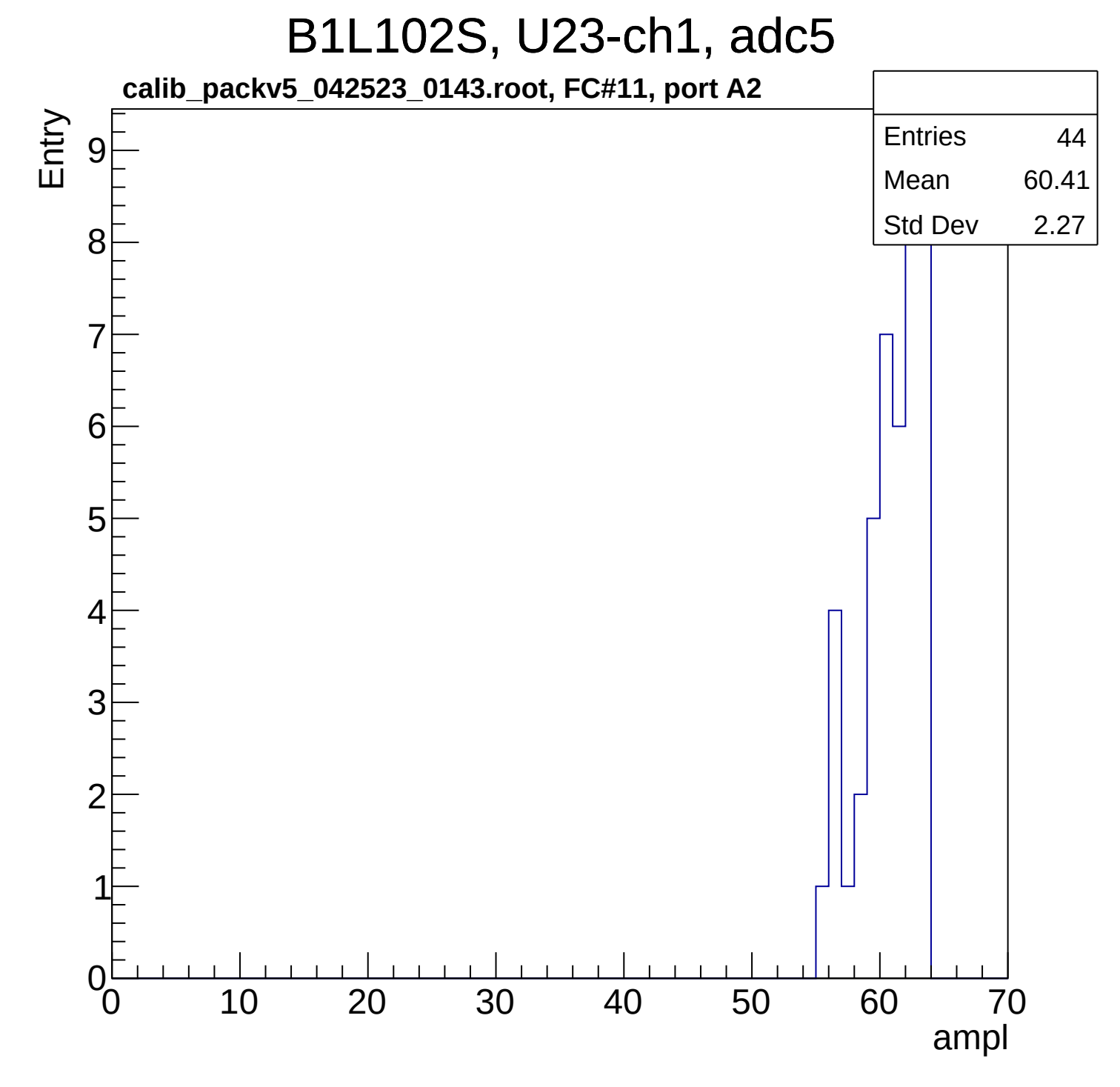

B1L102S, U23-ch1, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 29.43
Std Dev 4.722
Gaus mean : 30.5740
Gaus Width: 3.4055
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

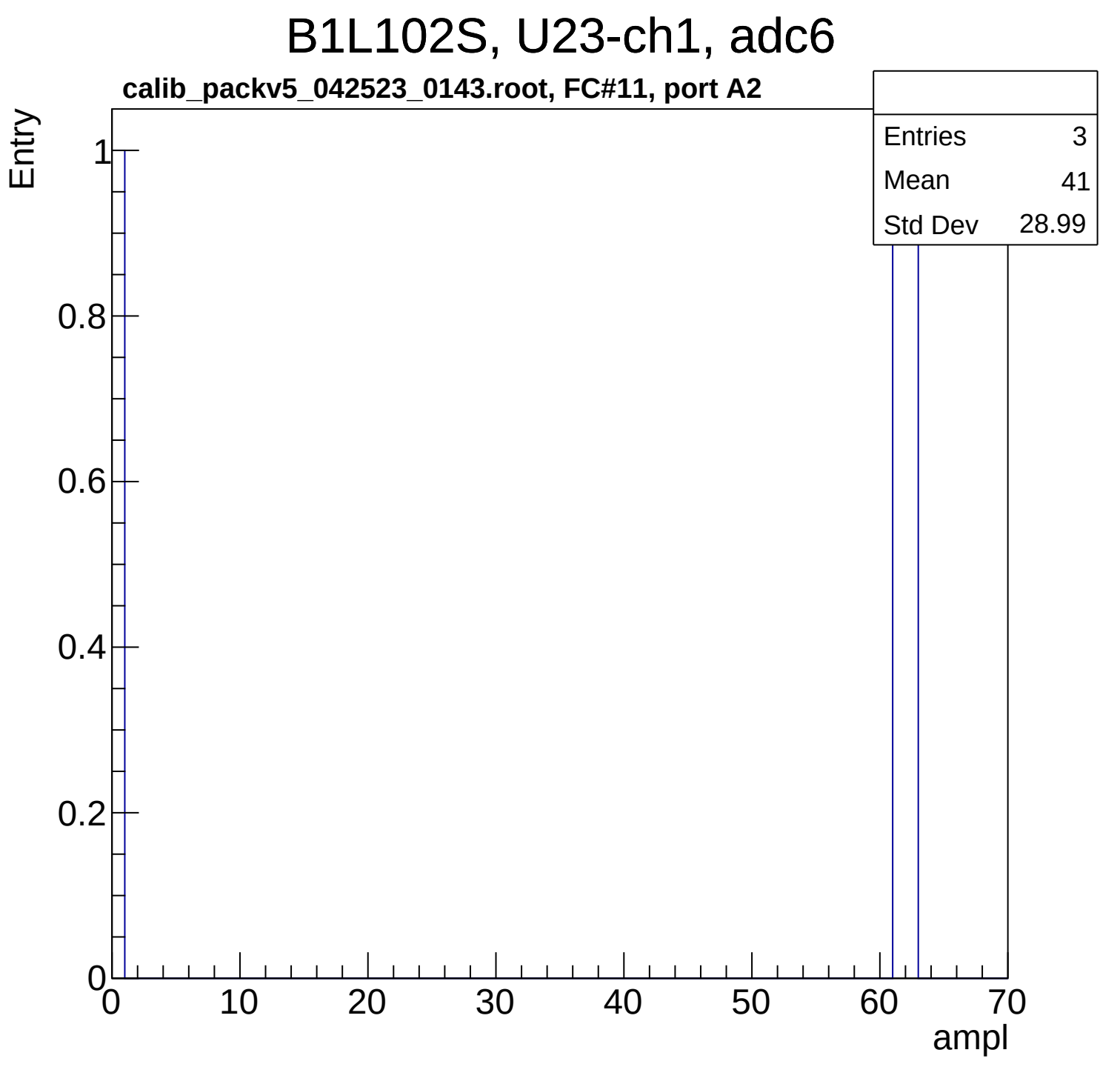

B1L102S, U23-ch1, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 36.6
Std Dev 3.551
Gaus mean : 38.1853
Gaus Width: 3.5524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

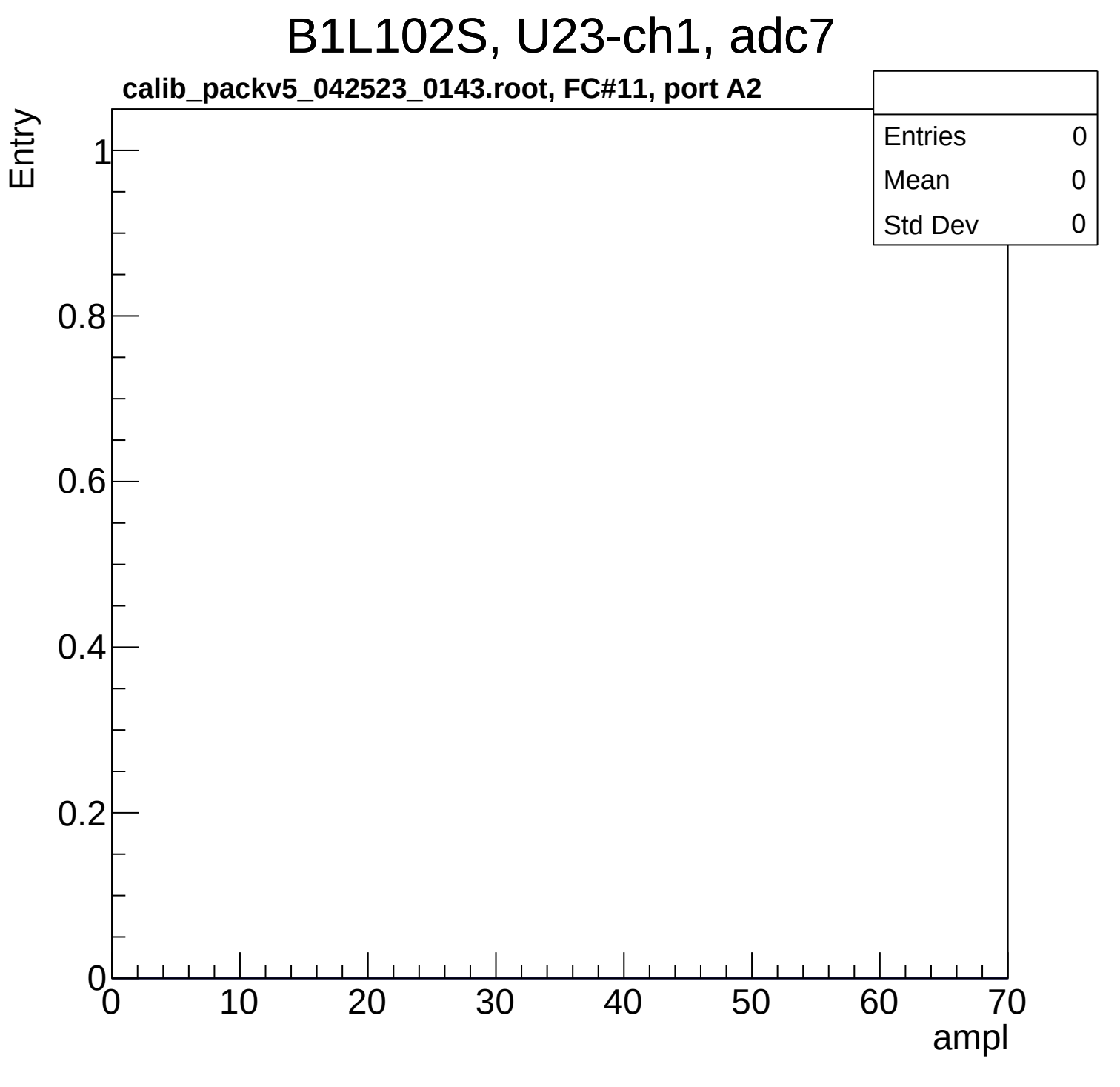

B1L102S, U23-ch1, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 44.05
Std Dev 3.504
Gaus mean : 44.9028
Gaus Width: 4.0408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

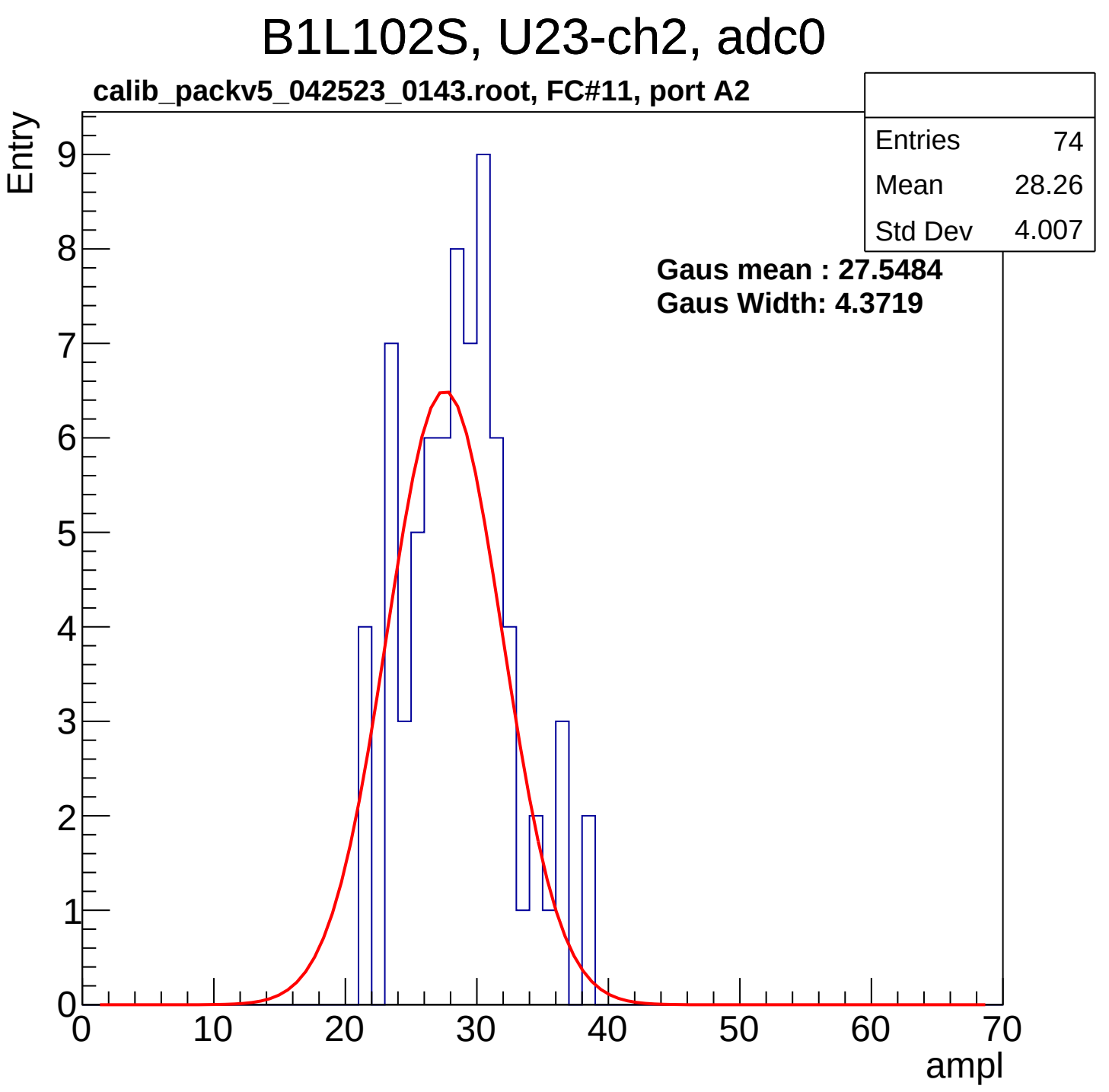

B1L102S, U23-ch1, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 49.76
Std Dev 3.766
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

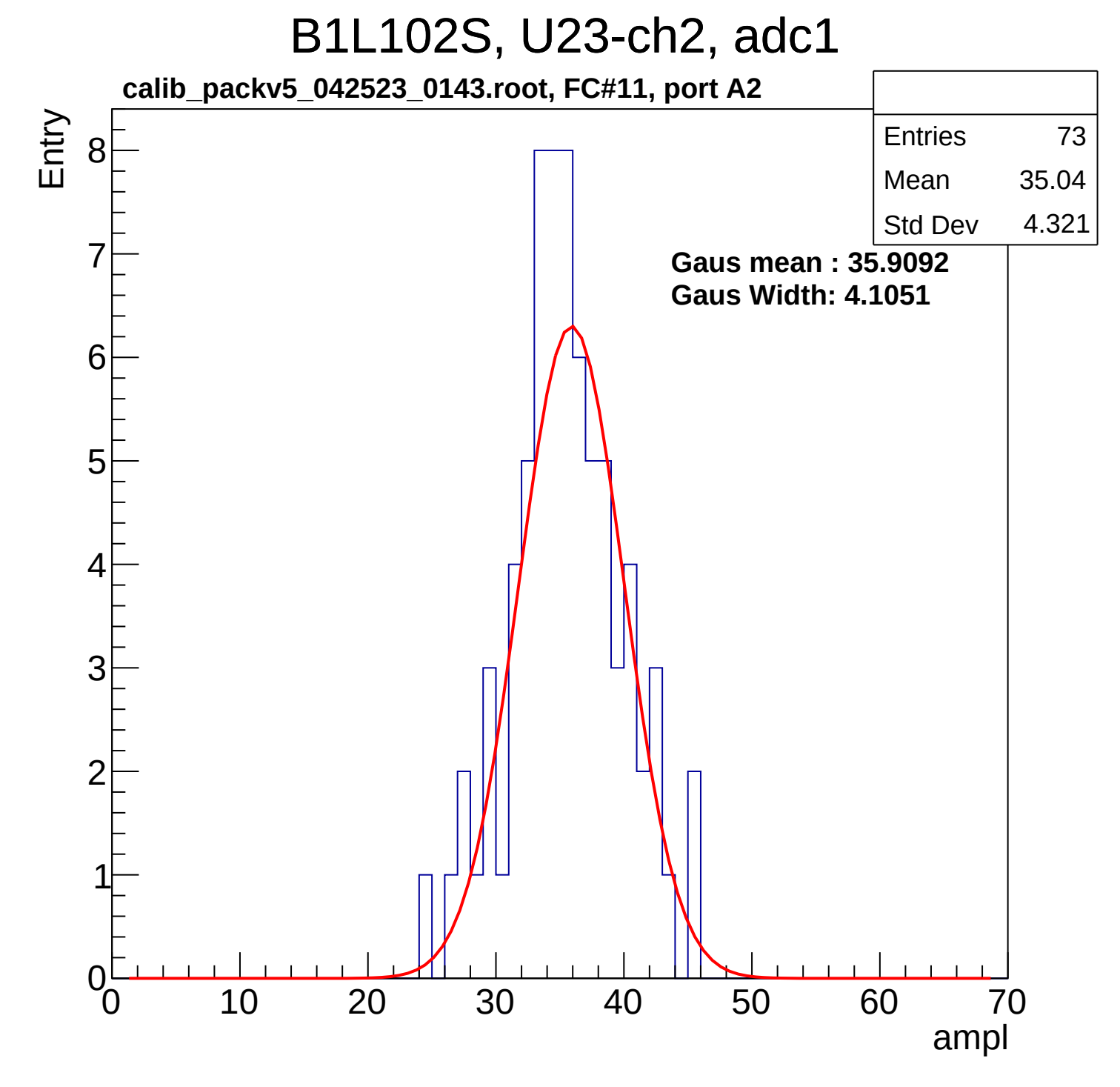

B1L102S, U23-ch1, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 56.51
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

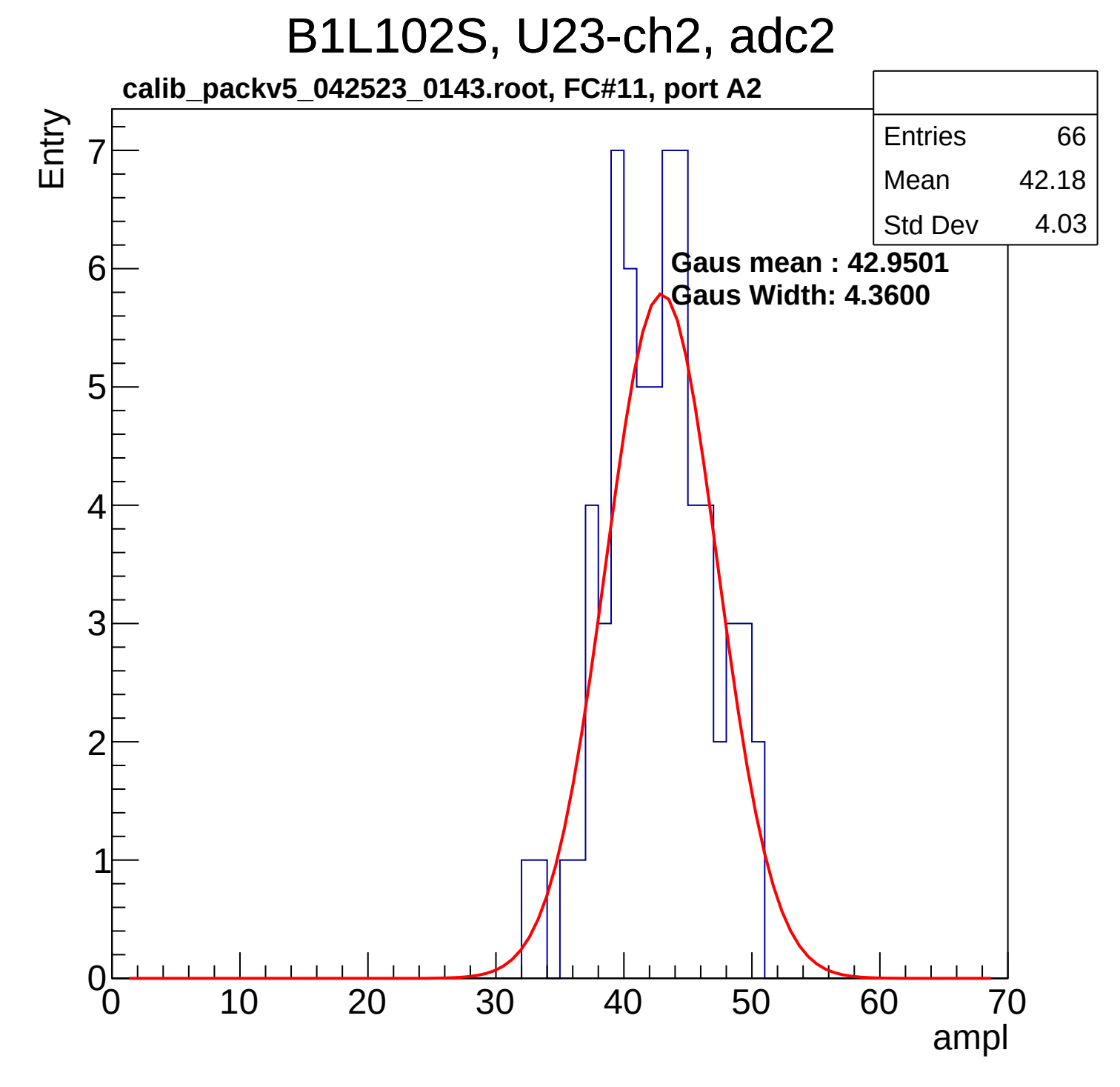

B1L102S, U23-ch1, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 60.41
Std Dev 2.27
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

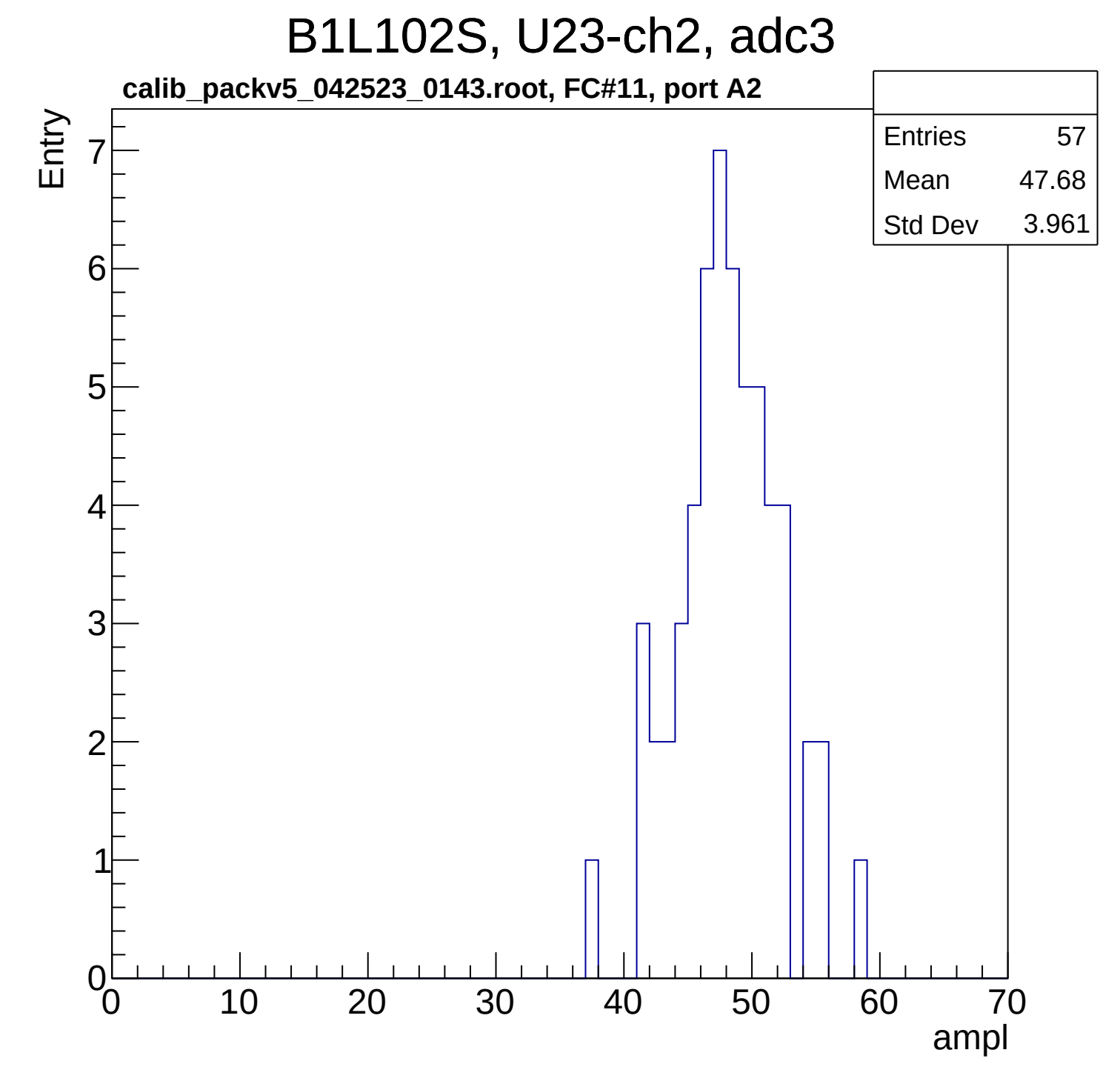

B1L102S, U23-ch1, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

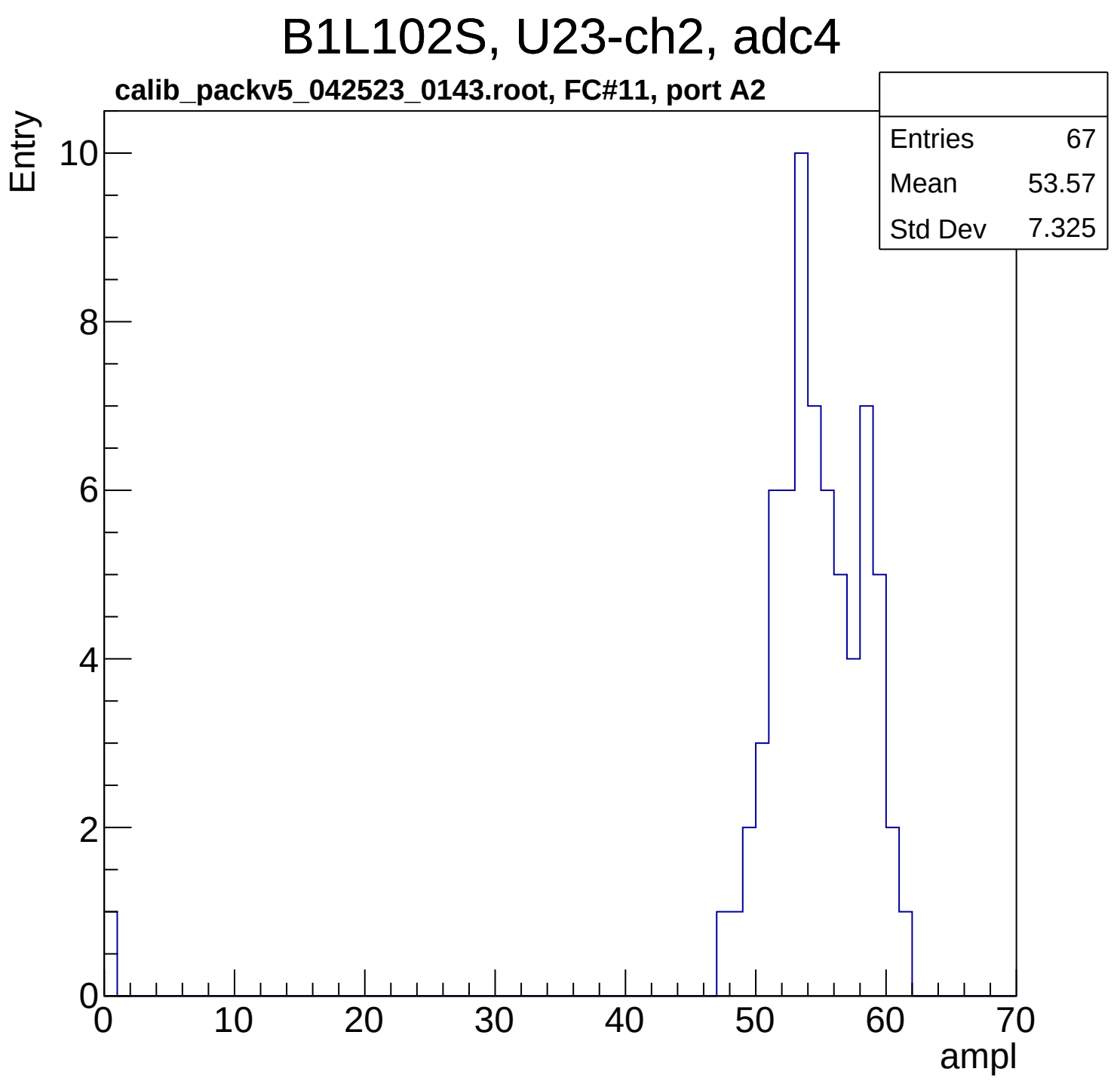

B1L102S, U23-ch1, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

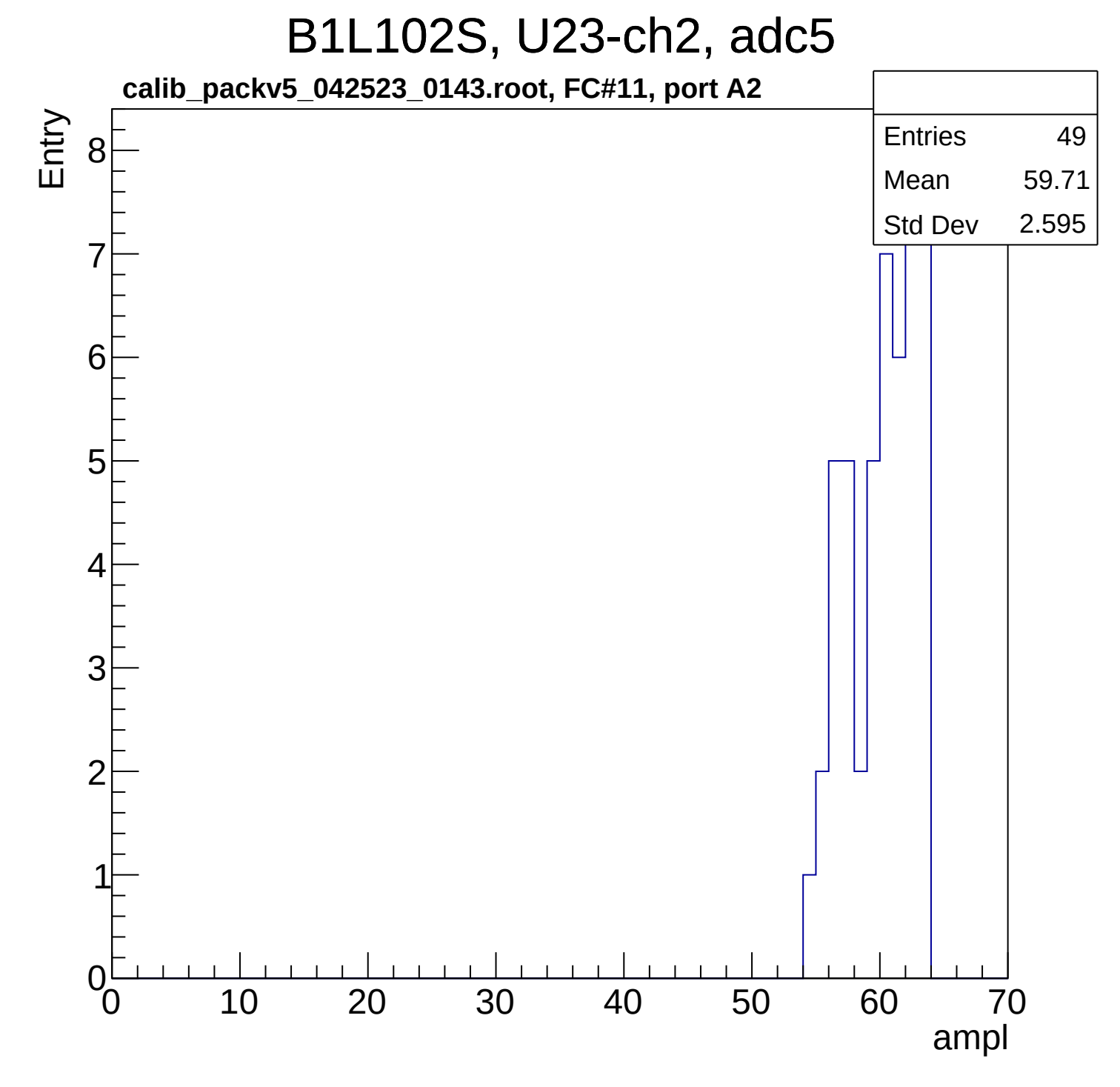

B1L102S, U23-ch2, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 28.26
Std Dev 4.007
Gaus mean : 27.5484
Gaus Width: 4.3719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

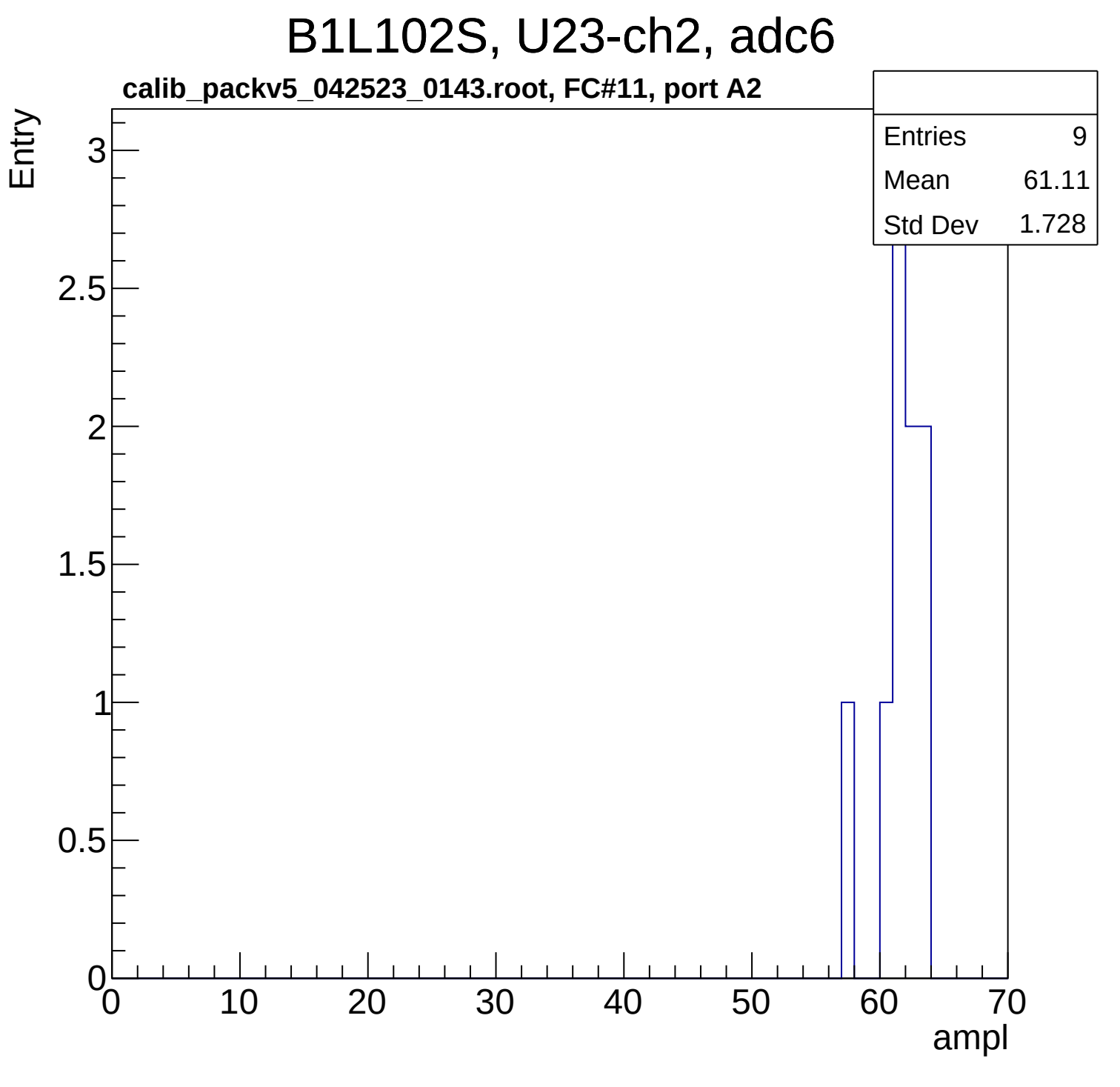

B1L102S, U23-ch2, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 35.04
Std Dev 4.321
Gaus mean : 35.9092
Gaus Width: 4.1051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

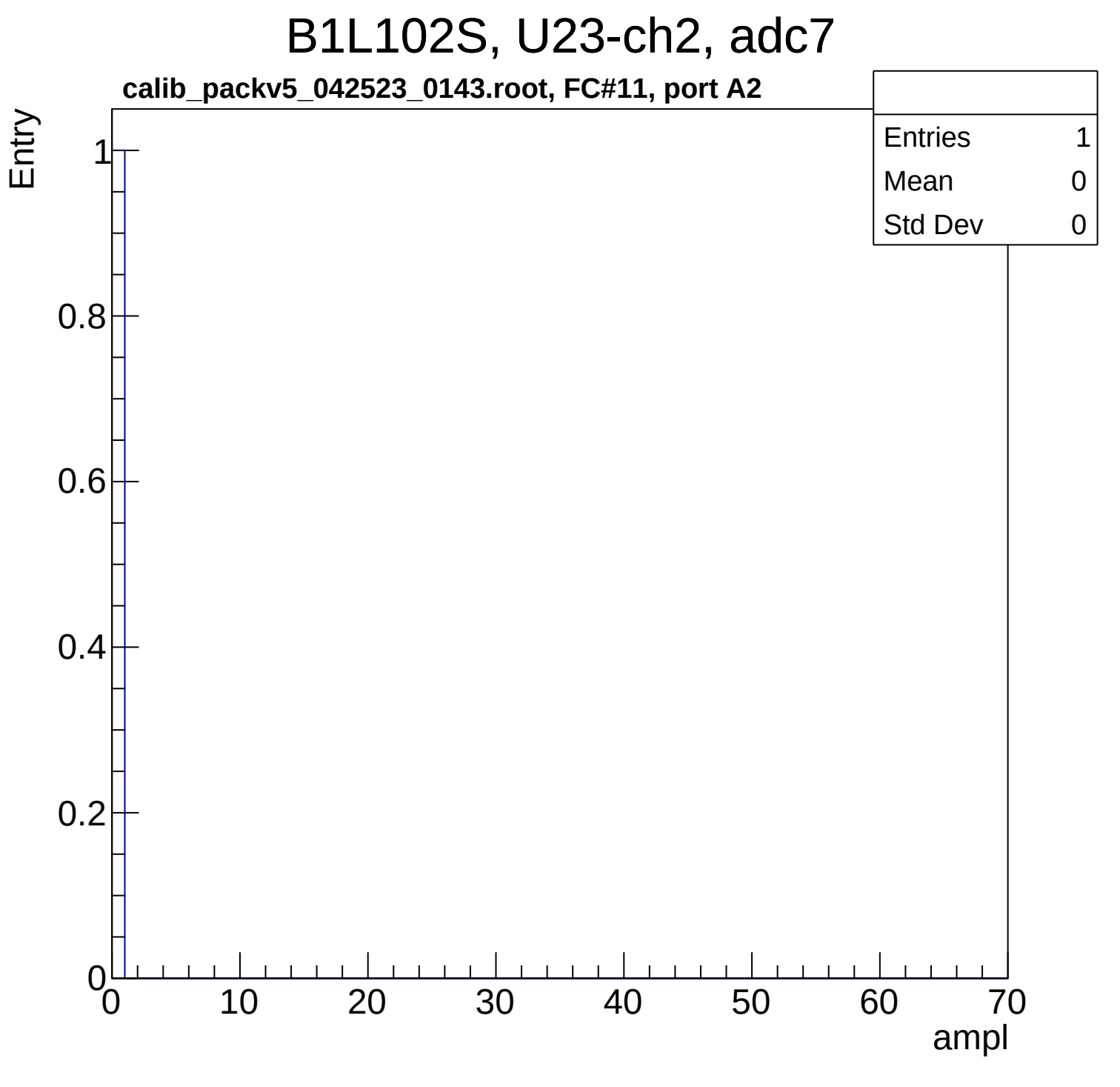

B1L102S, U23-ch2, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 42.18
Std Dev 4.03
Gaus mean : 42.9501
Gaus Width: 4.3600
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

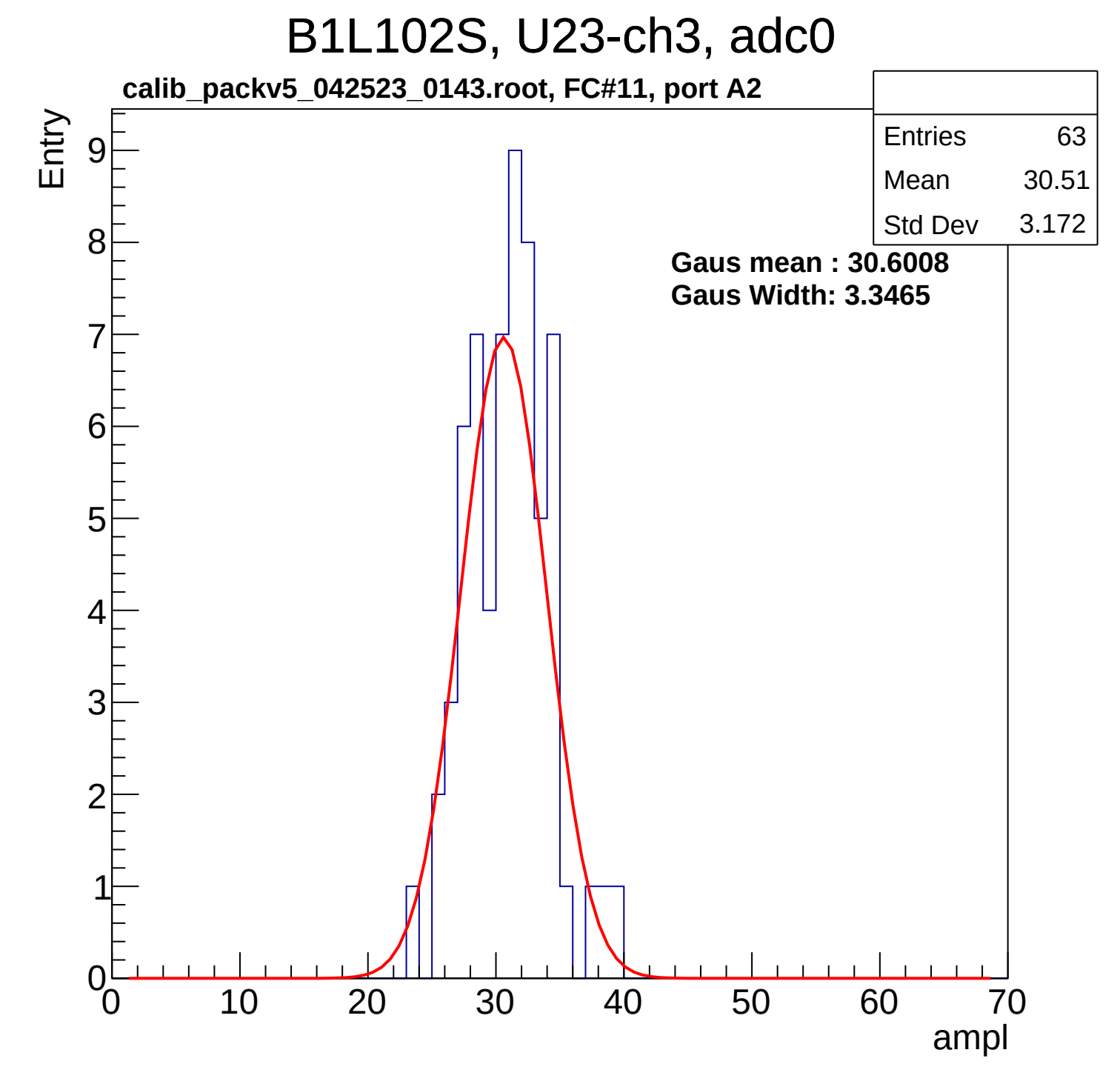

B1L102S, U23-ch2, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 47.68
Std Dev 3.961
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

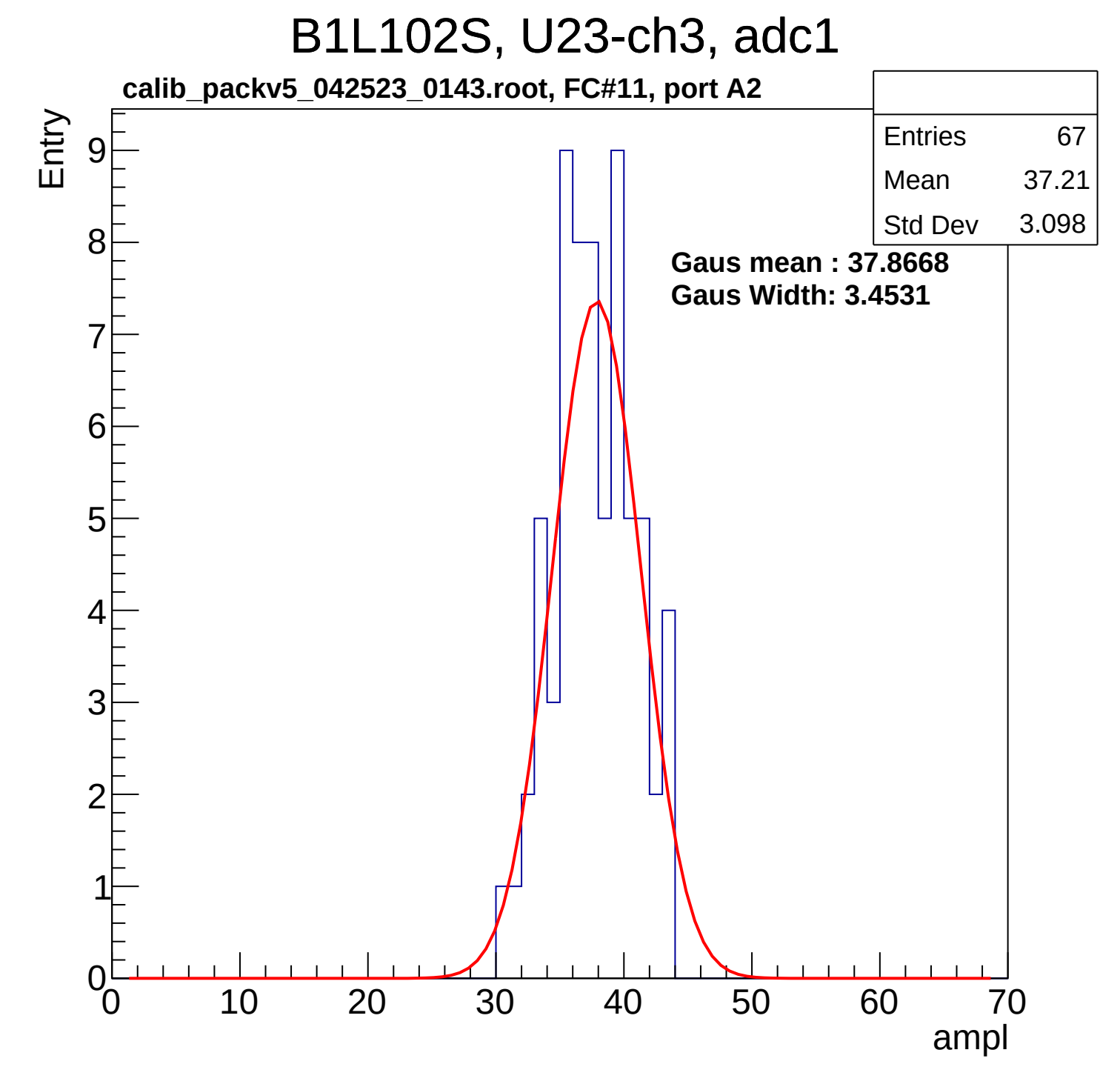

B1L102S, U23-ch2, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 53.57
Std Dev 7.325
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

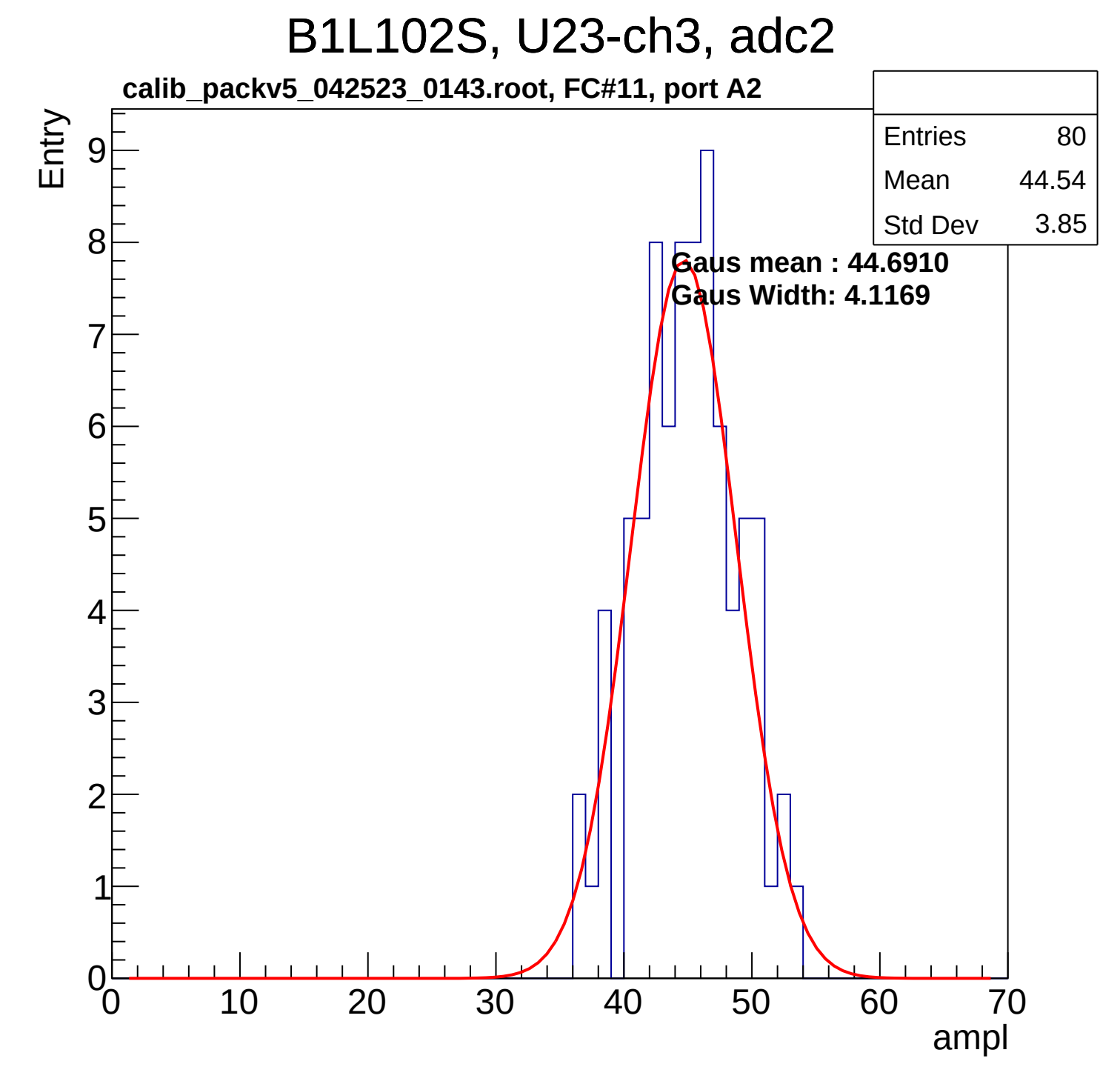

B1L102S, U23-ch2, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.71
Std Dev 2.595
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

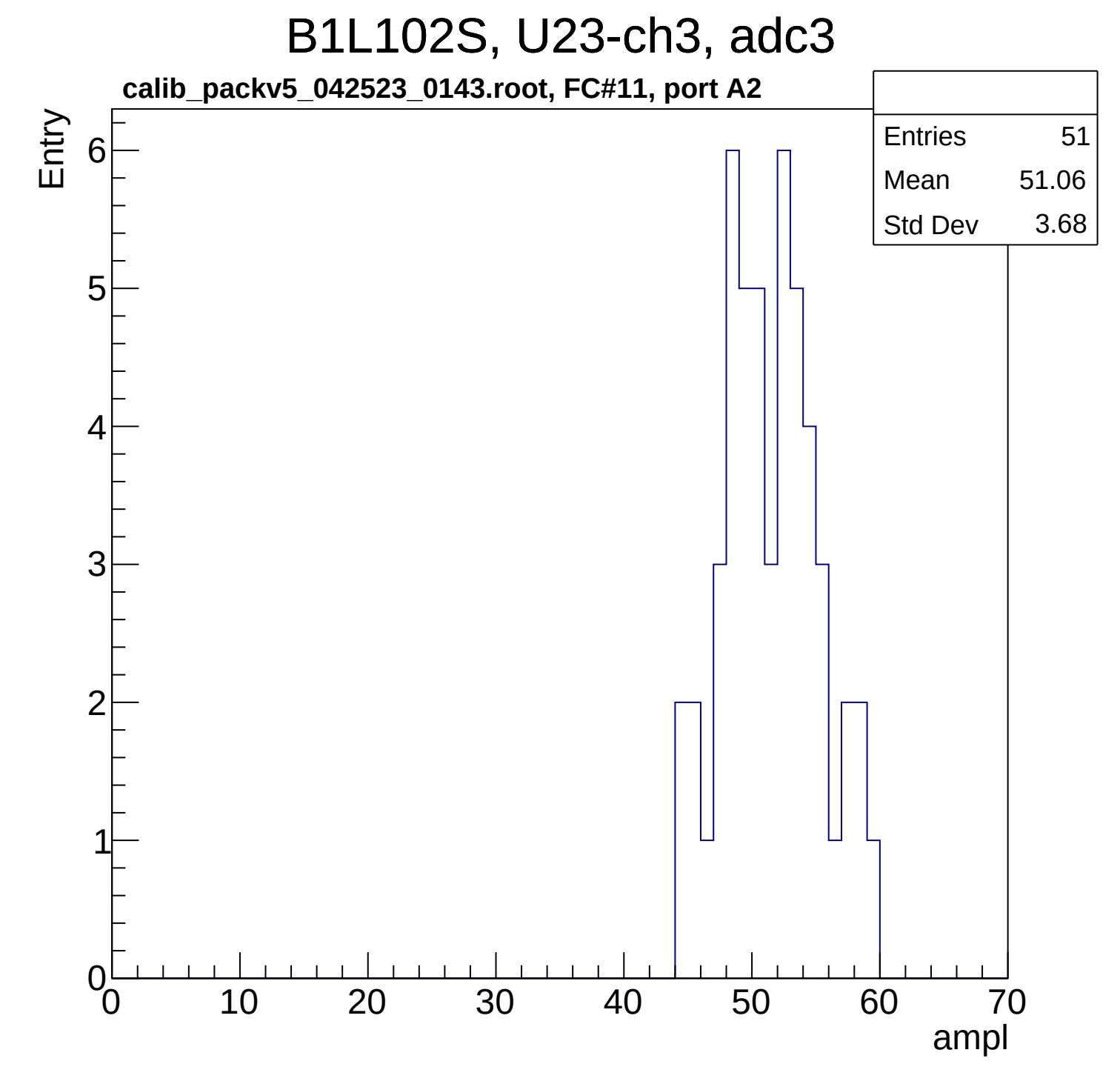

B1L102S, U23-ch2, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 61.11
Std Dev 1.728
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

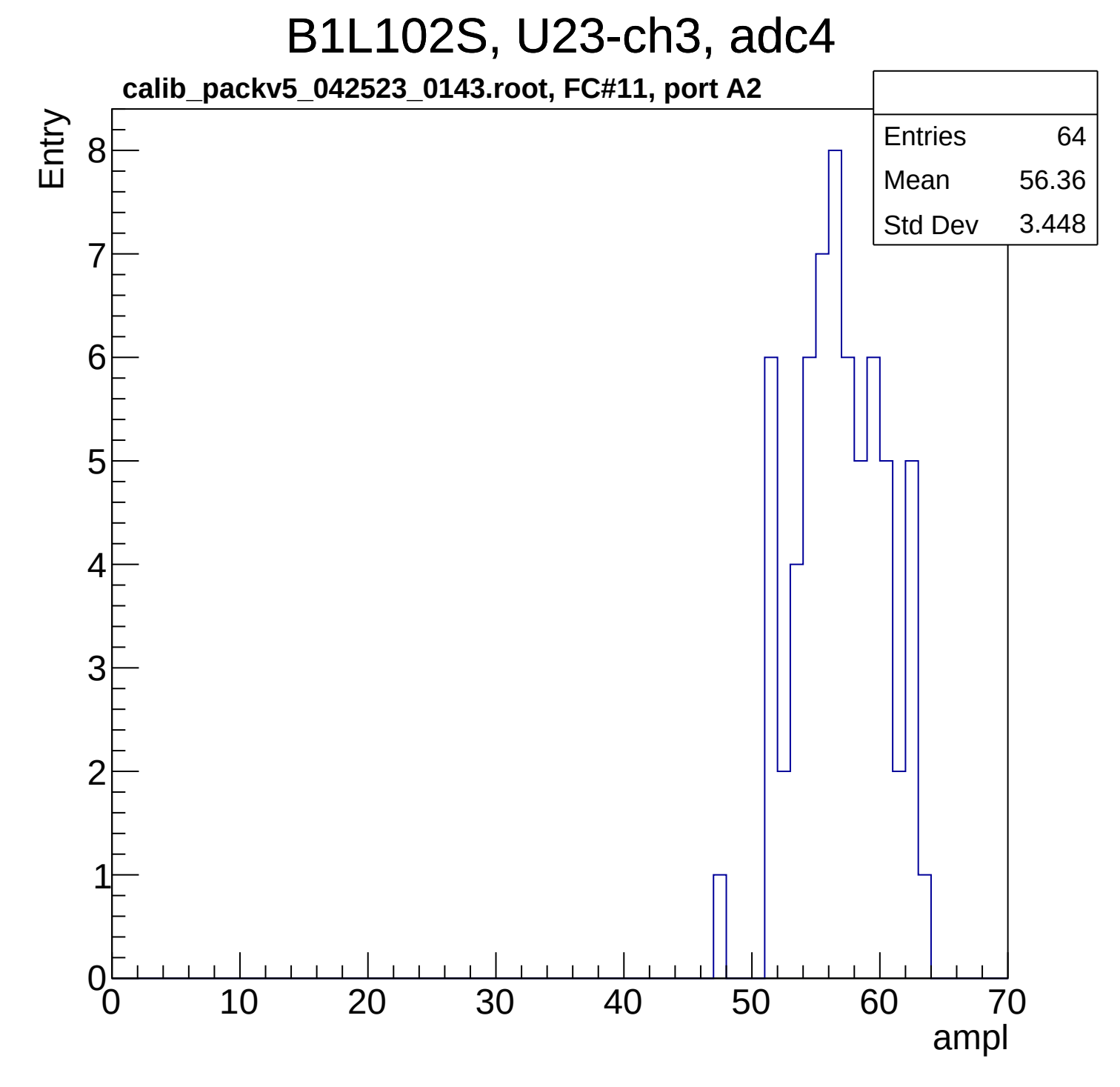

B1L102S, U23-ch2, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

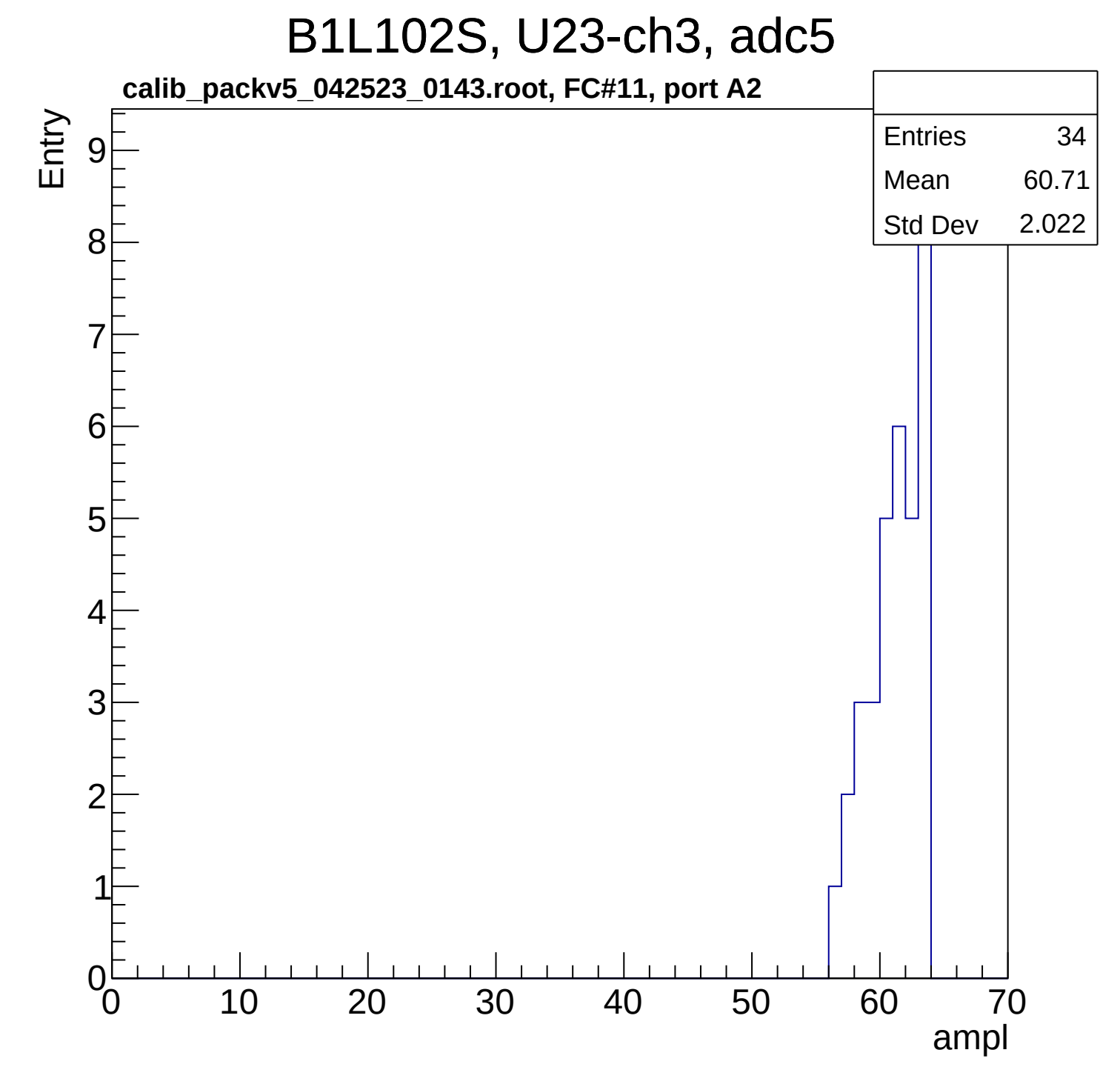

B1L102S, U23-ch3, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 30.51
Std Dev 3.172
Gaus mean : 30.6008
Gaus Width: 3.3465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

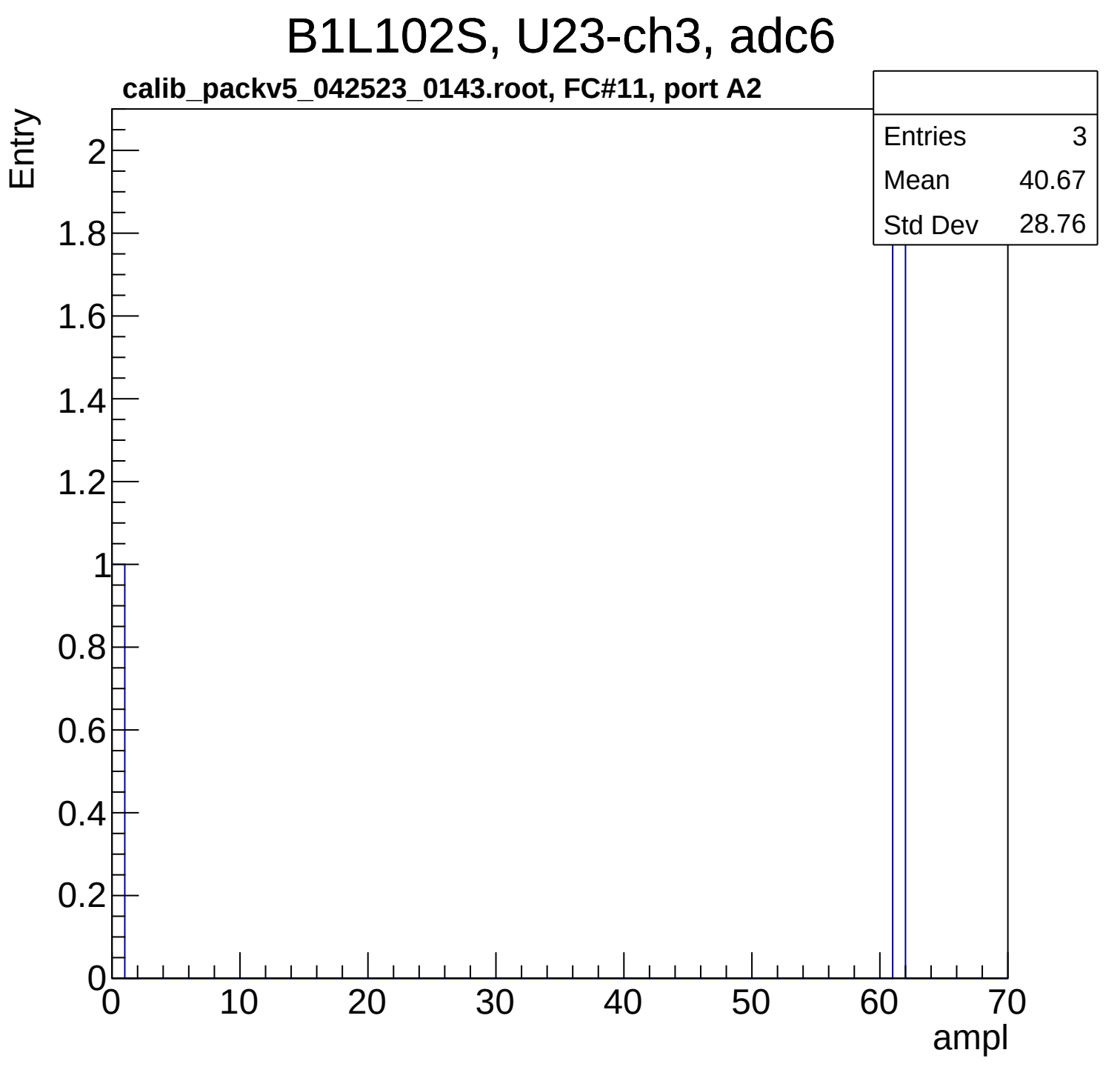

B1L102S, U23-ch3, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 37.21
Std Dev 3.098
Gaus mean : 37.8668
Gaus Width: 3.4531
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

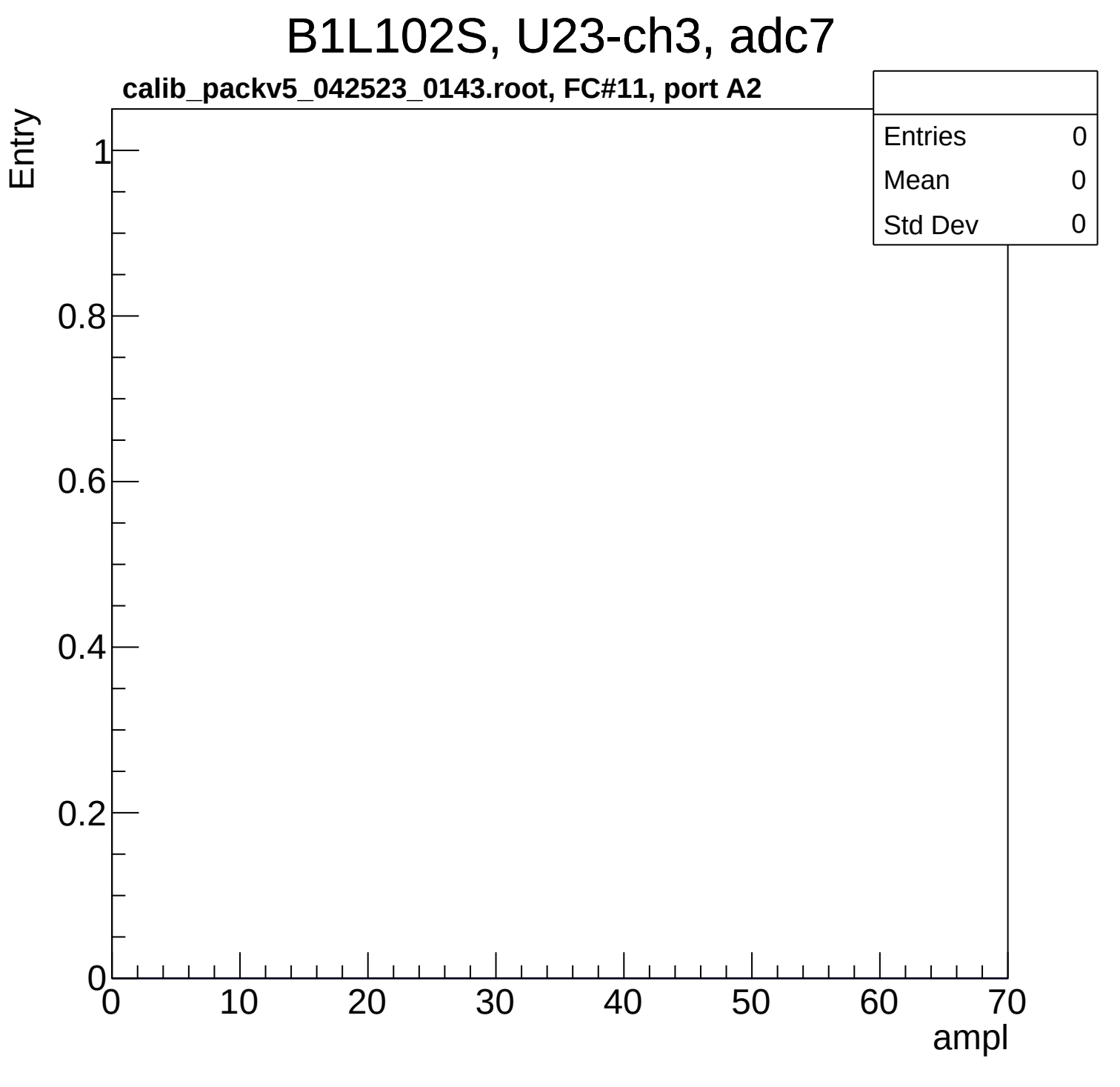

B1L102S, U23-ch3, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 44.54
Std Dev 3.85
Gaus mean : 44.6910
Gaus Width: 4.1169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

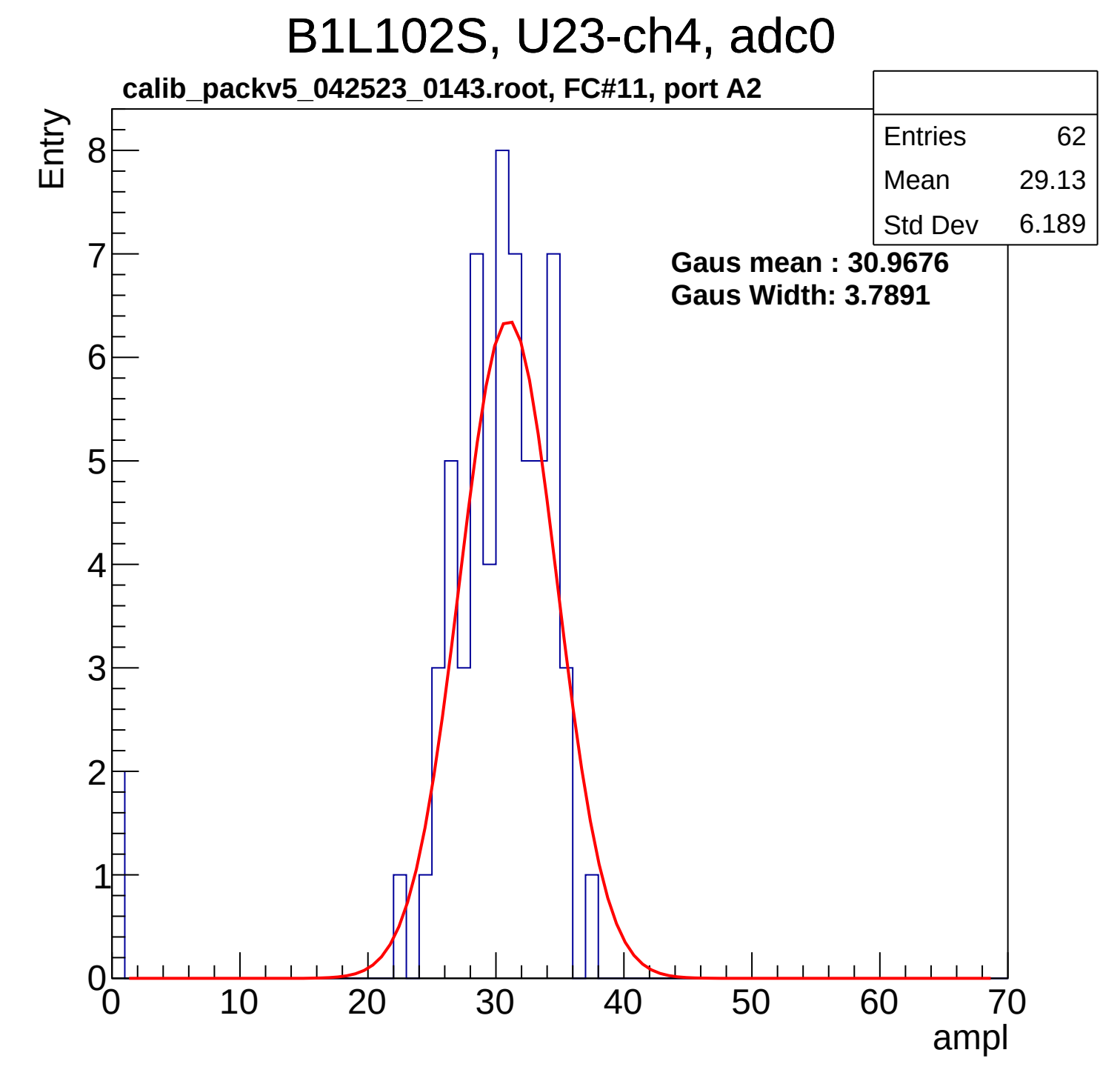

B1L102S, U23-ch3, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 51.06
Std Dev 3.68
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

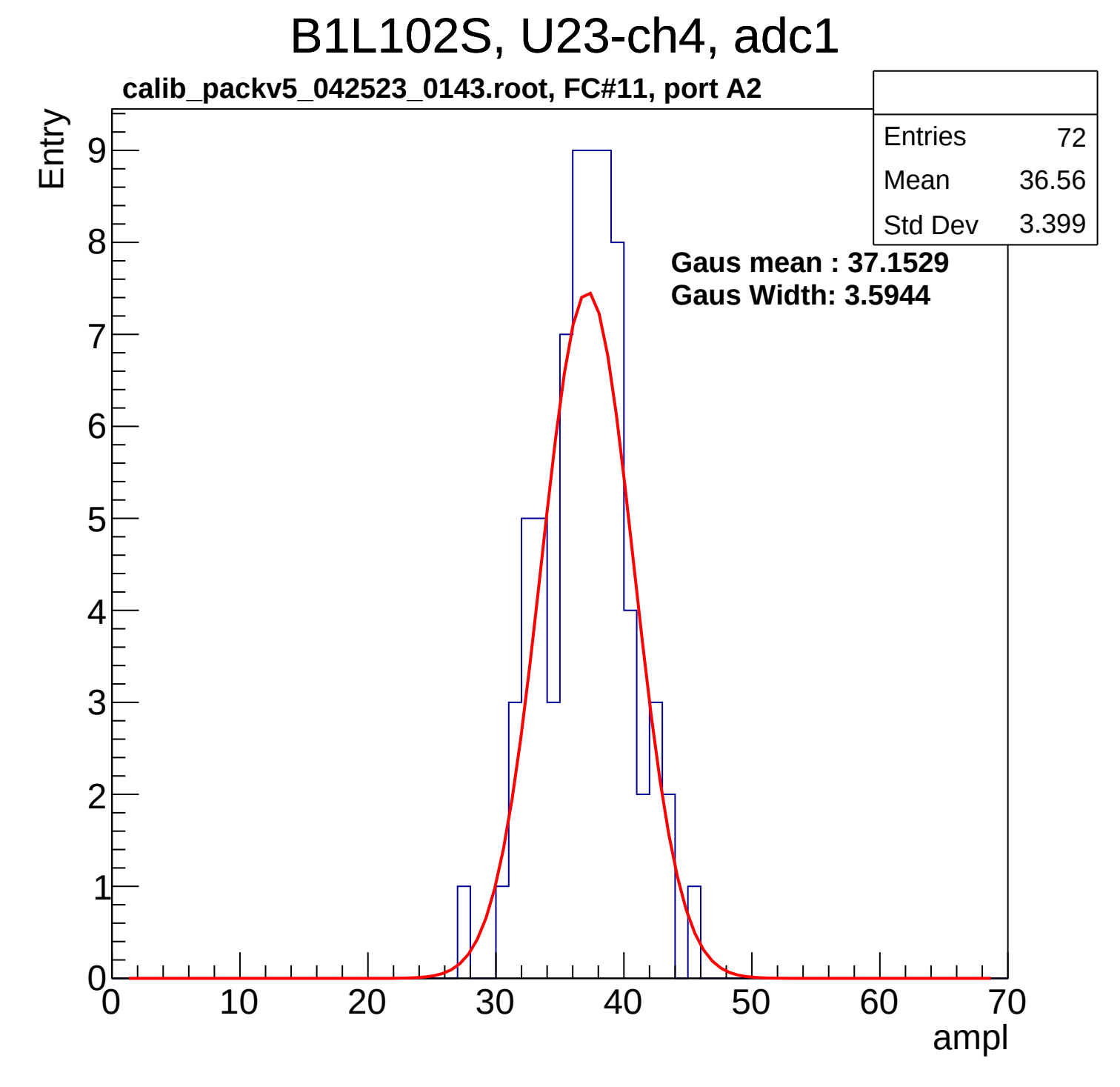

B1L102S, U23-ch3, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 56.36
Std Dev 3.448
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

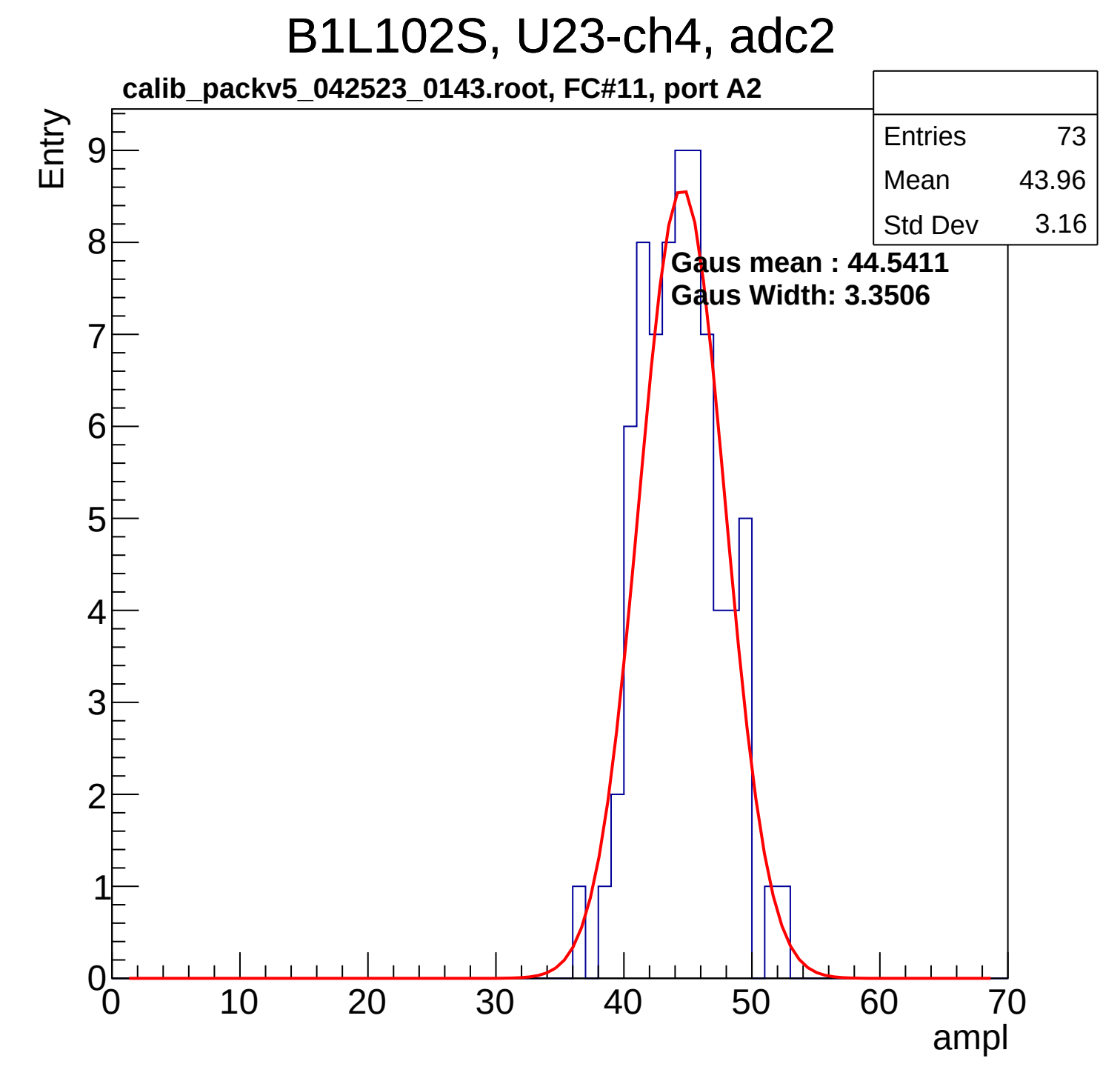

B1L102S, U23-ch3, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 34
Mean 60.71
Std Dev 2.022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

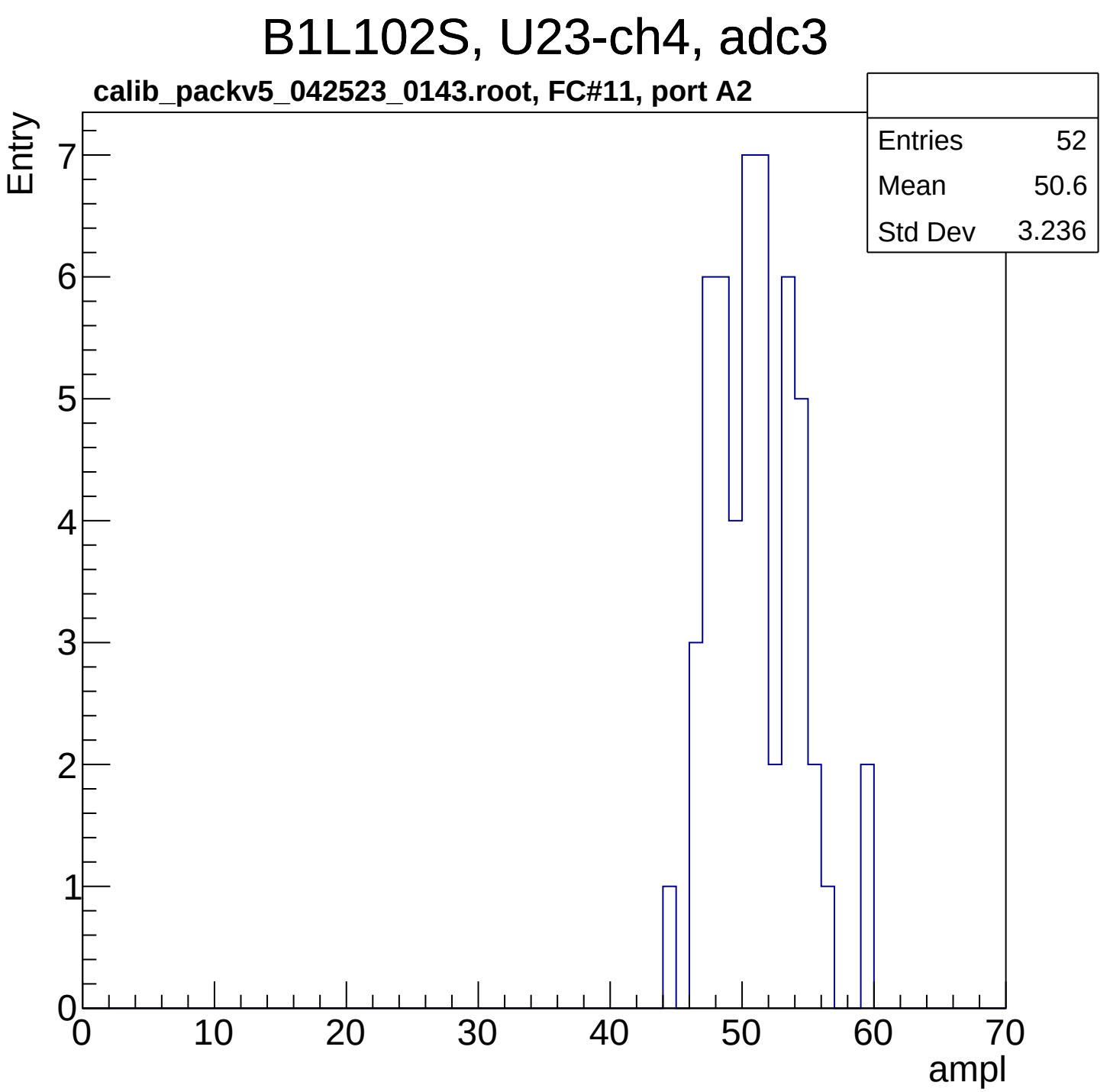

B1L102S, U23-ch3, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 40.67
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

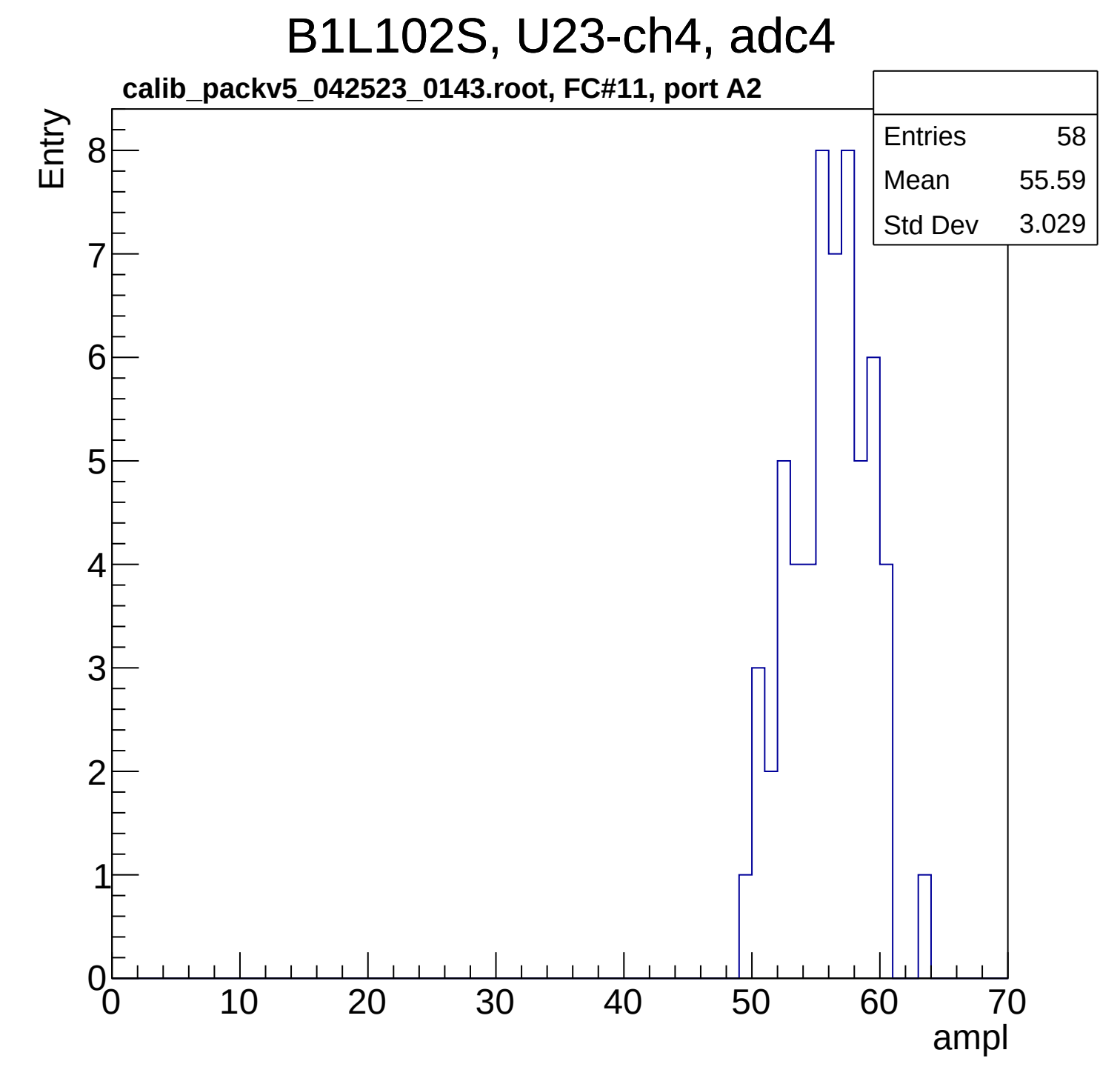

B1L102S, U23-ch3, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

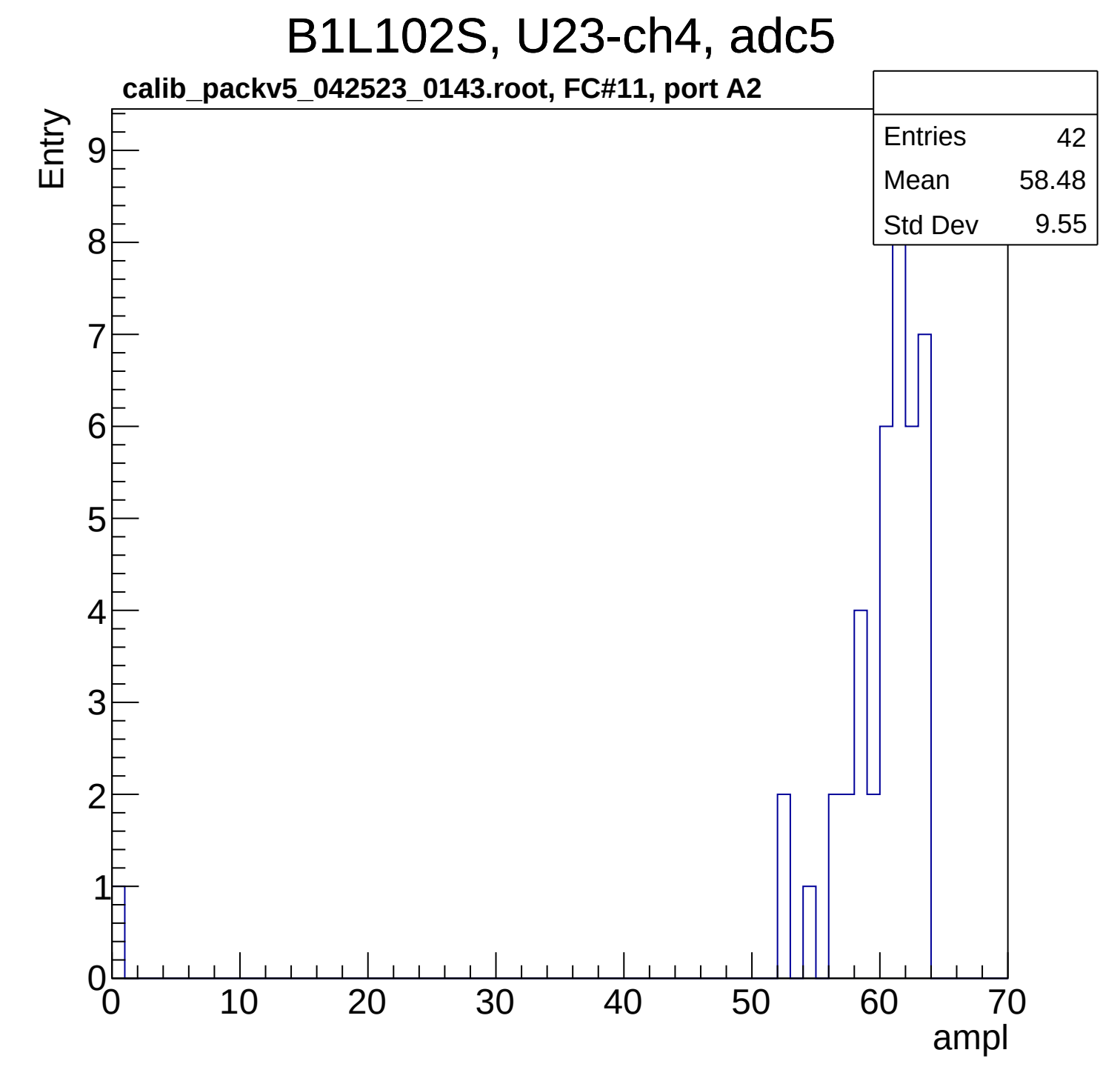

B1L102S, U23-ch4, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 29.13
Std Dev 6.189
Gaus mean : 30.9676
Gaus Width: 3.7891
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

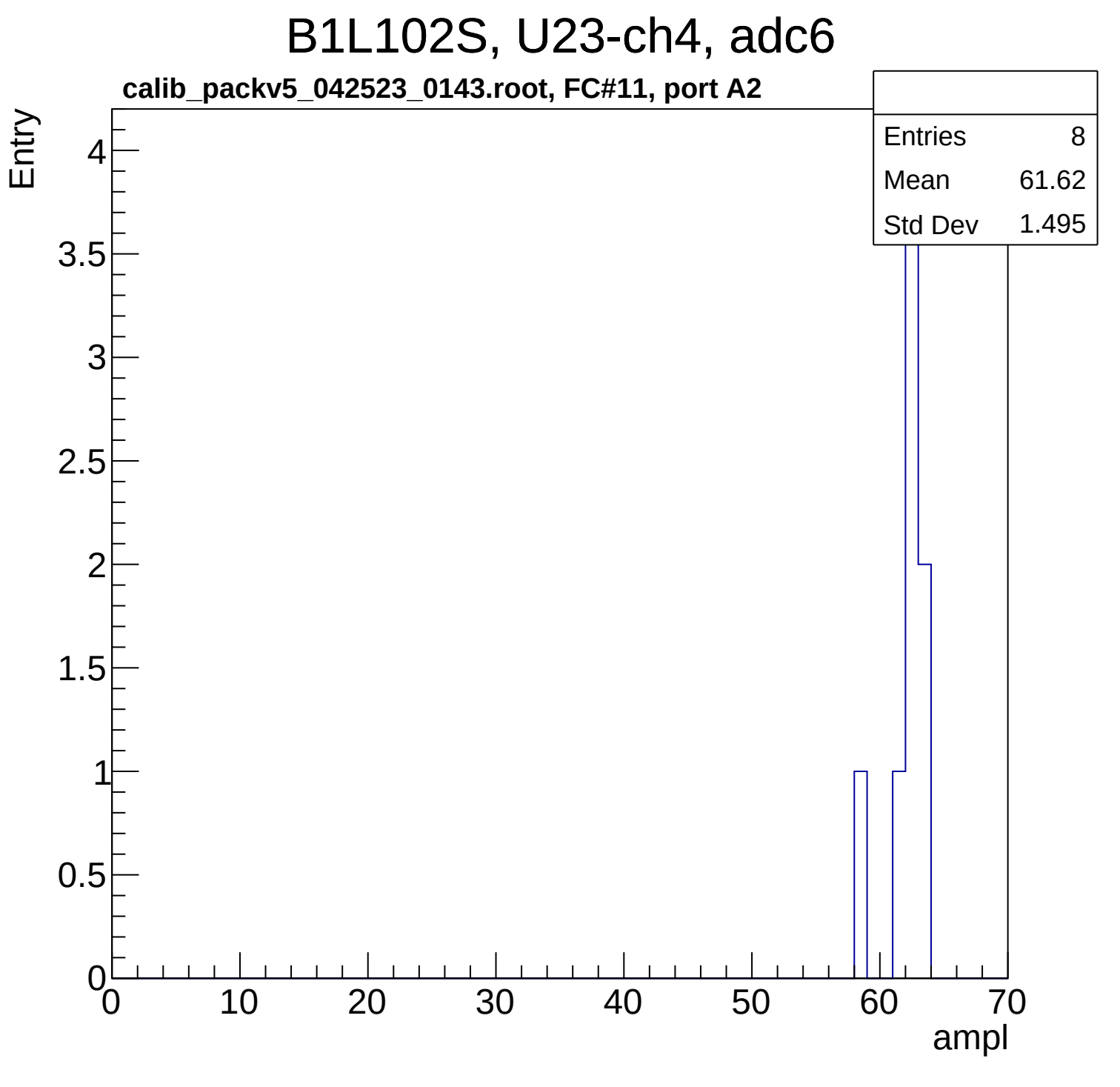

B1L102S, U23-ch4, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 36.56
Std Dev 3.399
Gaus mean : 37.1529
Gaus Width: 3.5944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

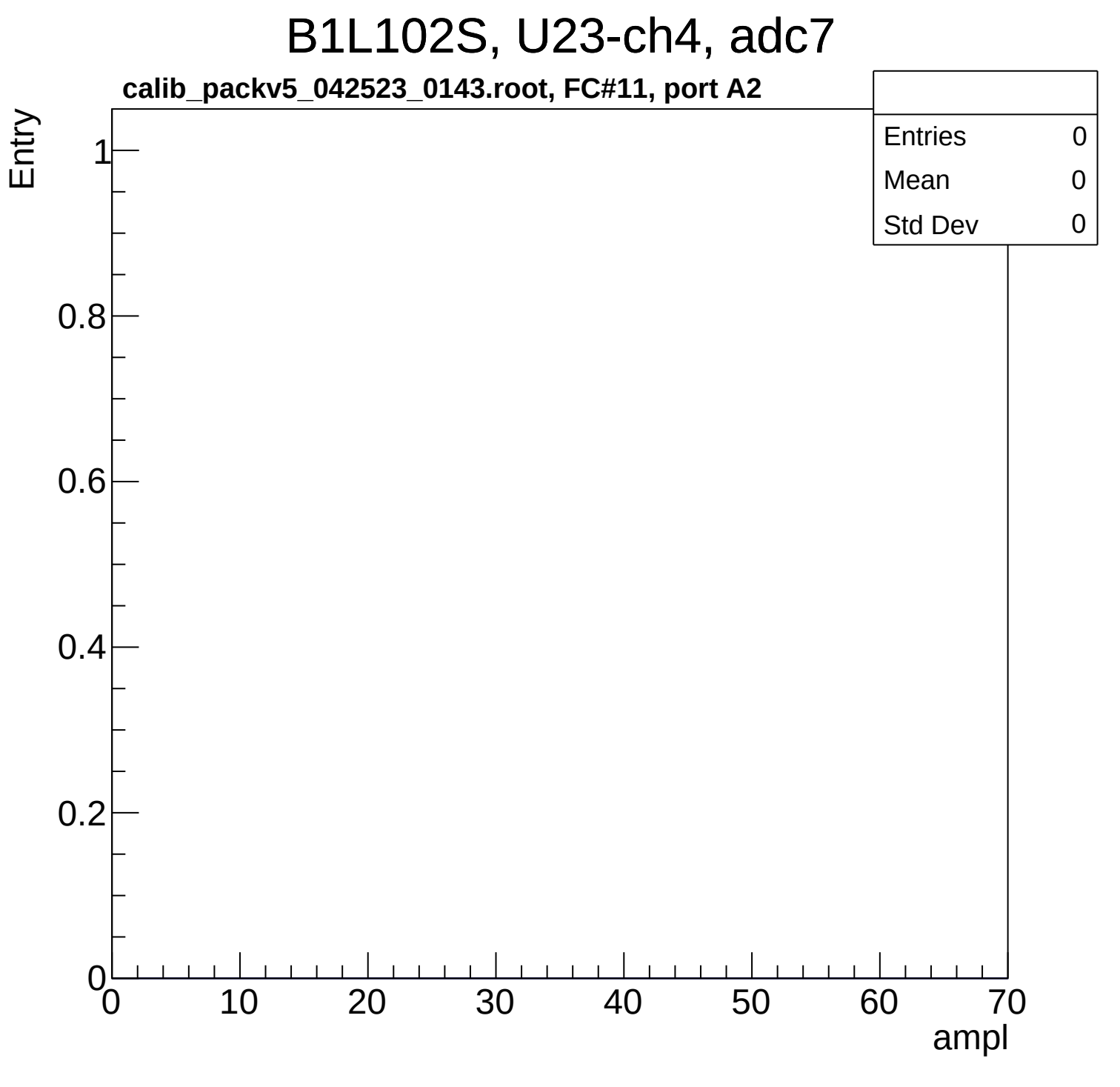

B1L102S, U23-ch4, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 43.96
Std Dev 3.16
Gaus mean : 44.5411
Gaus Width: 3.3506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

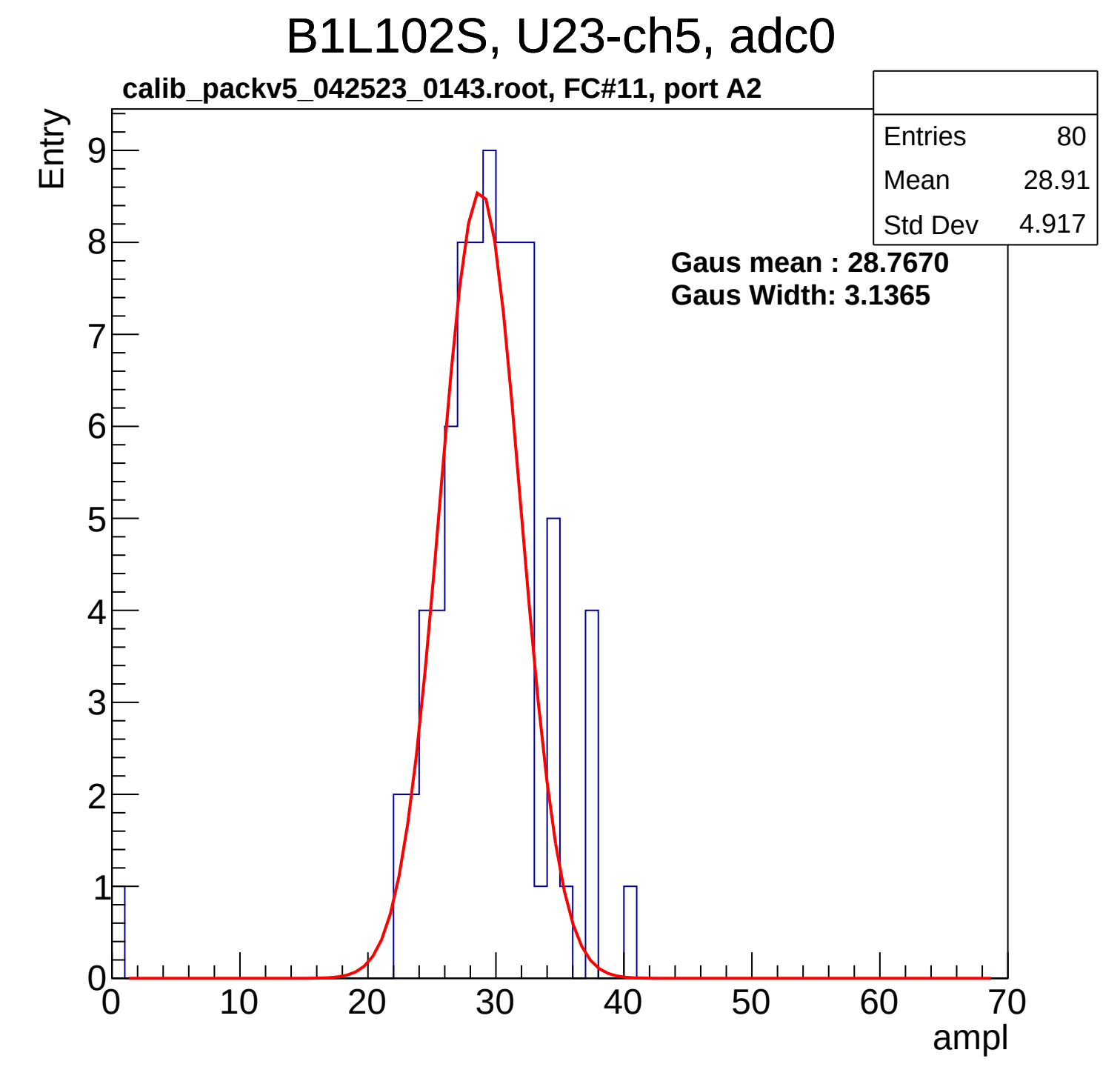

B1L102S, U23-ch4, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 50.6
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

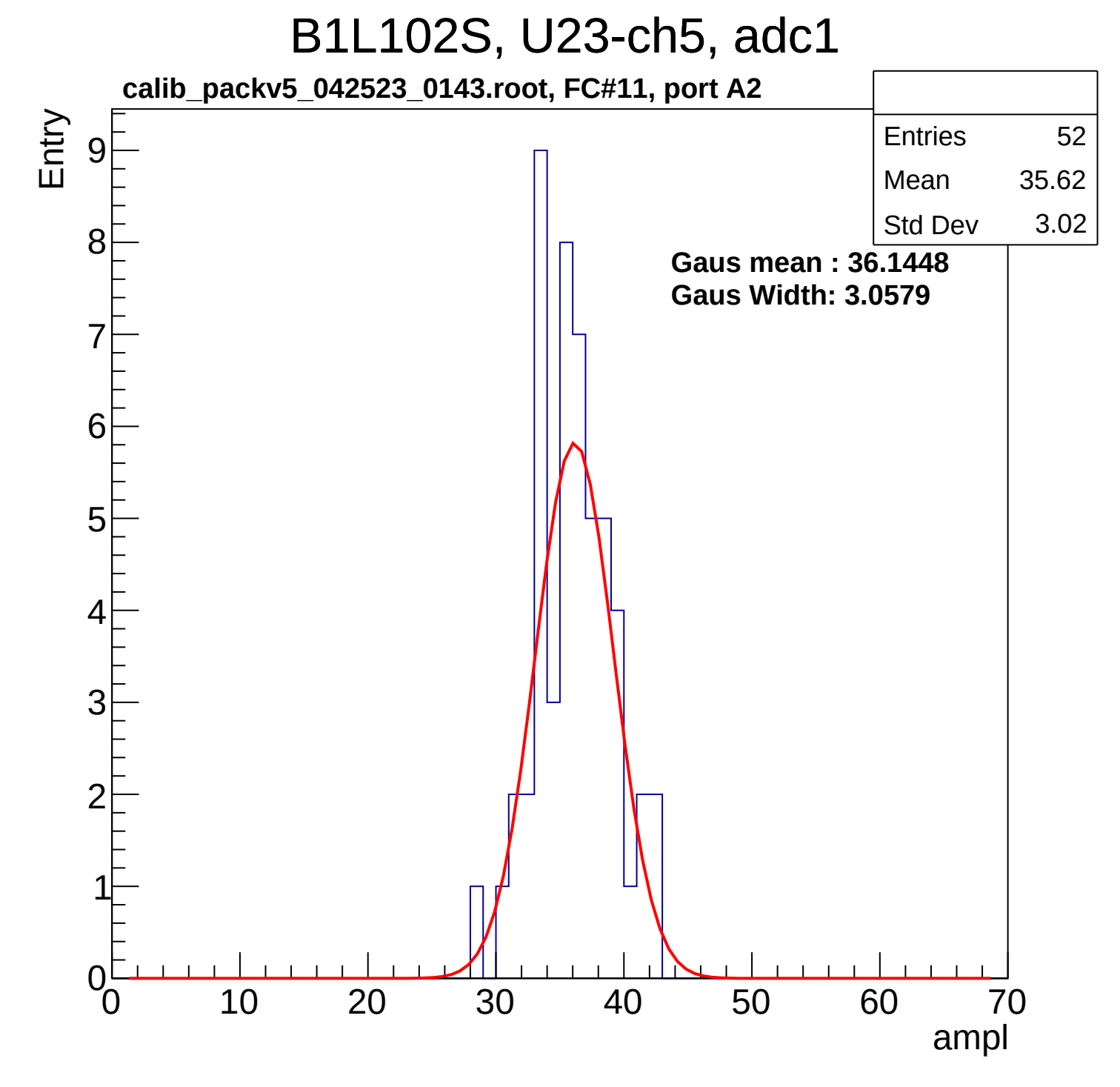

B1L102S, U23-ch4, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.59
Std Dev 3.029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

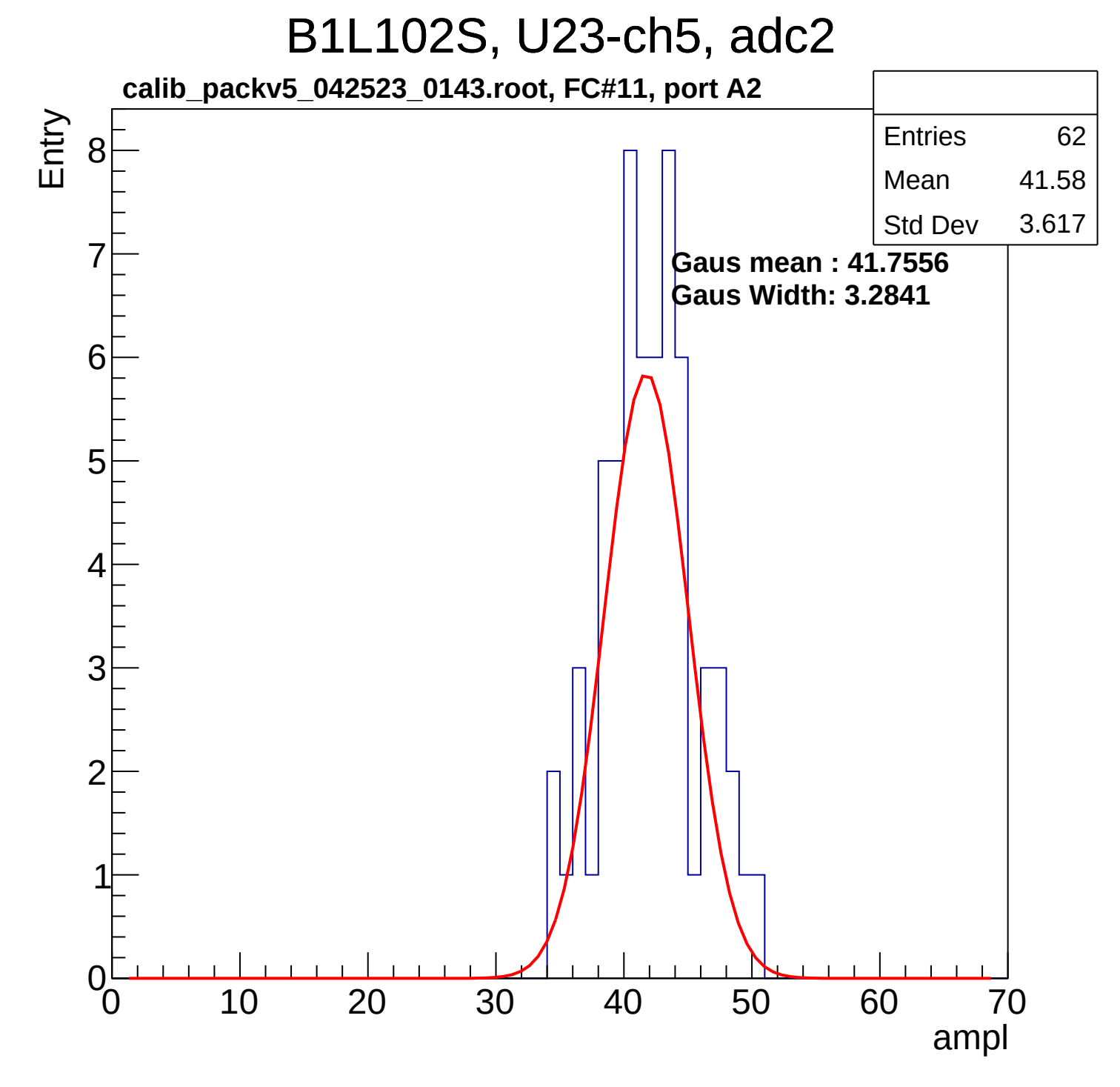

B1L102S, U23-ch4, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.48
Std Dev 9.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

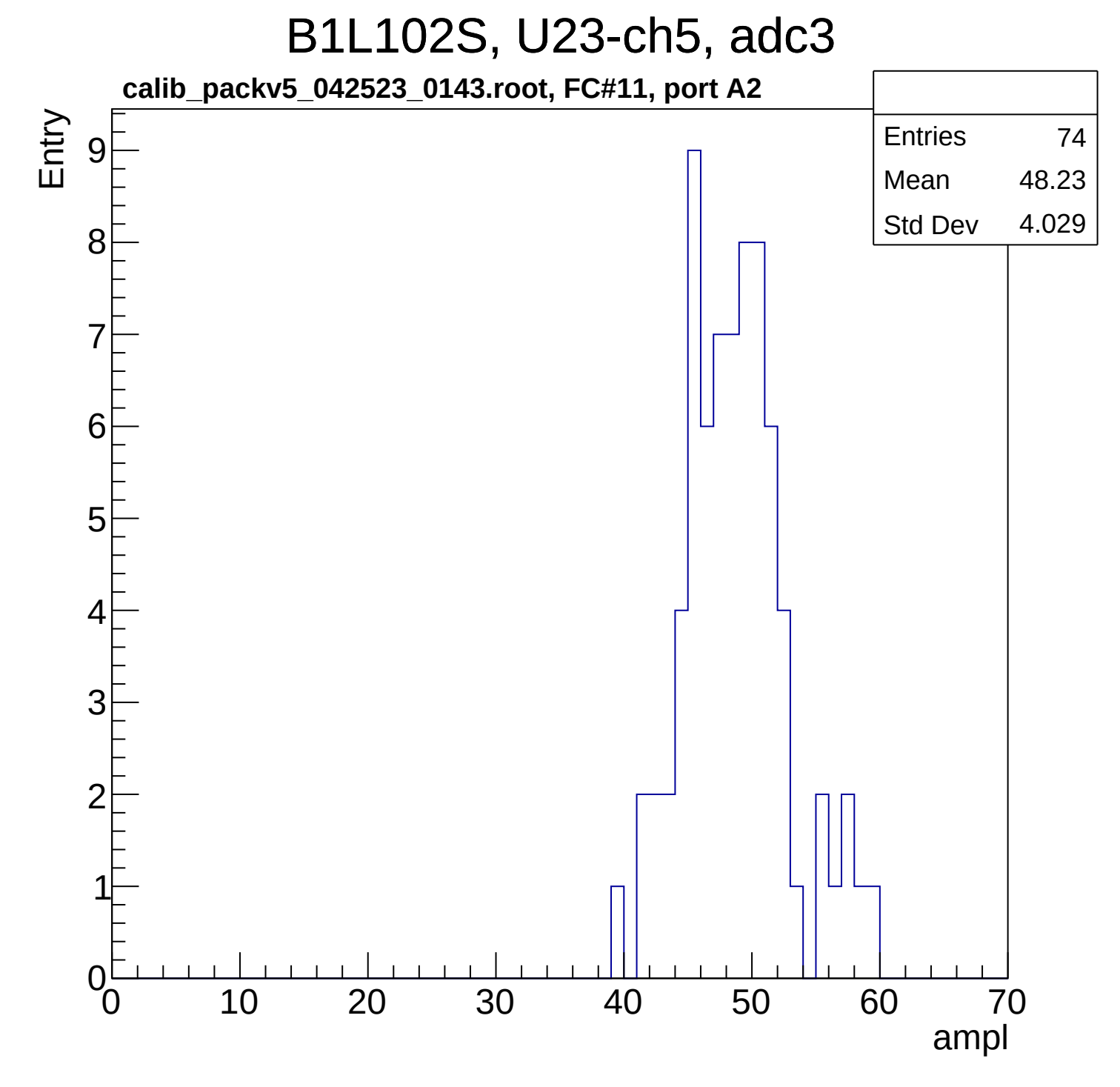

B1L102S, U23-ch4, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.62
Std Dev 1.495
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

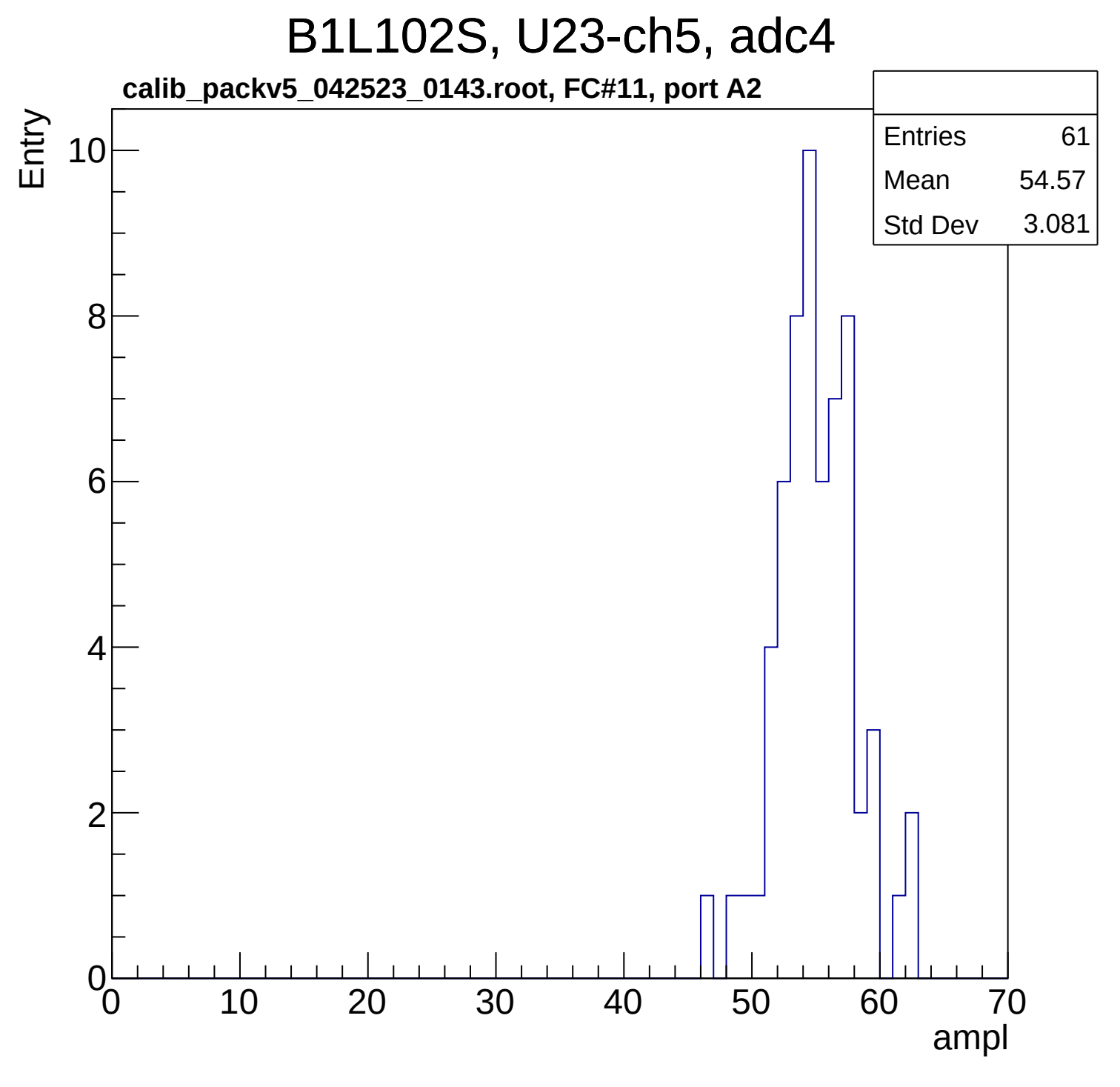

B1L102S, U23-ch4, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

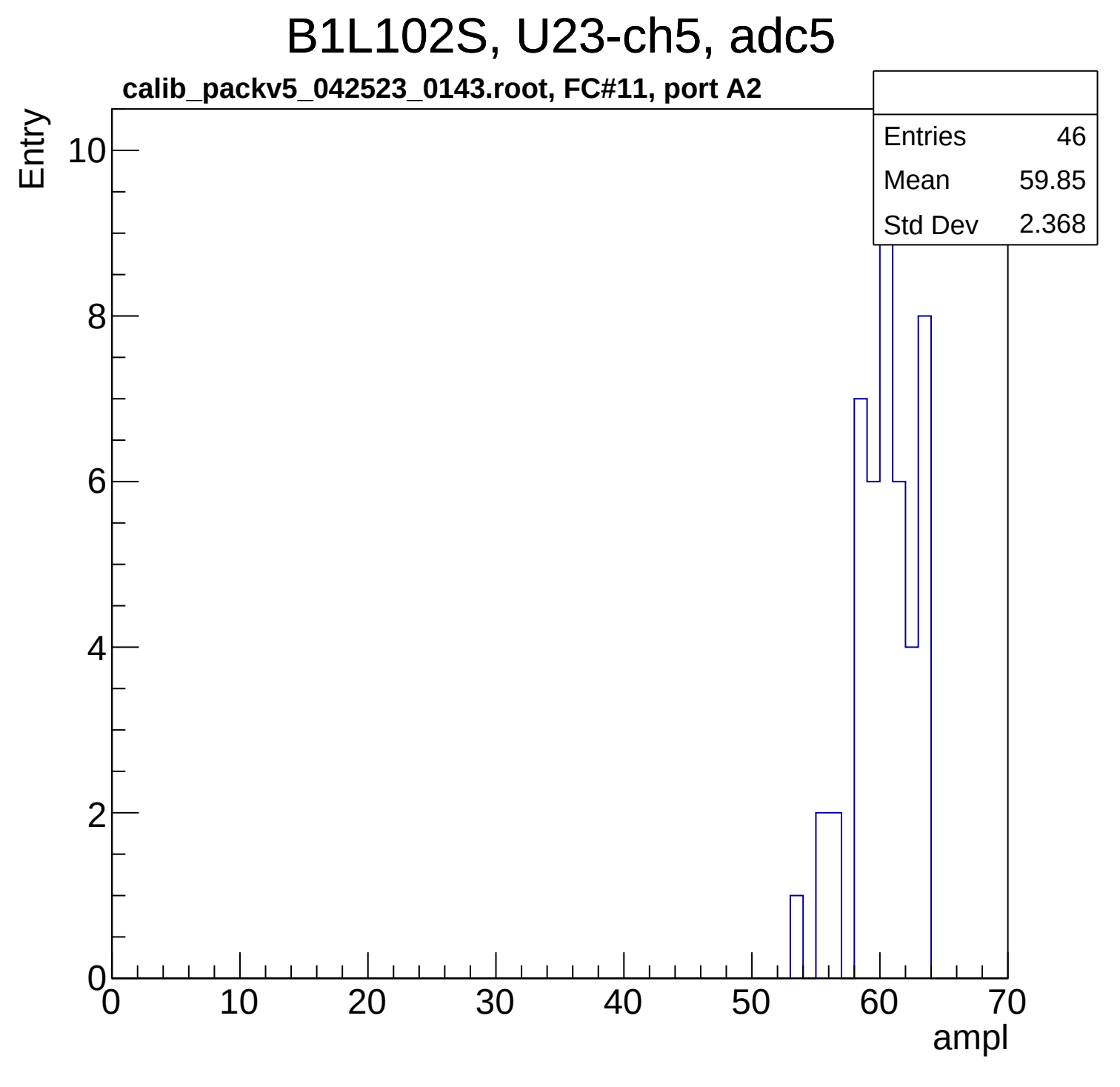

B1L102S, U23-ch5, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 28.91
Std Dev 4.917
Gaus mean : 28.7670
Gaus Width: 3.1365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

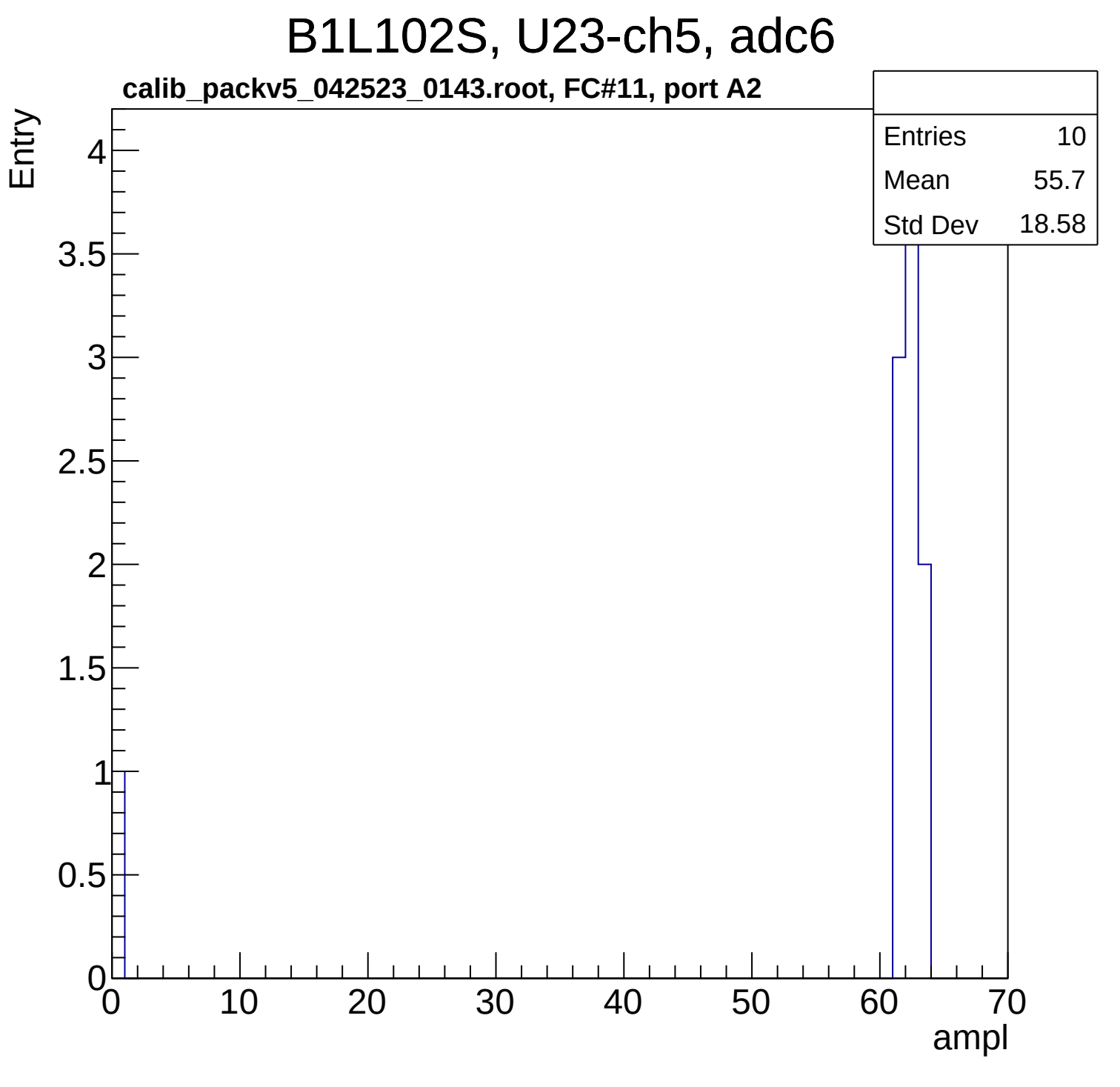

B1L102S, U23-ch5, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 35.62
Std Dev 3.02
Gaus mean : 36.1448
Gaus Width: 3.0579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

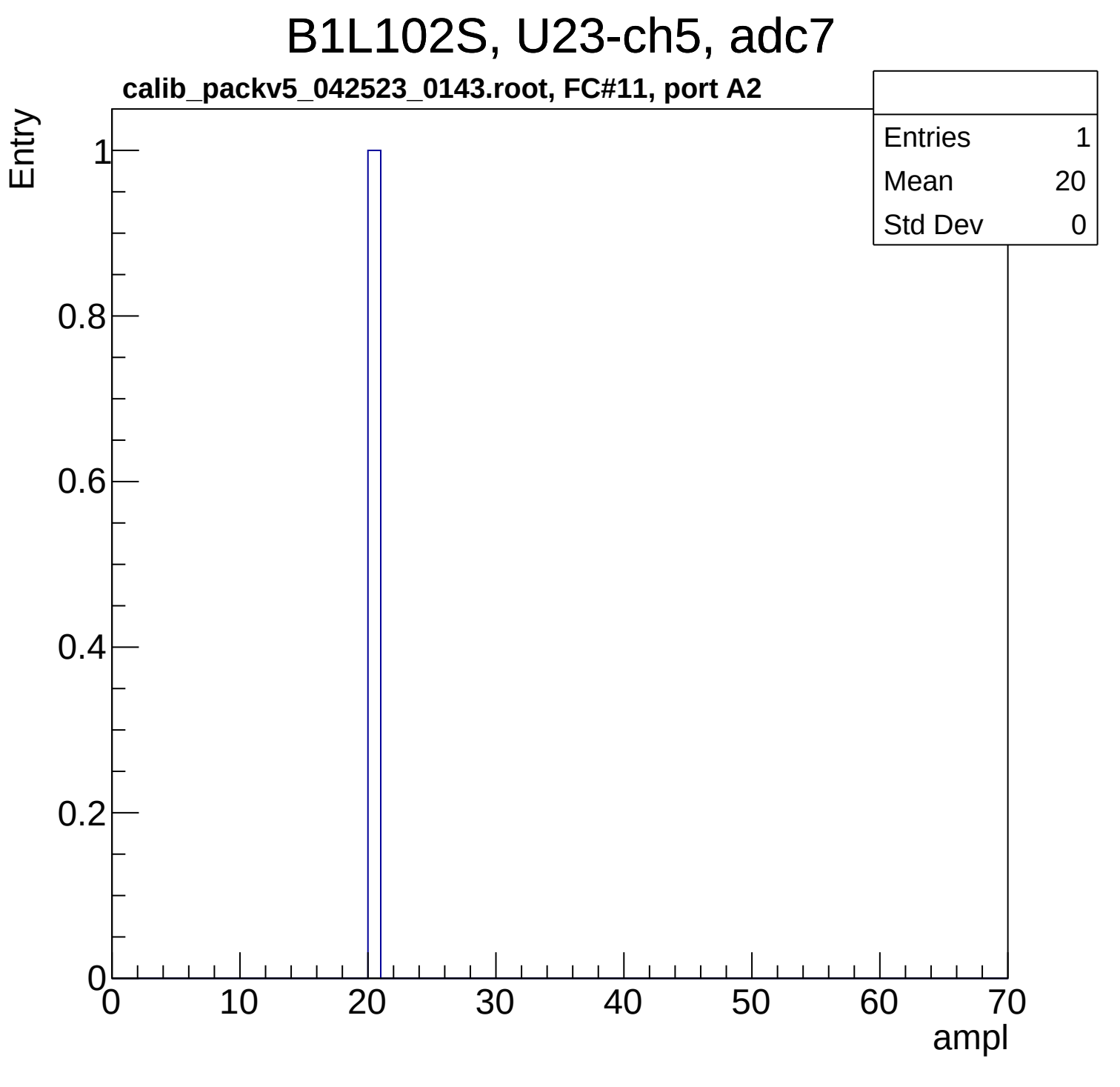

B1L102S, U23-ch5, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 41.58
Std Dev 3.617
Gaus mean : 41.7556
Gaus Width: 3.2841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

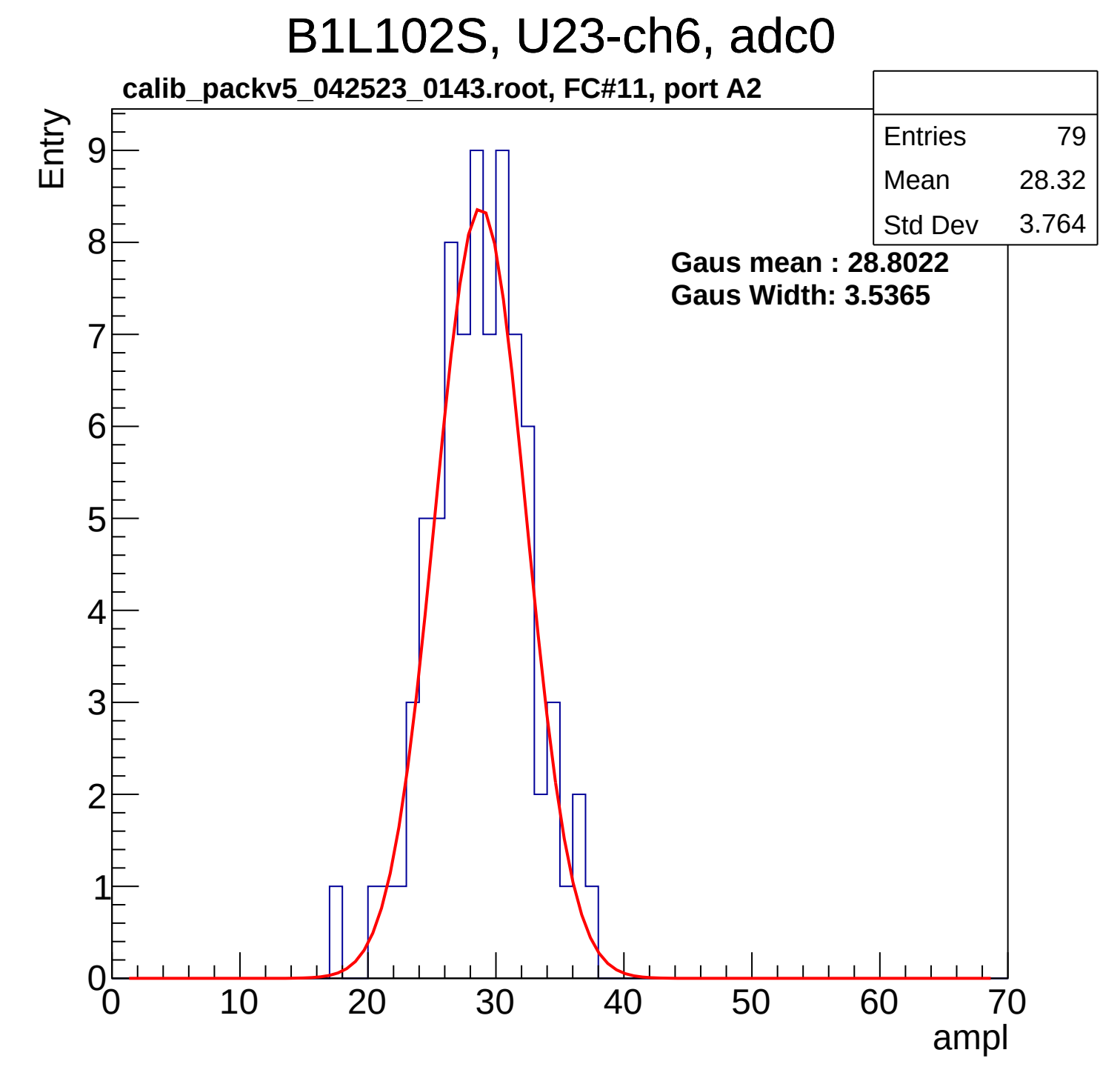

B1L102S, U23-ch5, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 48.23
Std Dev 4.029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

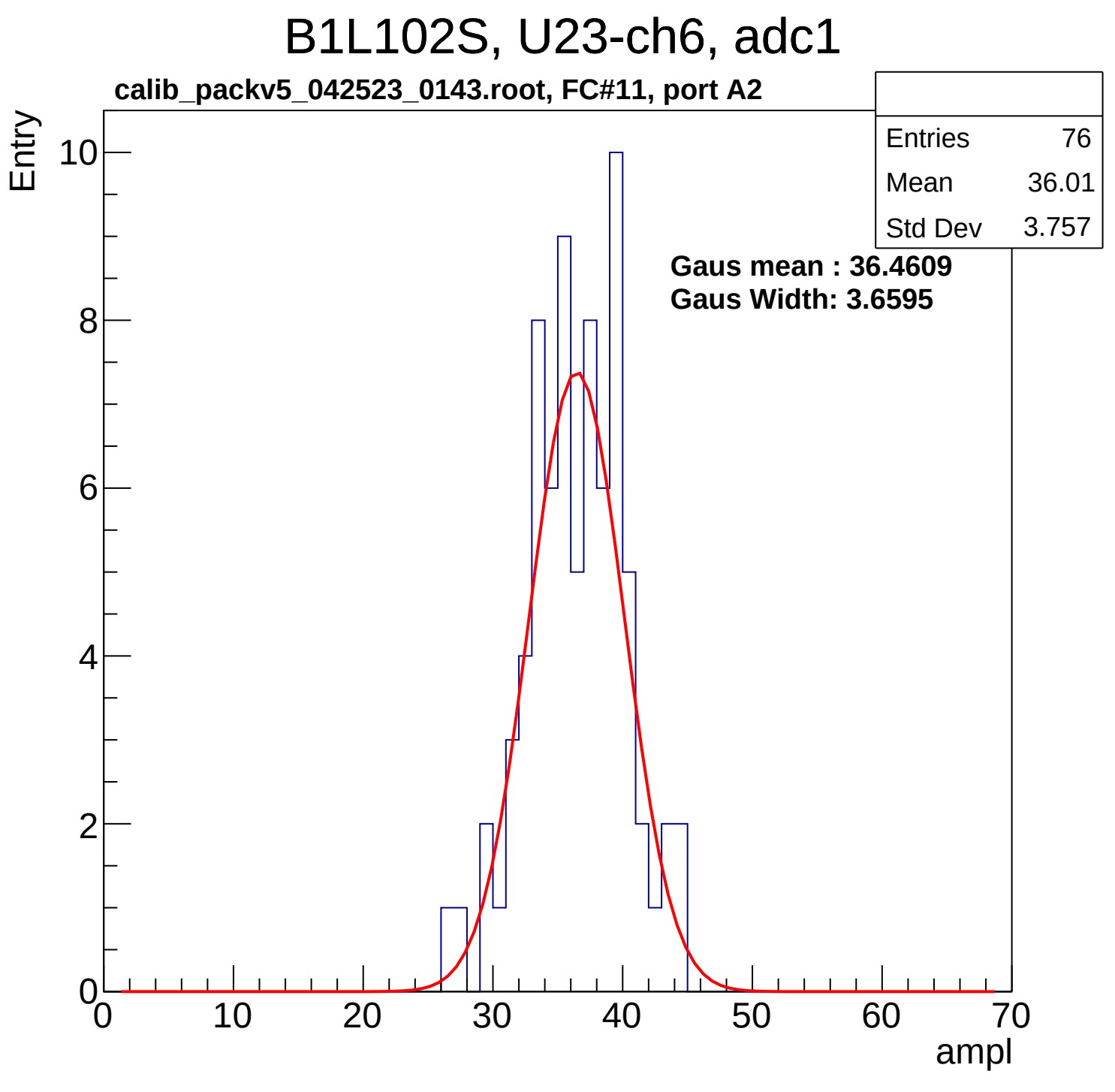

B1L102S, U23-ch5, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 54.57
Std Dev 3.081
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

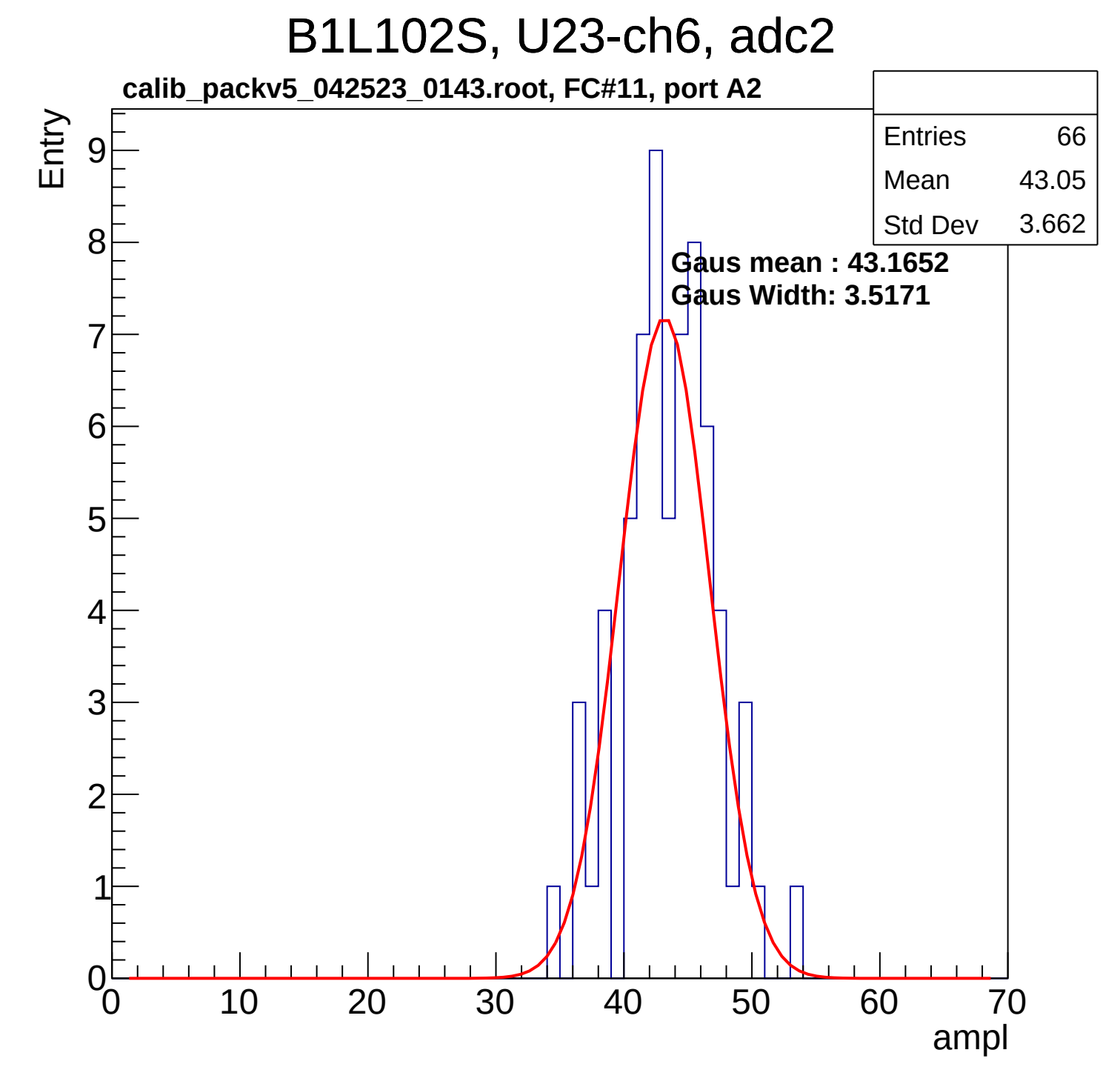

B1L102S, U23-ch5, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 59.85
Std Dev 2.368
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

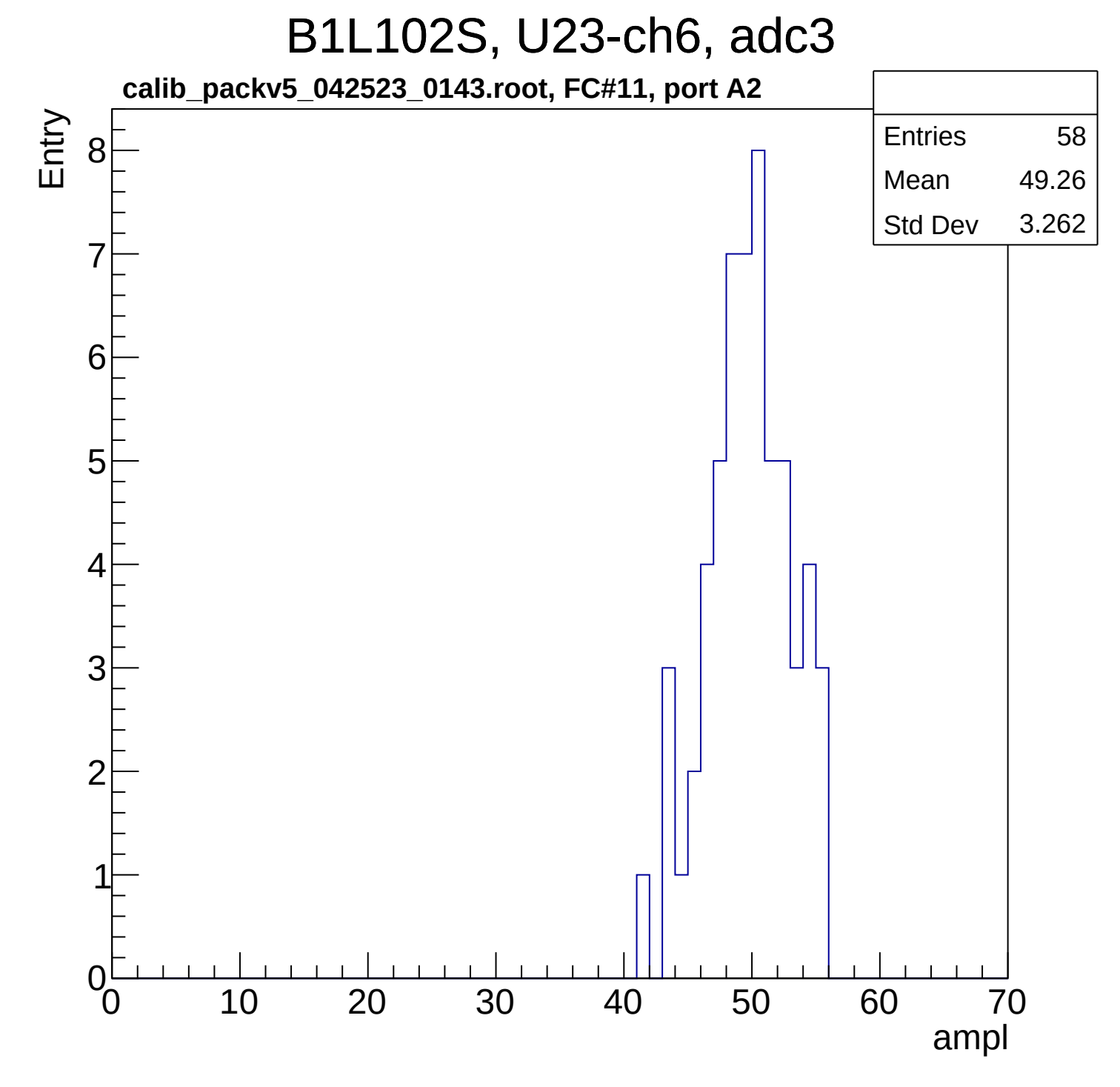

B1L102S, U23-ch5, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 55.7
Std Dev 18.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

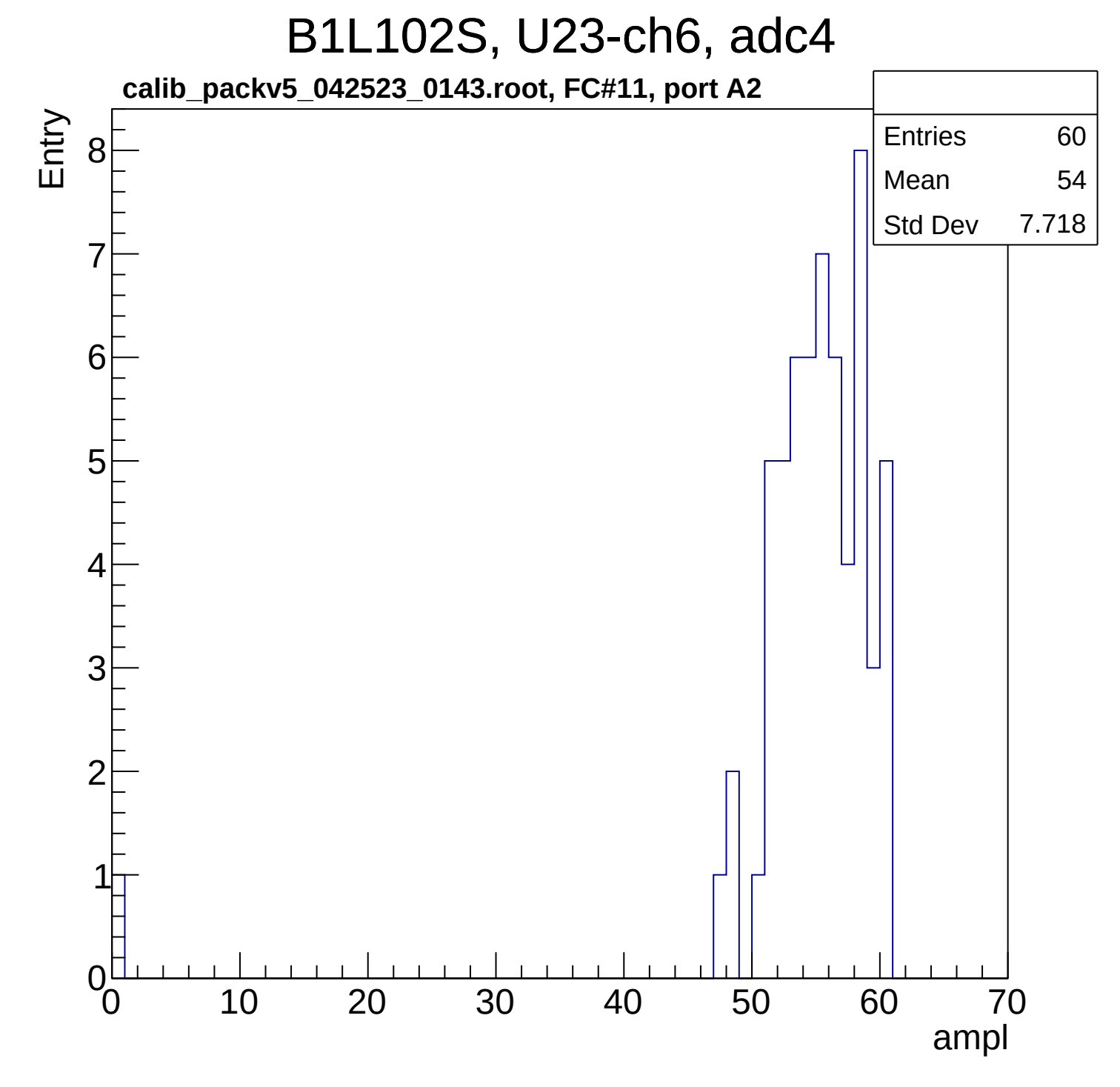

B1L102S, U23-ch5, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

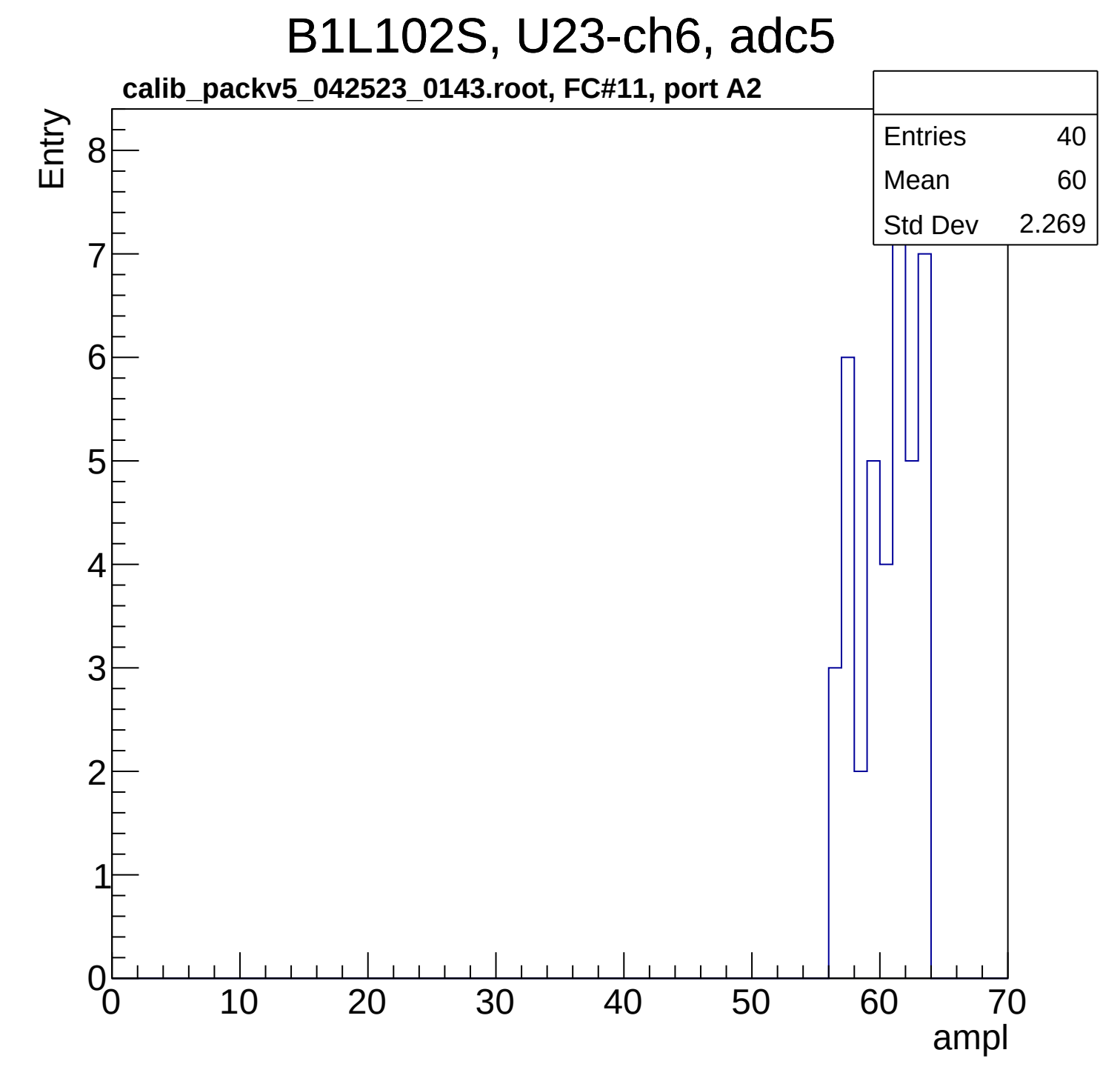

B1L102S, U23-ch6, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 28.32
Std Dev 3.764
Gaus mean : 28.8022
Gaus Width: 3.5365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

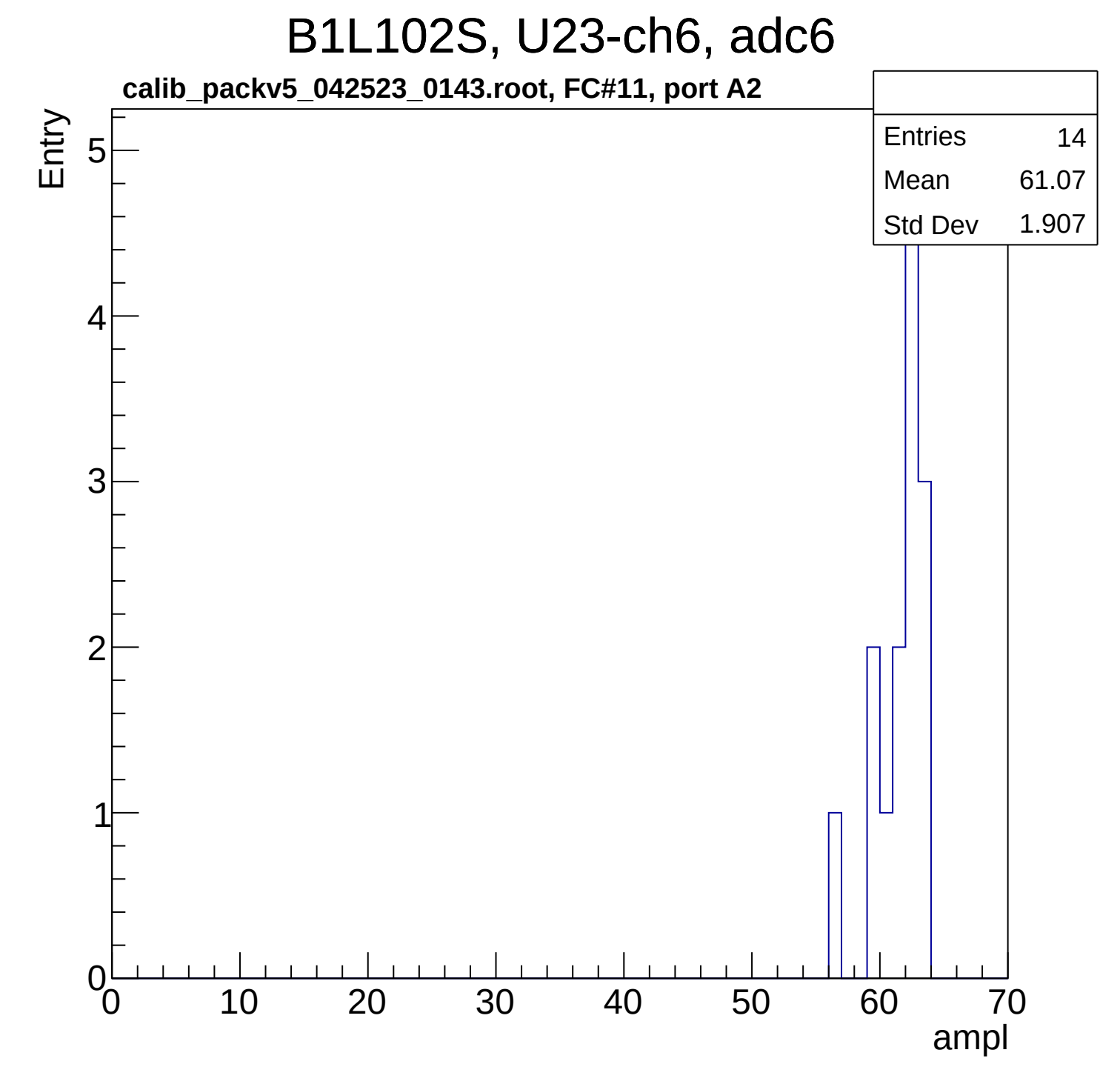

B1L102S, U23-ch6, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 36.01
Std Dev 3.757
Gaus mean : 36.4609
Gaus Width: 3.6595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

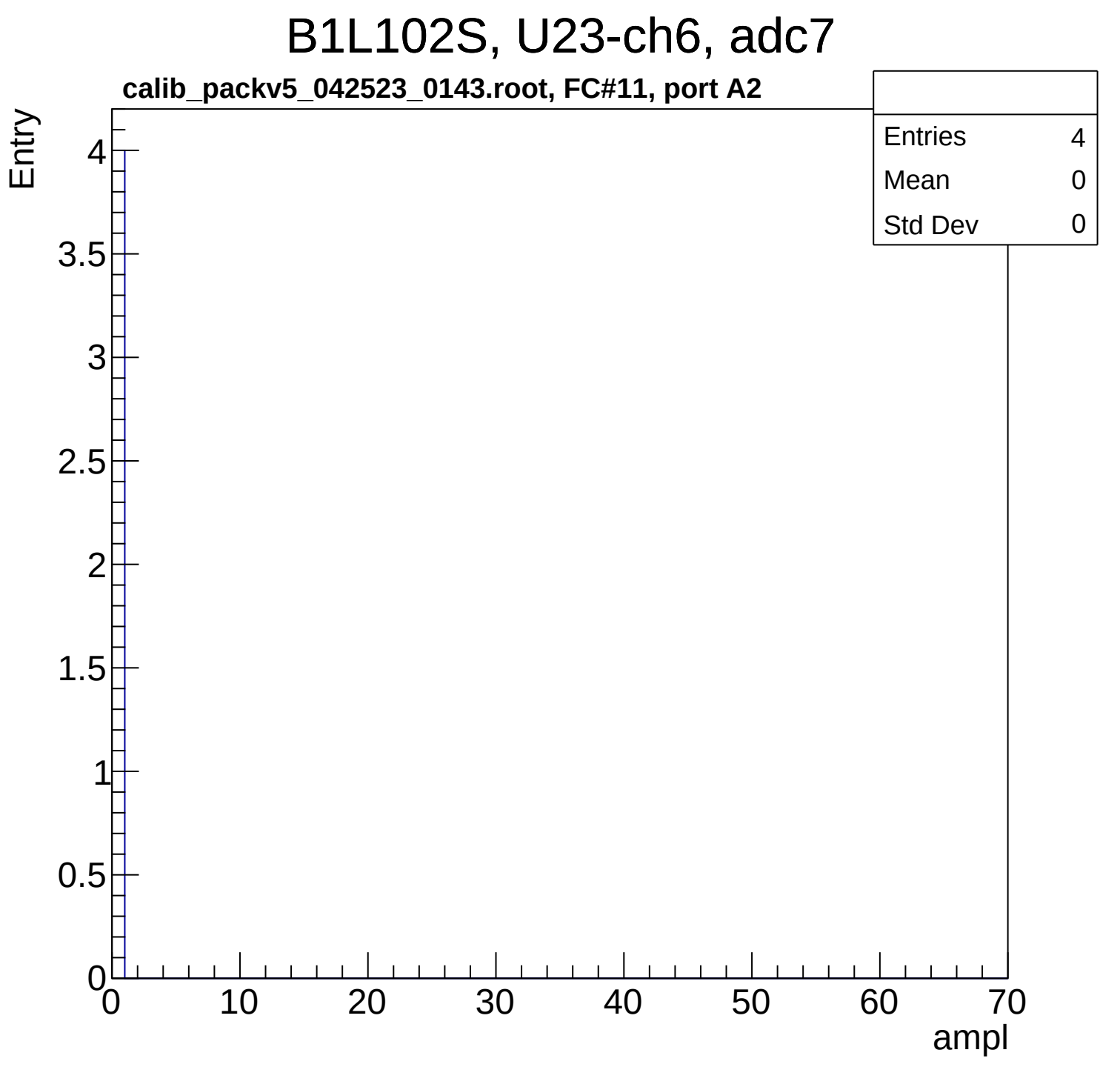

B1L102S, U23-ch6, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 43.05
Std Dev 3.662
Gaus mean : 43.1652
Gaus Width: 3.5171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

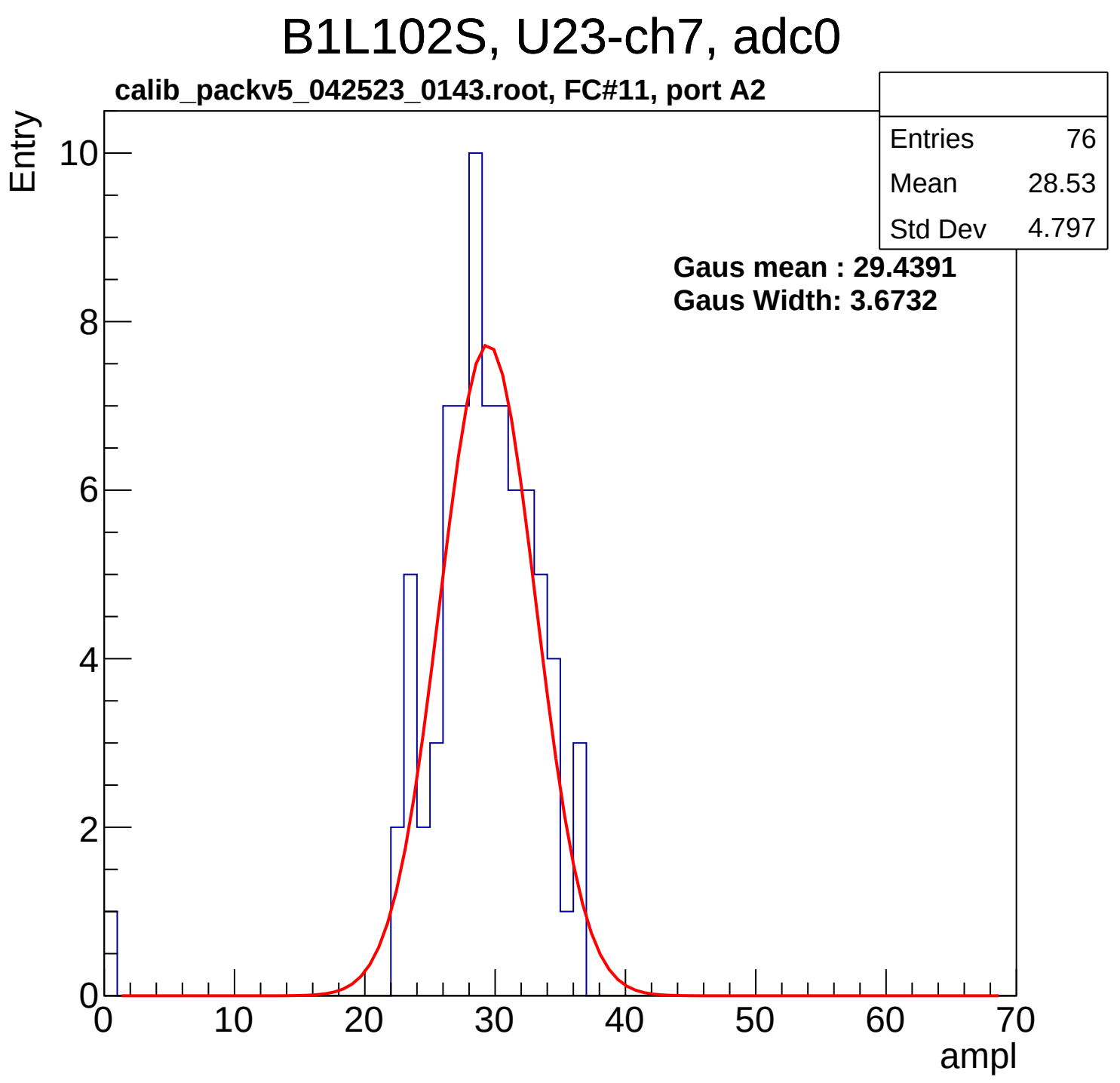

B1L102S, U23-ch6, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 49.26
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

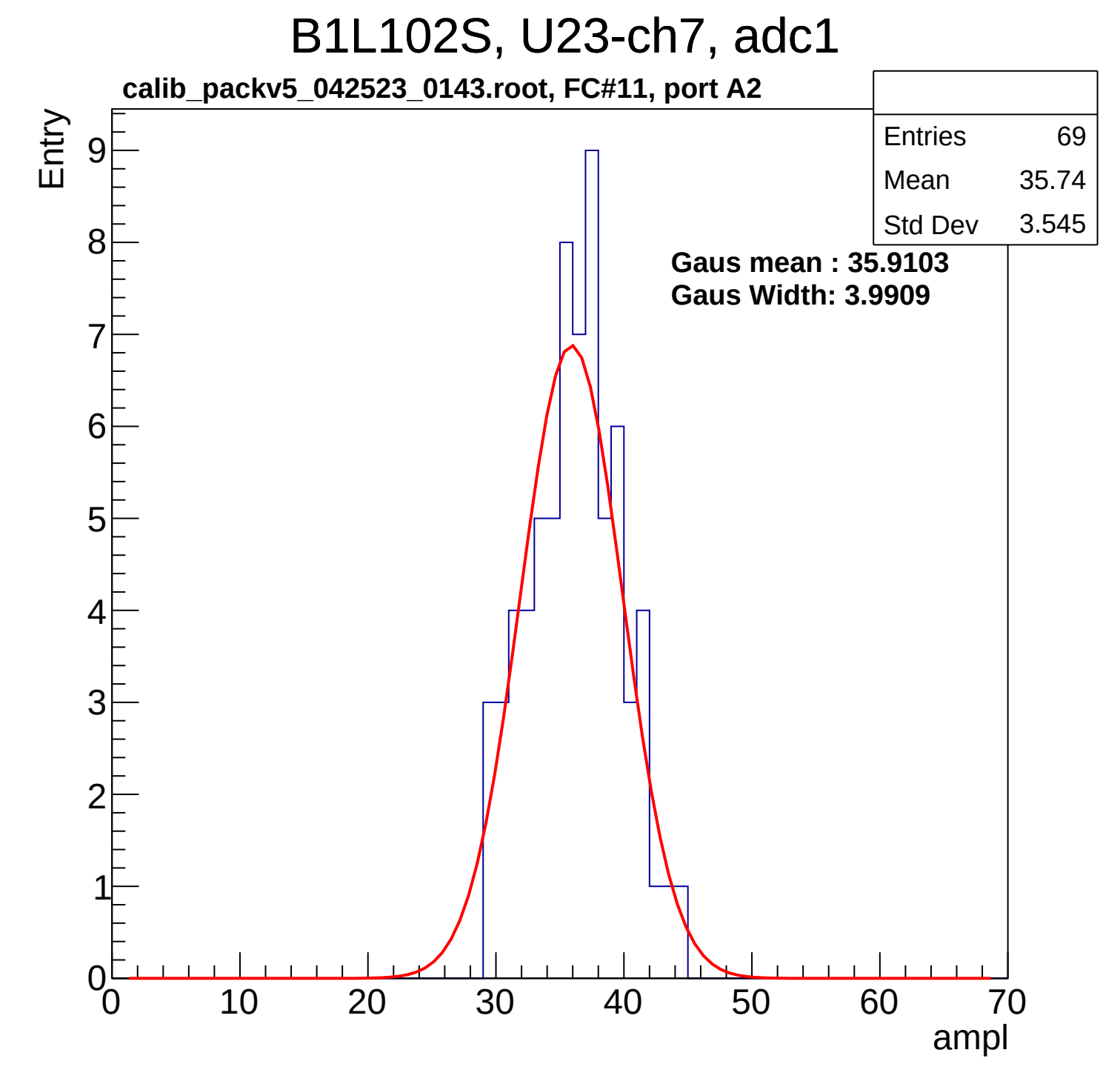

B1L102S, U23-ch6, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 54
Std Dev 7.718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

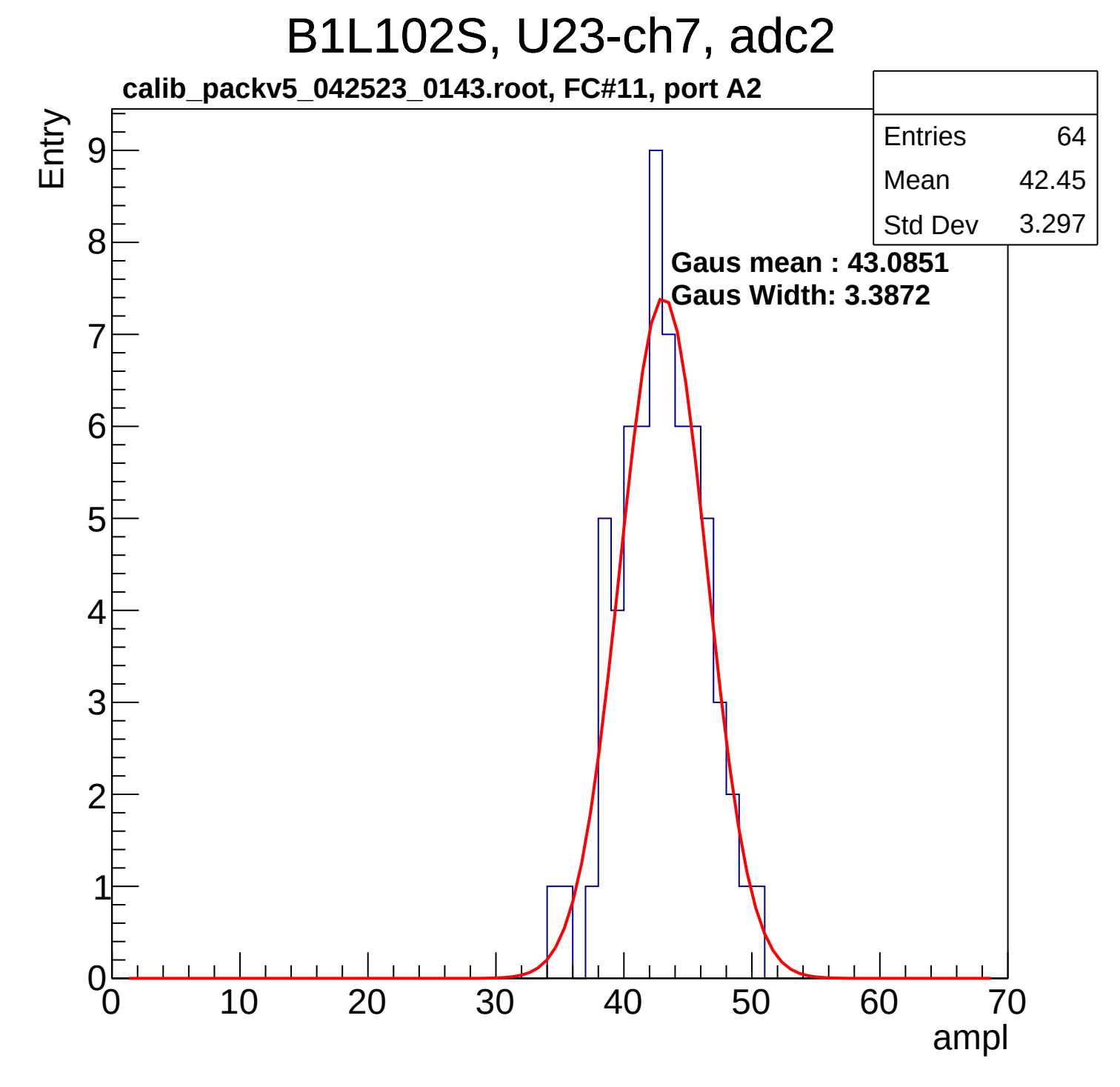

B1L102S, U23-ch6, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 60
Std Dev 2.269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

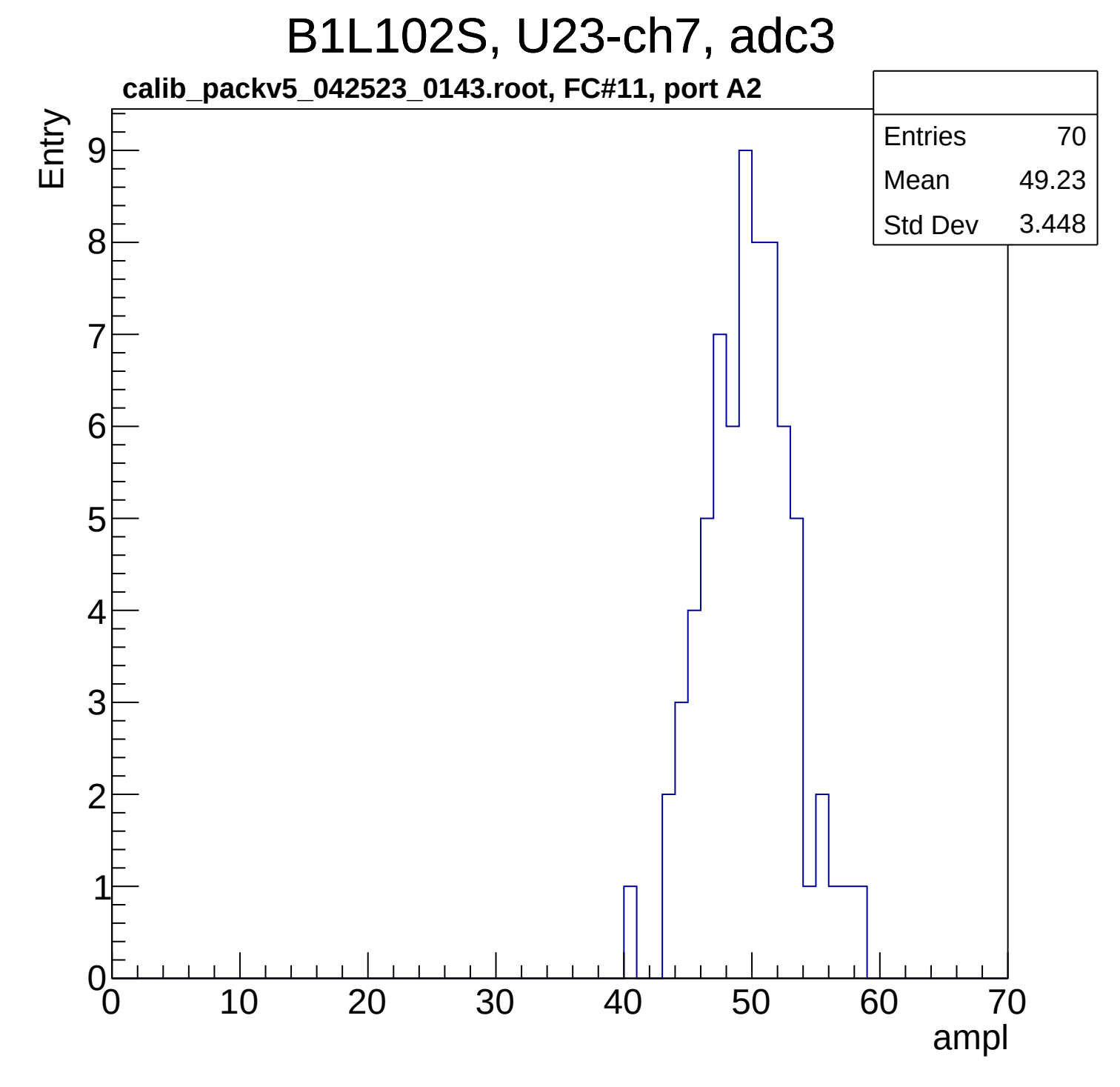

B1L102S, U23-ch6, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61.07
Std Dev 1.907
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

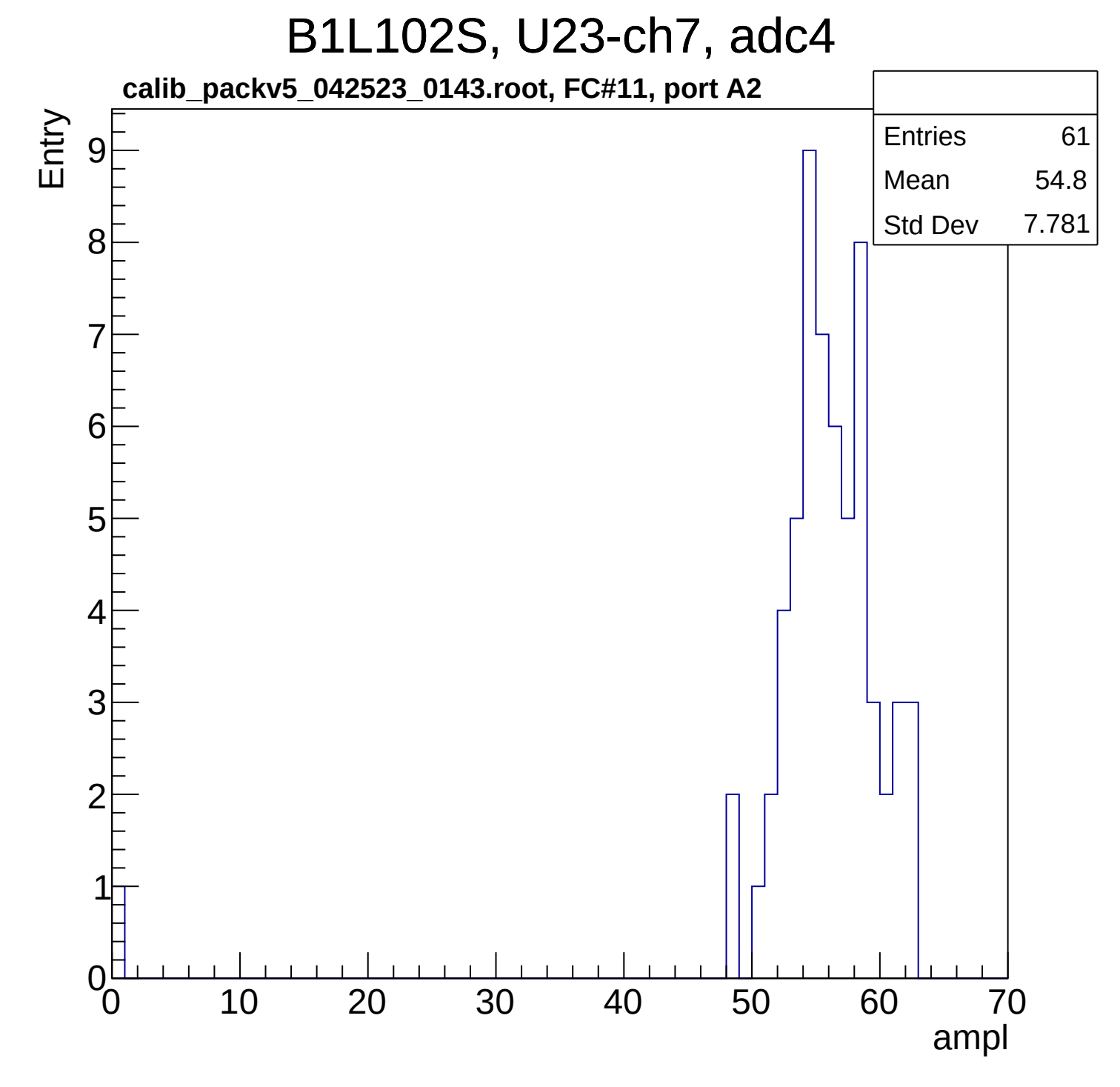

B1L102S, U23-ch6, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

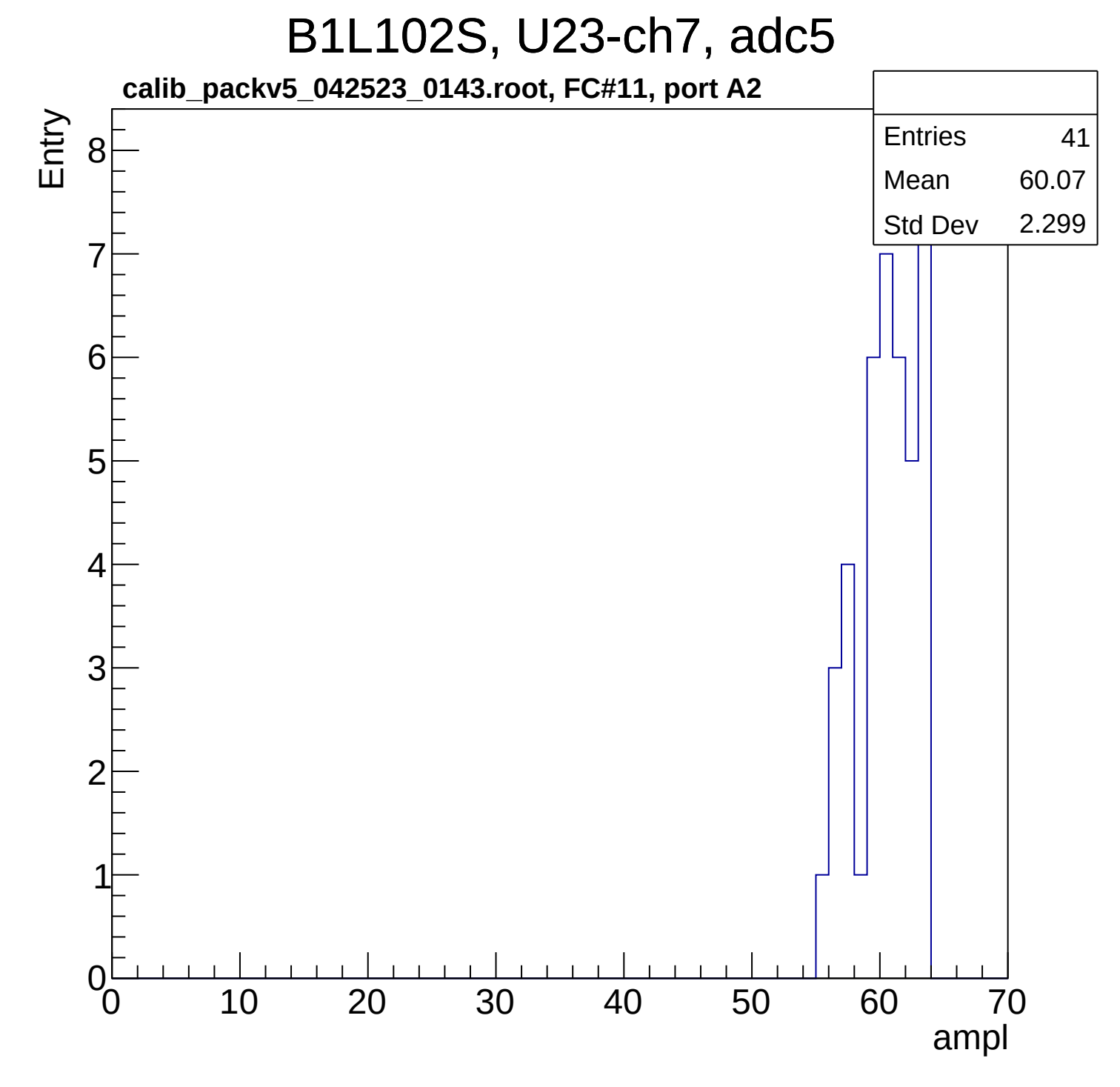

B1L102S, U23-ch7, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 28.53
Std Dev 4.797
Gaus mean : 29.4391
Gaus Width: 3.6732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

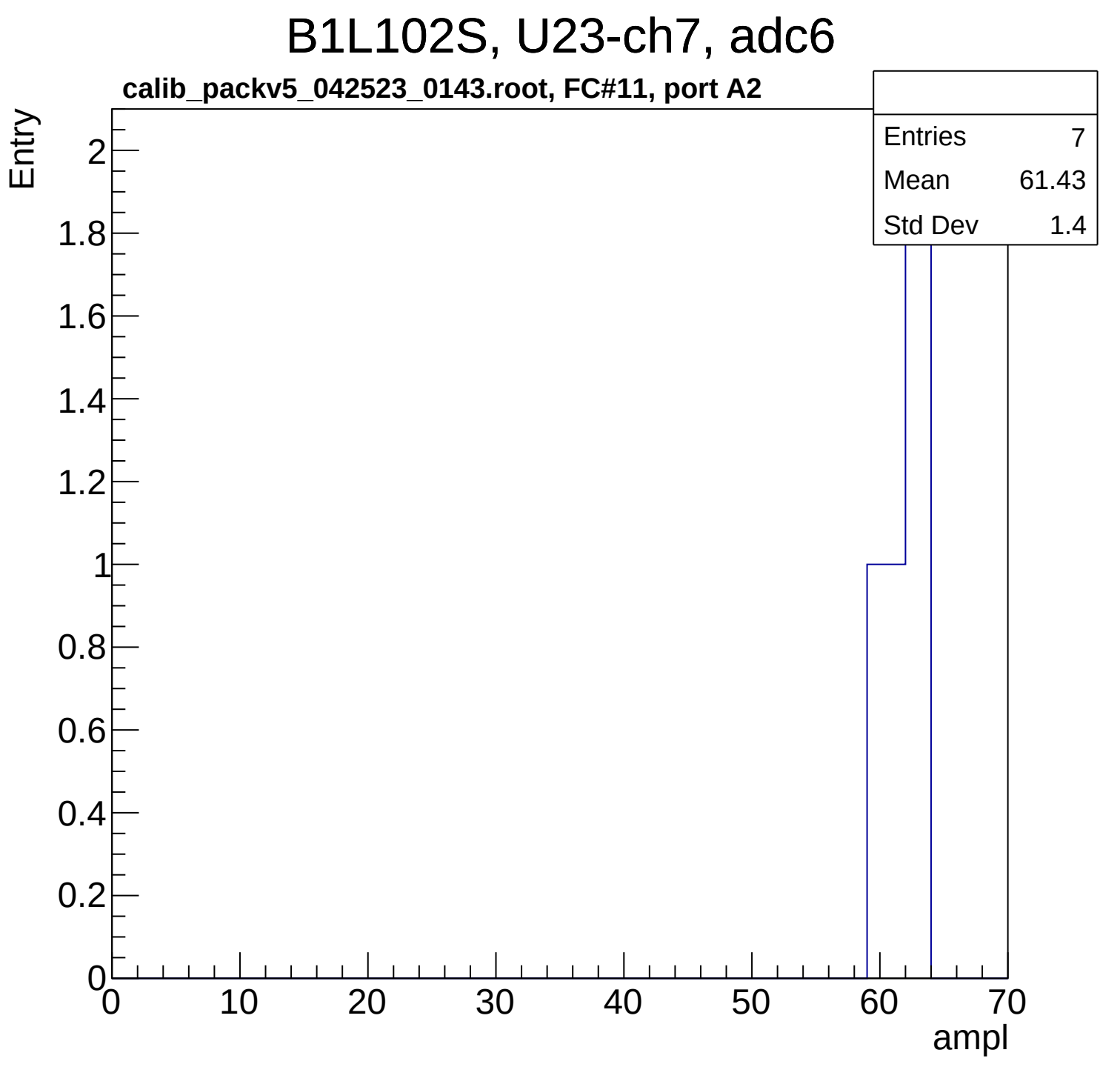

B1L102S, U23-ch7, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 35.74
Std Dev 3.545
Gaus mean : 35.9103
Gaus Width: 3.9909
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

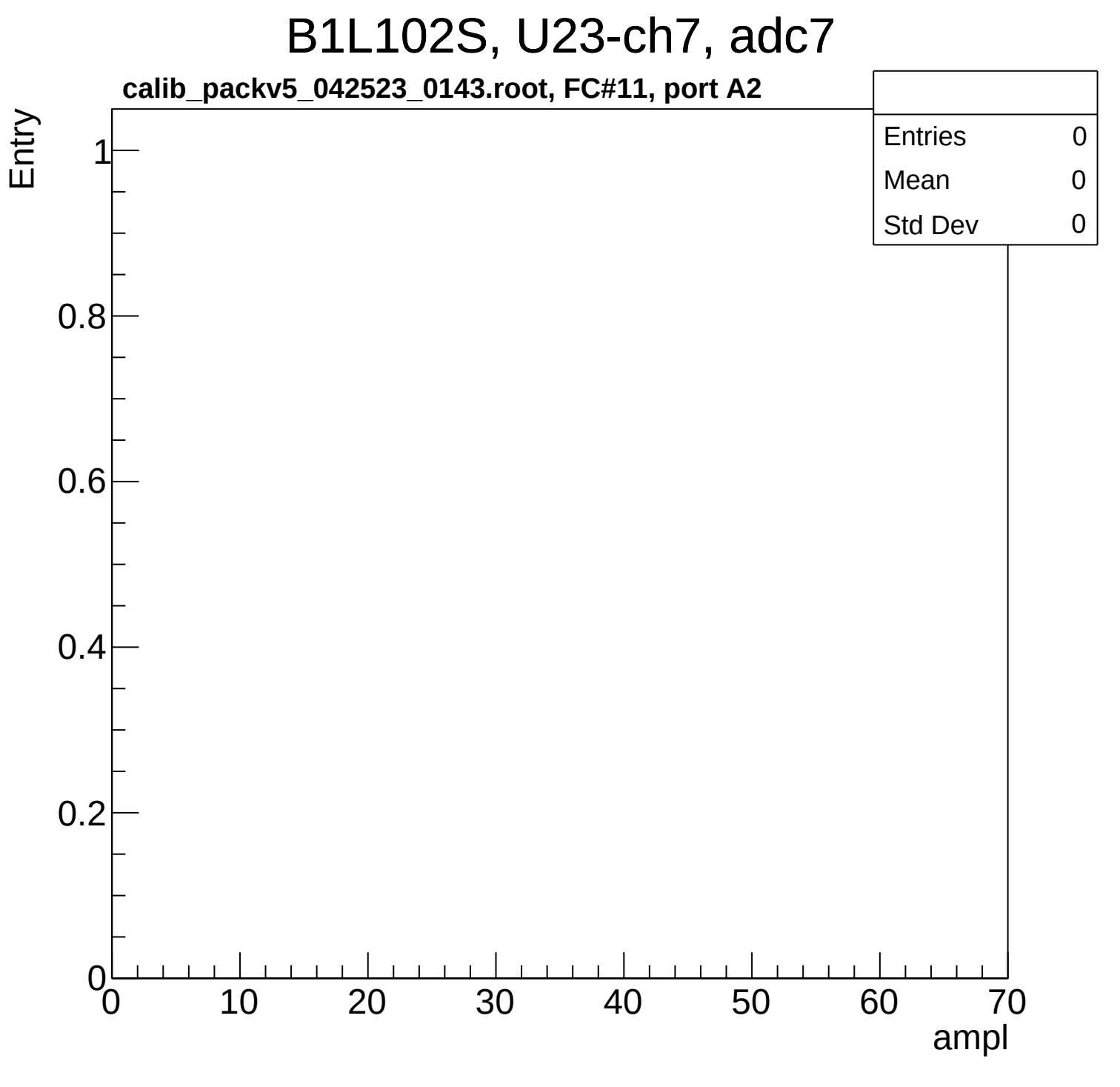

B1L102S, U23-ch7, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 42.45
Std Dev 3.297
Gaus mean : 43.0851
Gaus Width: 3.3872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

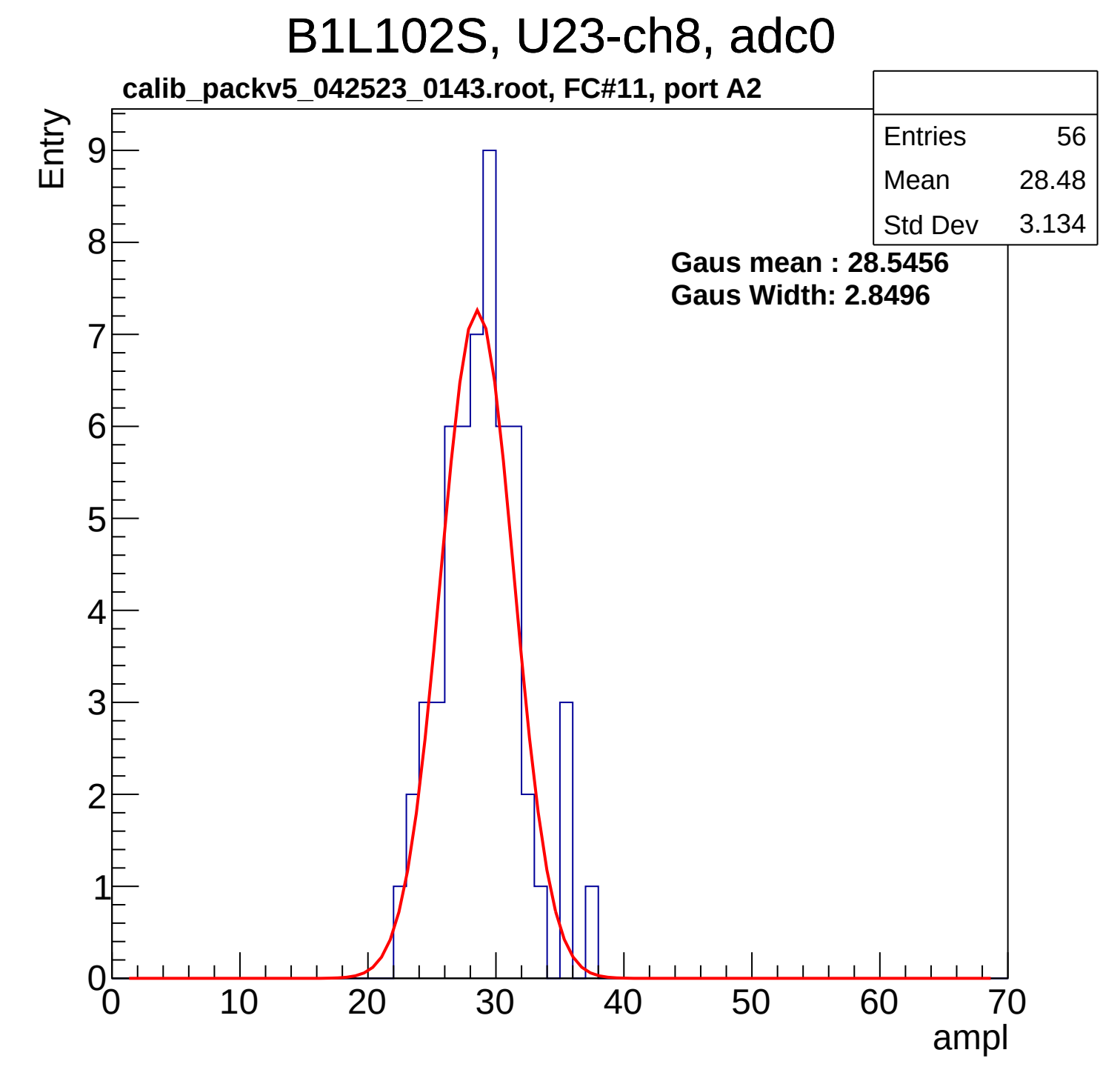

B1L102S, U23-ch7, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 49.23
Std Dev 3.448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

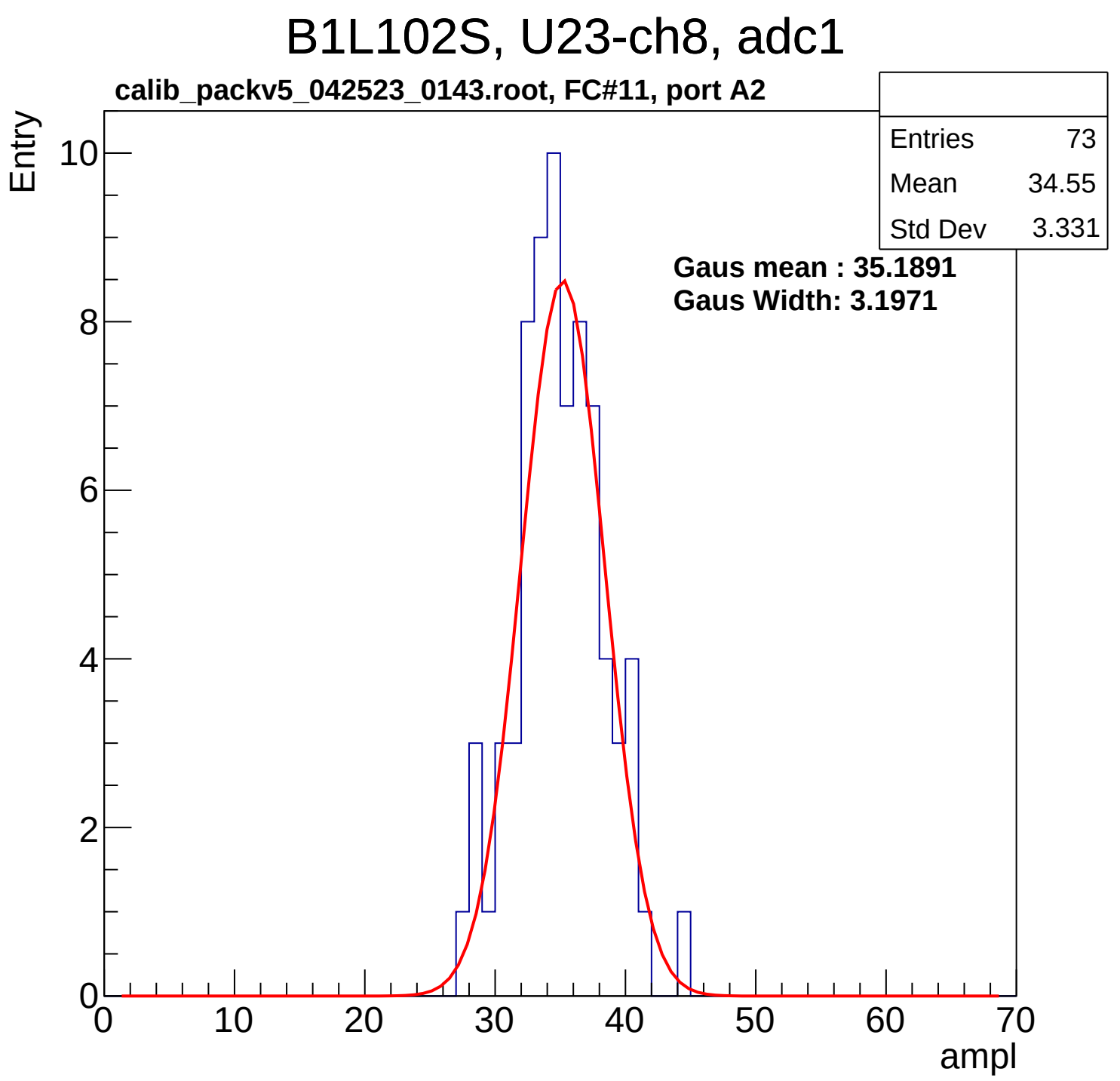

B1L102S, U23-ch7, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 54.8
Std Dev 7.781
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

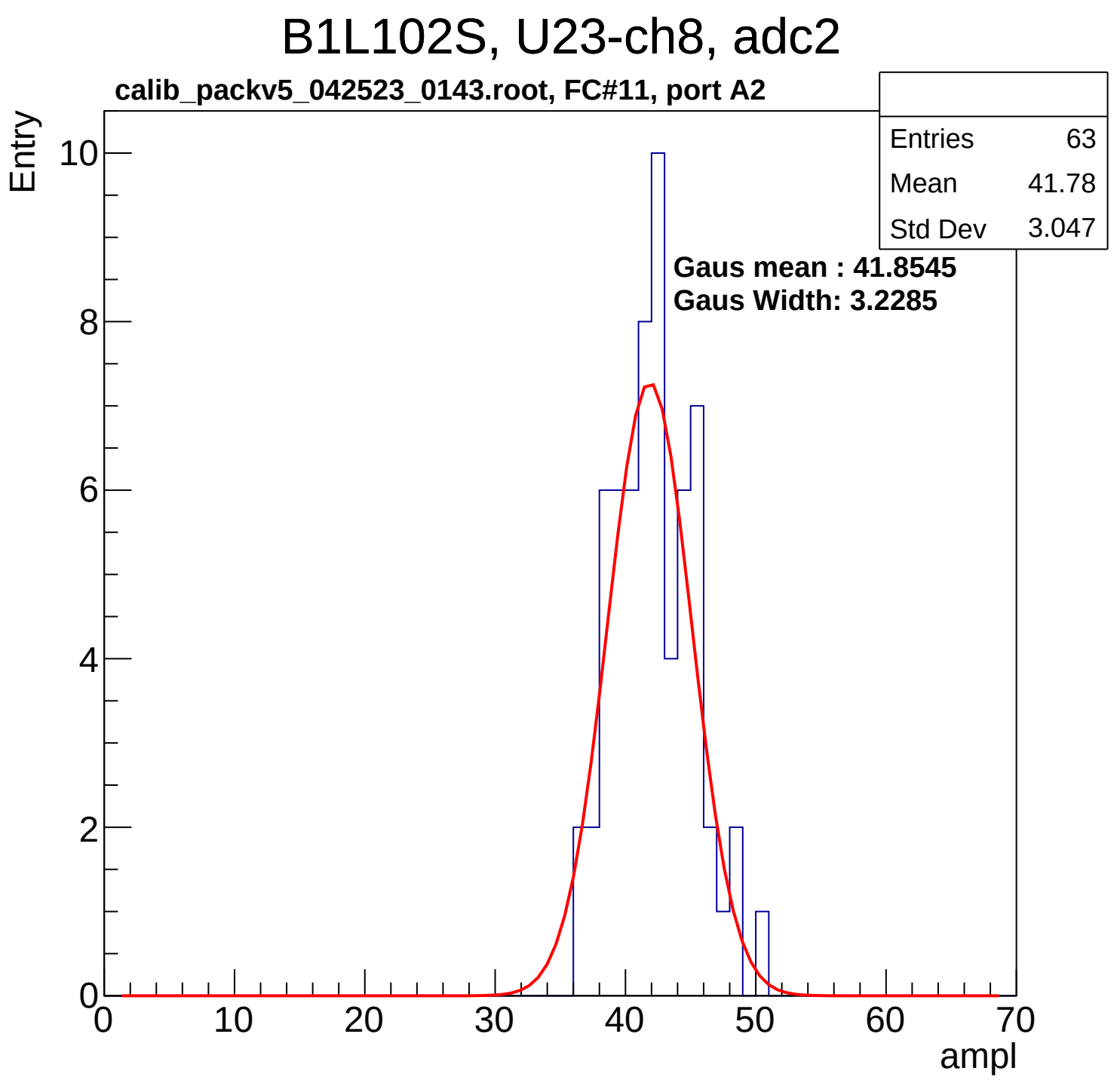

B1L102S, U23-ch7, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 60.07
Std Dev 2.299
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

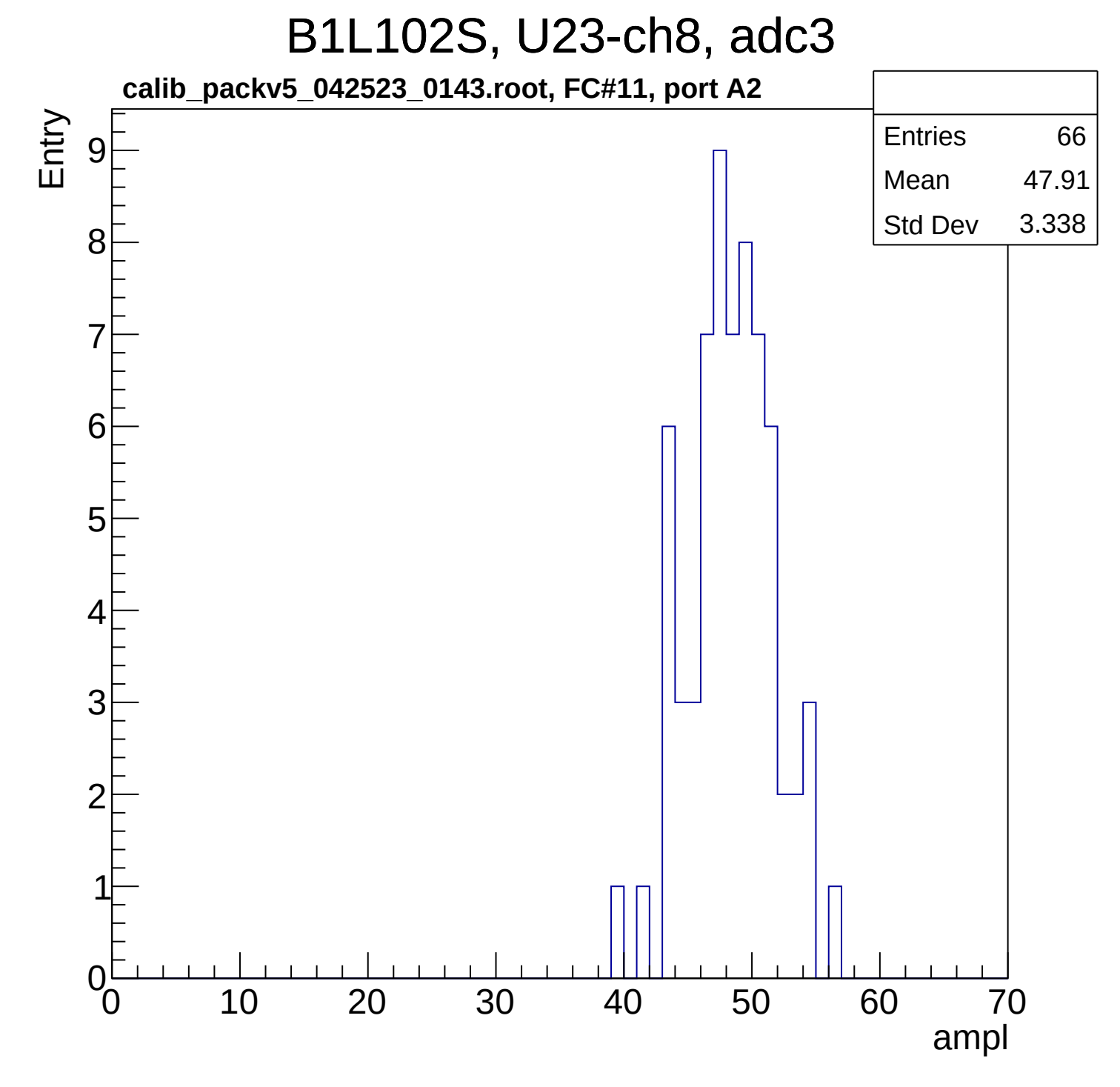

B1L102S, U23-ch7, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 61.43
Std Dev 1.4
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

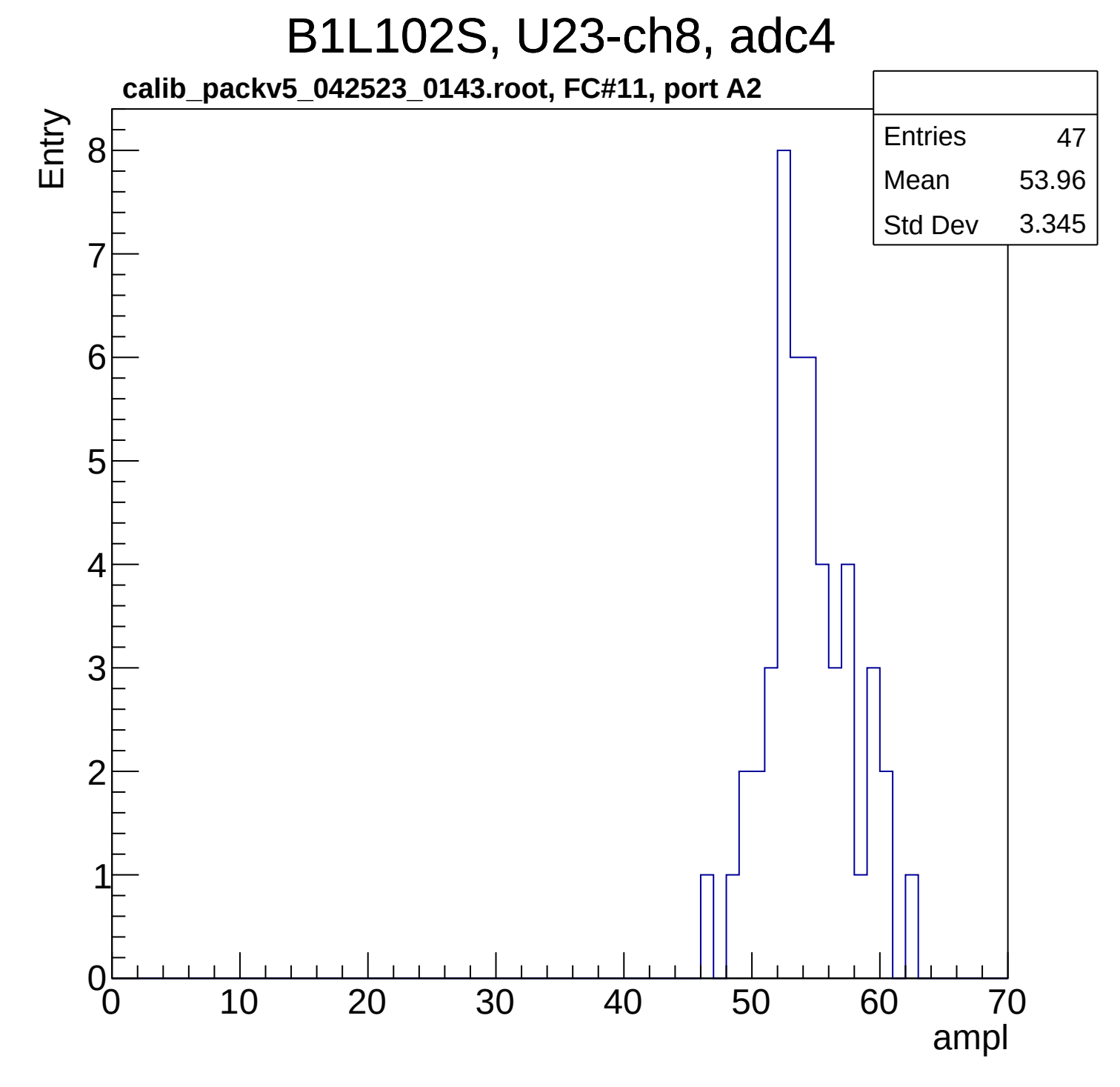

B1L102S, U23-ch7, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

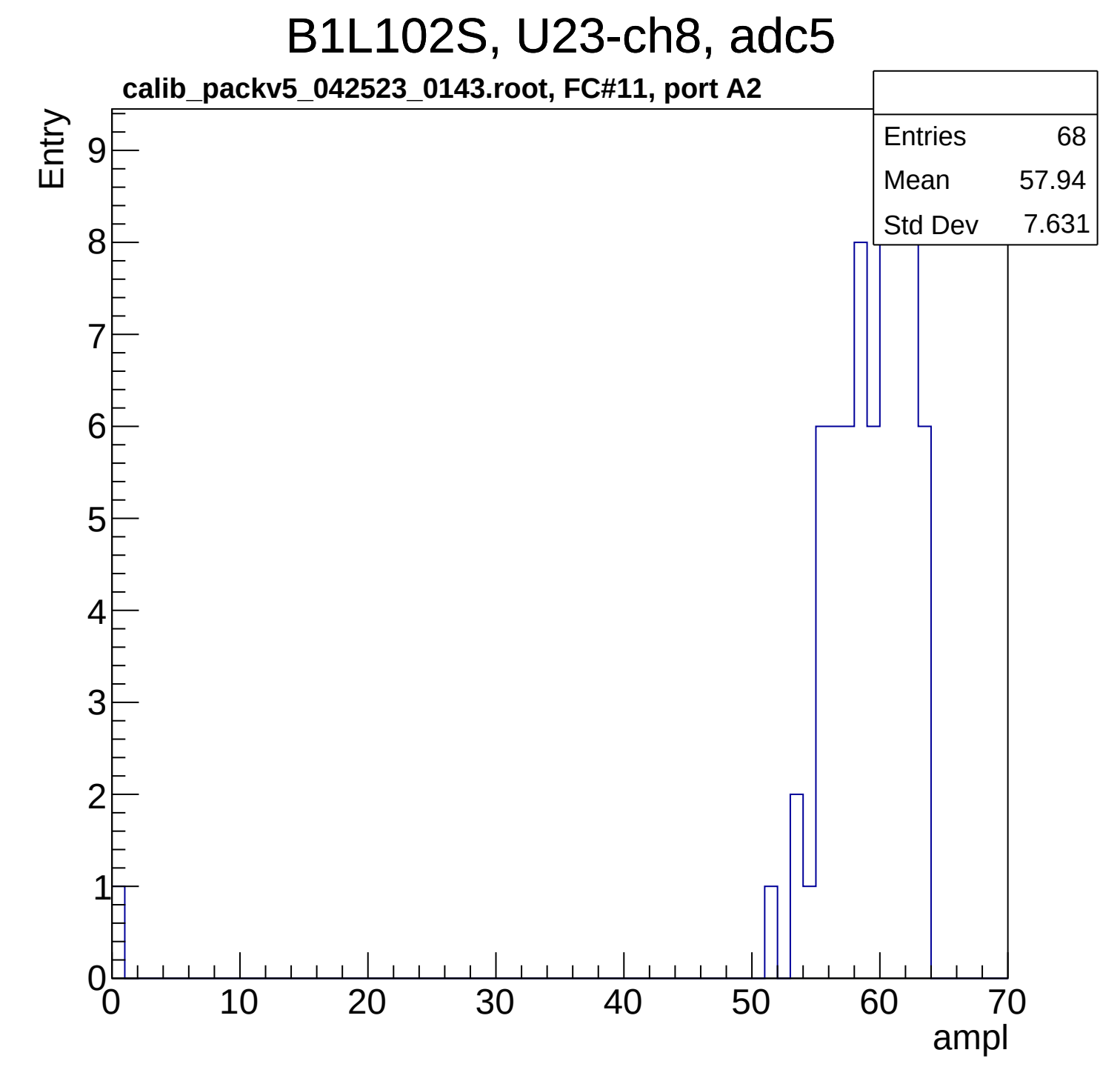

B1L102S, U23-ch8, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 28.48
Std Dev 3.134
Gaus mean : 28.5456
Gaus Width: 2.8496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

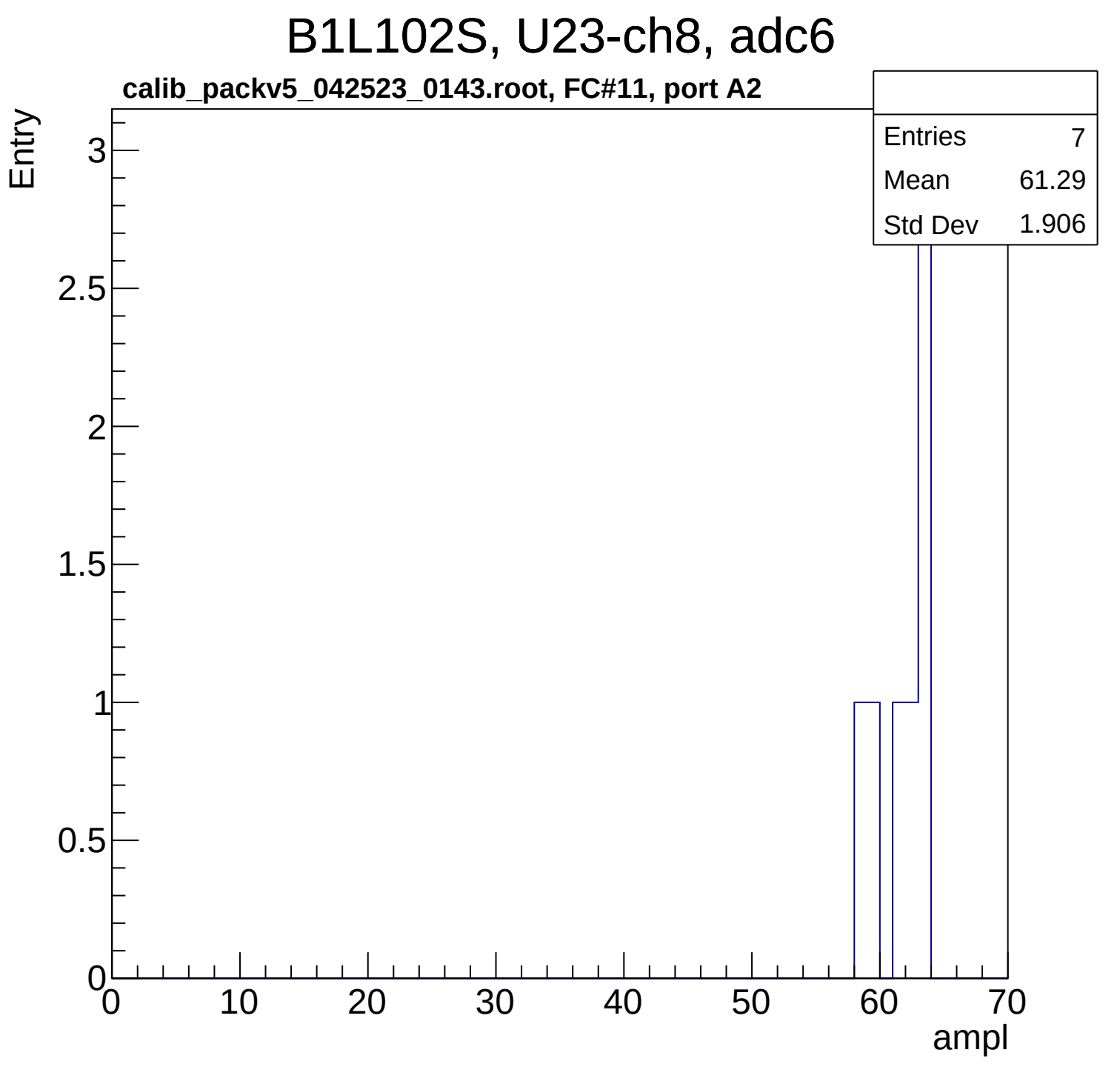

B1L102S, U23-ch8, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 34.55
Std Dev 3.331
Gaus mean : 35.1891
Gaus Width: 3.1971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

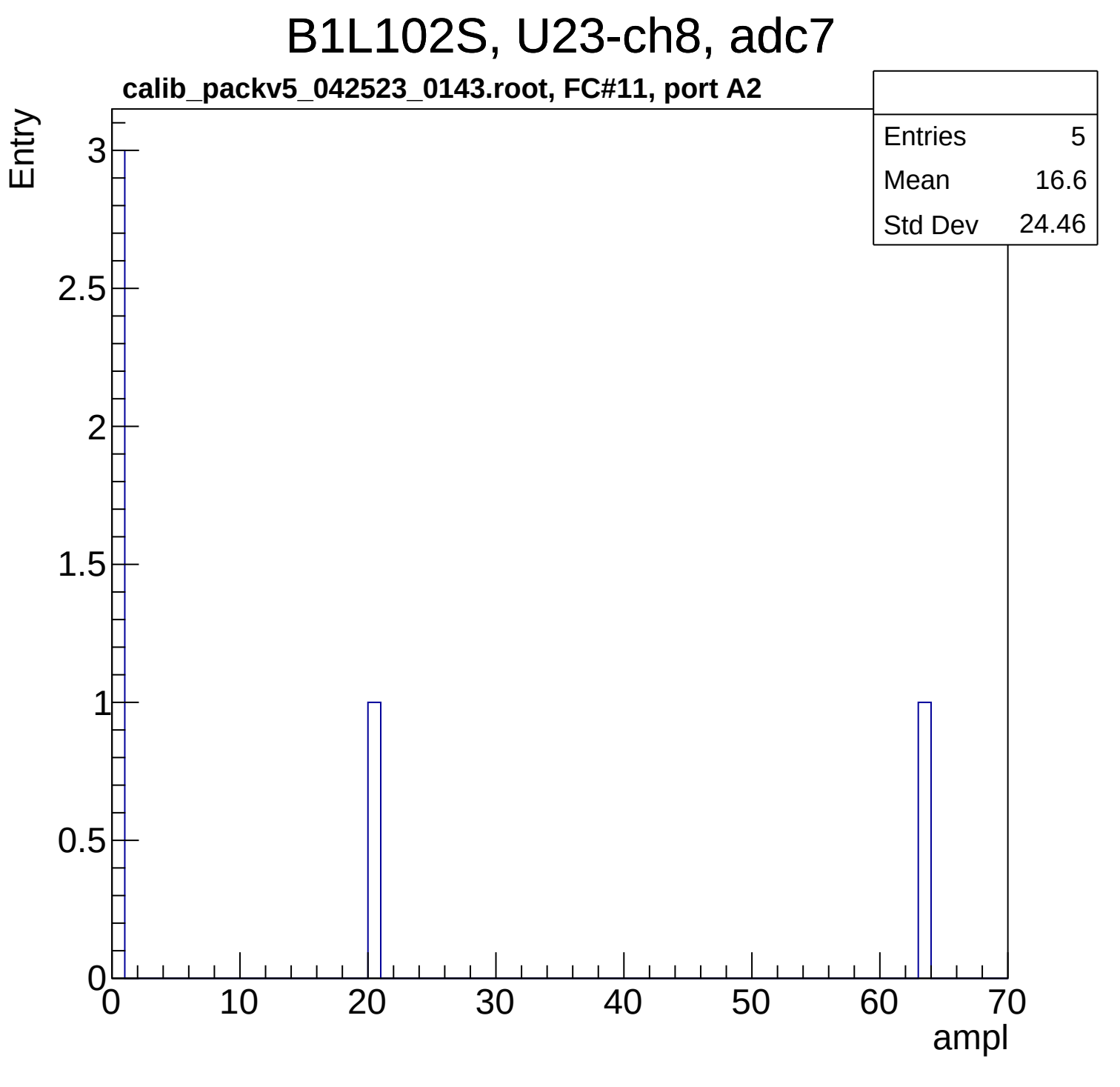

B1L102S, U23-ch8, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 41.78
Std Dev 3.047
Gaus mean : 41.8545
Gaus Width: 3.2285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

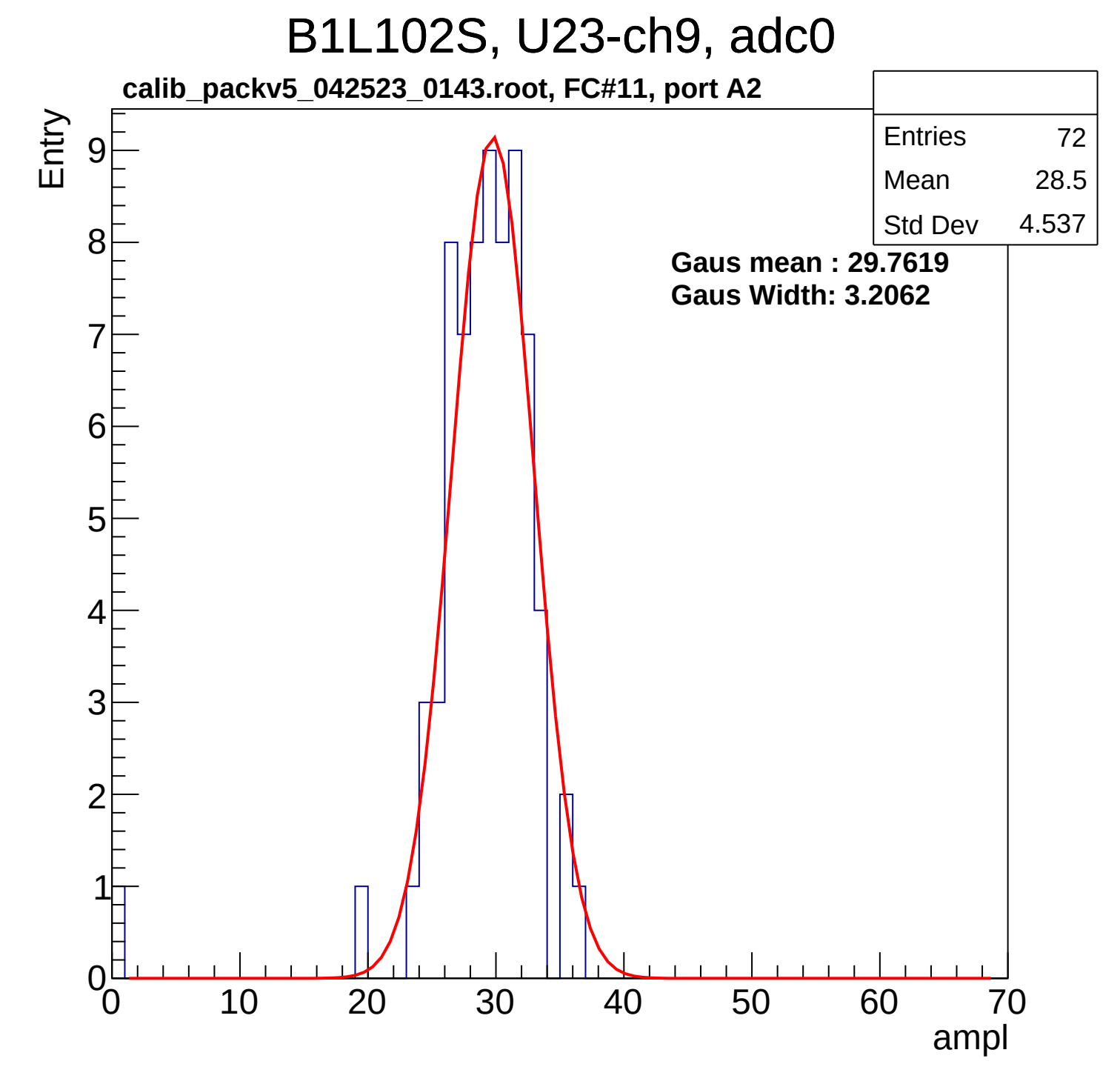

B1L102S, U23-ch8, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 47.91
Std Dev 3.338
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

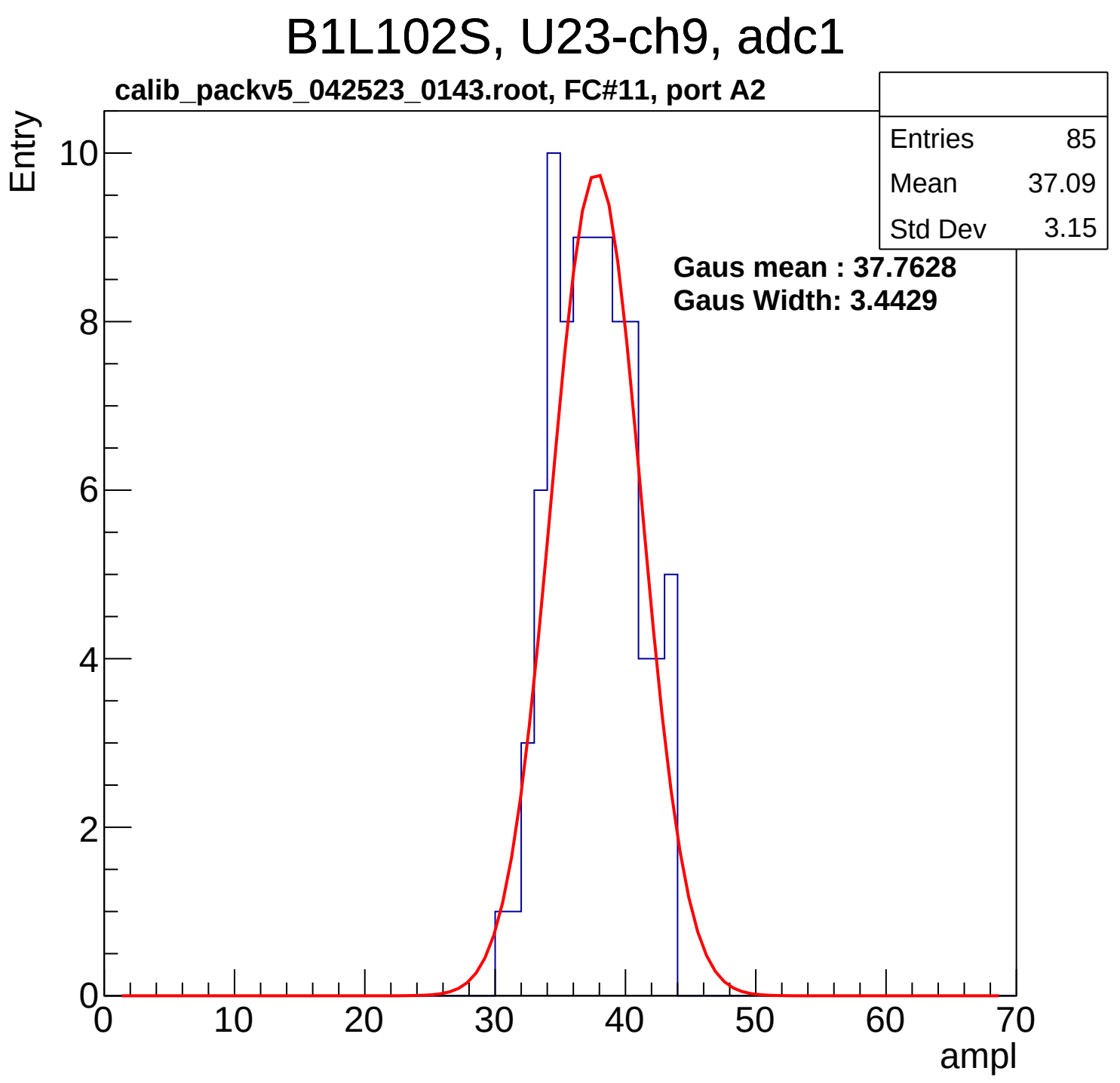

B1L102S, U23-ch8, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 53.96
Std Dev 3.345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

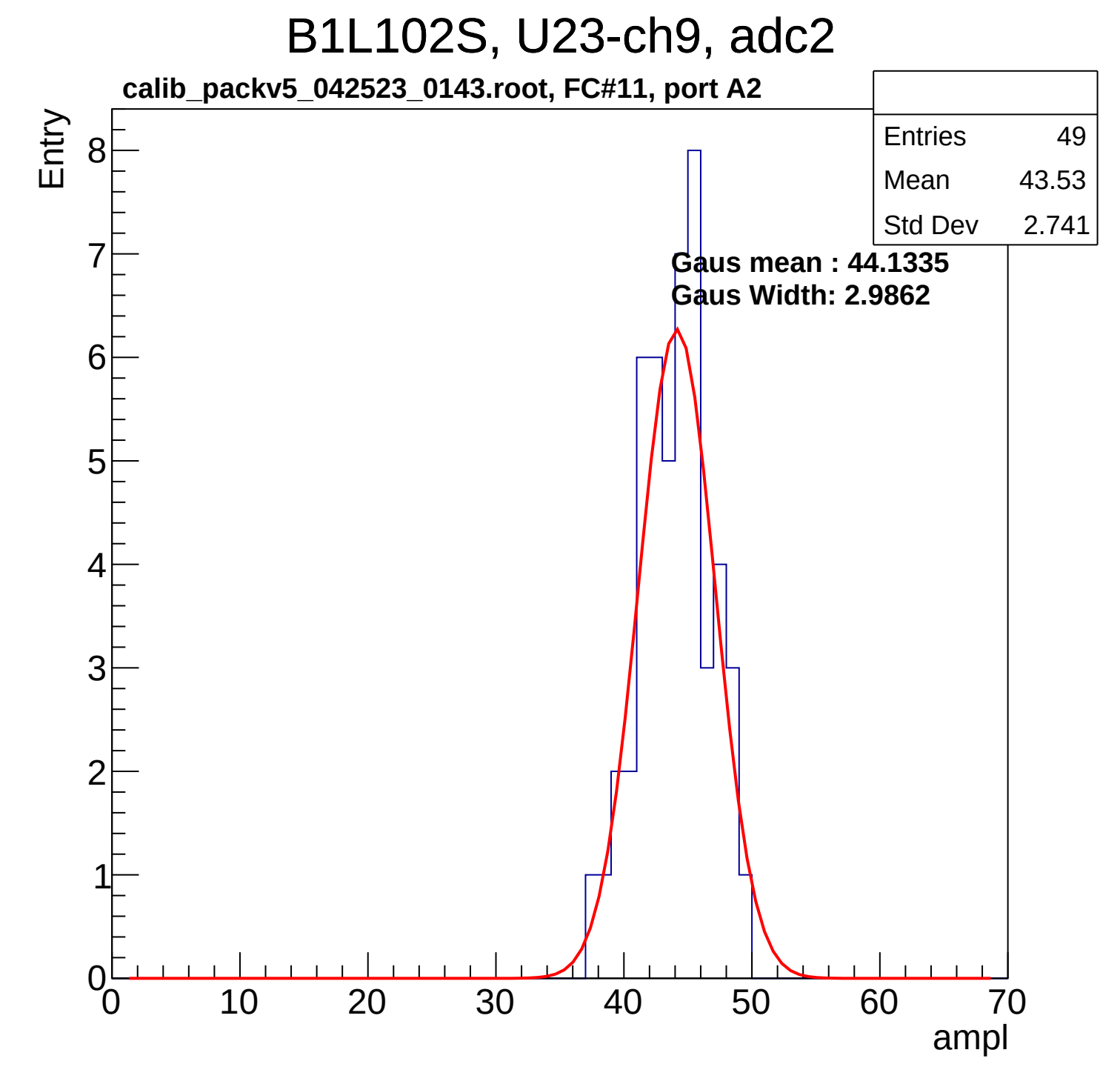

B1L102S, U23-ch8, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 57.94
Std Dev 7.631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

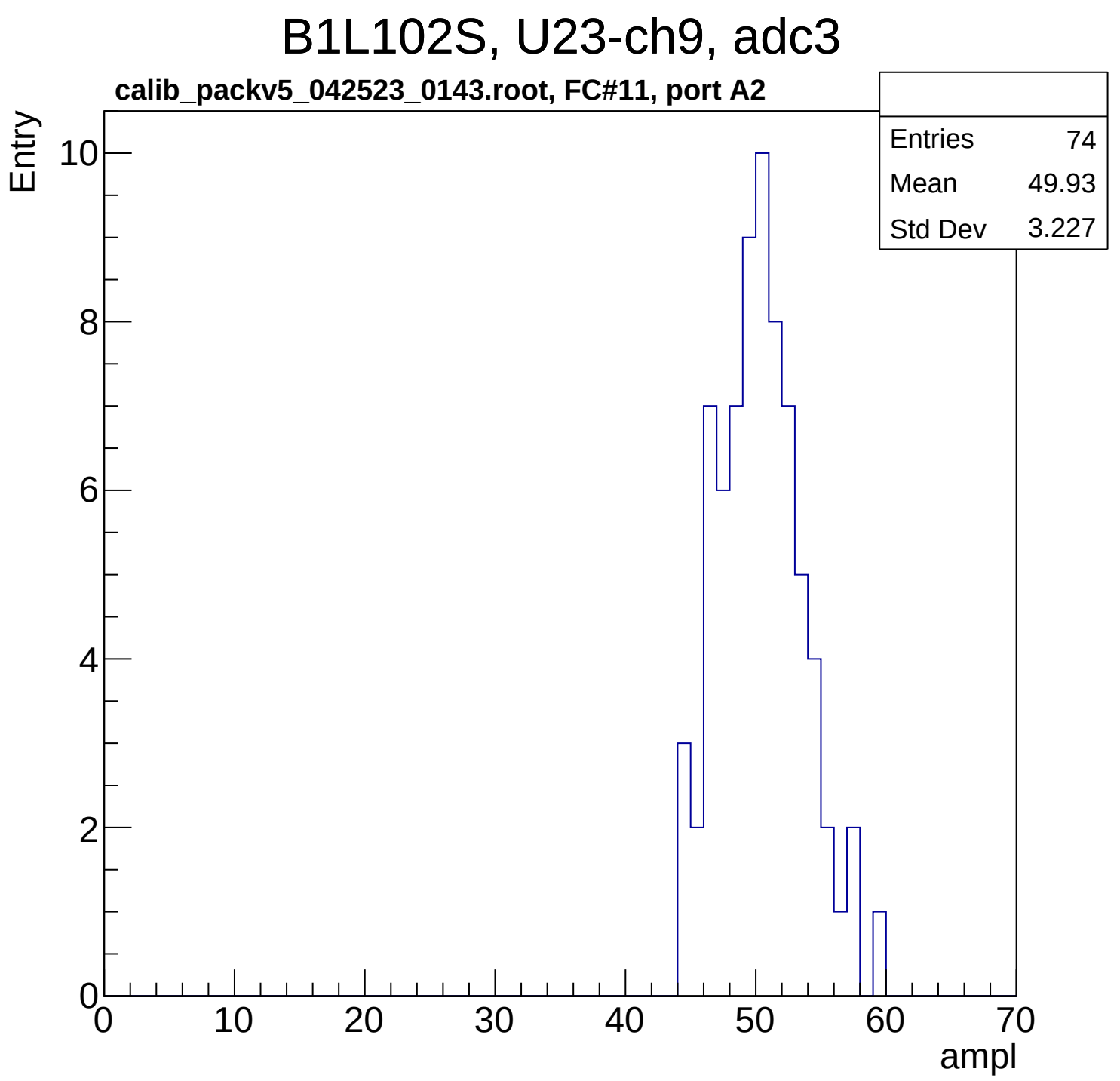

B1L102S, U23-ch8, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 61.29
Std Dev 1.906
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

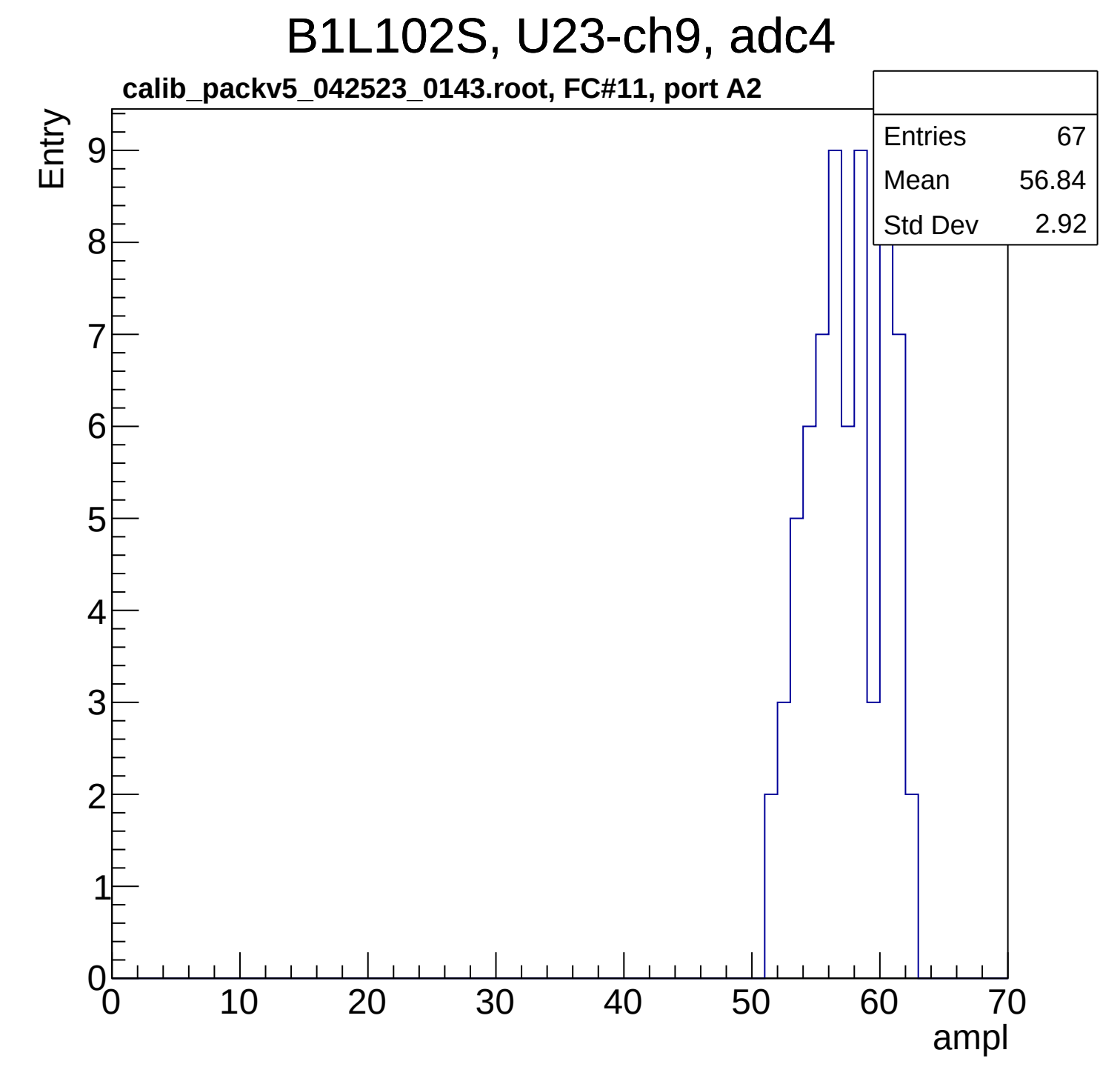

B1L102S, U23-ch8, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 16.6
Std Dev 24.46
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

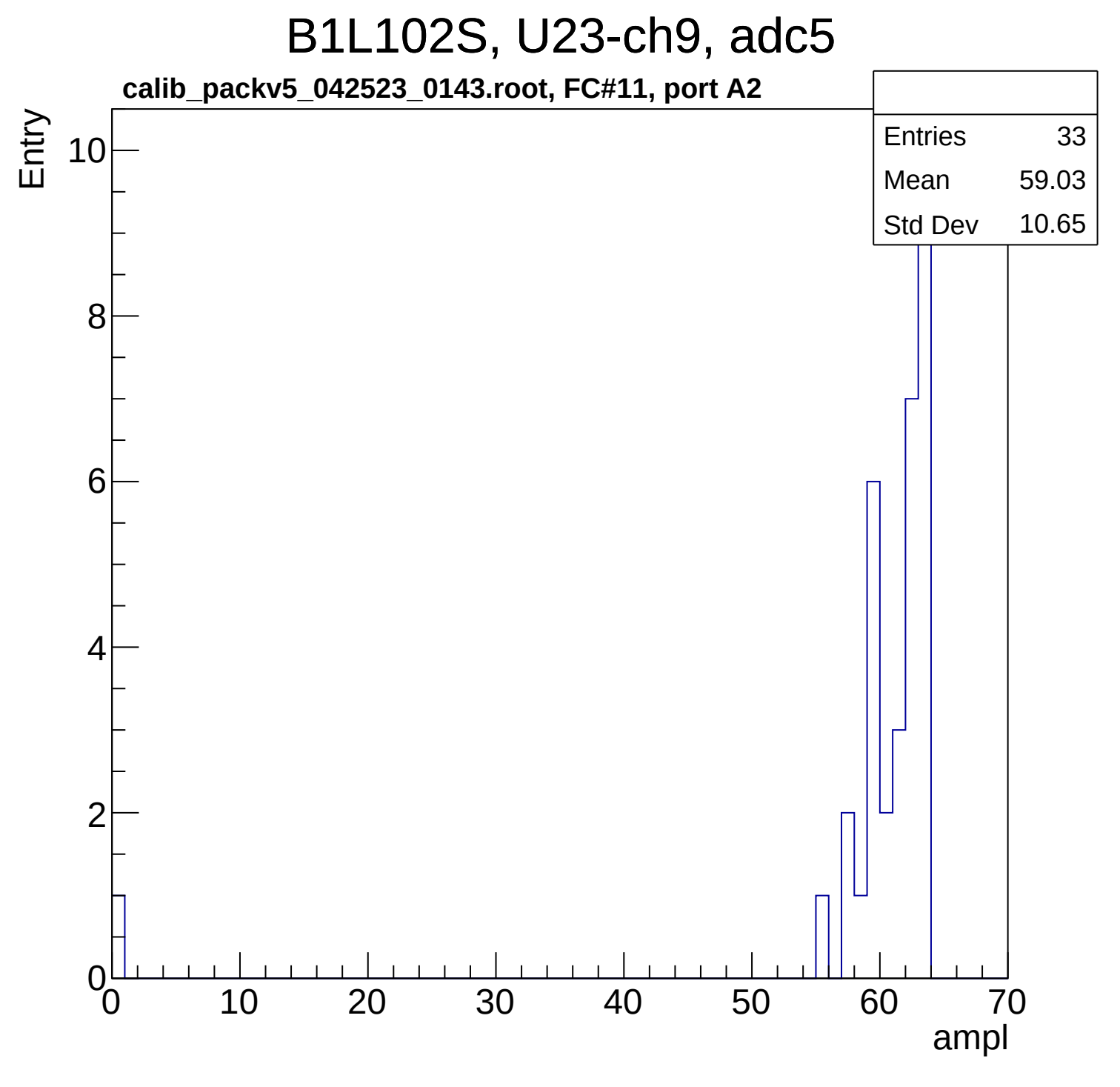

B1L102S, U23-ch9, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 28.5
Std Dev 4.537
Gaus mean : 29.7619
Gaus Width: 3.2062
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

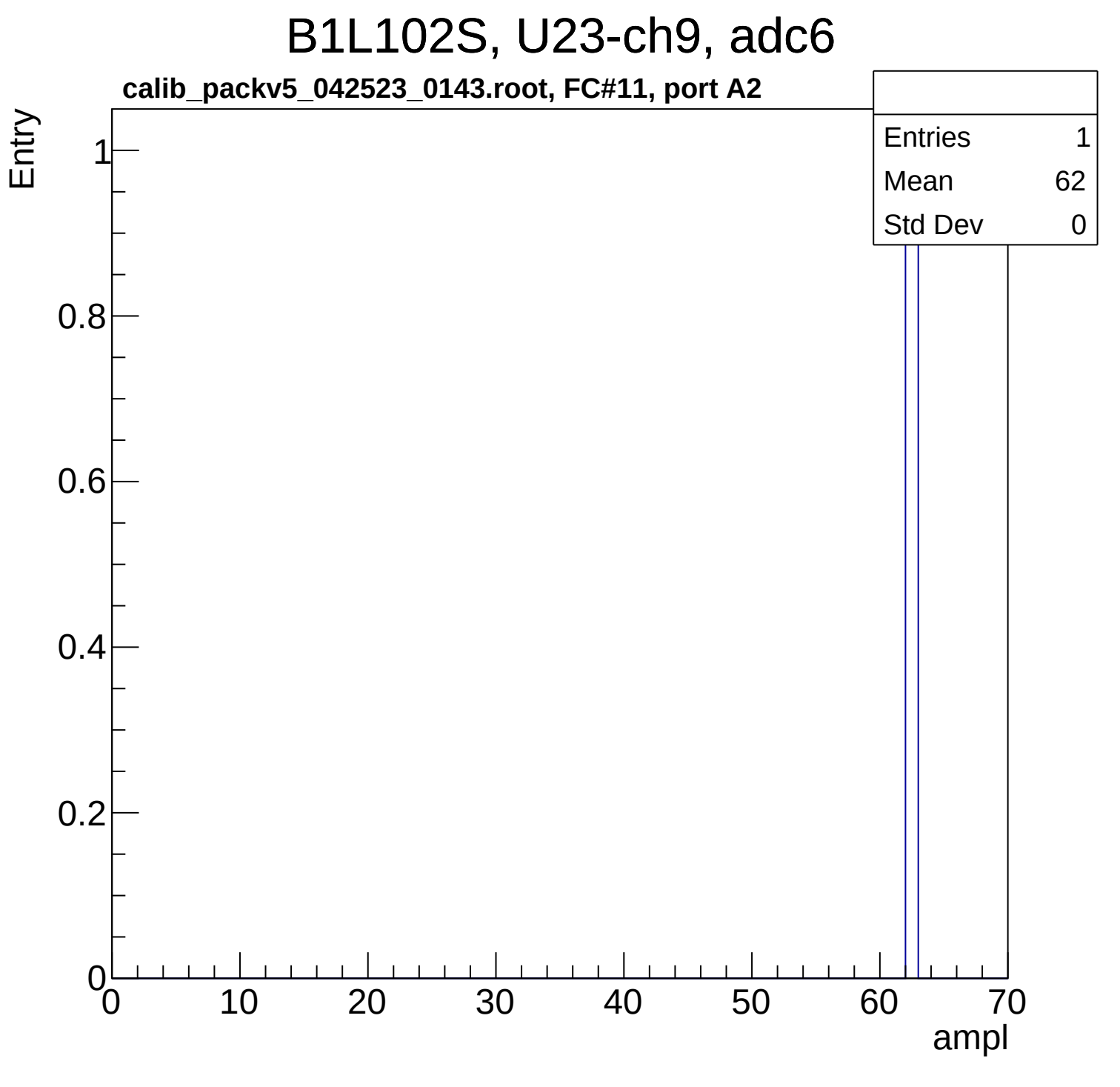

B1L102S, U23-ch9, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 37.09
Std Dev 3.15
Gaus mean : 37.7628
Gaus Width: 3.4429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

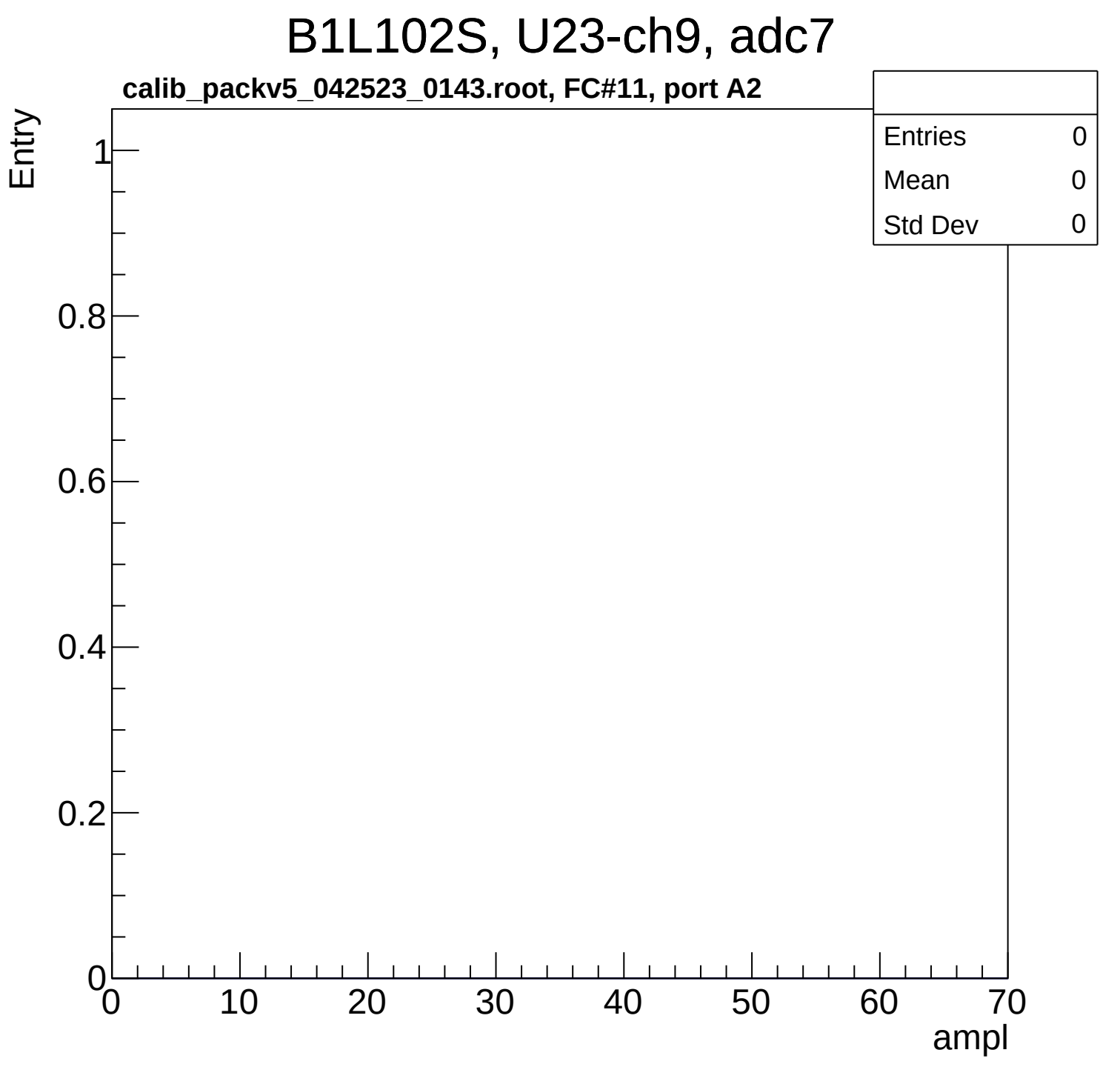

B1L102S, U23-ch9, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 43.53
Std Dev 2.741
Gaus mean : 44.1335
Gaus Width: 2.9862
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

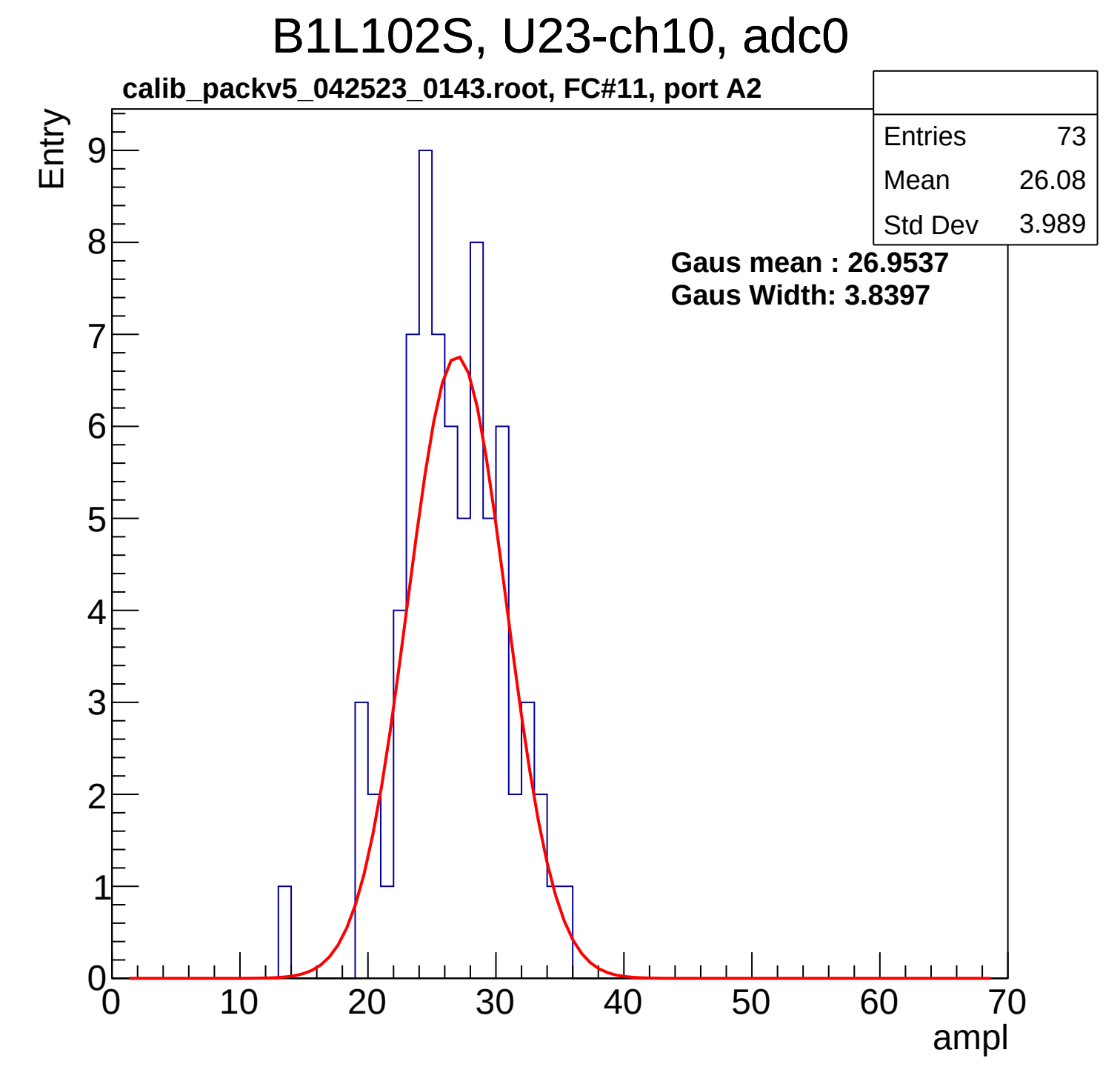

B1L102S, U23-ch9, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 49.93
Std Dev 3.227
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

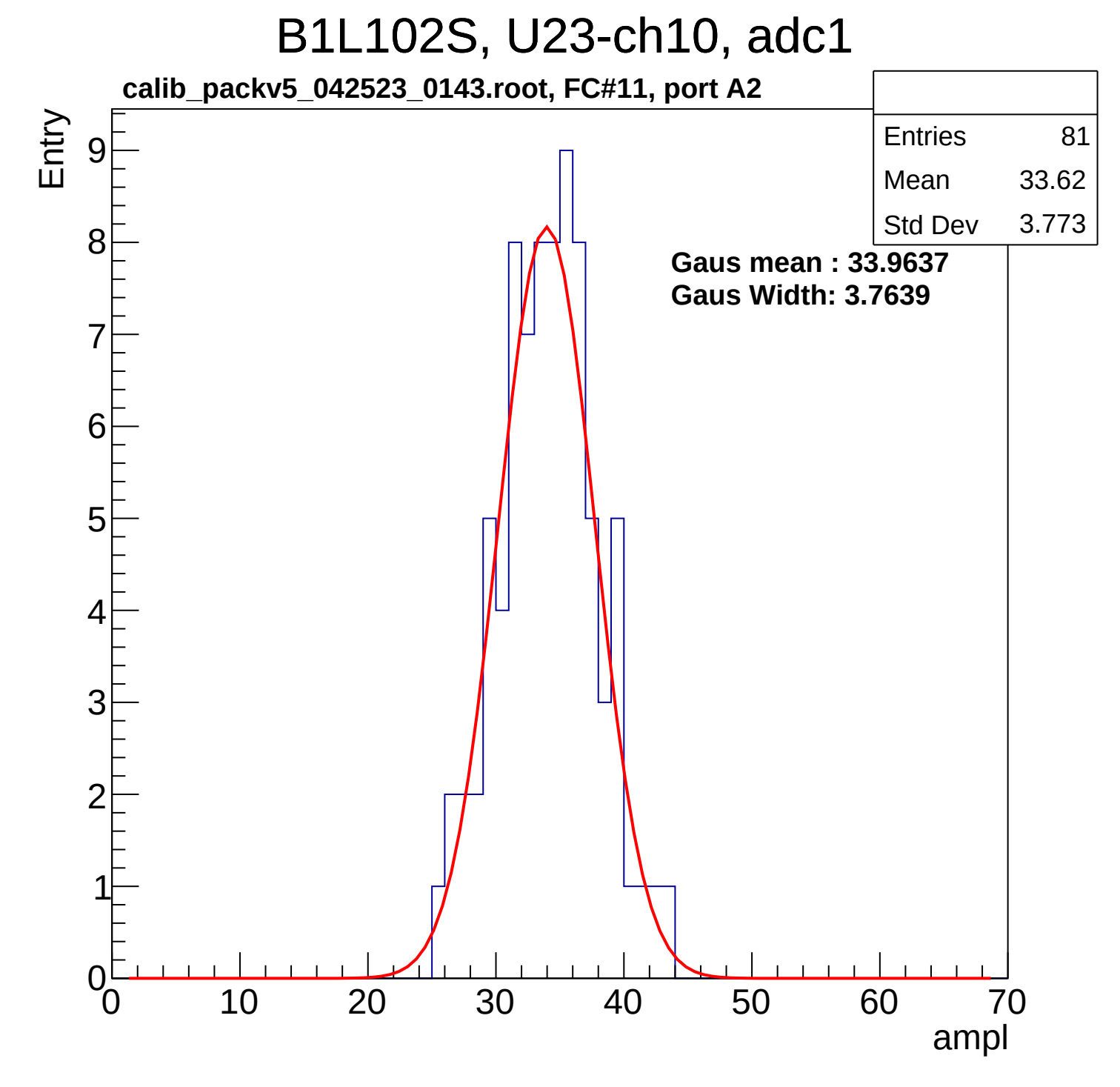

B1L102S, U23-ch9, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 56.84
Std Dev 2.92
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

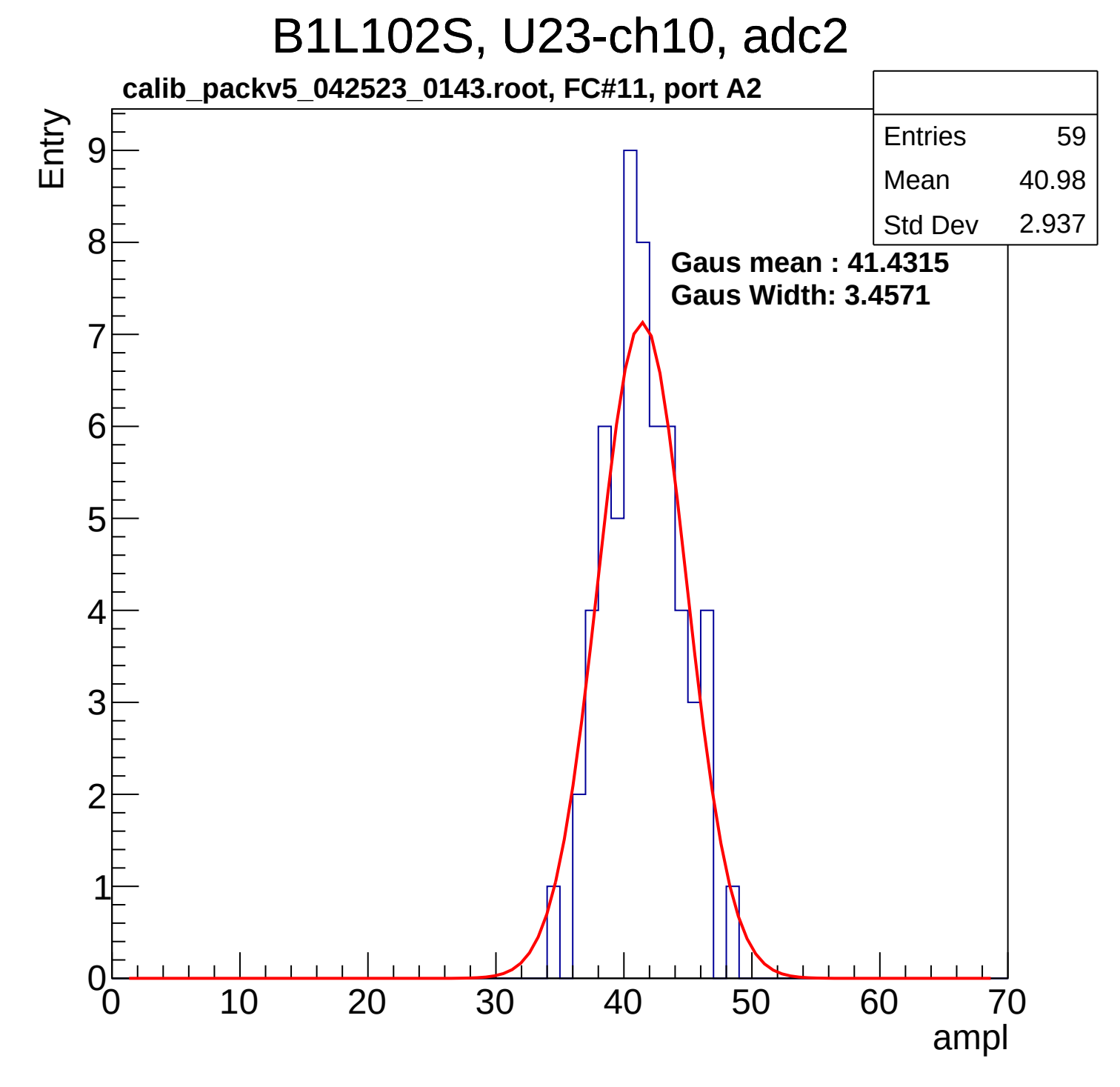

B1L102S, U23-ch9, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 33
Mean 59.03
Std Dev 10.65
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

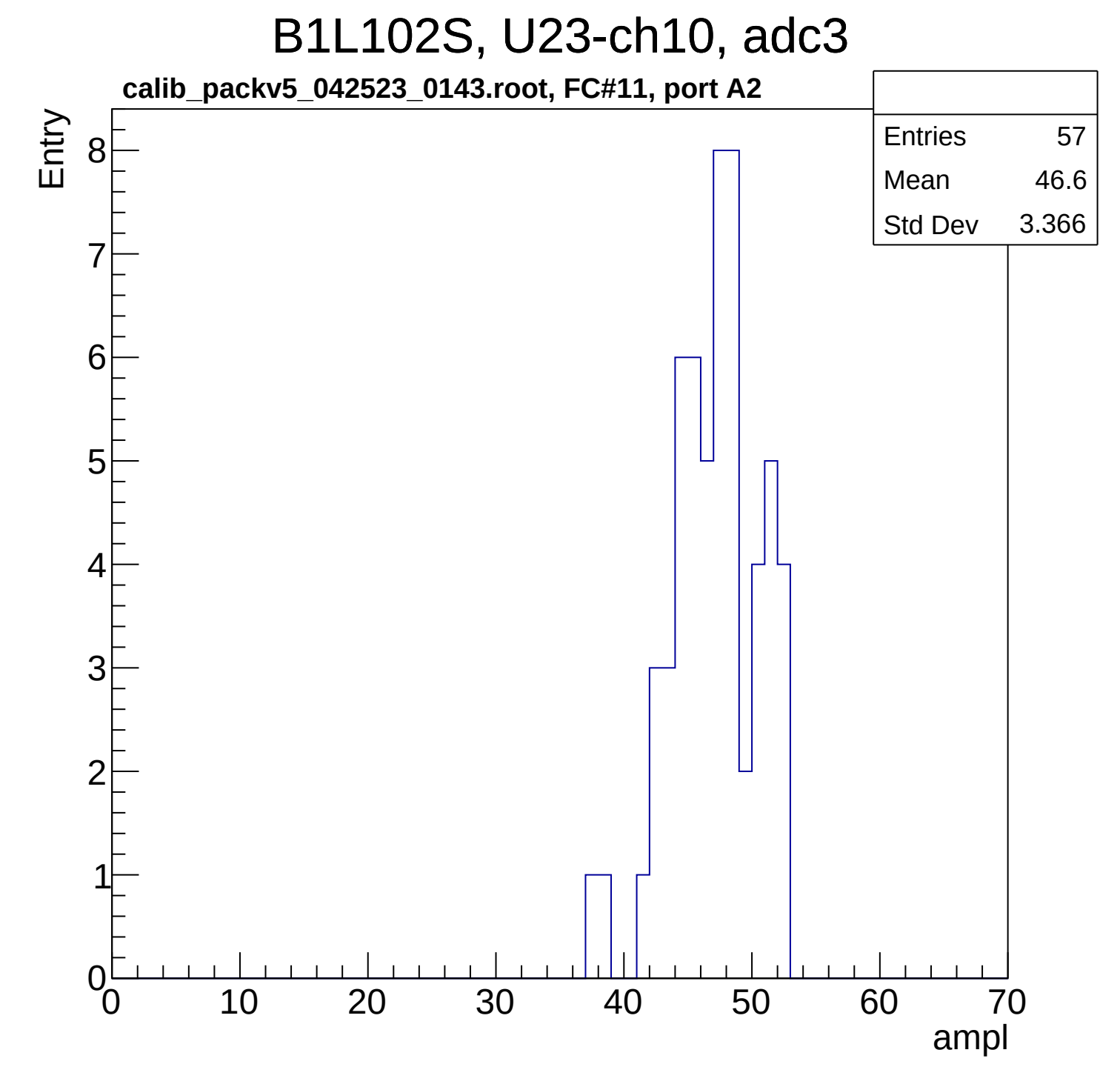

B1L102S, U23-ch9, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

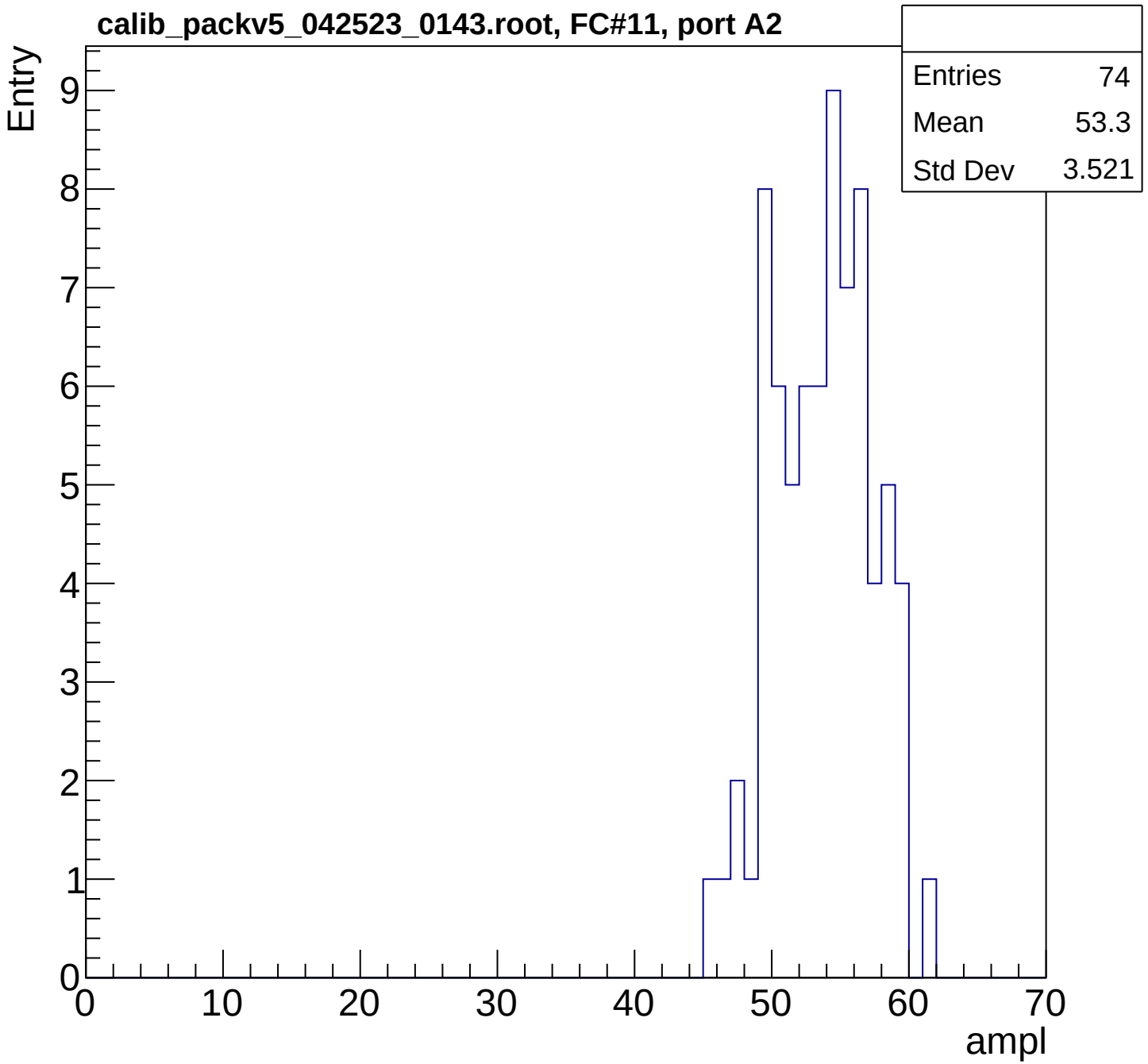

B1L102S, U23-ch9, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

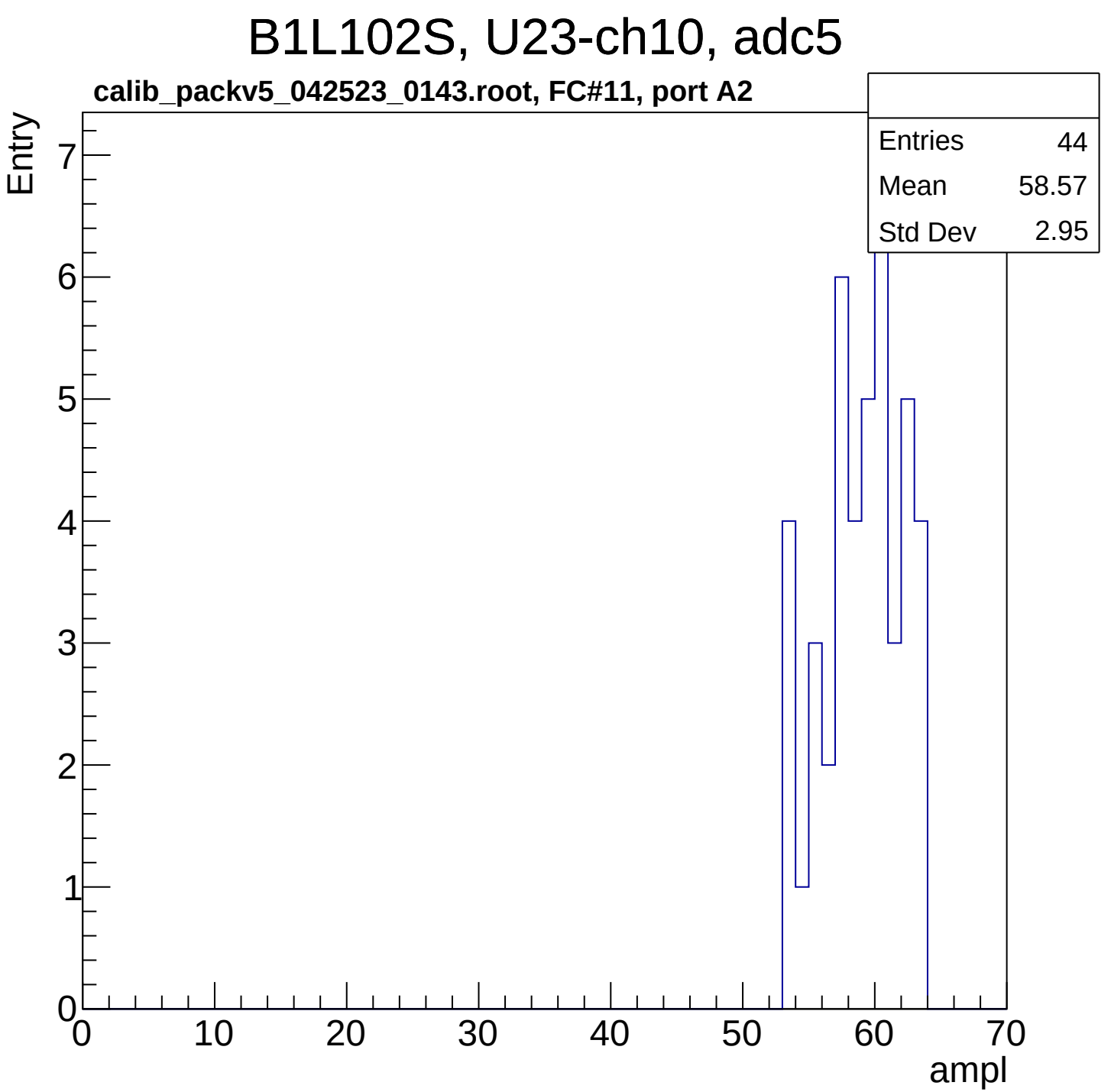

B1L102S, U23-ch10, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 26.08
Std Dev 3.989
Gaus mean : 26.9537
Gaus Width: 3.8397
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

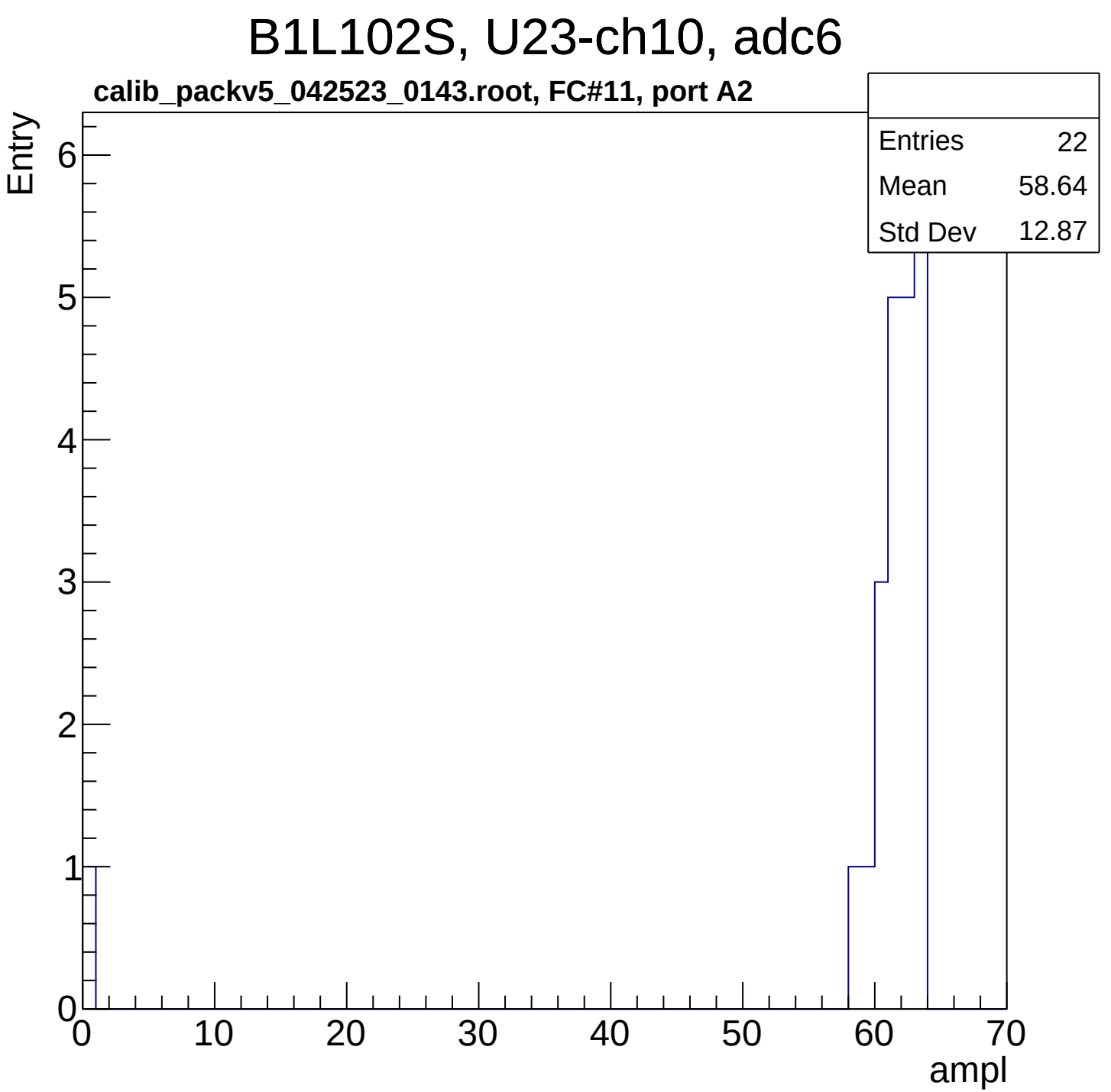

B1L102S, U23-ch10, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 33.62
Std Dev 3.773
Gaus mean : 33.9637
Gaus Width: 3.7639
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

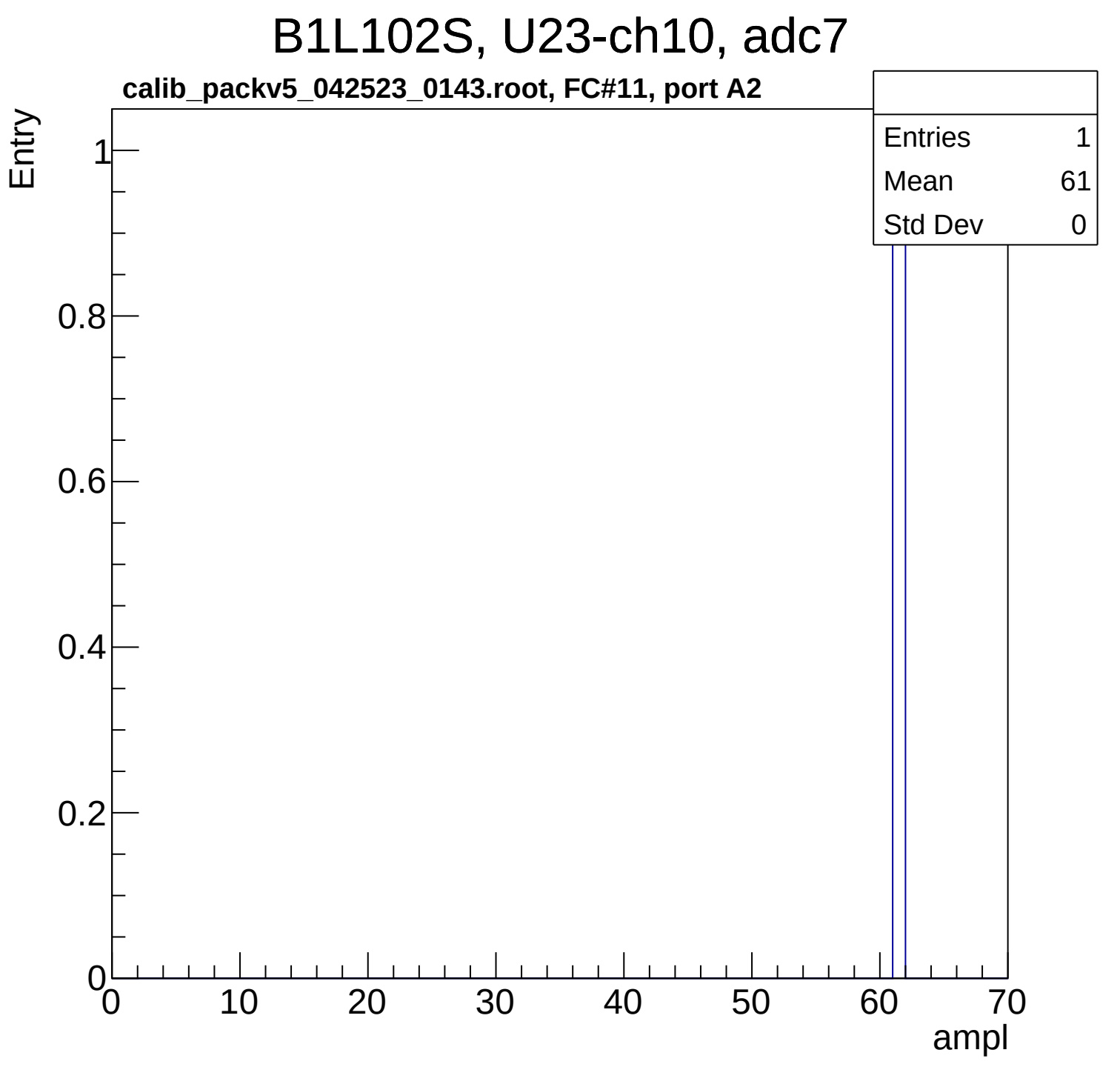

B1L102S, U23-ch10, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 40.98
Std Dev 2.937
Gaus mean : 41.4315
Gaus Width: 3.4571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

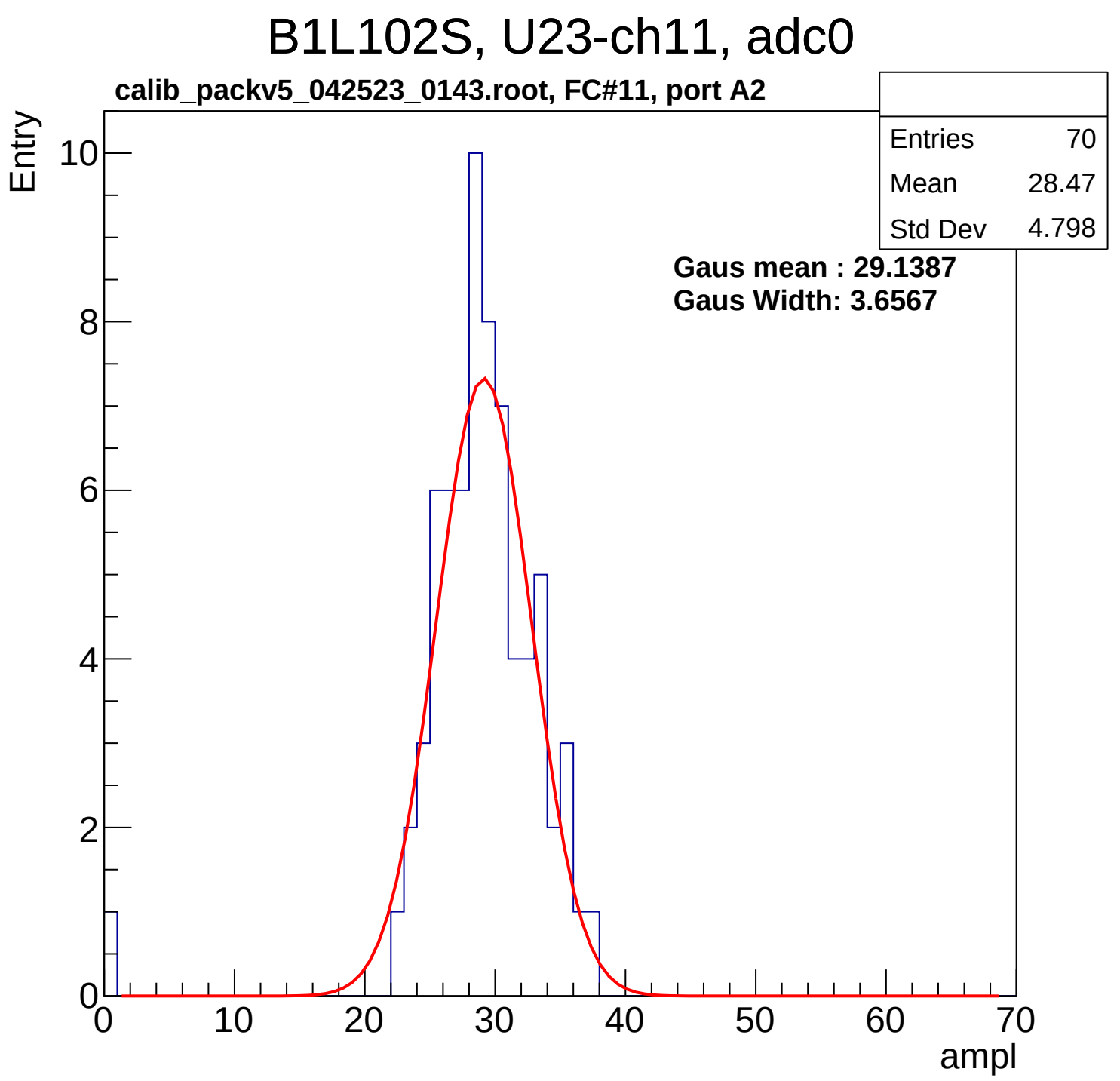

B1L102S, U23-ch10, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 46.6
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

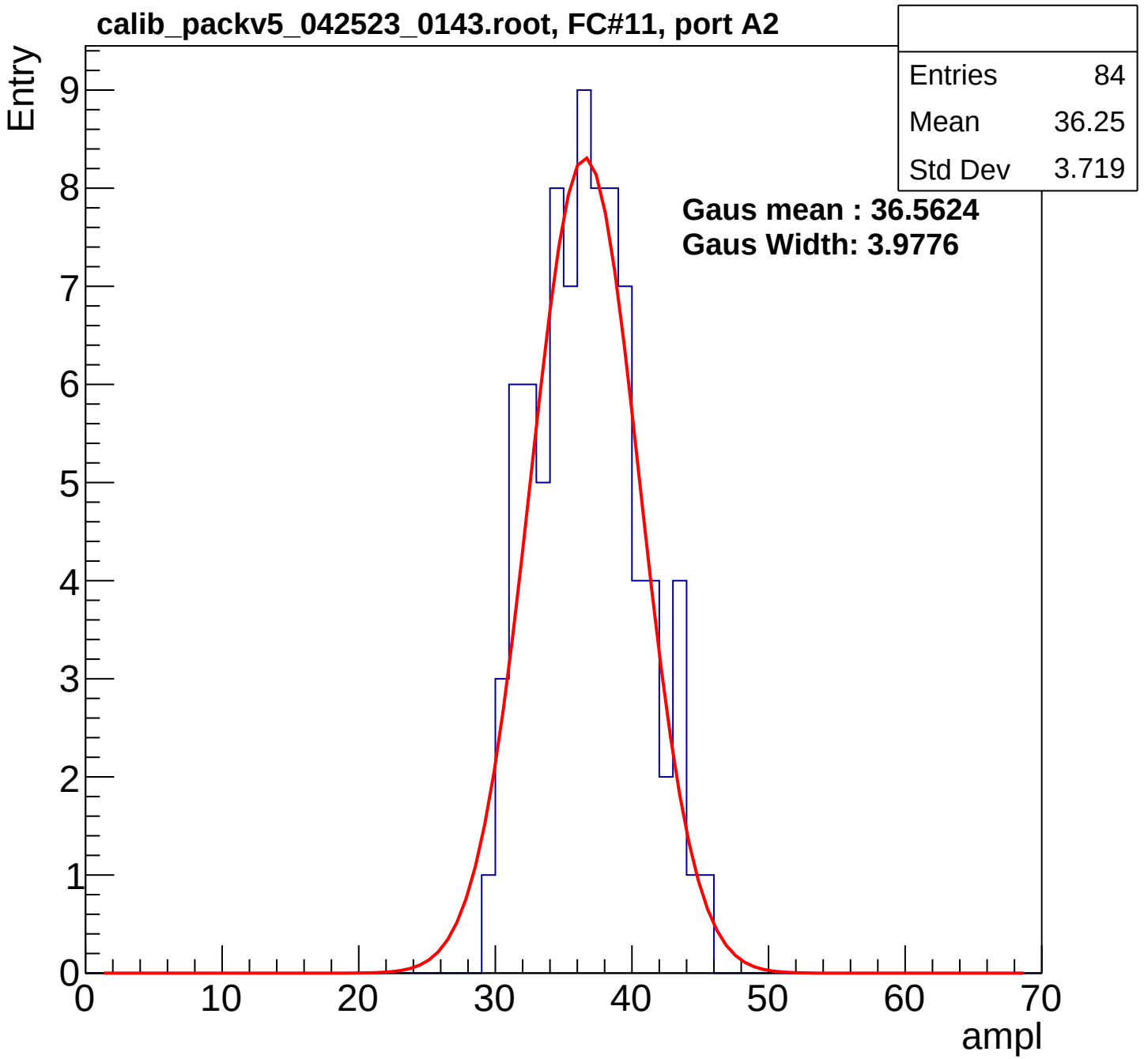

B1L102S, U23-ch10, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 53.3
Std Dev 3.521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

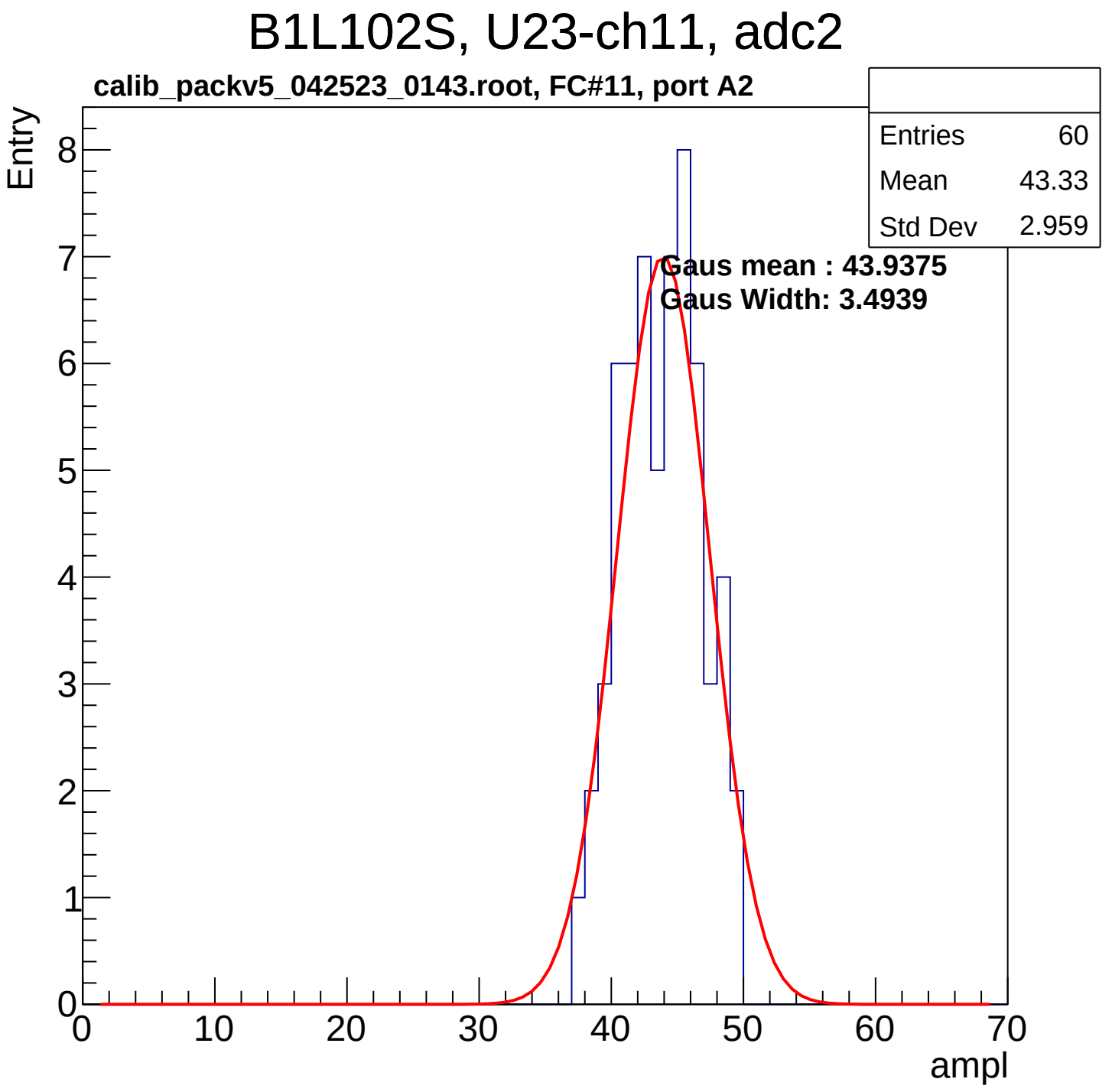

B1L102S, U23-ch10, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 58.57
Std Dev 2.95
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

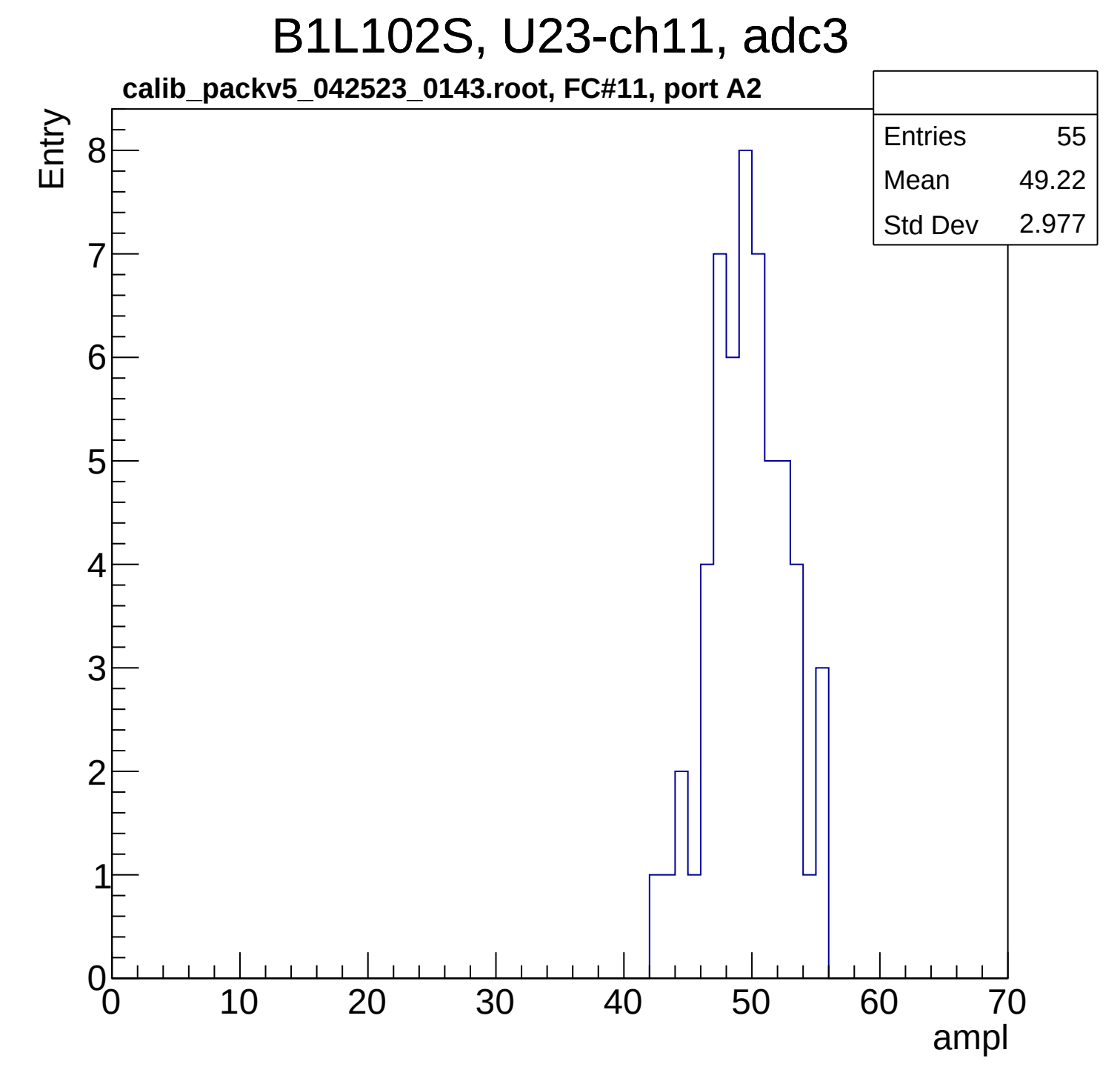

B1L102S, U23-ch10, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 22
Mean 58.64
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

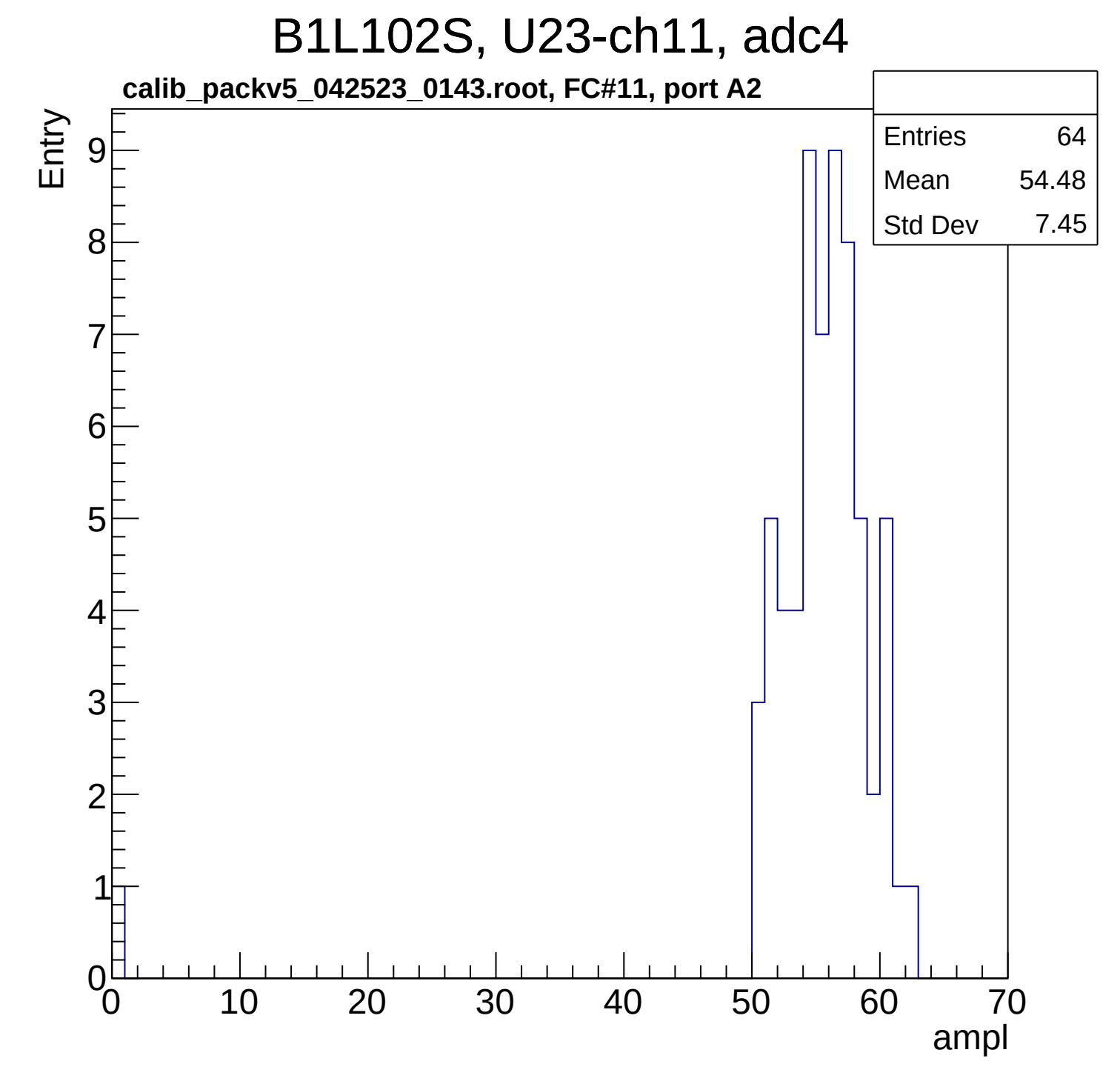

B1L102S, U23-ch10, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

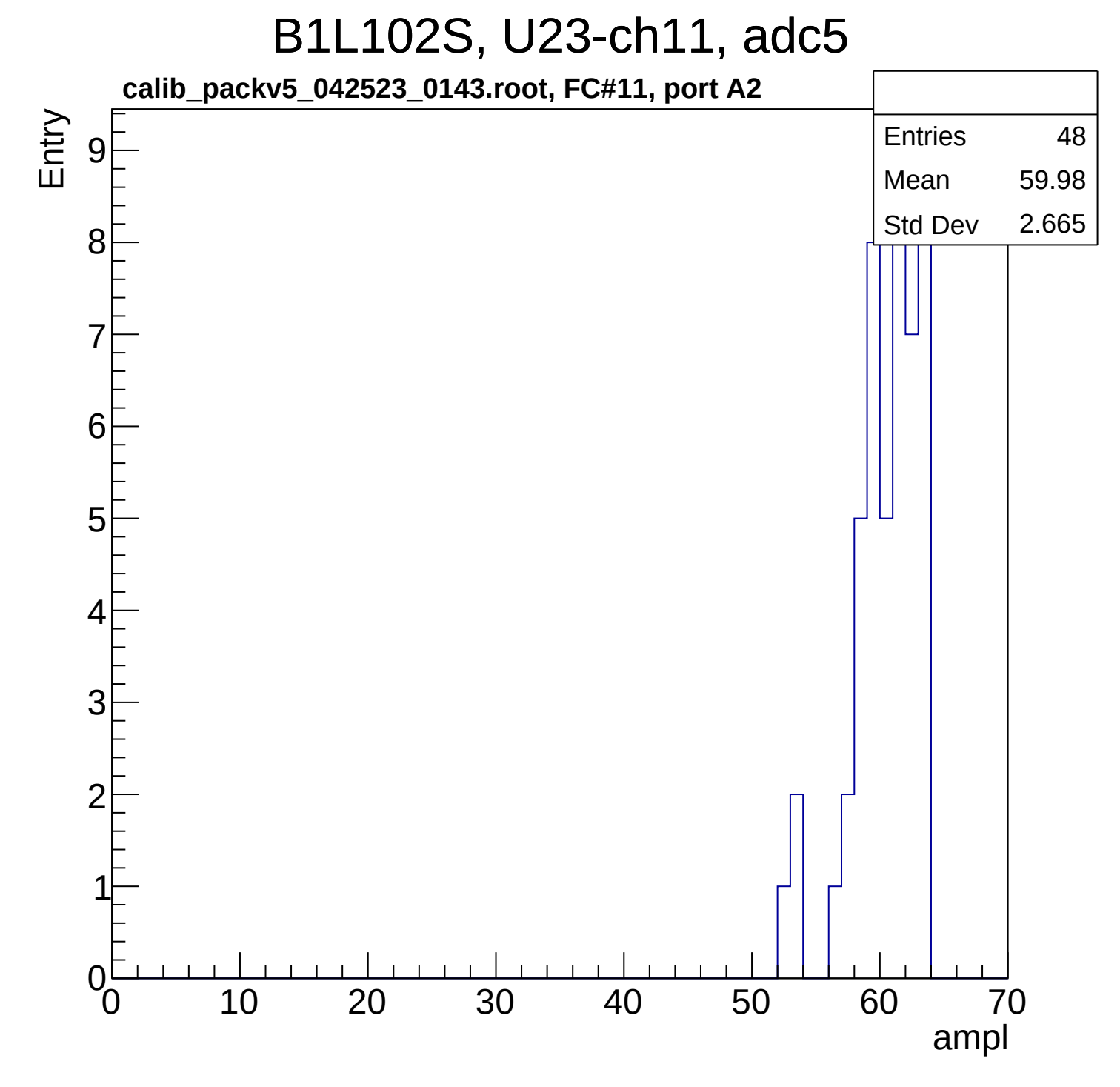

B1L102S, U23-ch11, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.47
Std Dev 4.798
Gaus mean : 29.1387
Gaus Width: 3.6567
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

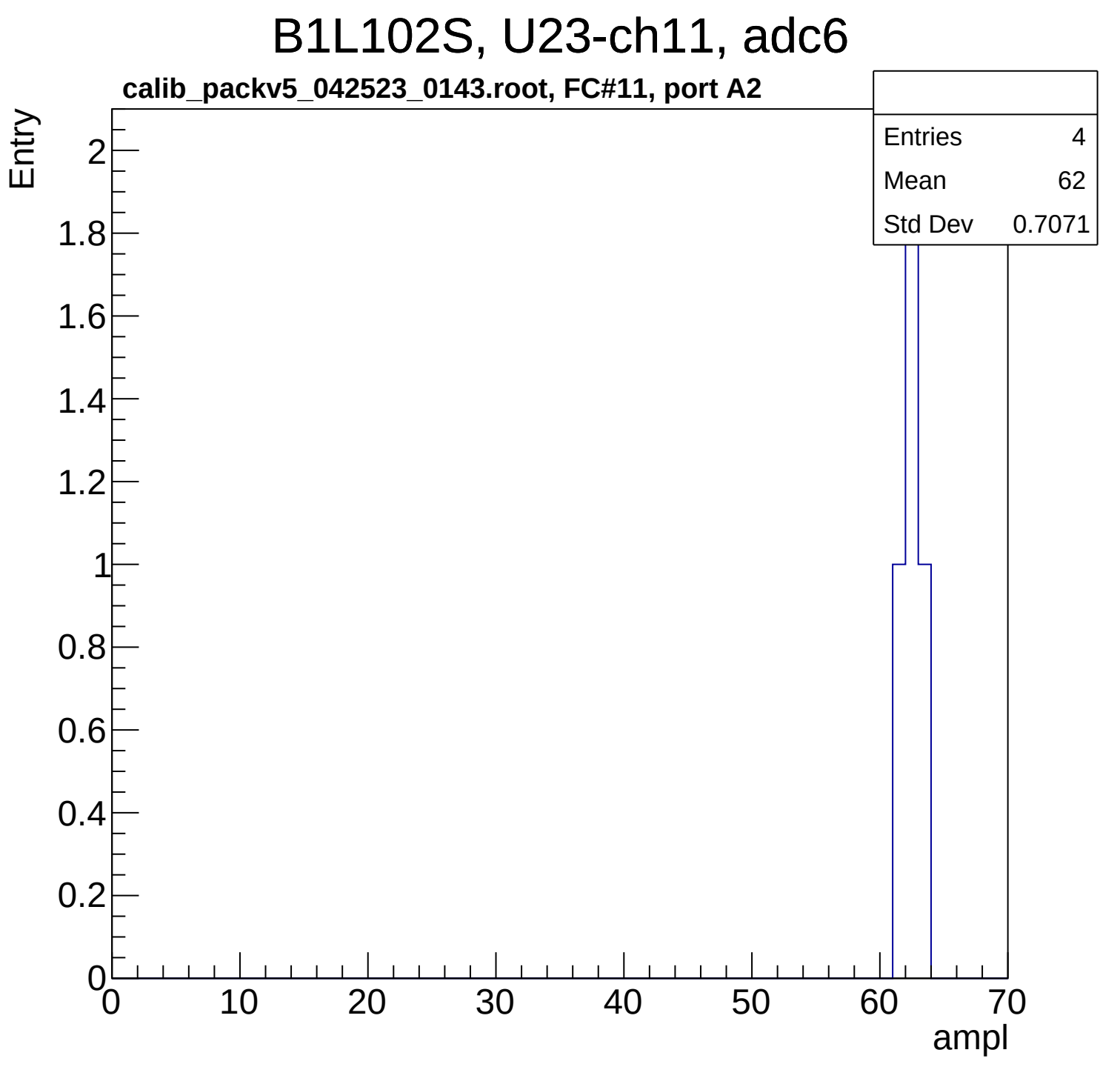

B1L102S, U23-ch11, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 36.25
Std Dev 3.719
Gaus mean : 36.5624
Gaus Width: 3.9776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

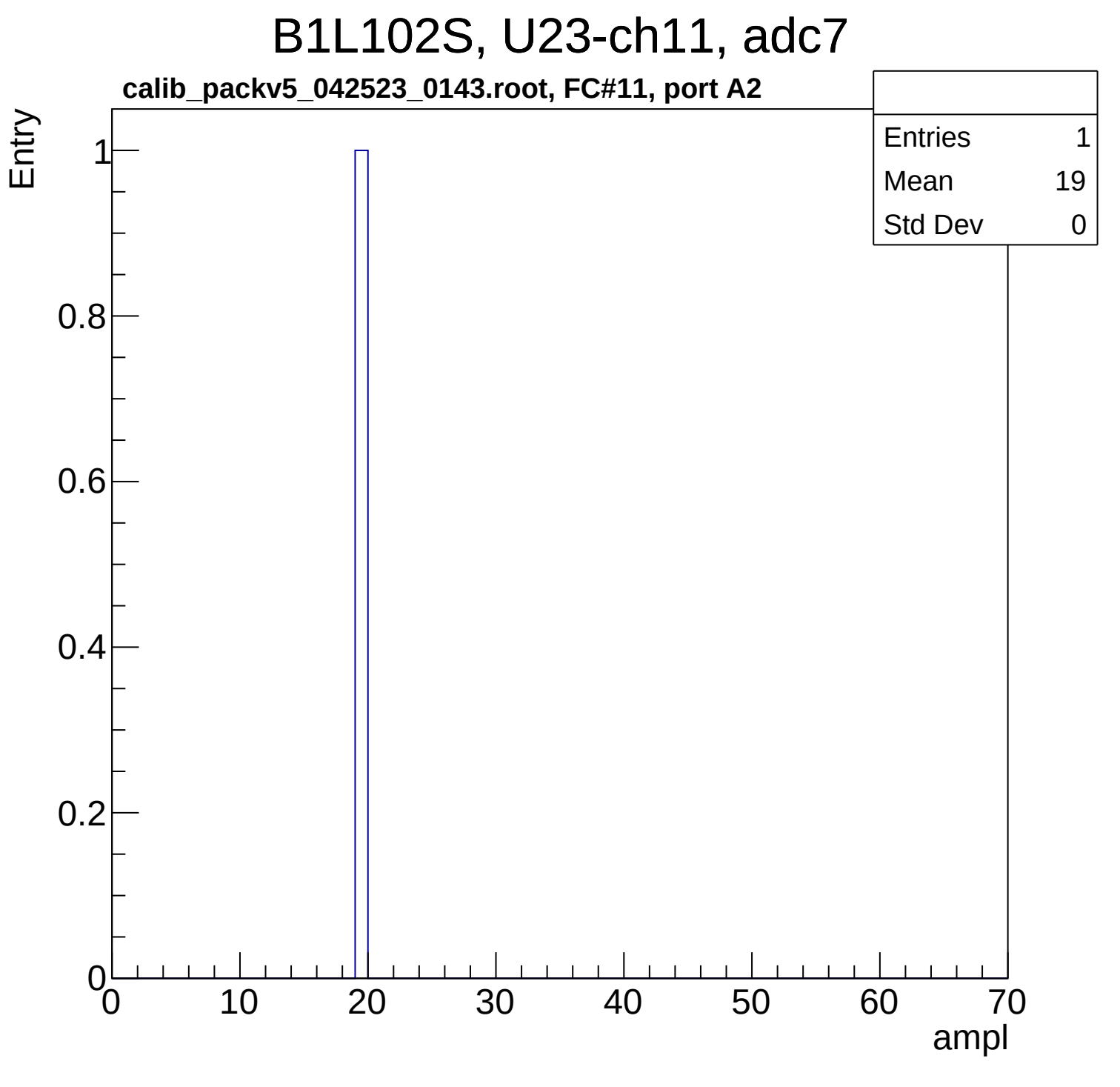

B1L102S, U23-ch11, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 43.33
Std Dev 2.959
Gaus mean : 43.9375
Gaus Width: 3.4939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

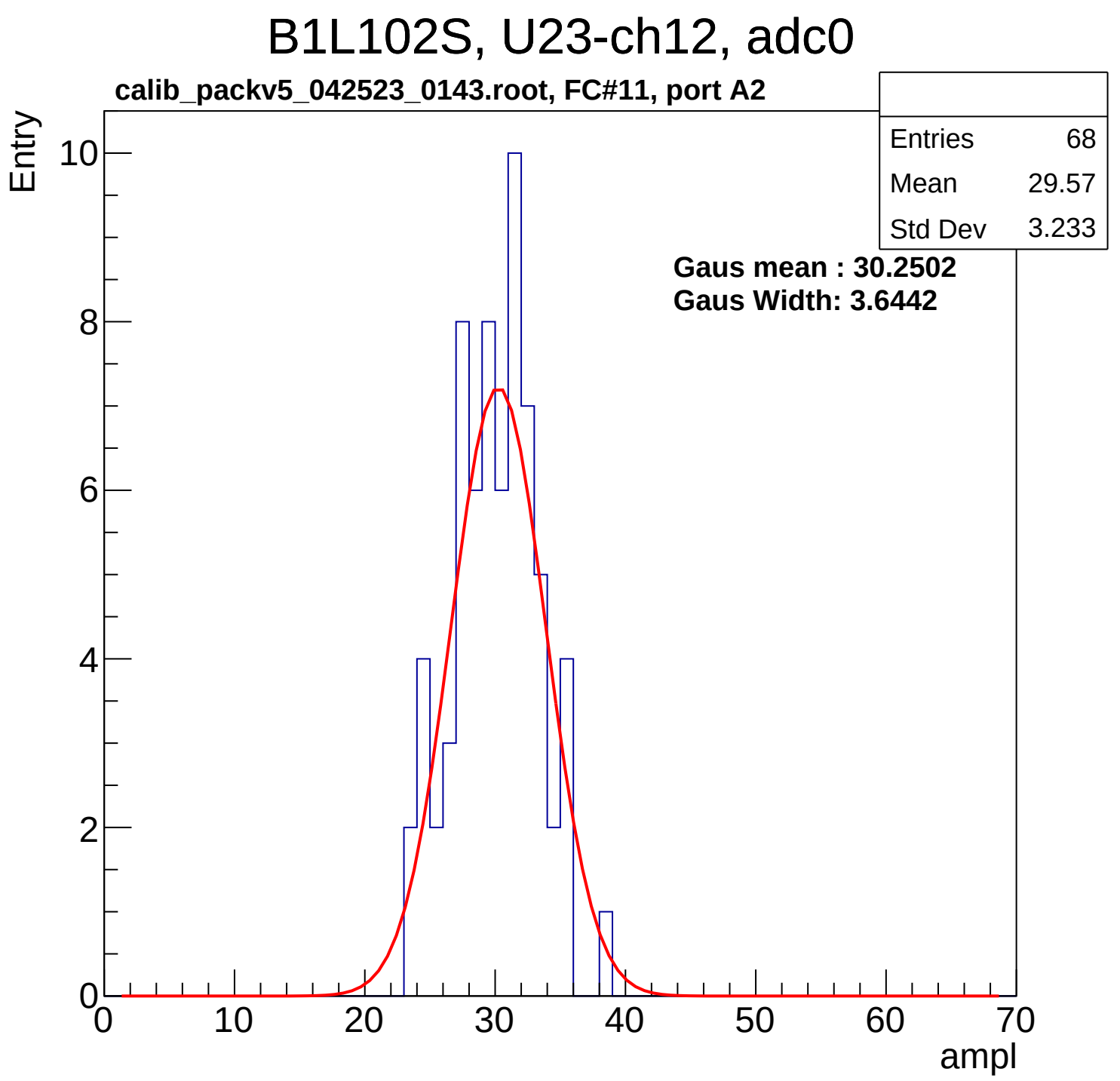

B1L102S, U23-ch11, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 49.22
Std Dev 2.977
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

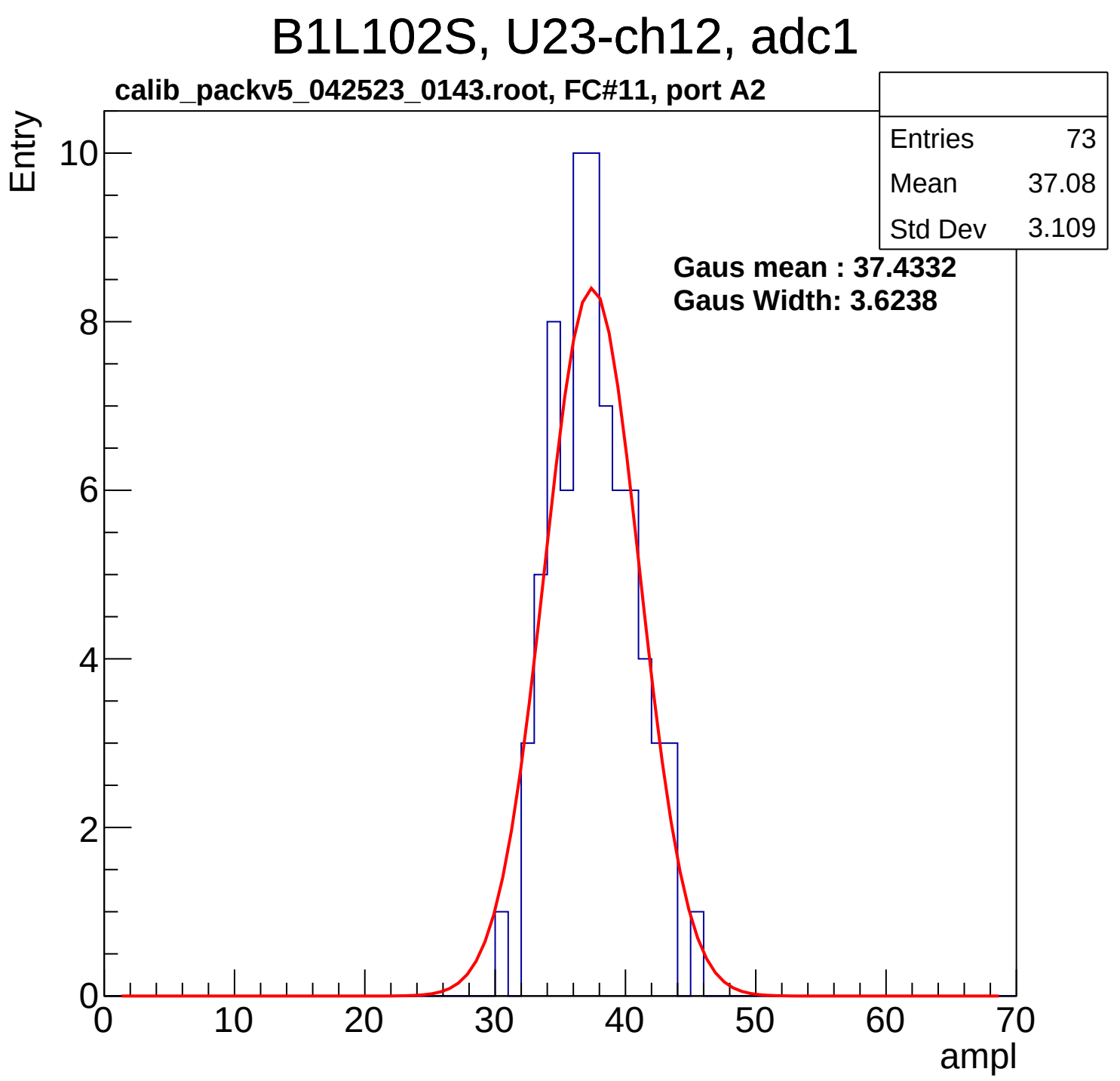

B1L102S, U23-ch11, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 54.48
Std Dev 7.45
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

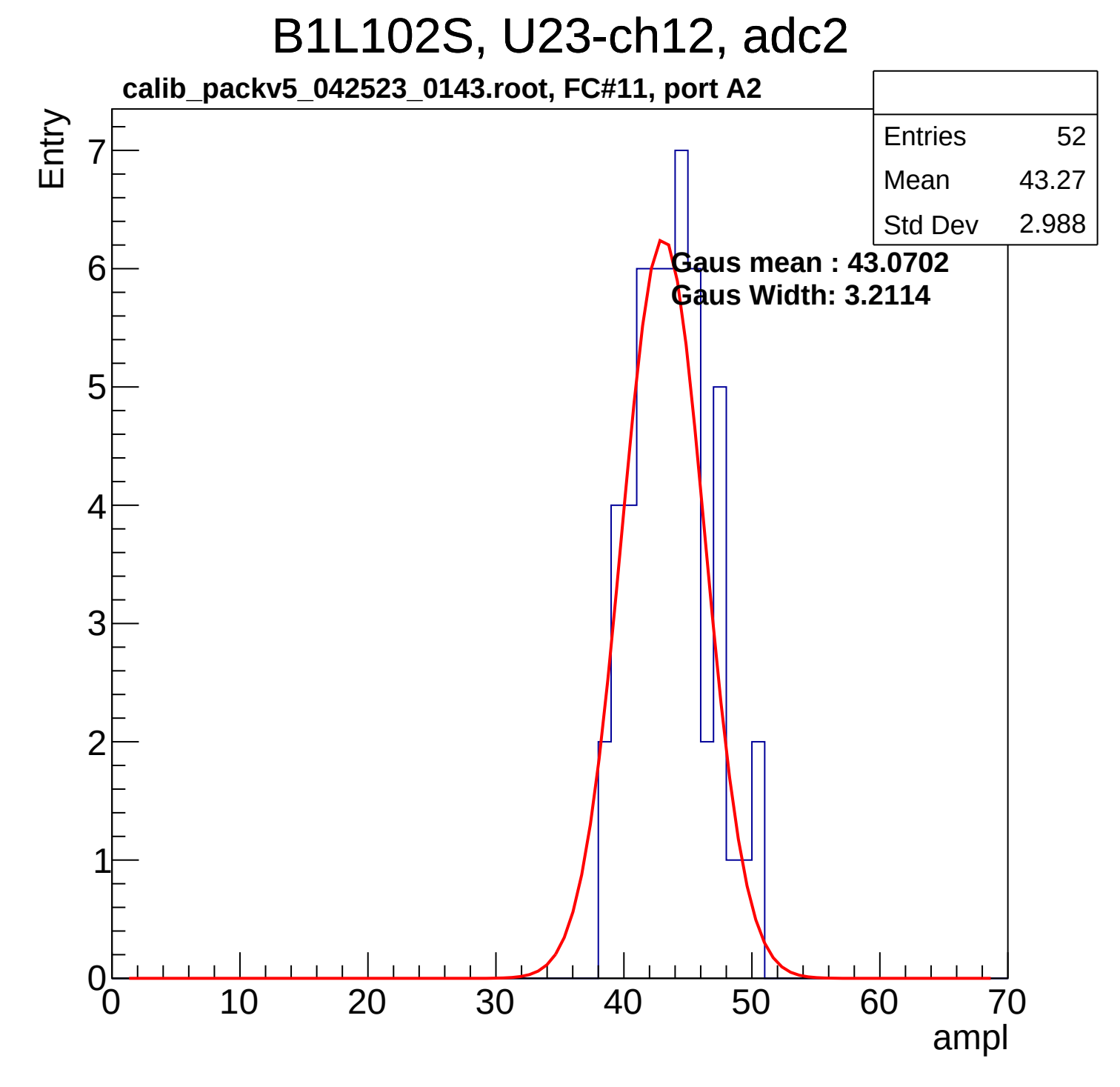

B1L102S, U23-ch11, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 59.98
Std Dev 2.665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

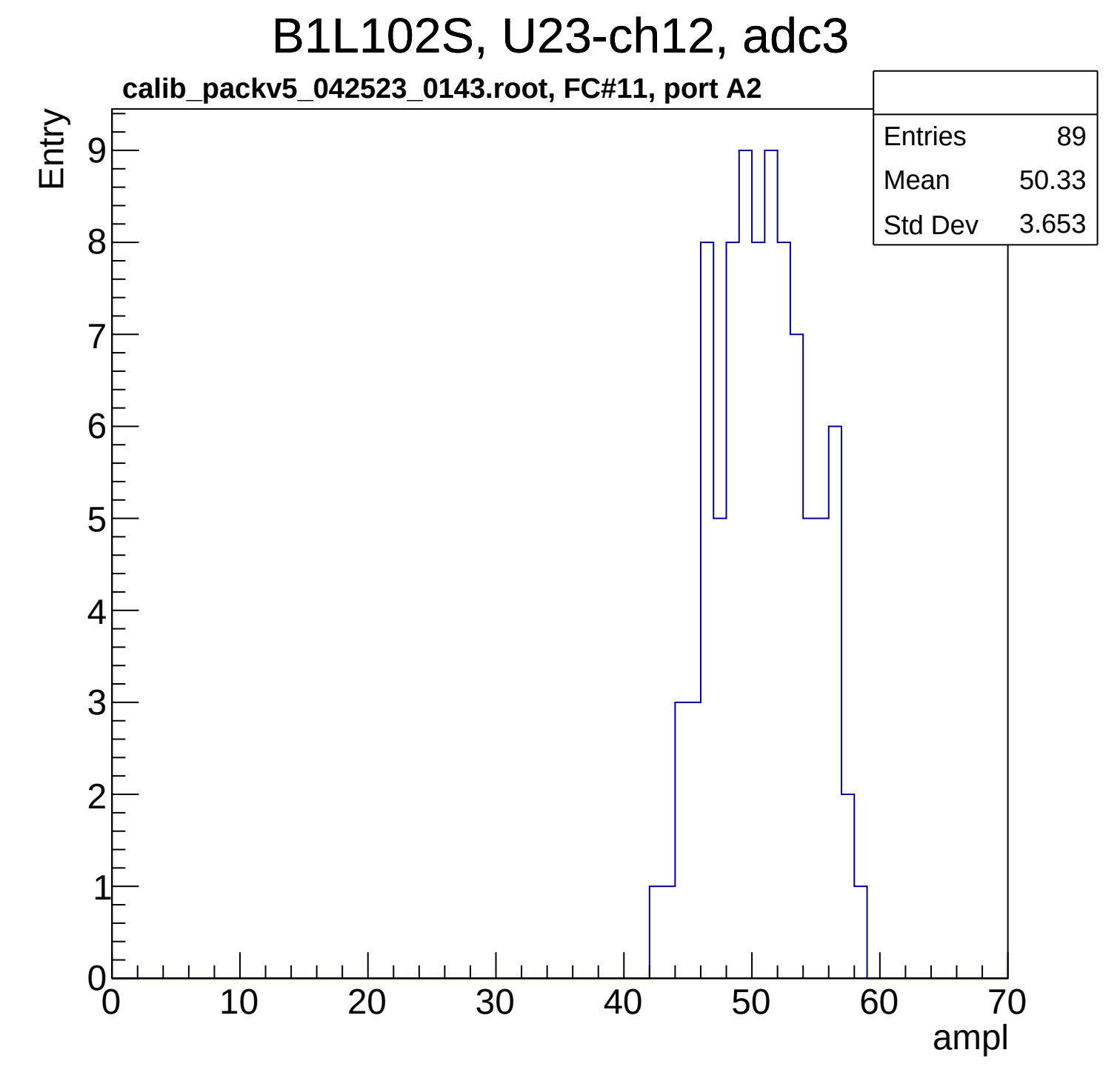

B1L102S, U23-ch11, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

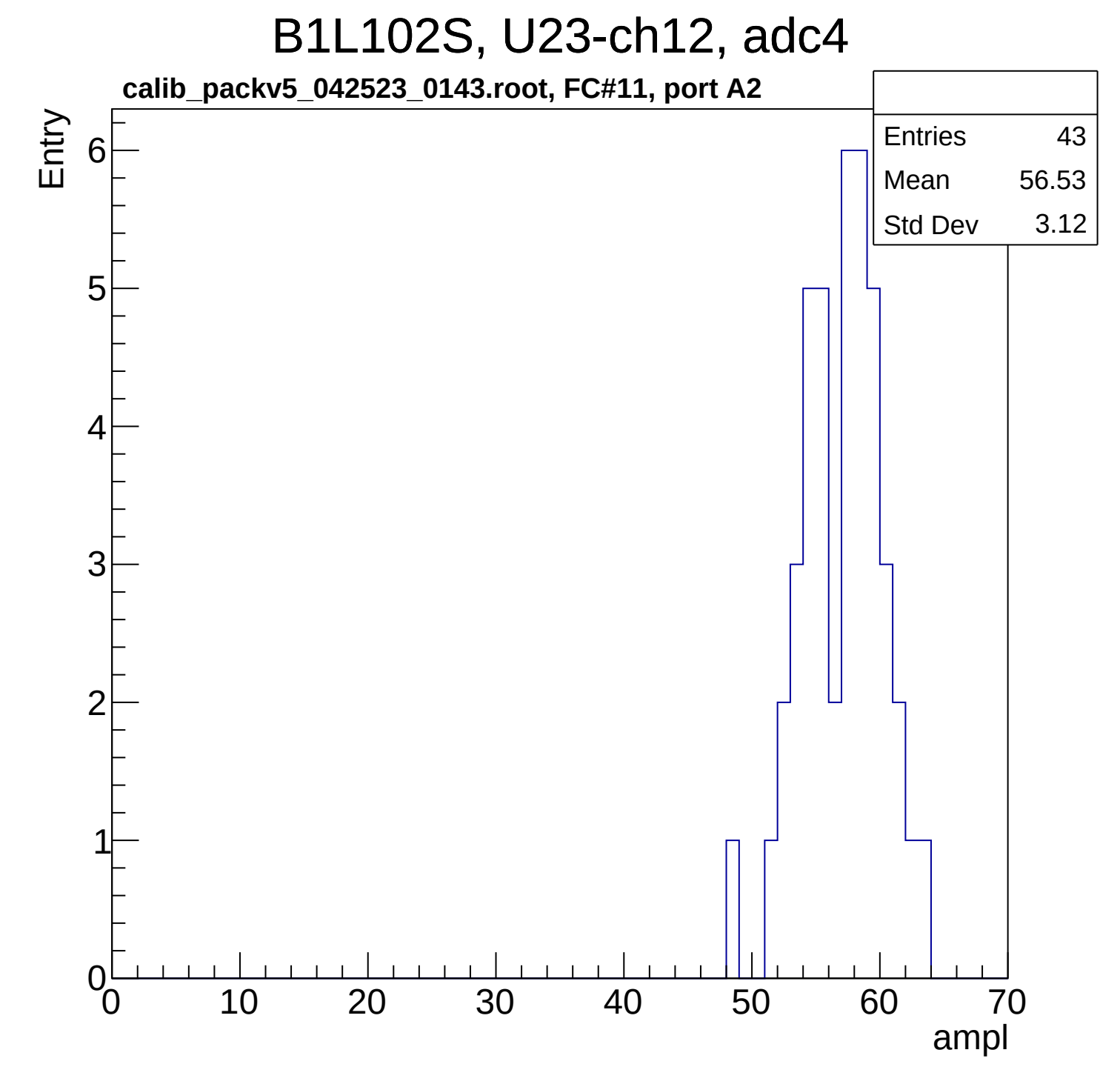

B1L102S, U23-ch11, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

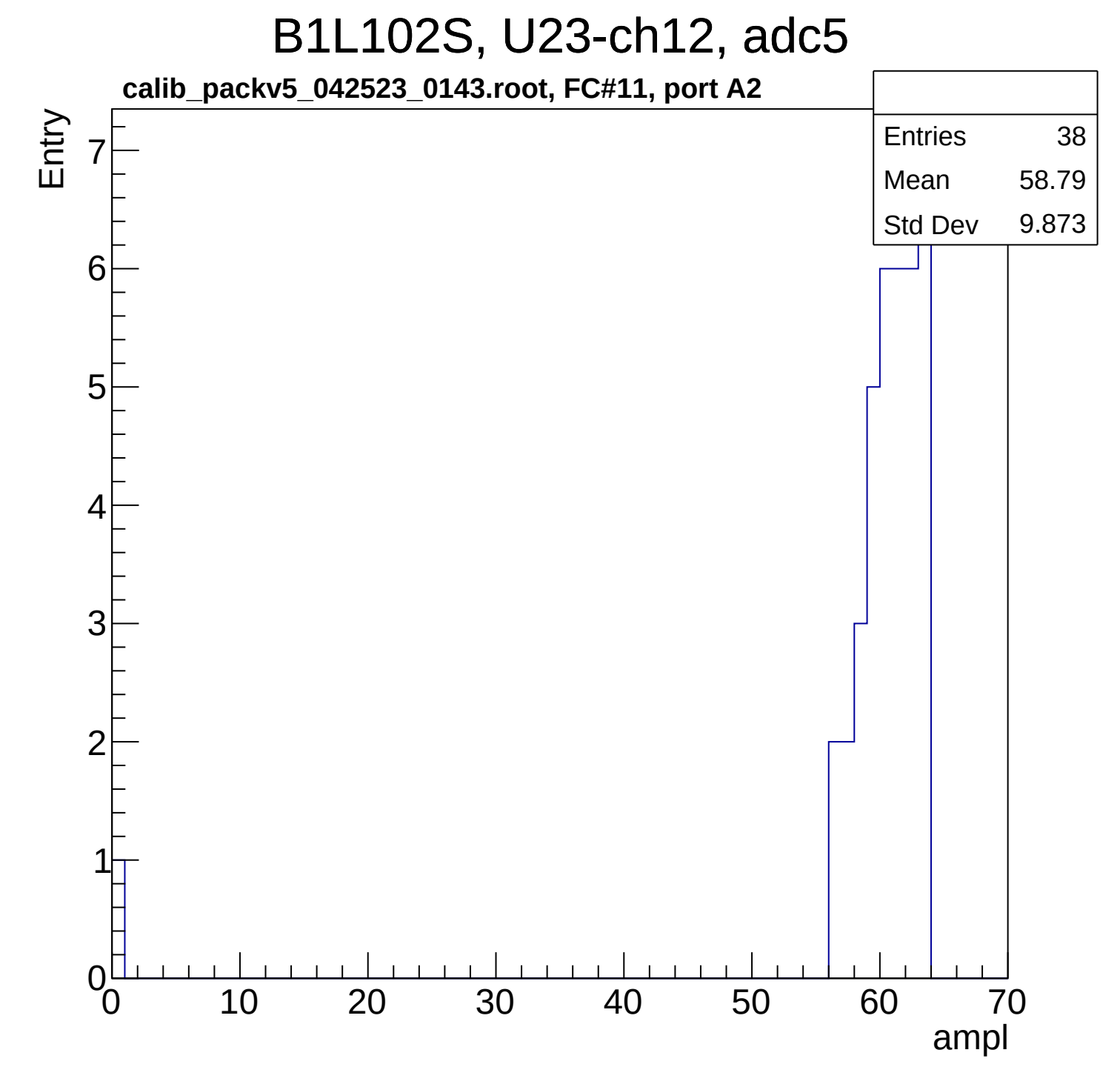

B1L102S, U23-ch12, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.57
Std Dev 3.233
Gaus mean : 30.2502
Gaus Width: 3.6442
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

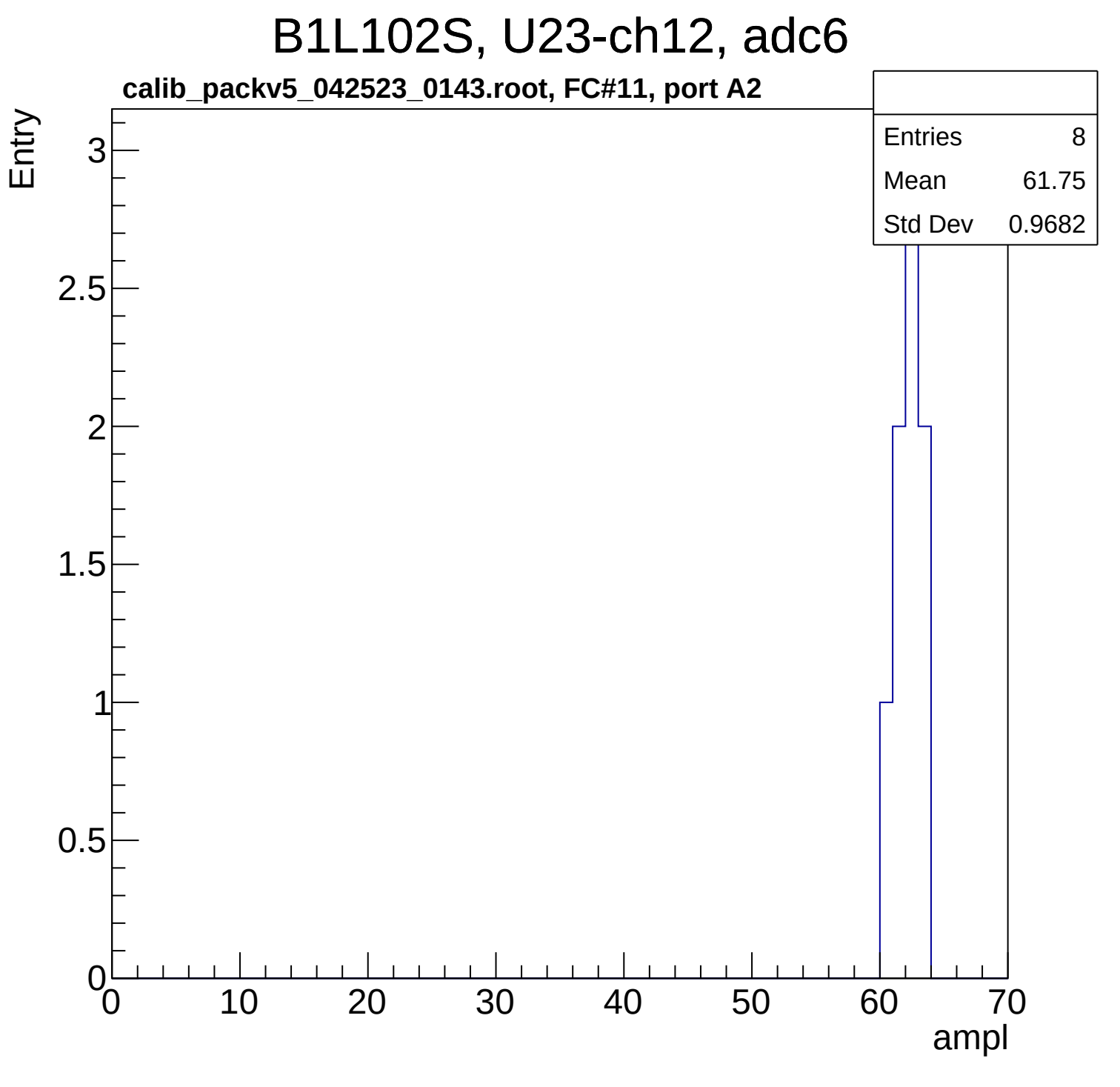

B1L102S, U23-ch12, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 37.08
Std Dev 3.109
Gaus mean : 37.4332
Gaus Width: 3.6238
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

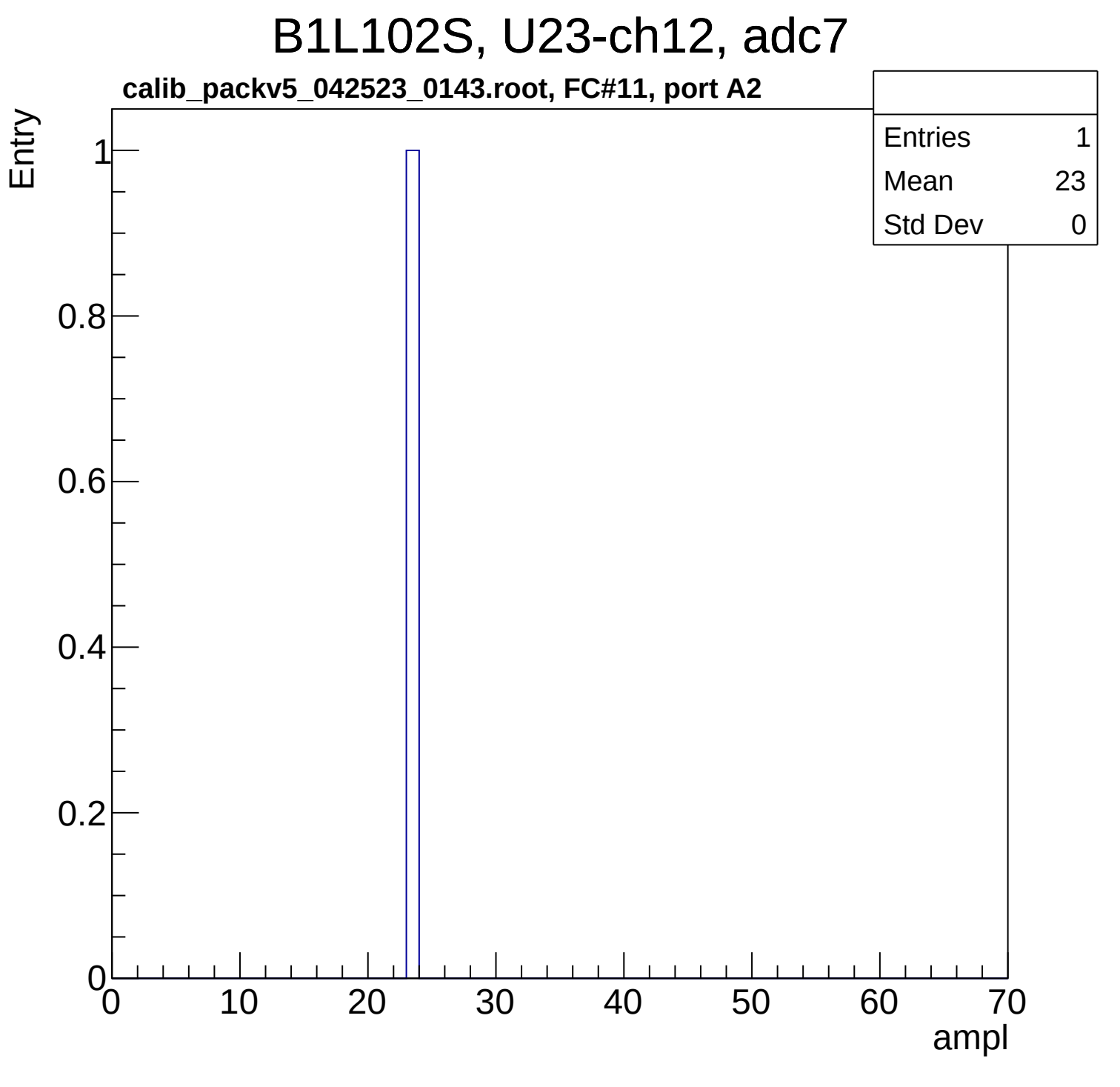

B1L102S, U23-ch12, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 43.27
Std Dev 2.988
Gaus mean : 43.0702
Gaus Width: 3.2114
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

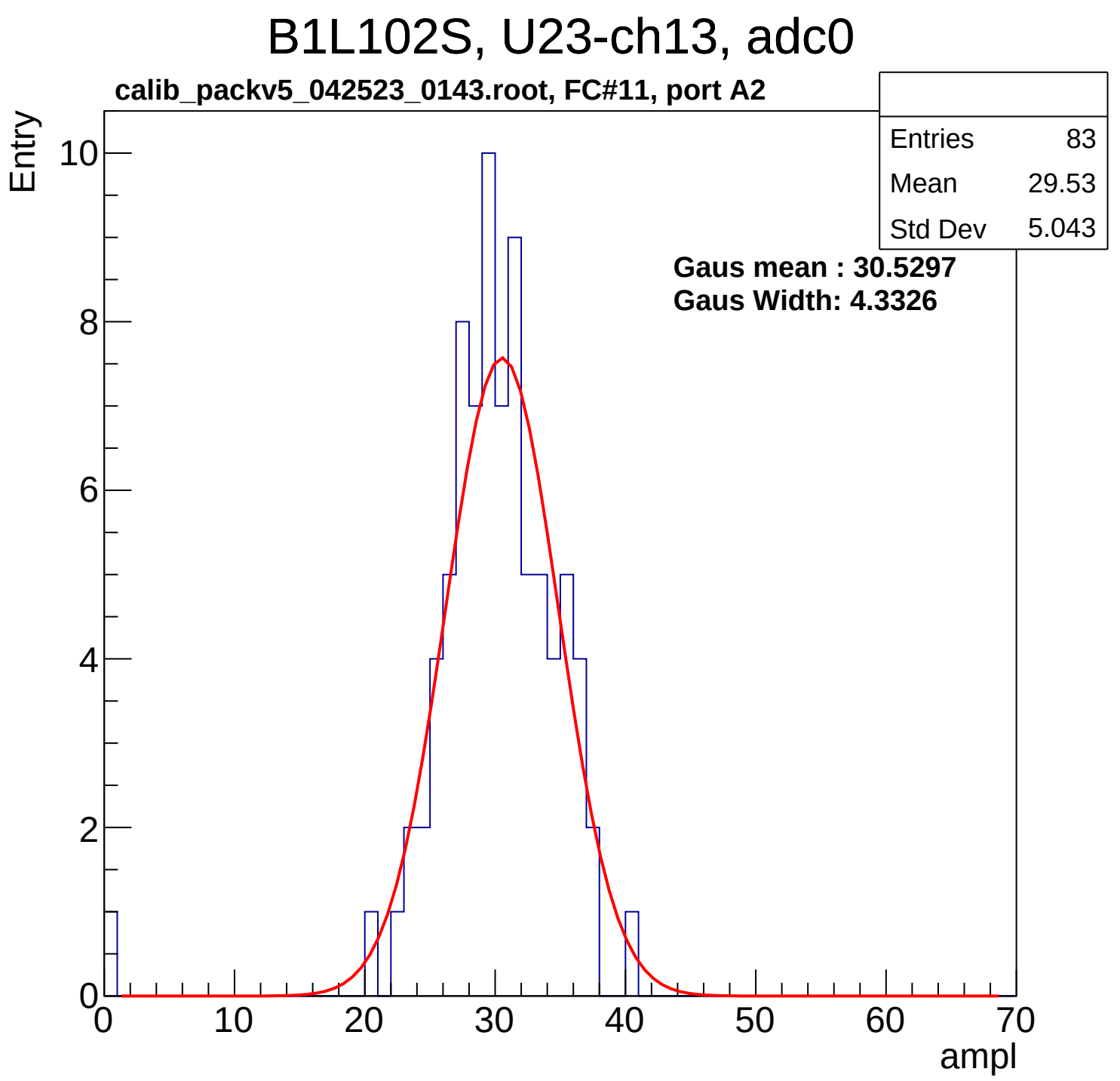

B1L102S, U23-ch12, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 50.33
Std Dev 3.653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

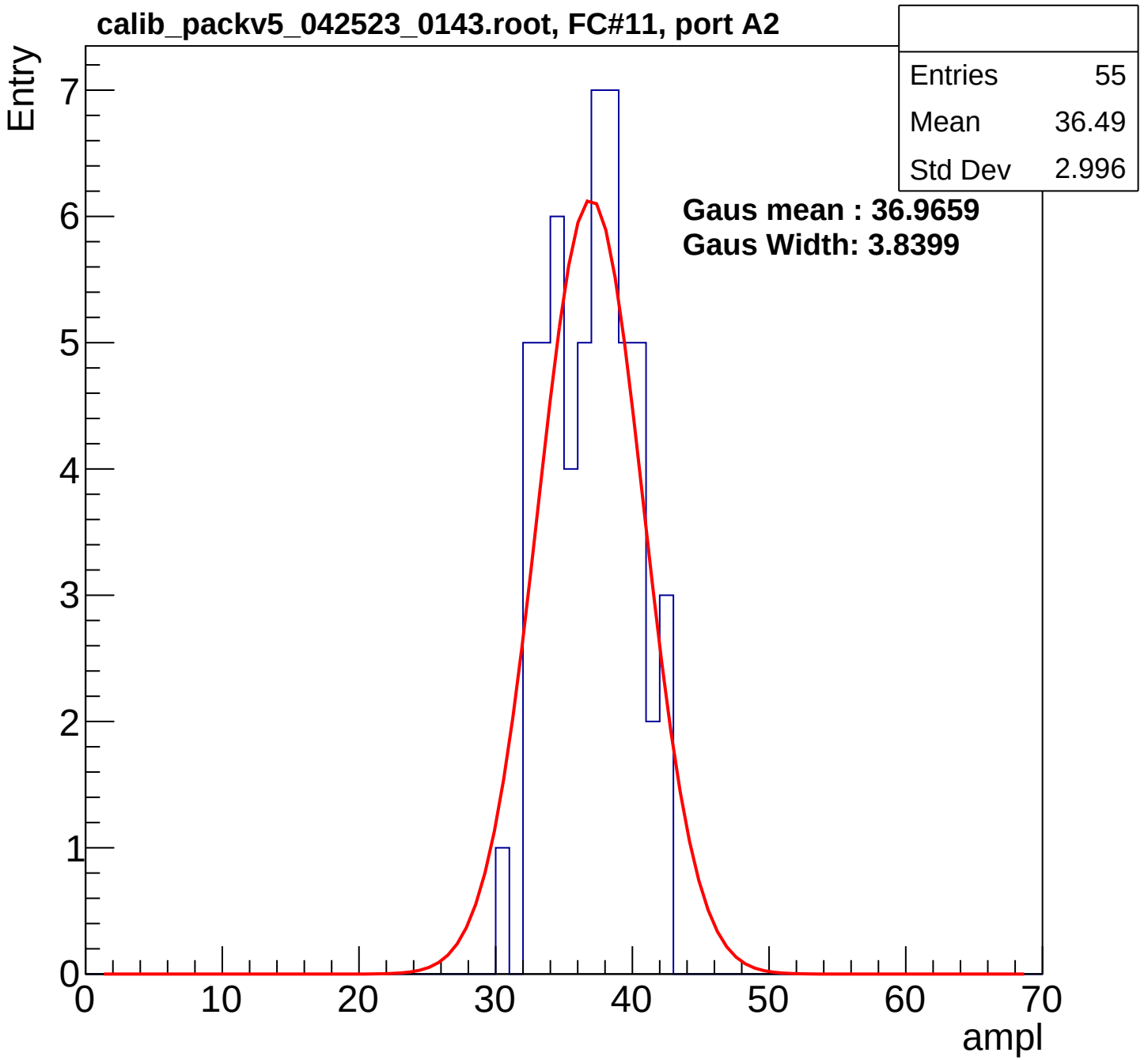

B1L102S, U23-ch12, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 56.53
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

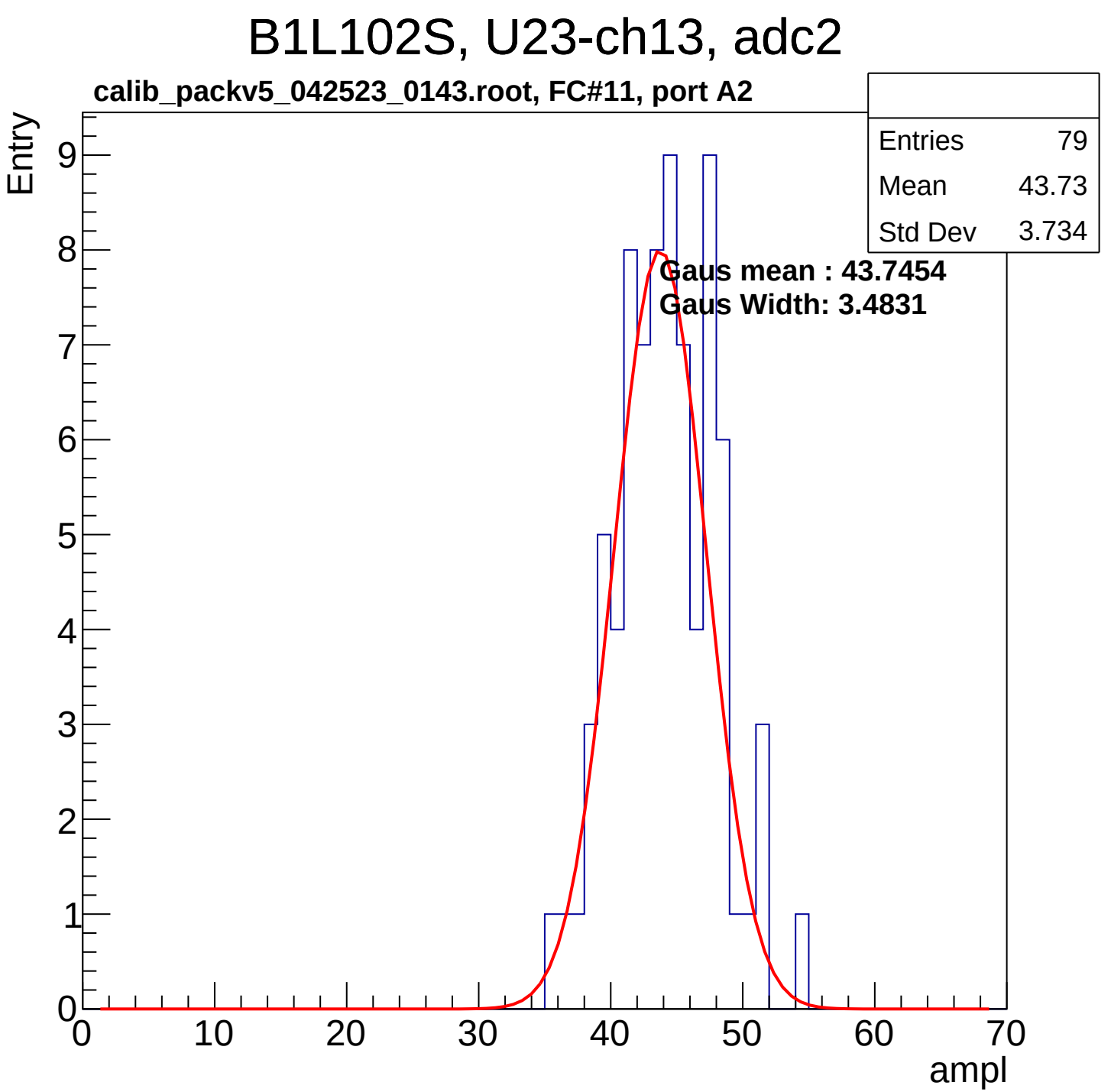

B1L102S, U23-ch12, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.79
Std Dev 9.873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

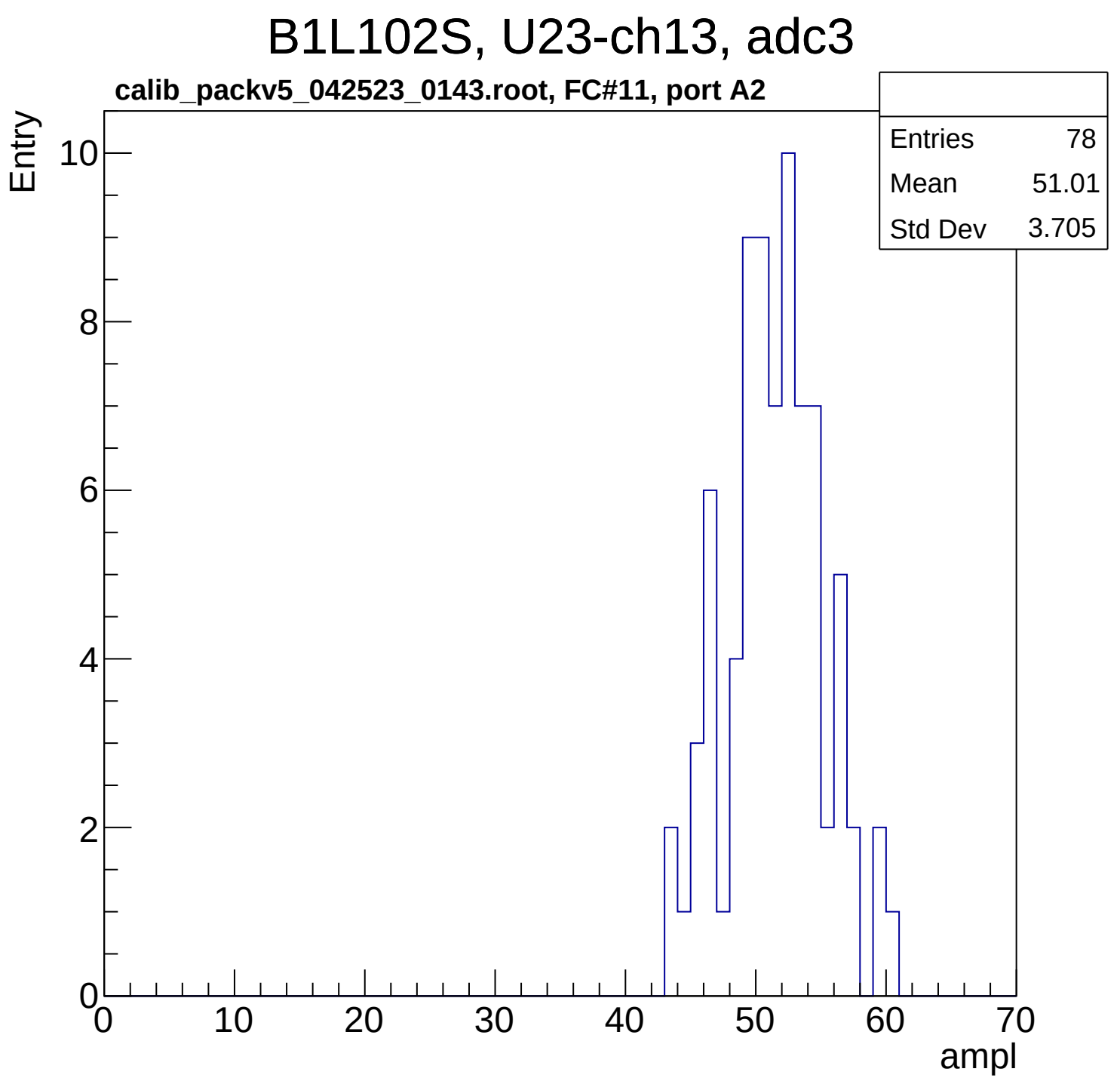

B1L102S, U23-ch12, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.75
Std Dev 0.9682
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

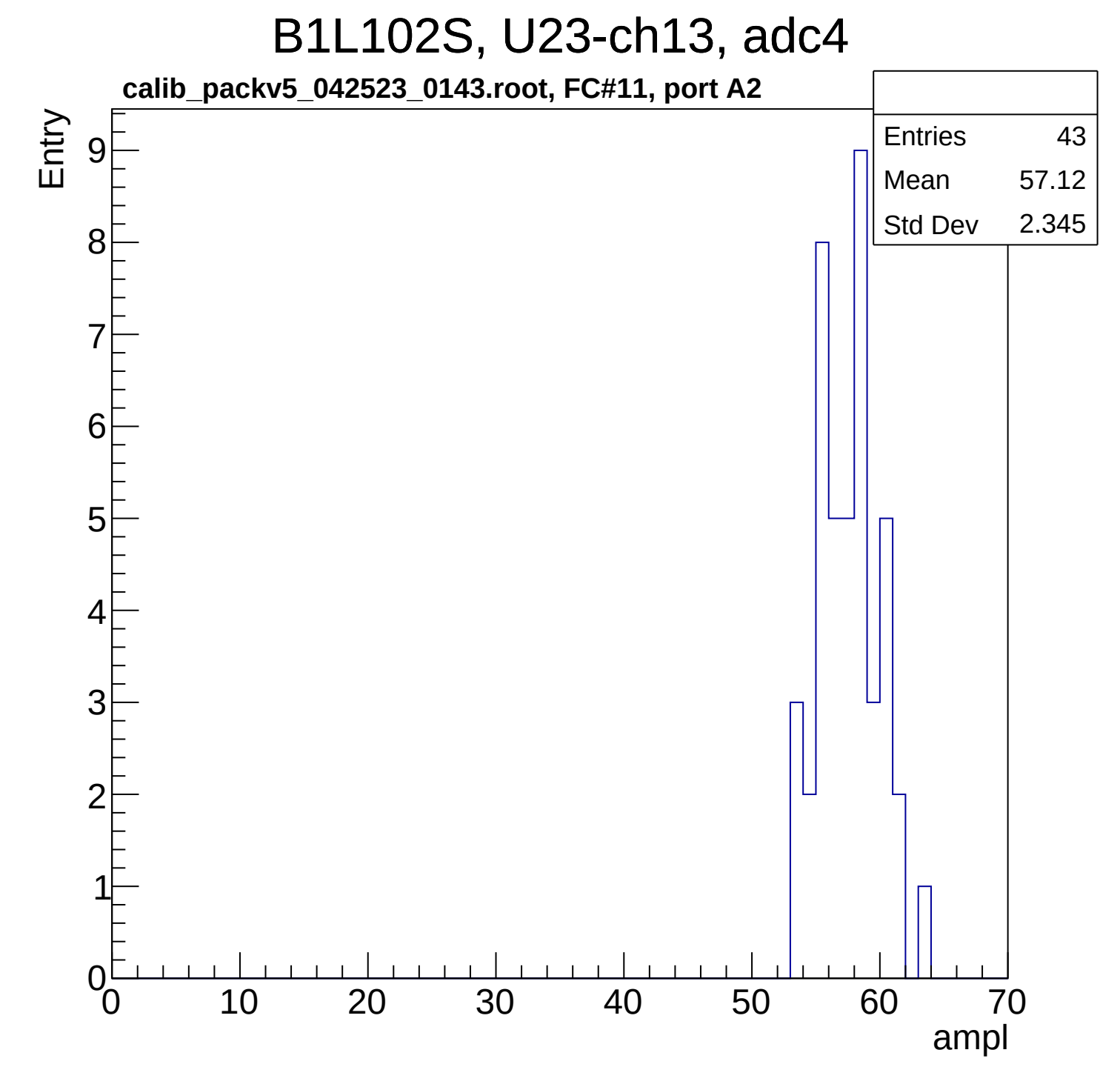

B1L102S, U23-ch12, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

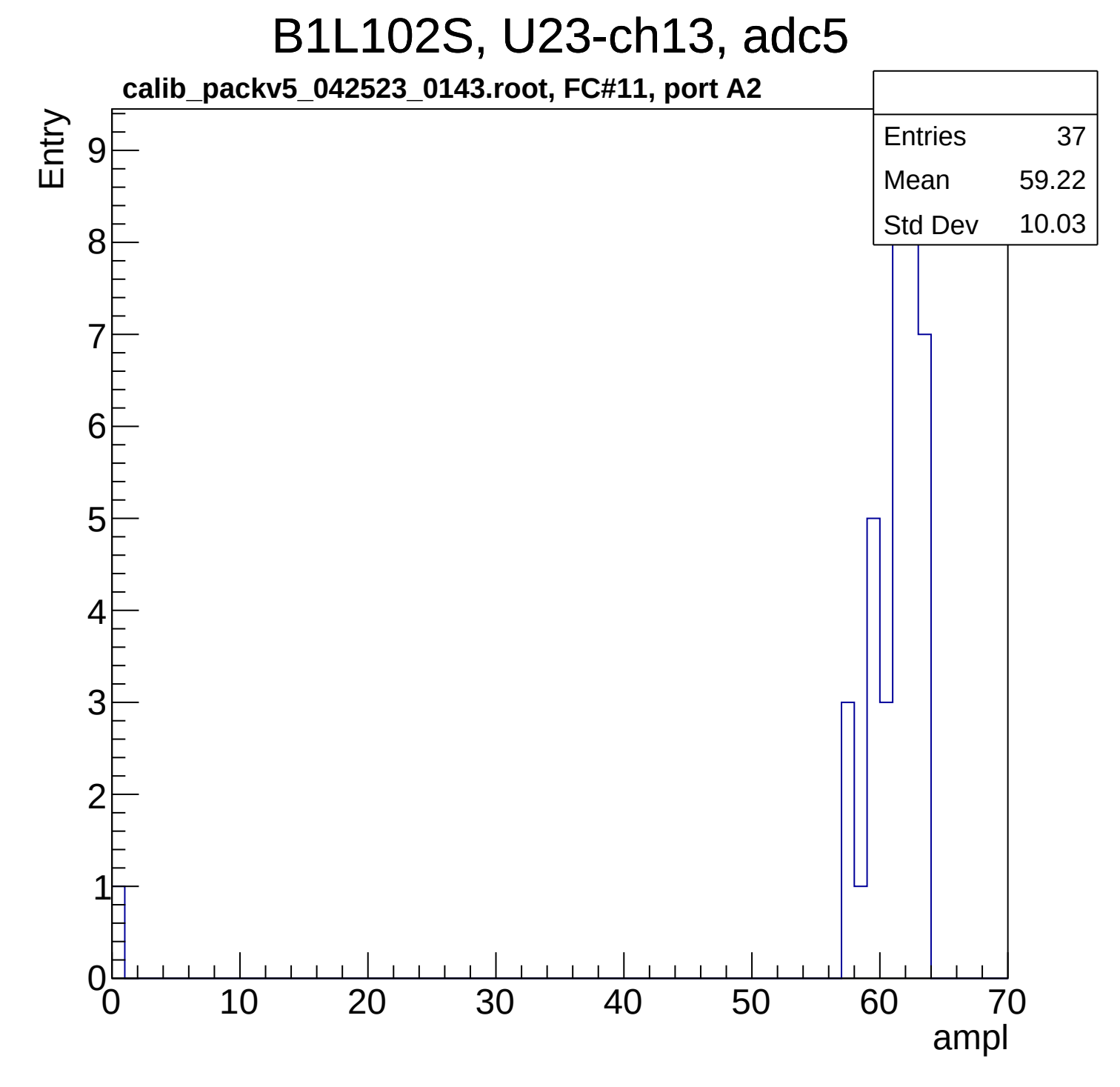

B1L102S, U23-ch13, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 29.53
Std Dev 5.043
Gaus mean : 30.5297
Gaus Width: 4.3326
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

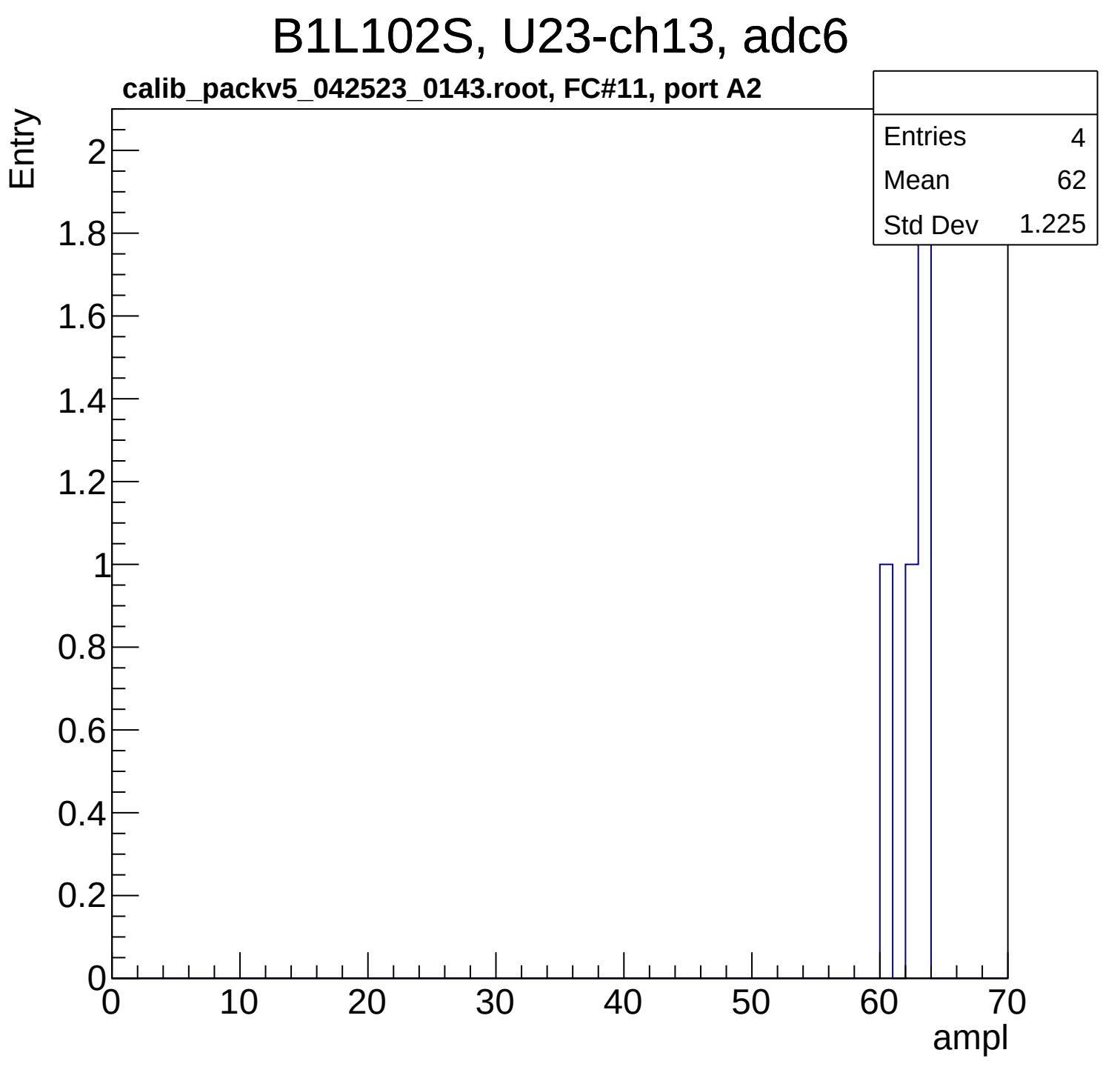

B1L102S, U23-ch13, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 36.49
Std Dev 2.996
Gaus mean : 36.9659
Gaus Width: 3.8399
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

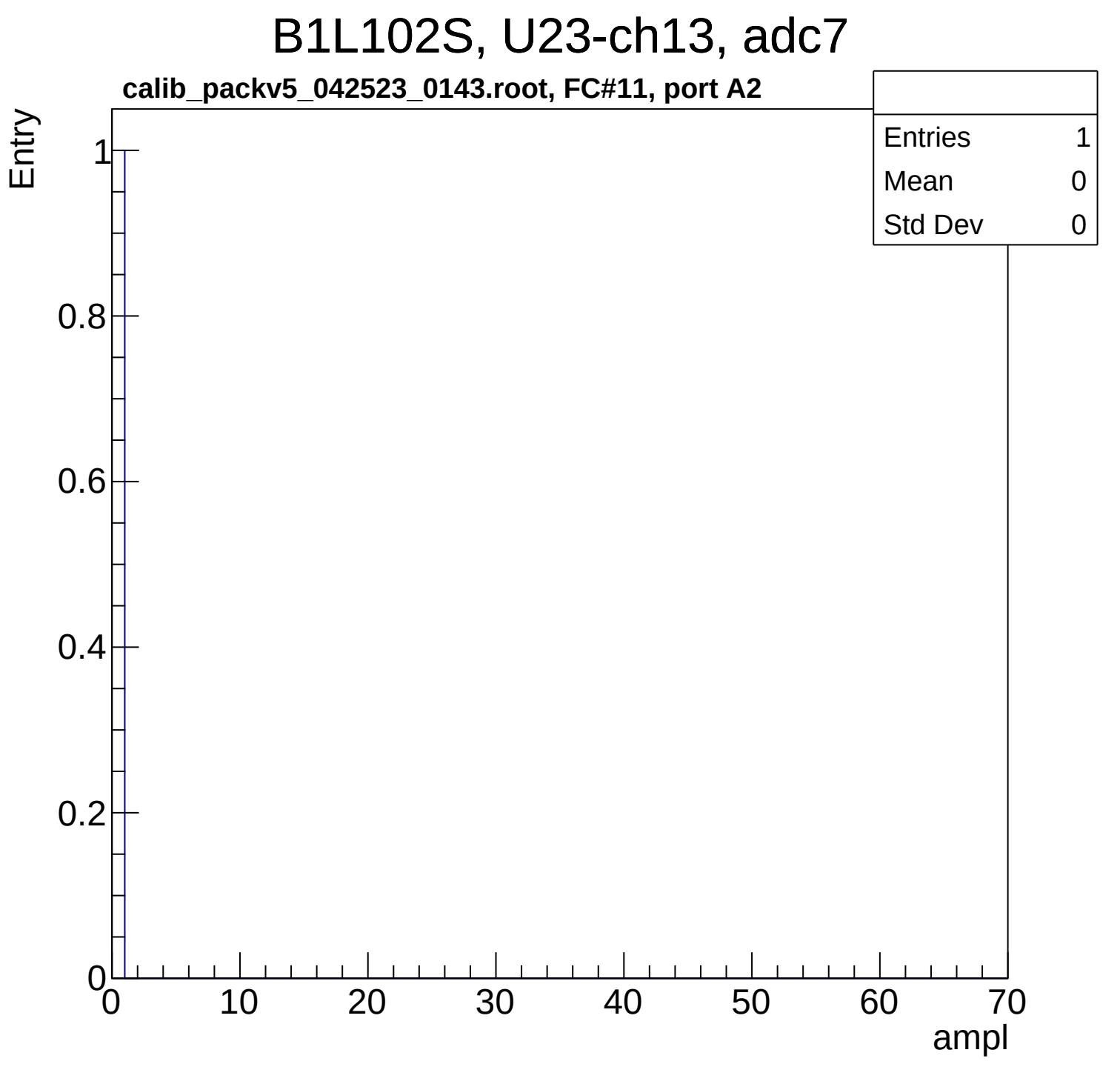

B1L102S, U23-ch13, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 43.73
Std Dev 3.734
Gaus mean : 43.7454
Gaus Width: 3.4831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

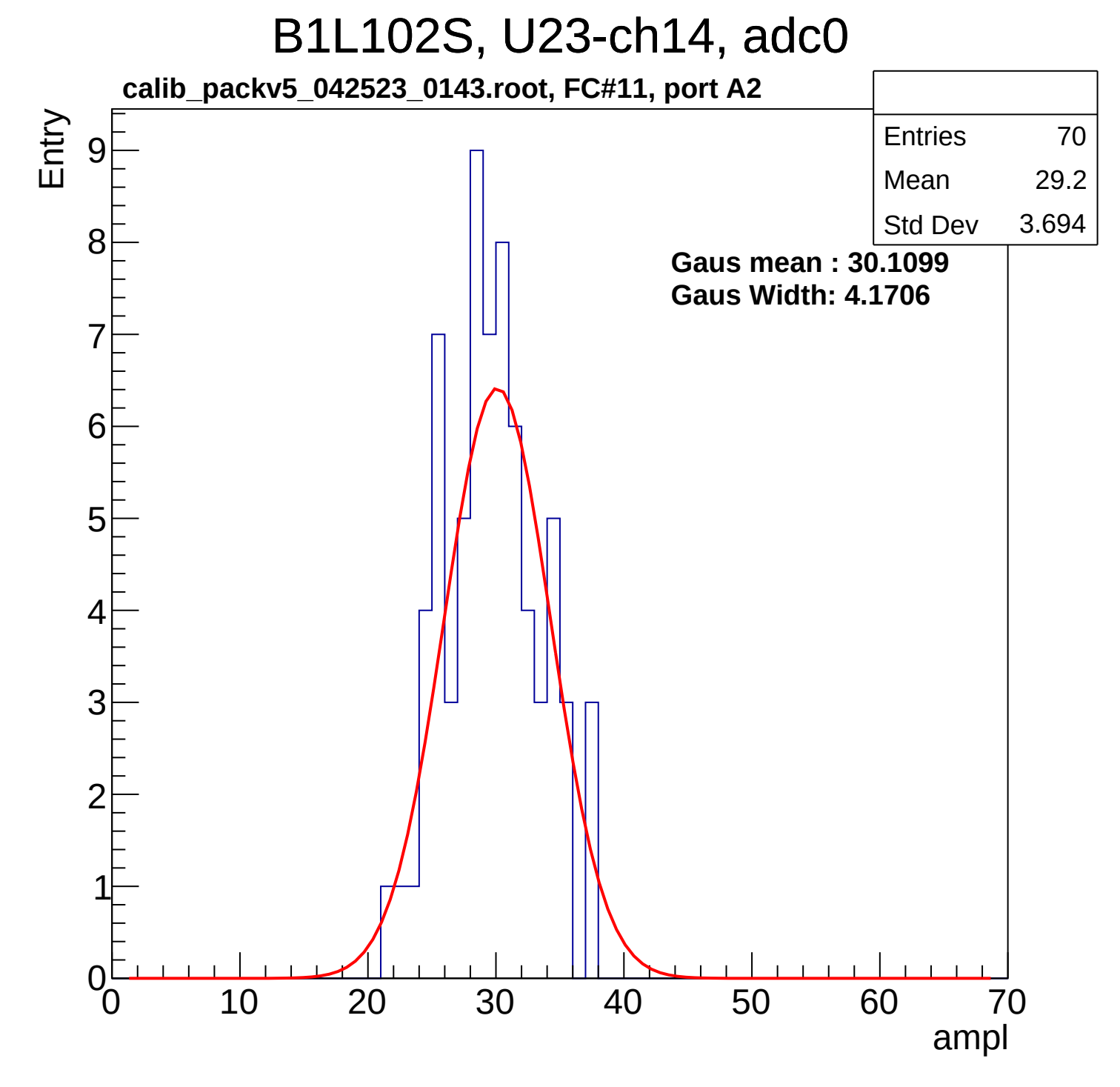

B1L102S, U23-ch13, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 51.01
Std Dev 3.705
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

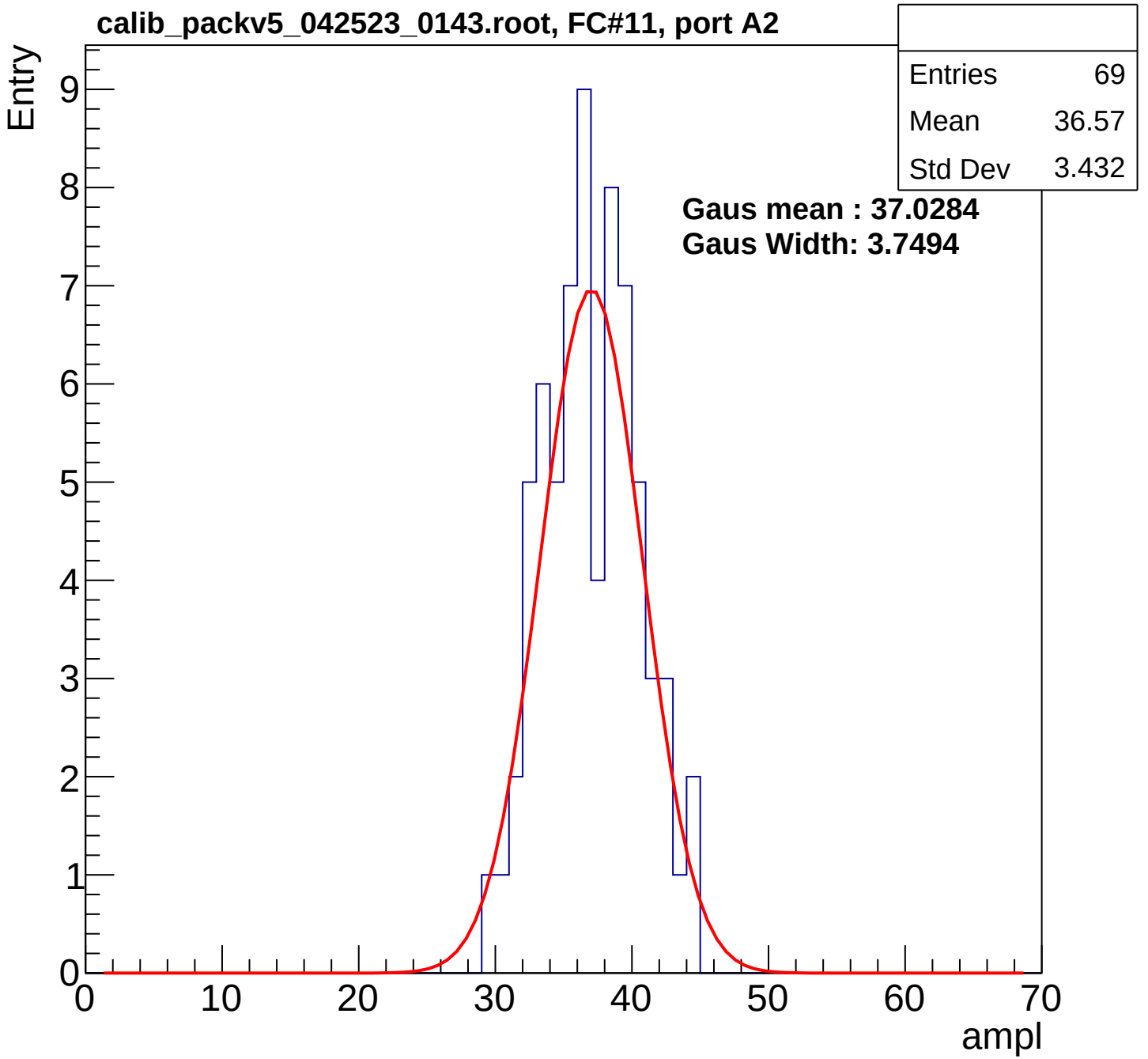

B1L102S, U23-ch13, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 57.12
Std Dev 2.345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

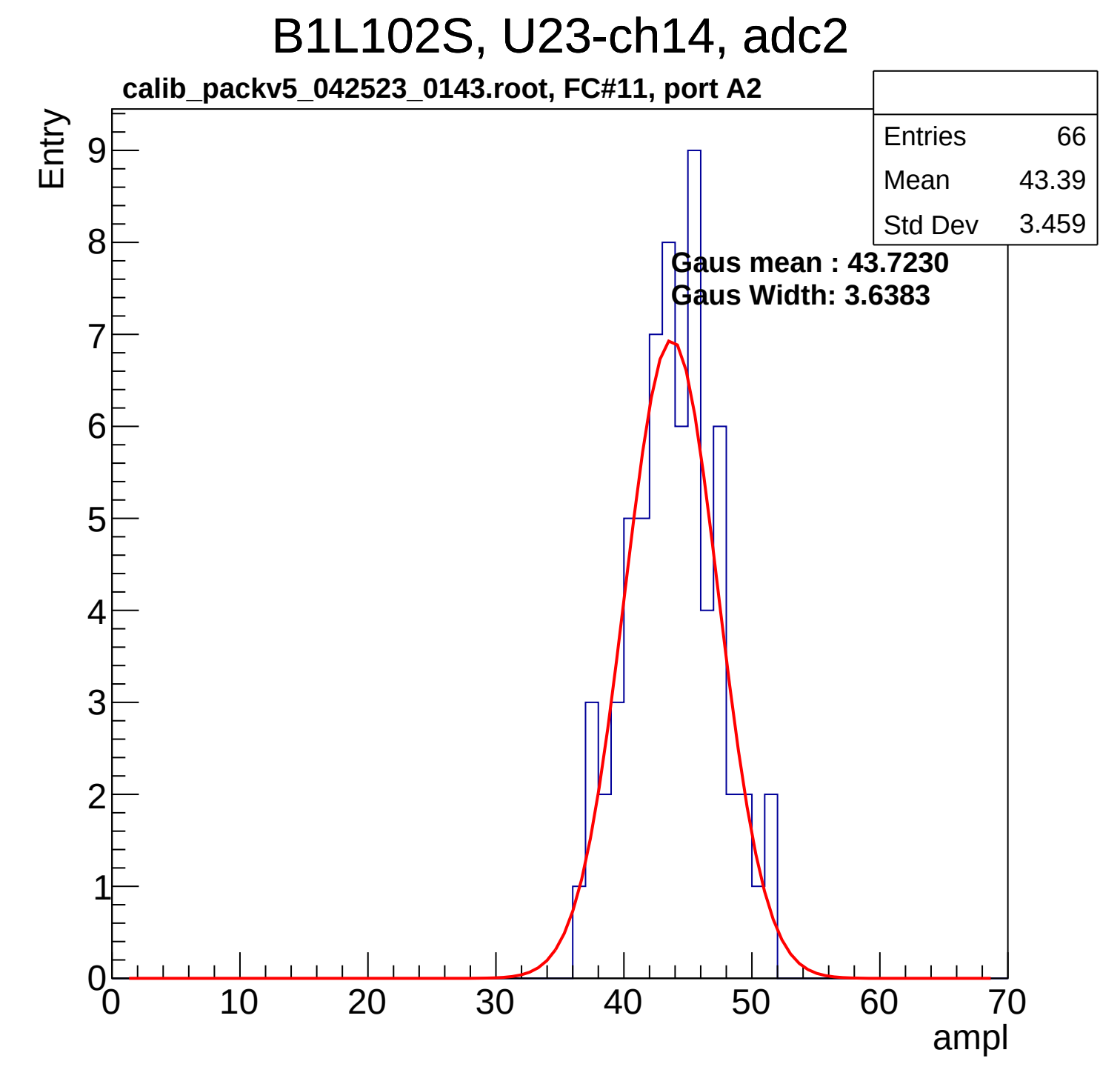

B1L102S, U23-ch13, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 59.22
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

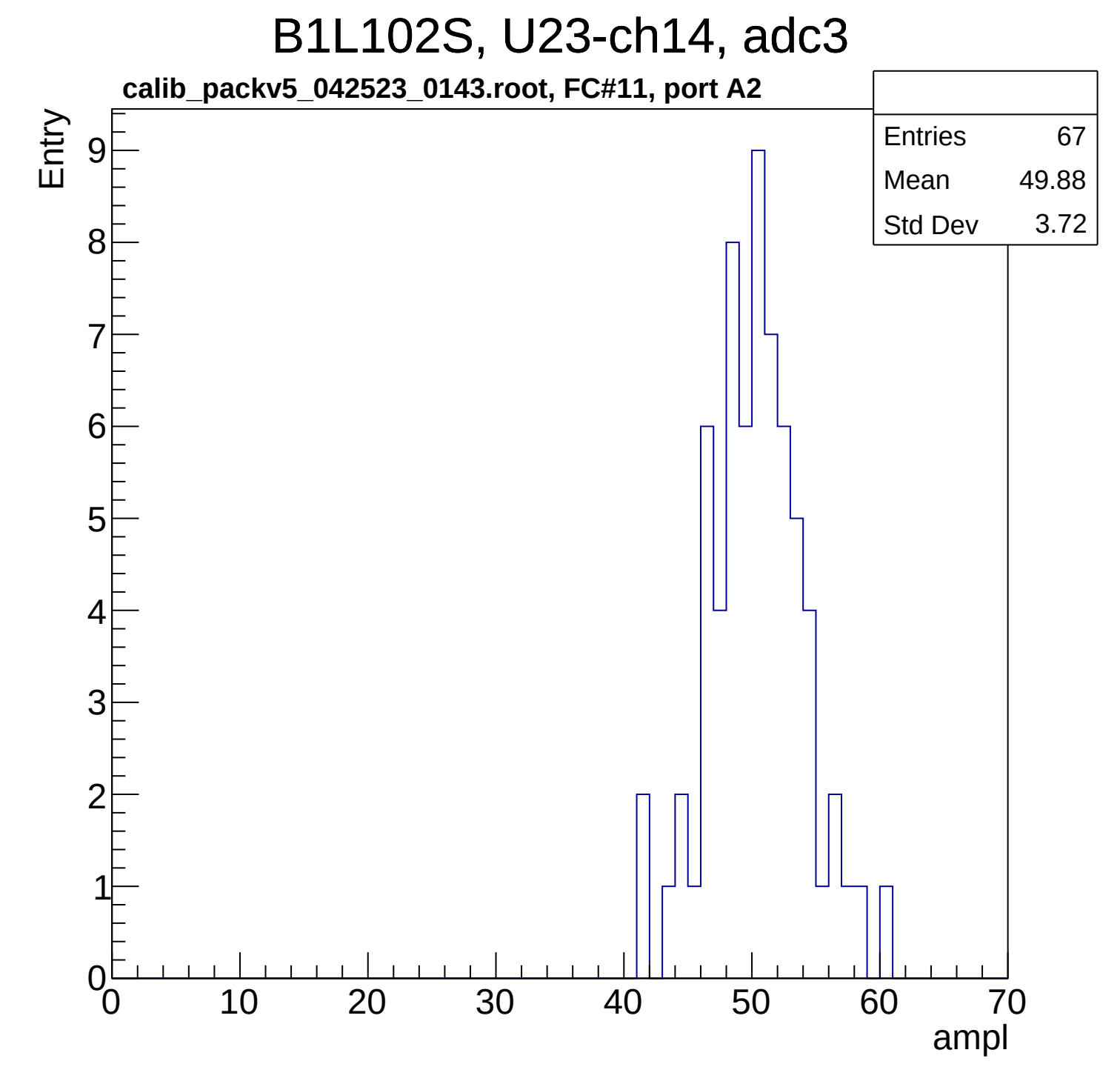

B1L102S, U23-ch13, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

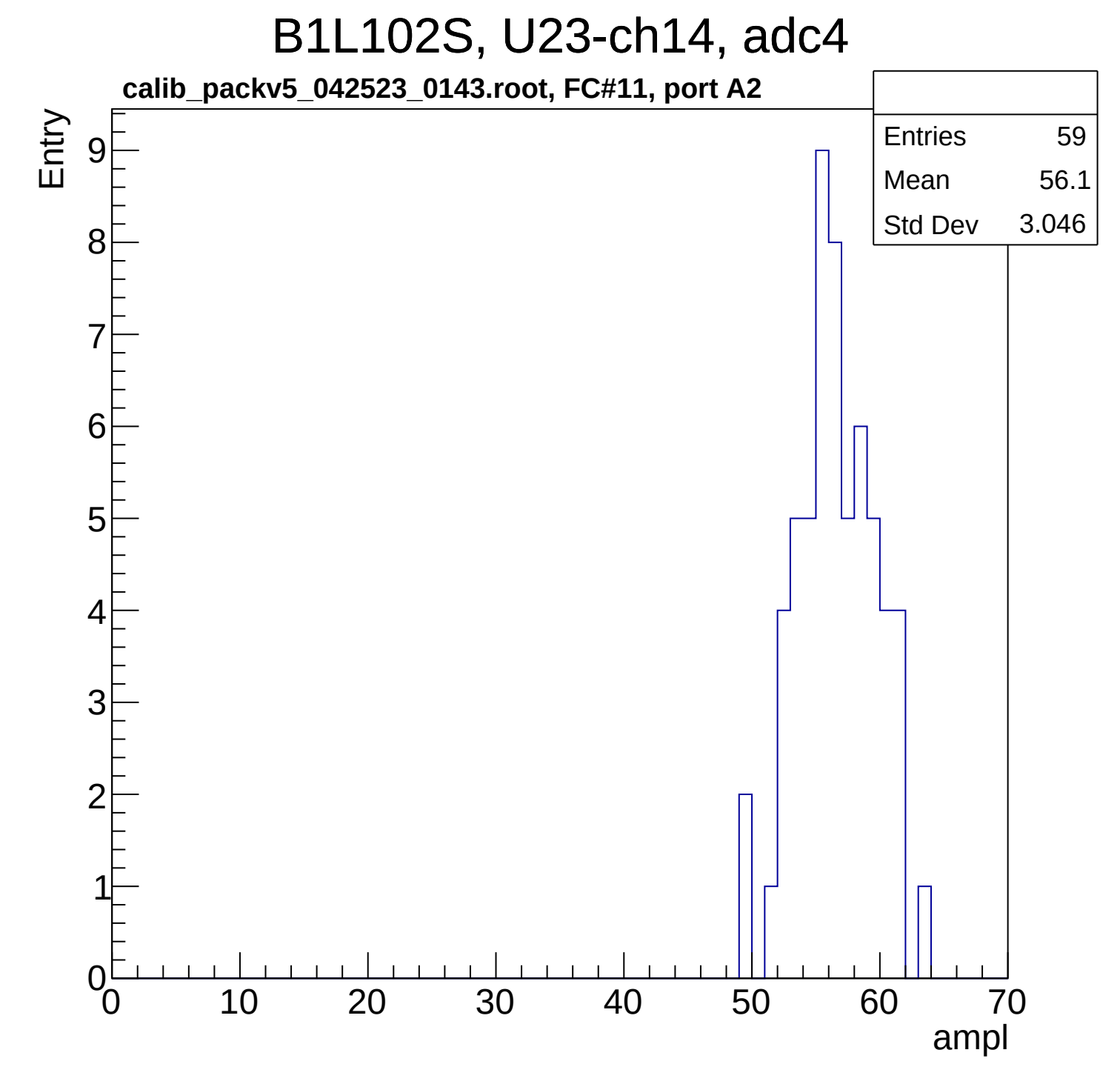

B1L102S, U23-ch13, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

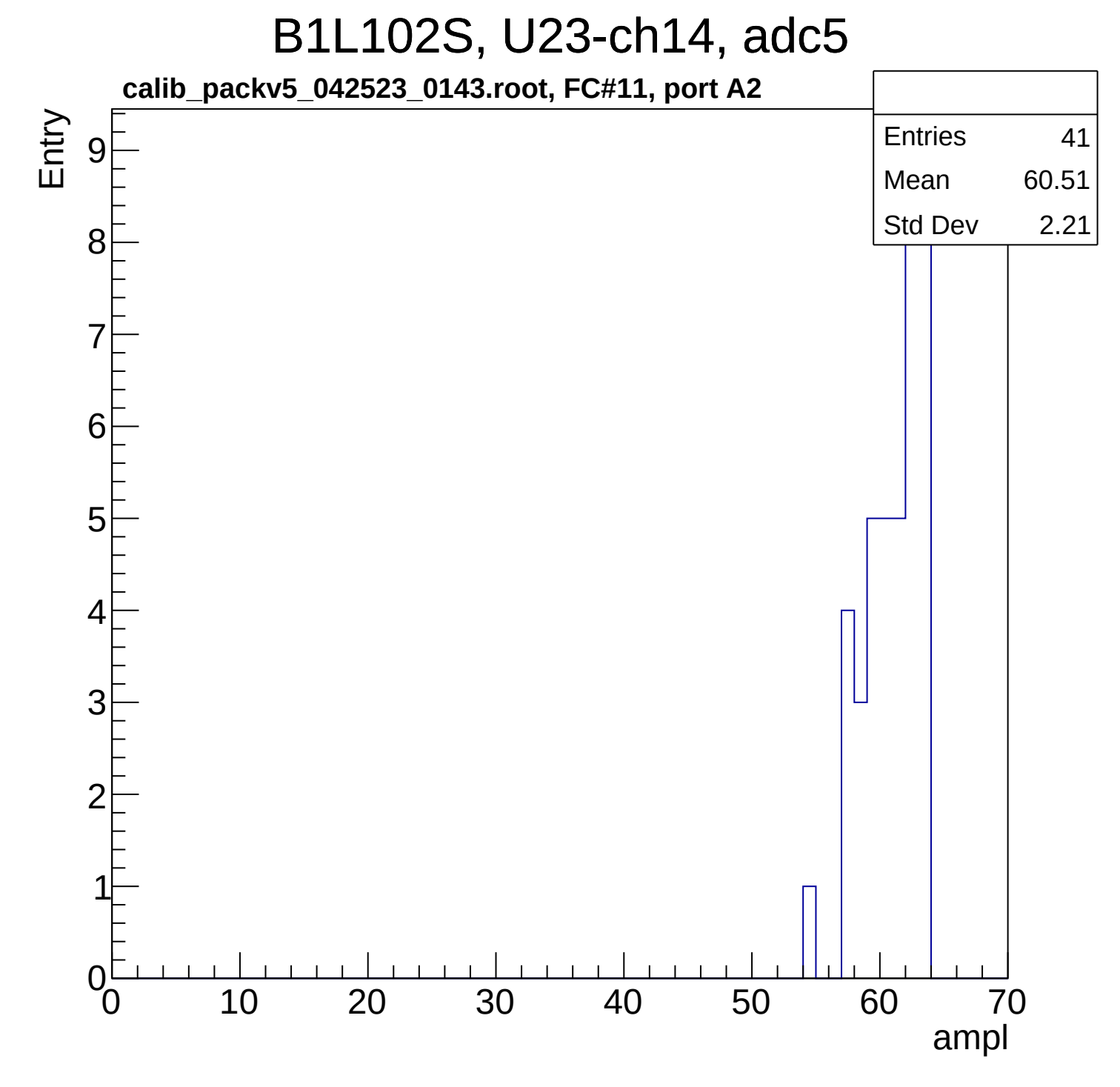

B1L102S, U23-ch14, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 29.2
Std Dev 3.694
Gaus mean : 30.1099
Gaus Width: 4.1706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

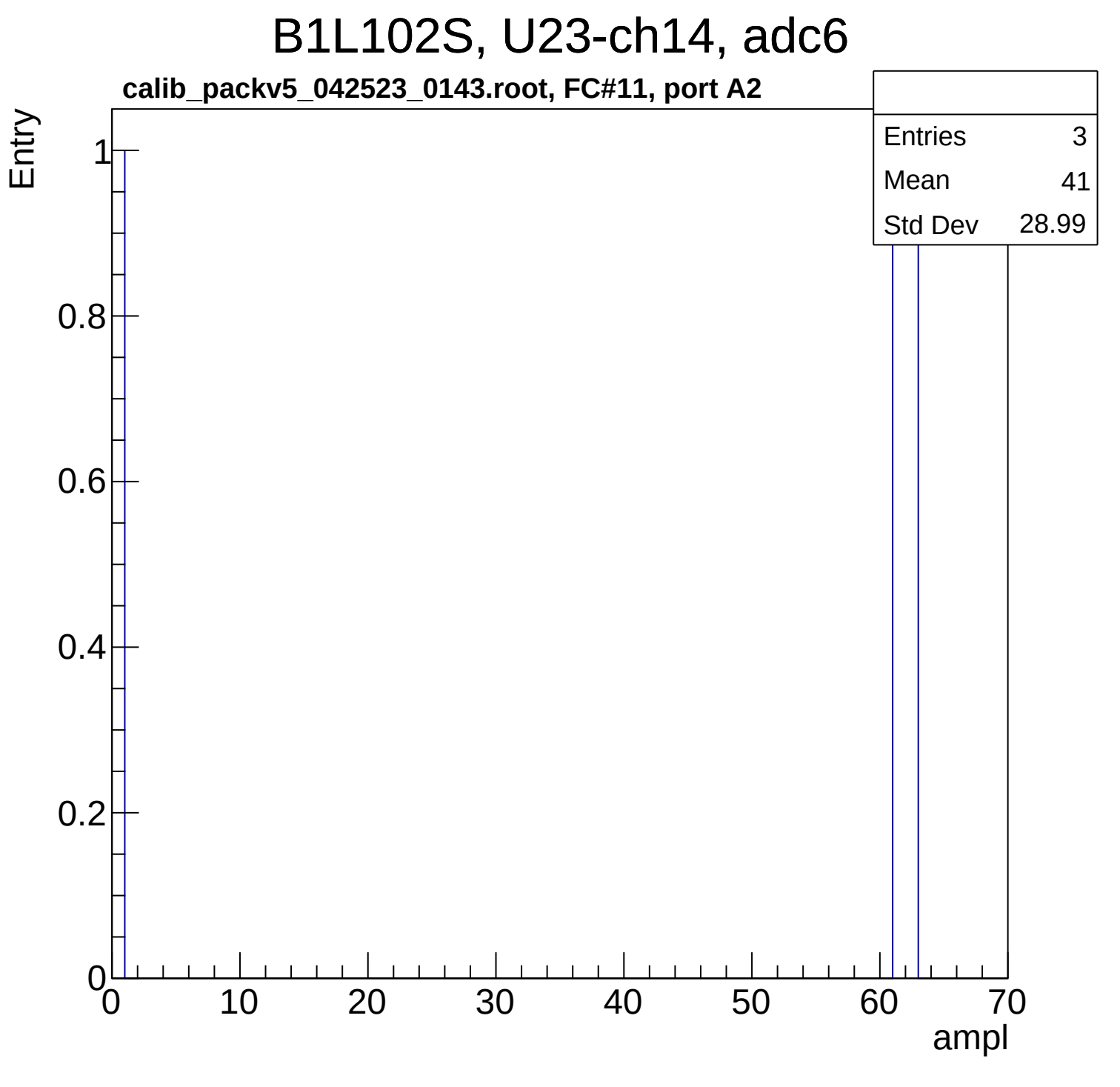

B1L102S, U23-ch14, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 36.57
Std Dev 3.432
Gaus mean : 37.0284
Gaus Width: 3.7494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

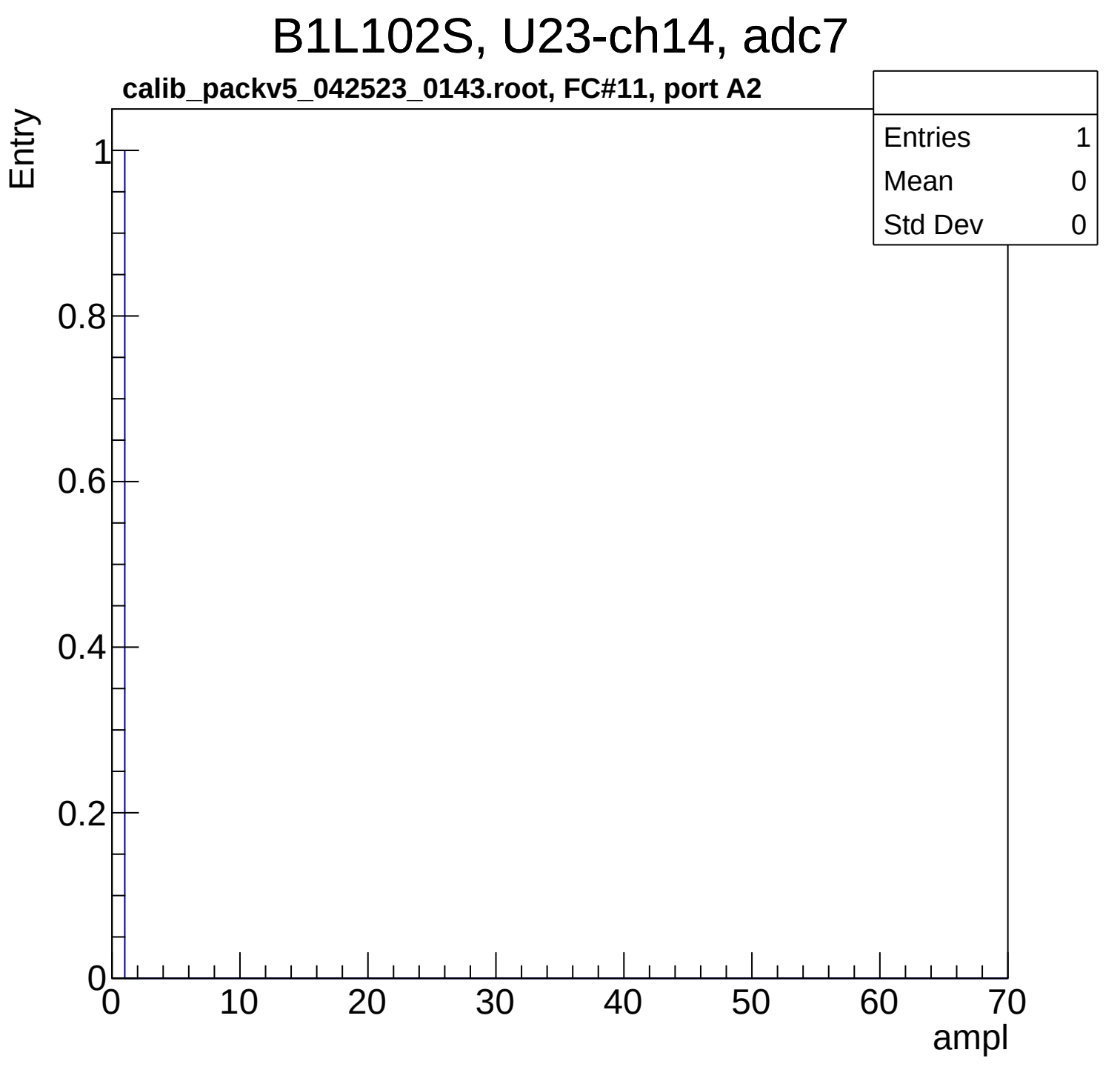

B1L102S, U23-ch14, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 43.39
Std Dev 3.459
Gaus mean : 43.7230
Gaus Width: 3.6383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

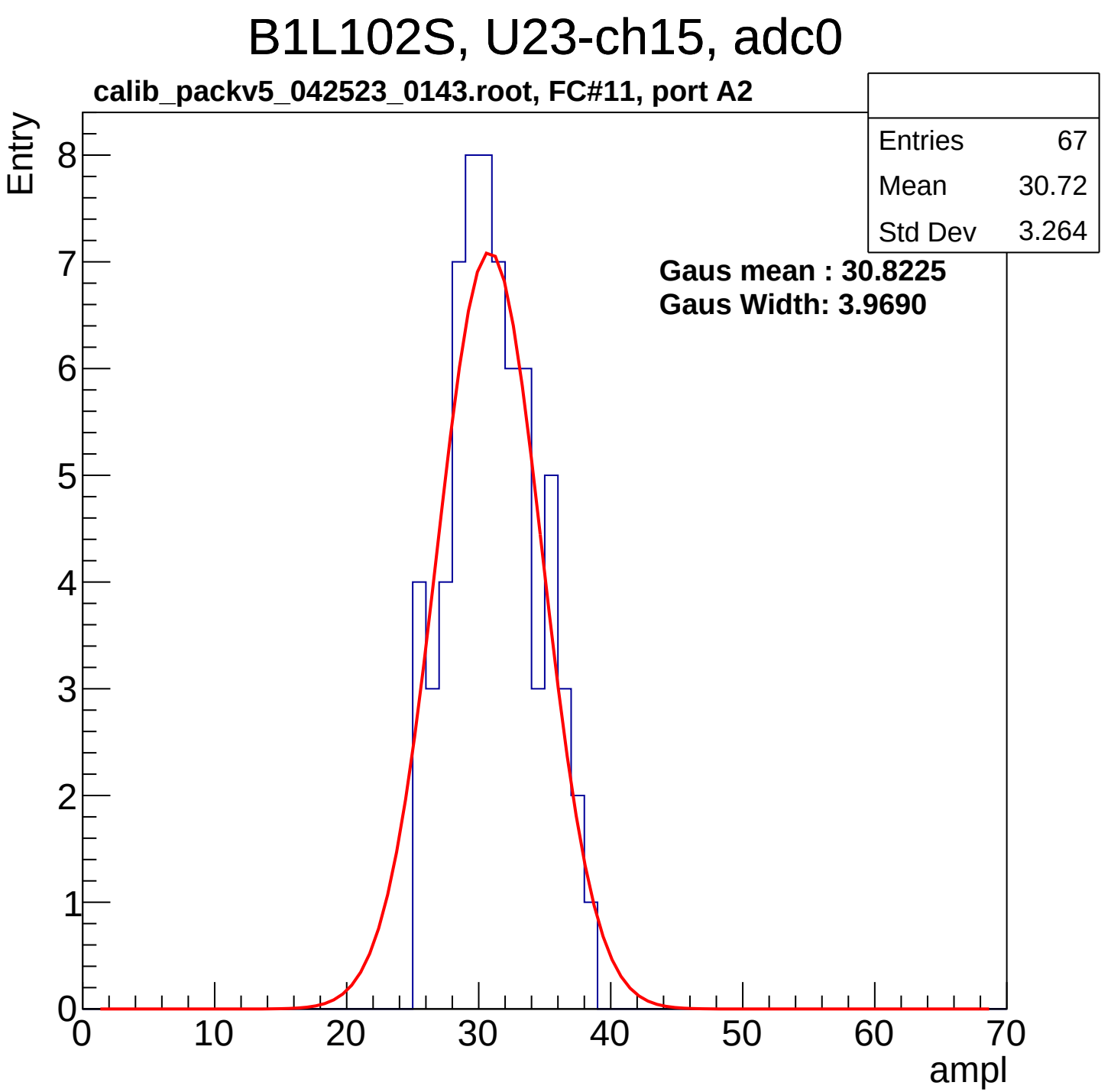

B1L102S, U23-ch14, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 49.88
Std Dev 3.72
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

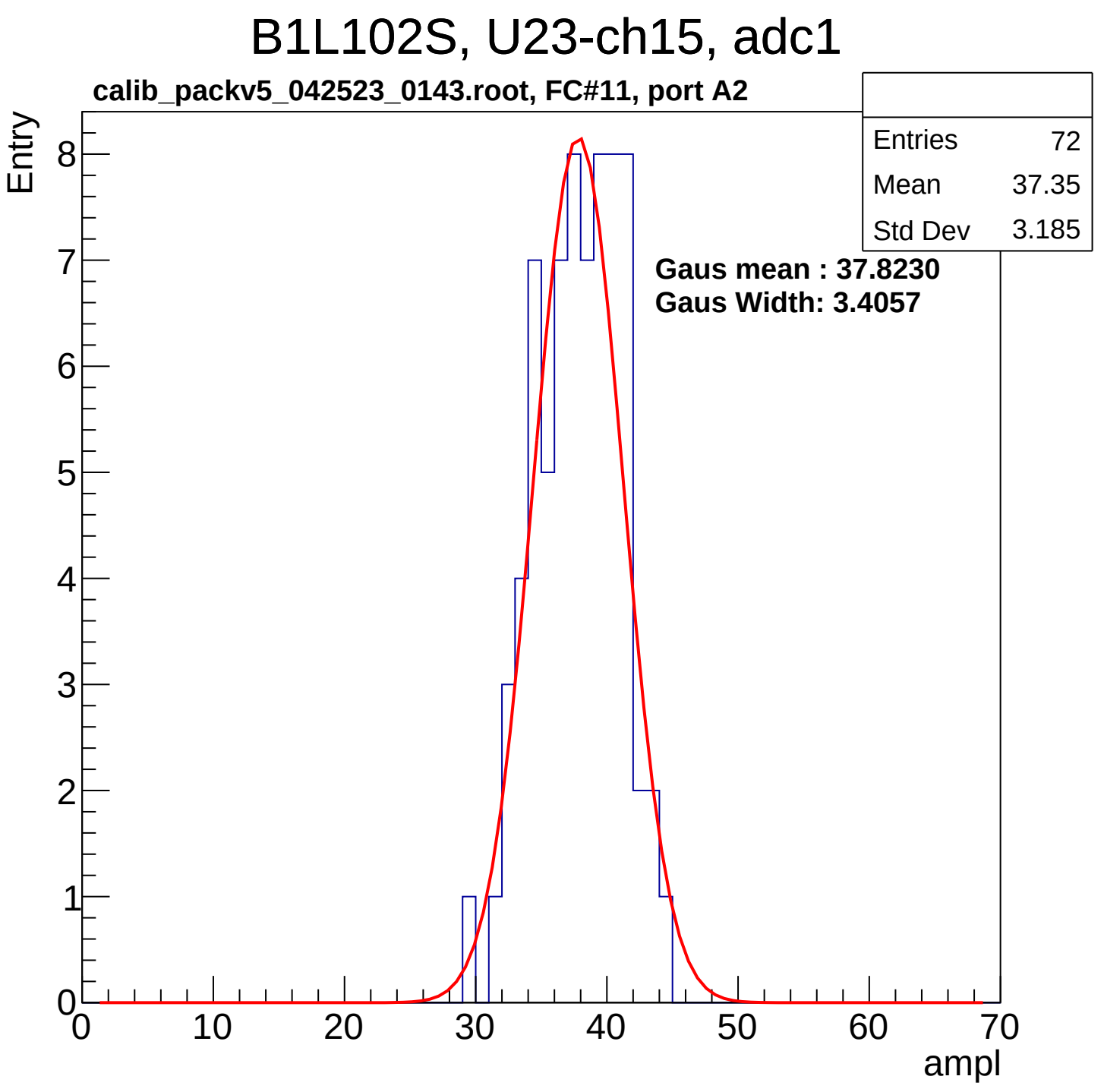

B1L102S, U23-ch14, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 56.1
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

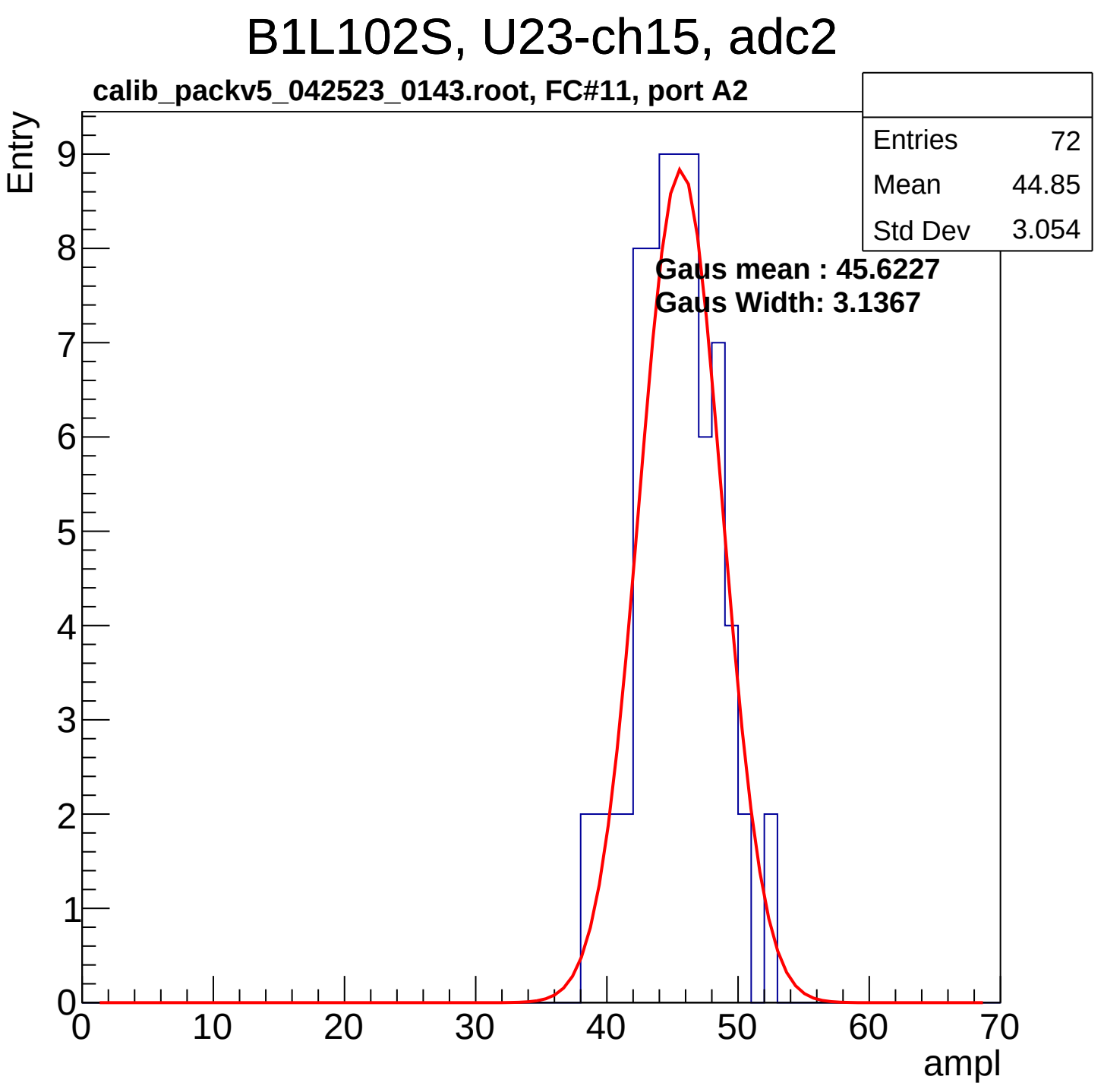

B1L102S, U23-ch14, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 60.51
Std Dev 2.21
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

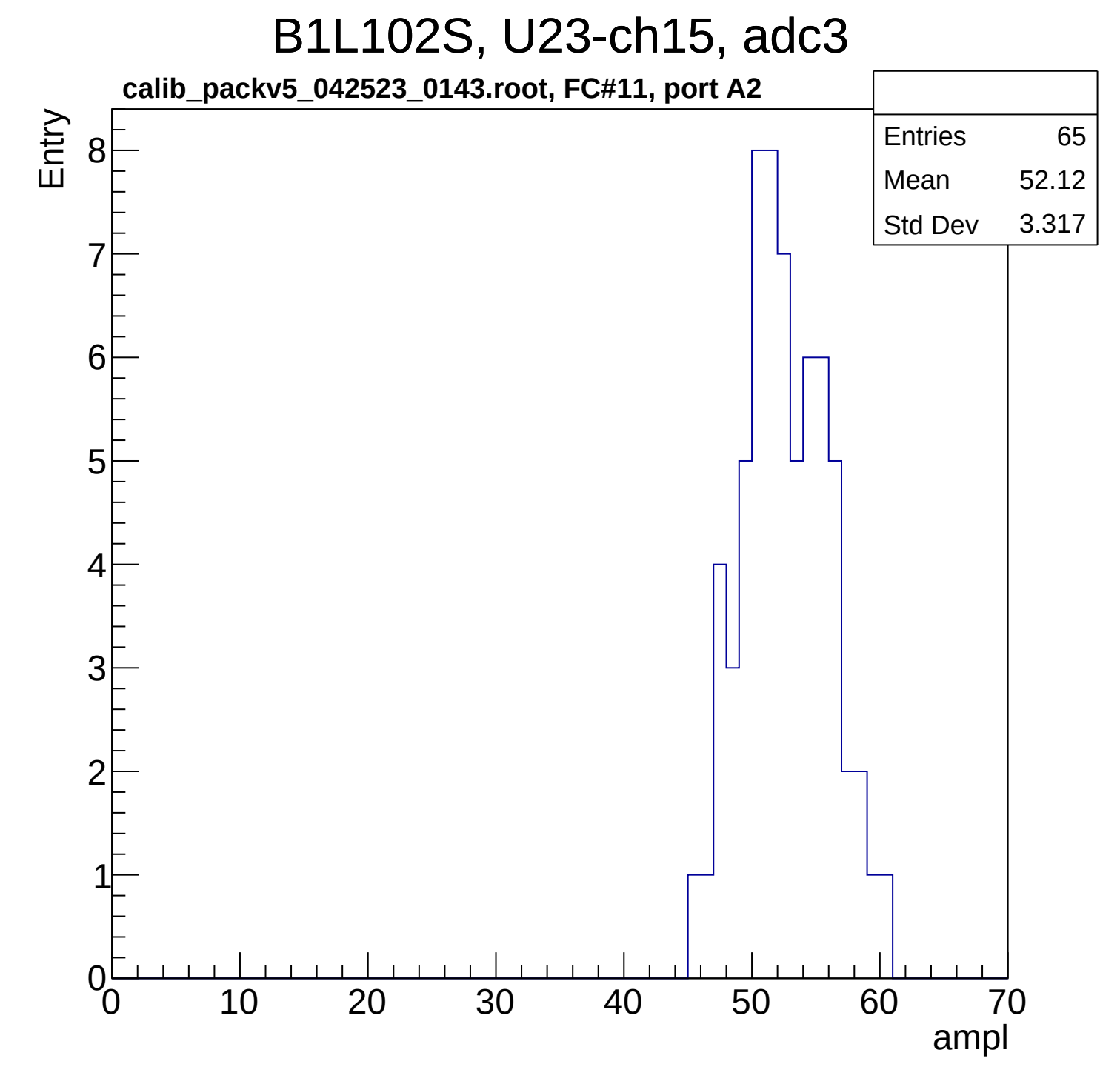

B1L102S, U23-ch14, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

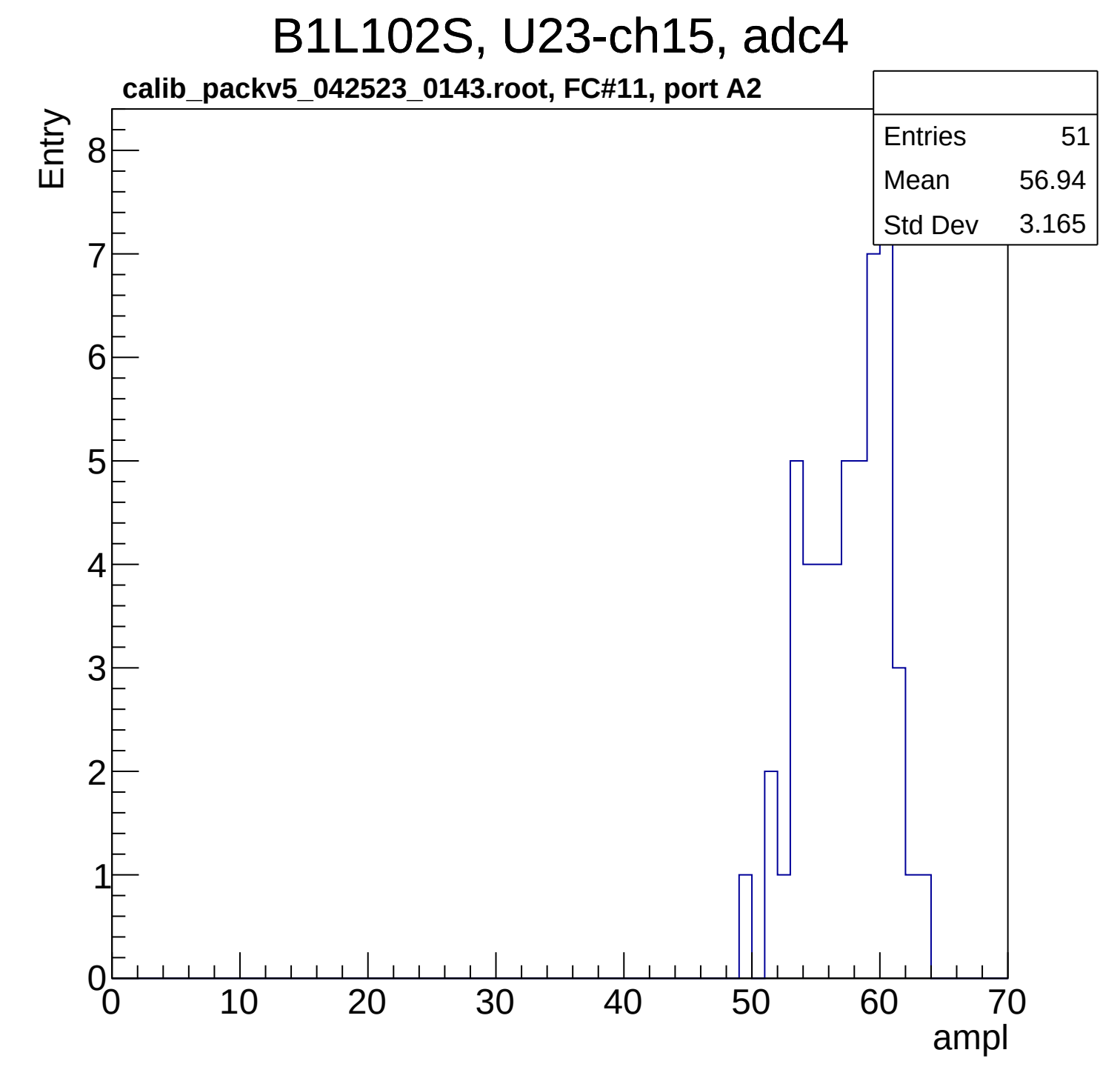

B1L102S, U23-ch14, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

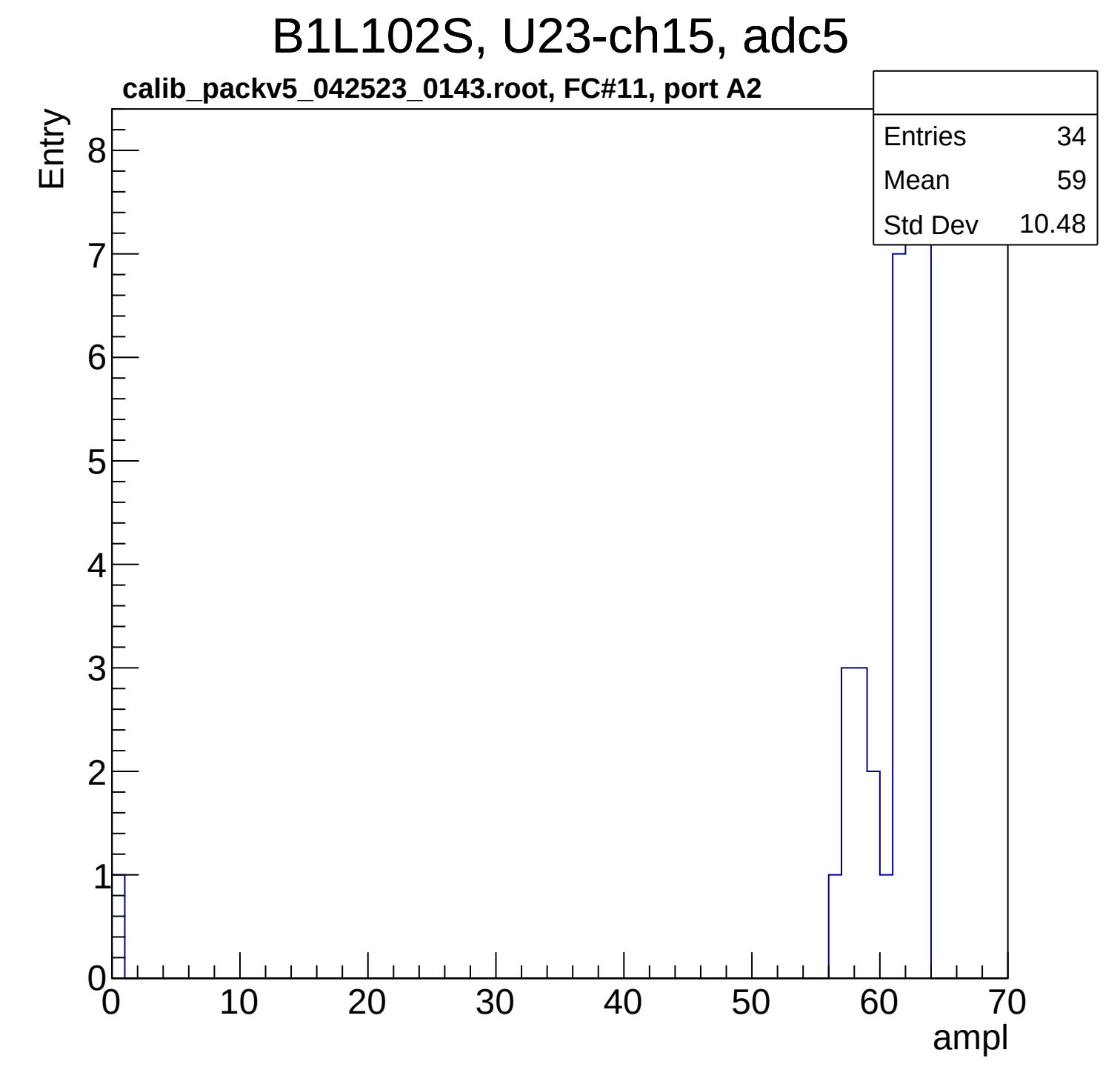

B1L102S, U23-ch15, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 30.72
Std Dev 3.264
Gaus mean : 30.8225
Gaus Width: 3.9690
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

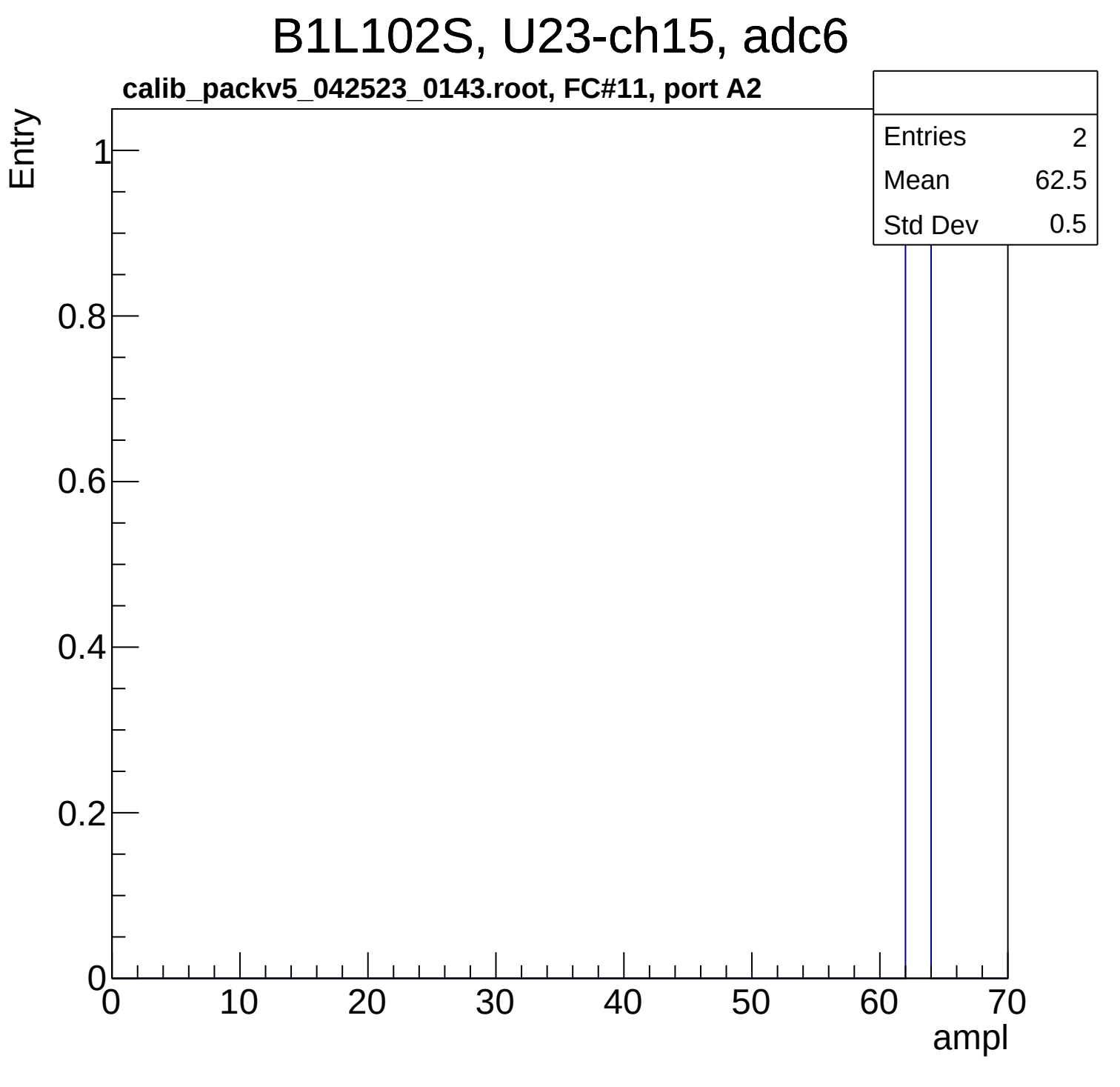

B1L102S, U23-ch15, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 37.35
Std Dev 3.185
Gaus mean : 37.8230
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

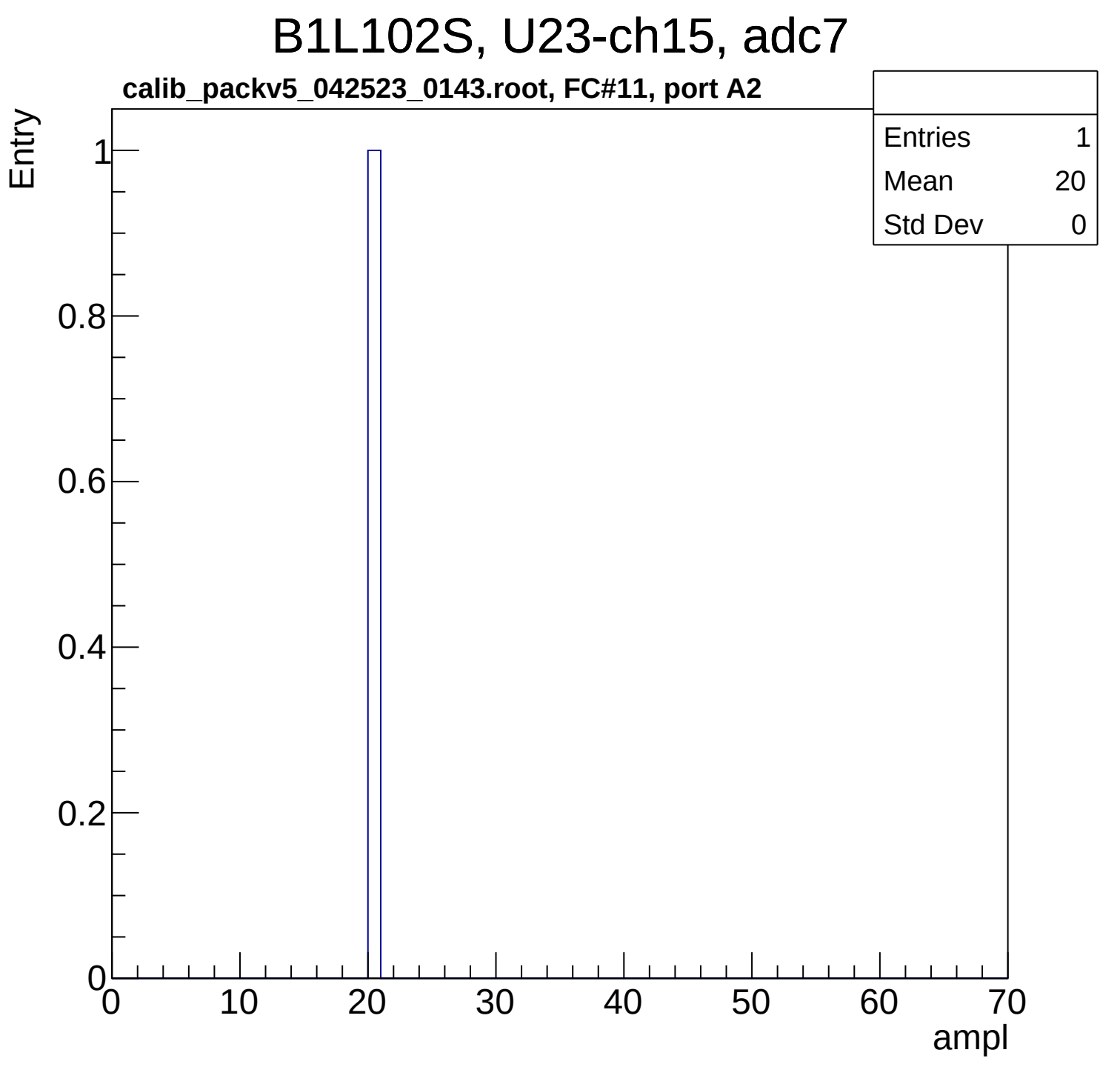

B1L102S, U23-ch15, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 44.85
Std Dev 3.054
Gaus mean : 45.6227
Gaus Width: 3.1367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

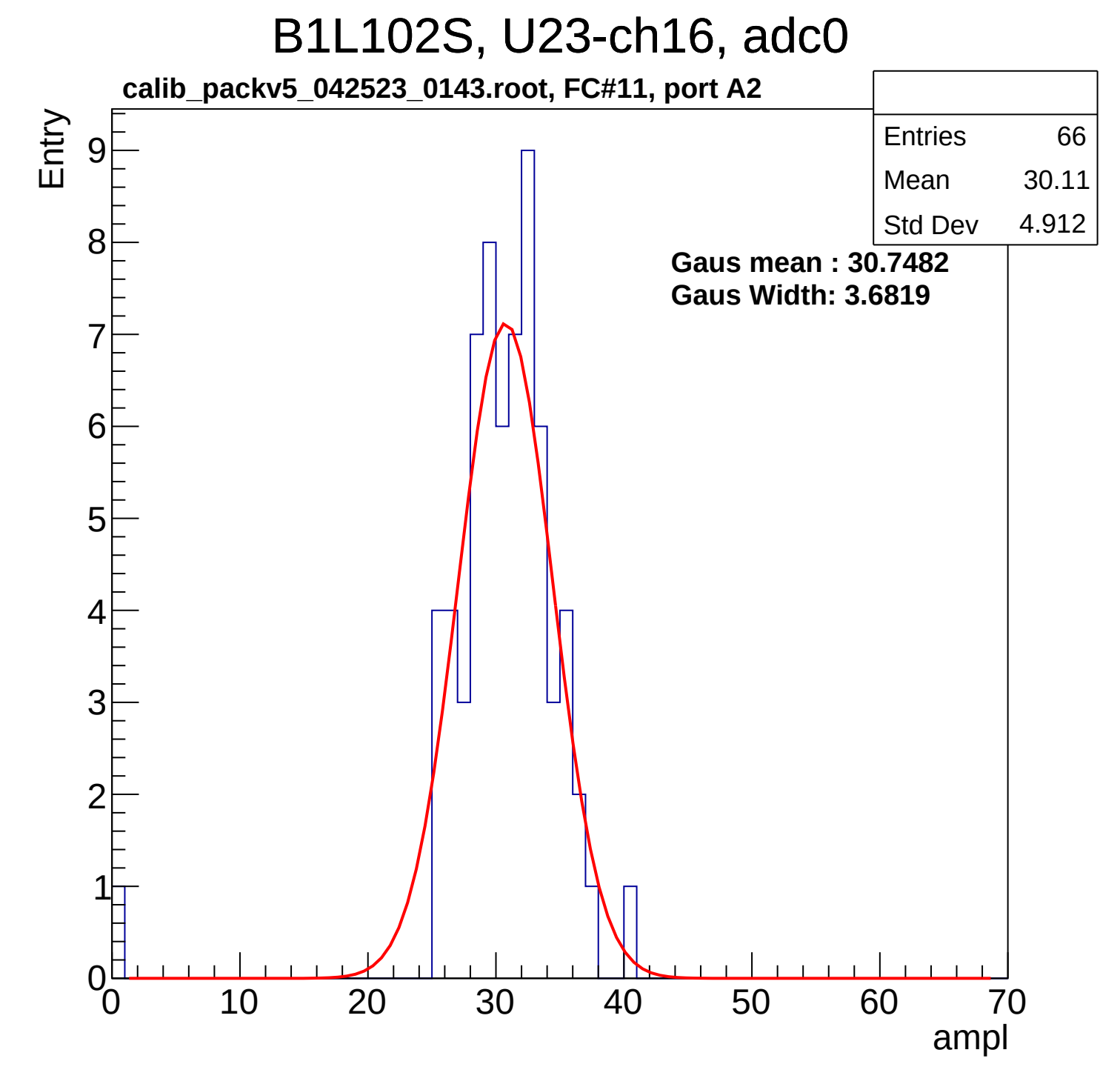

B1L102S, U23-ch15, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 52.12
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

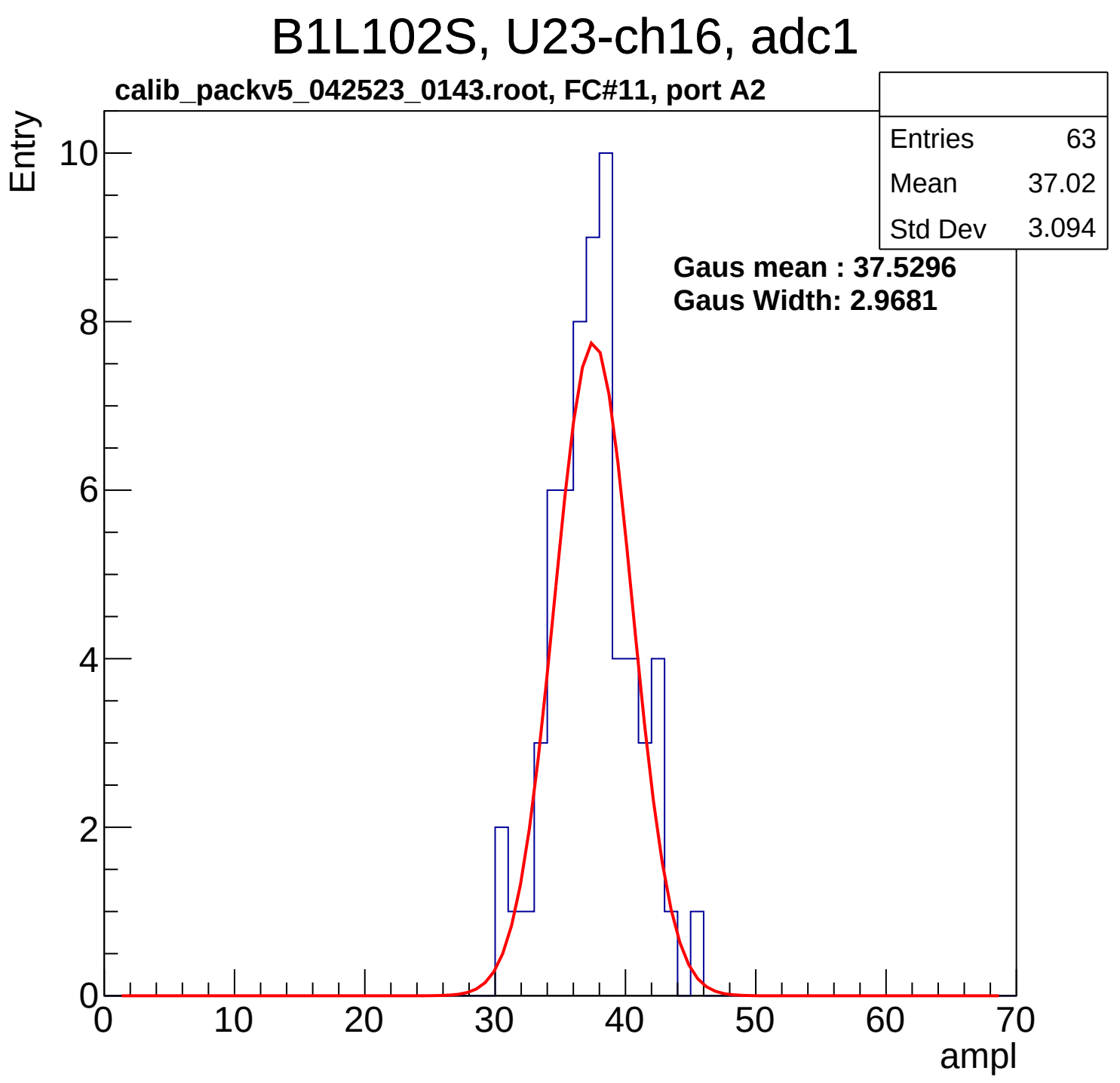

B1L102S, U23-ch15, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 56.94
Std Dev 3.165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

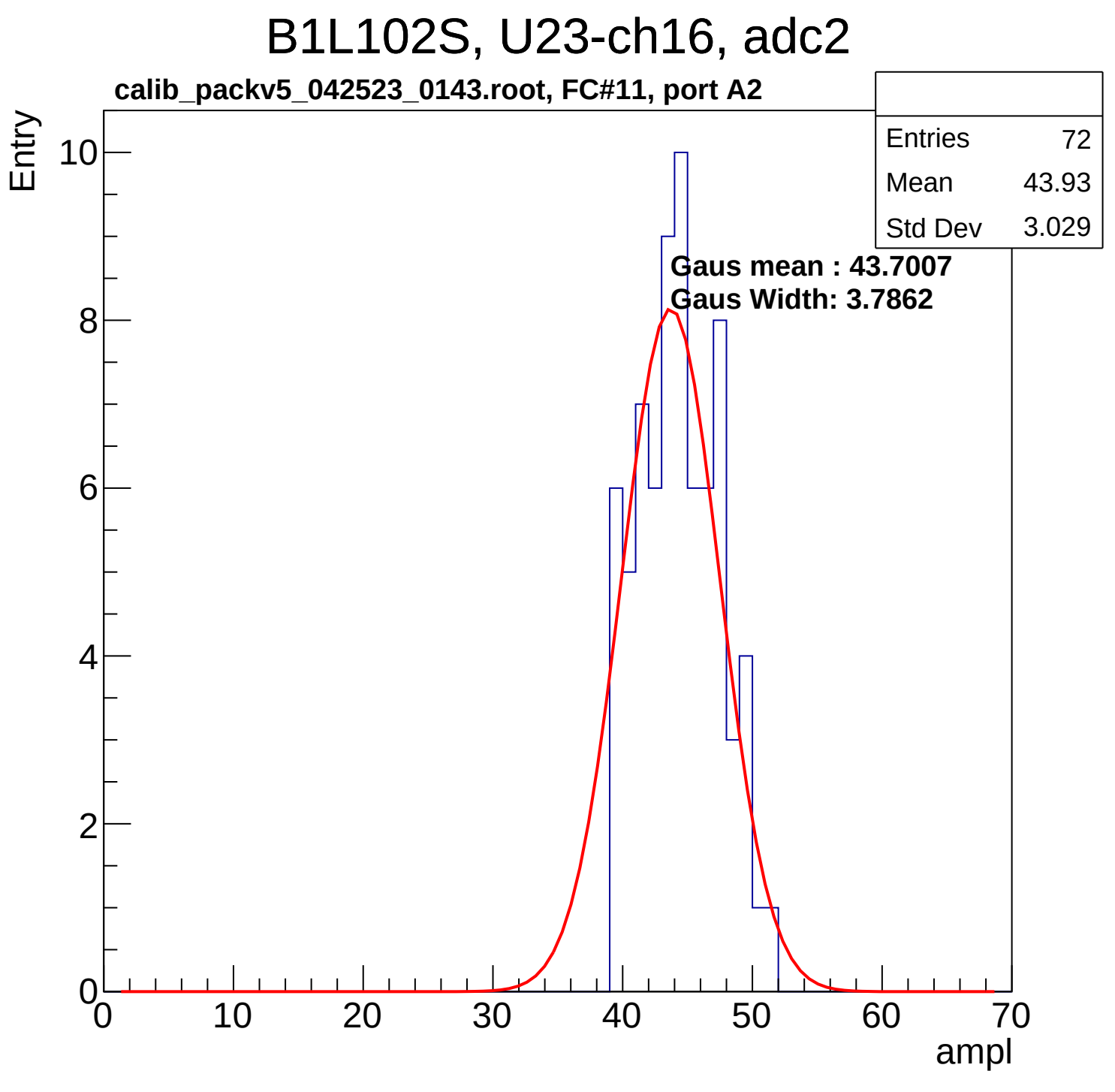

B1L102S, U23-ch15, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 34
Mean 59
Std Dev 10.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

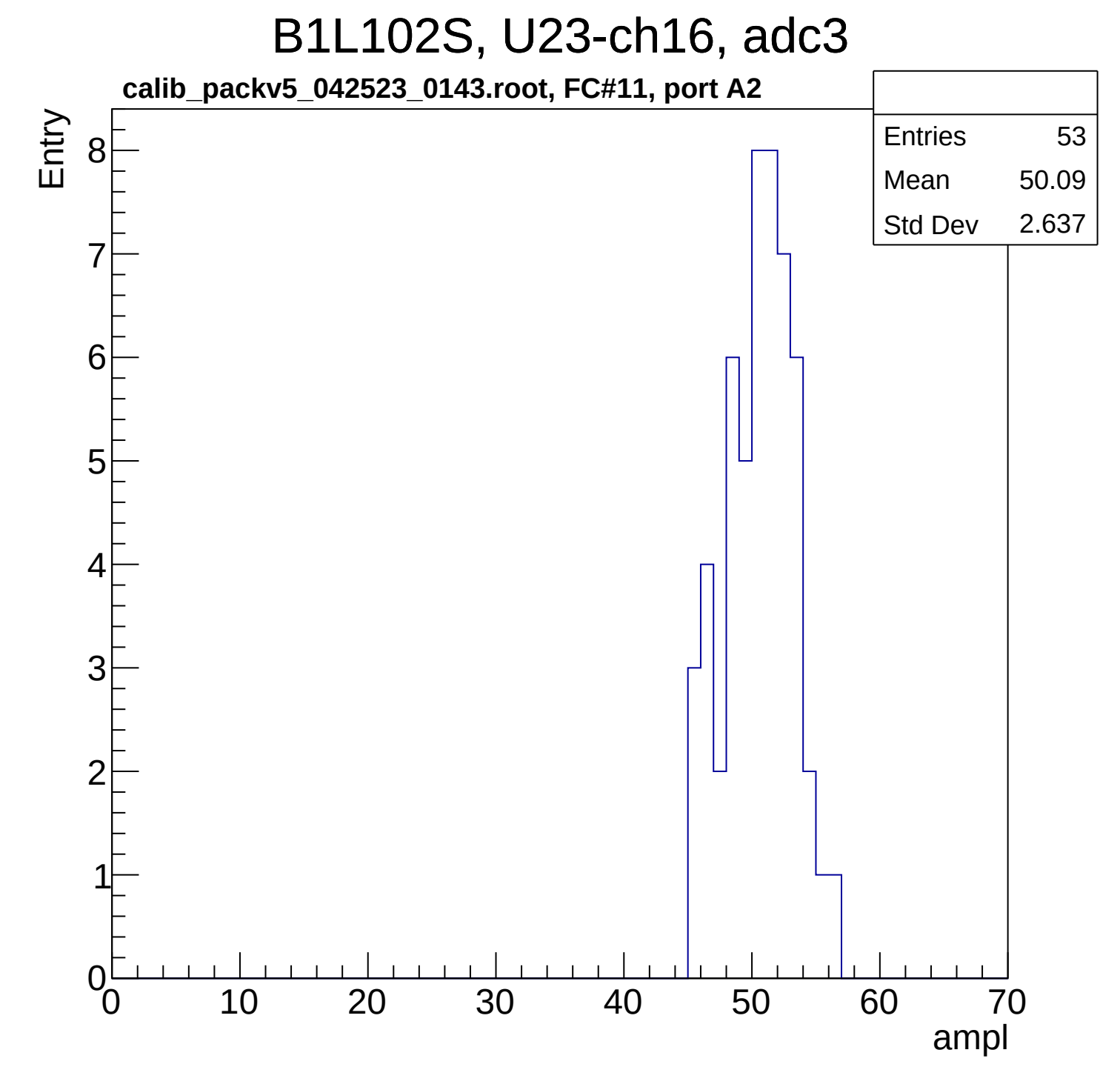

B1L102S, U23-ch15, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

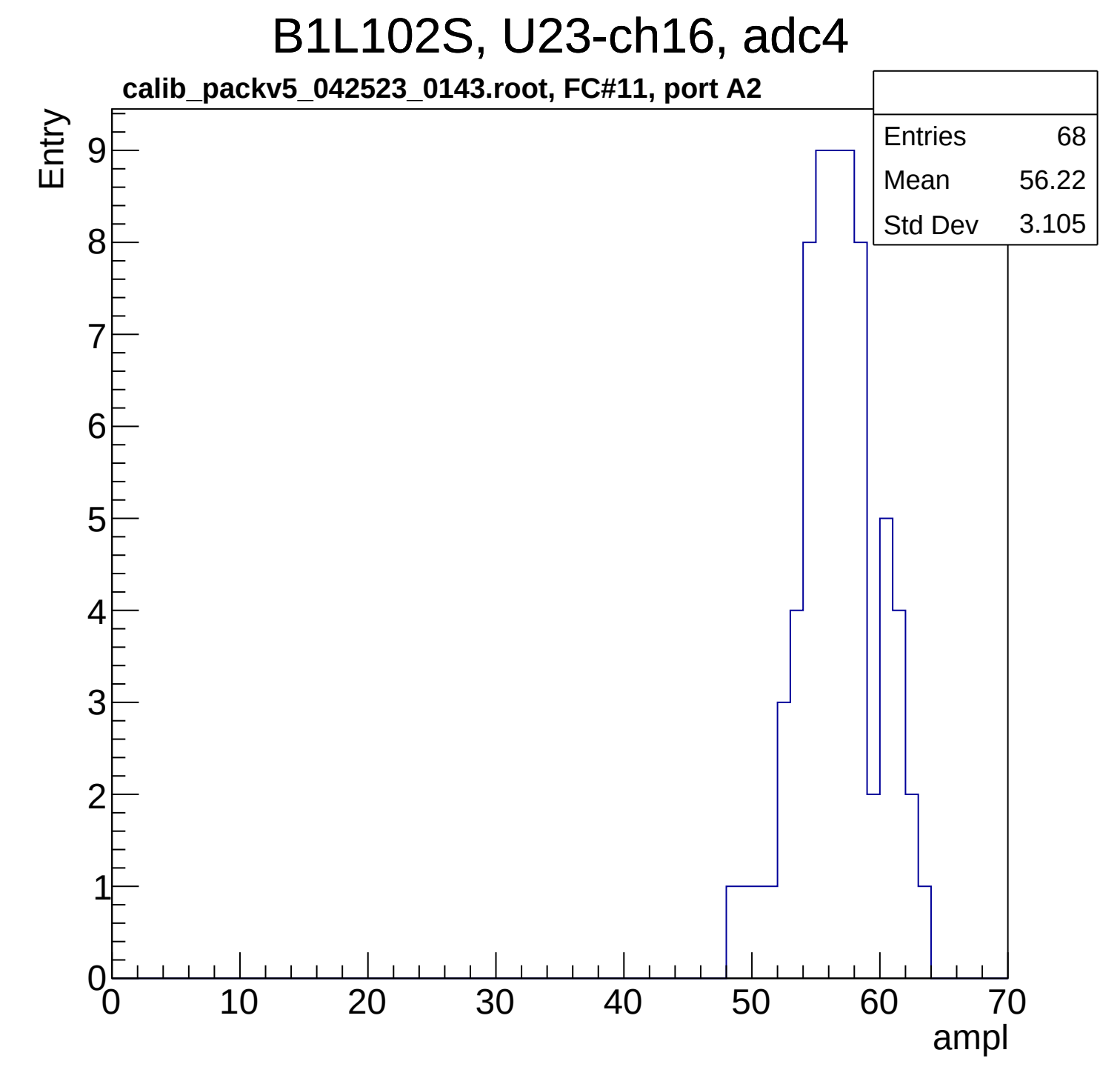

B1L102S, U23-ch15, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

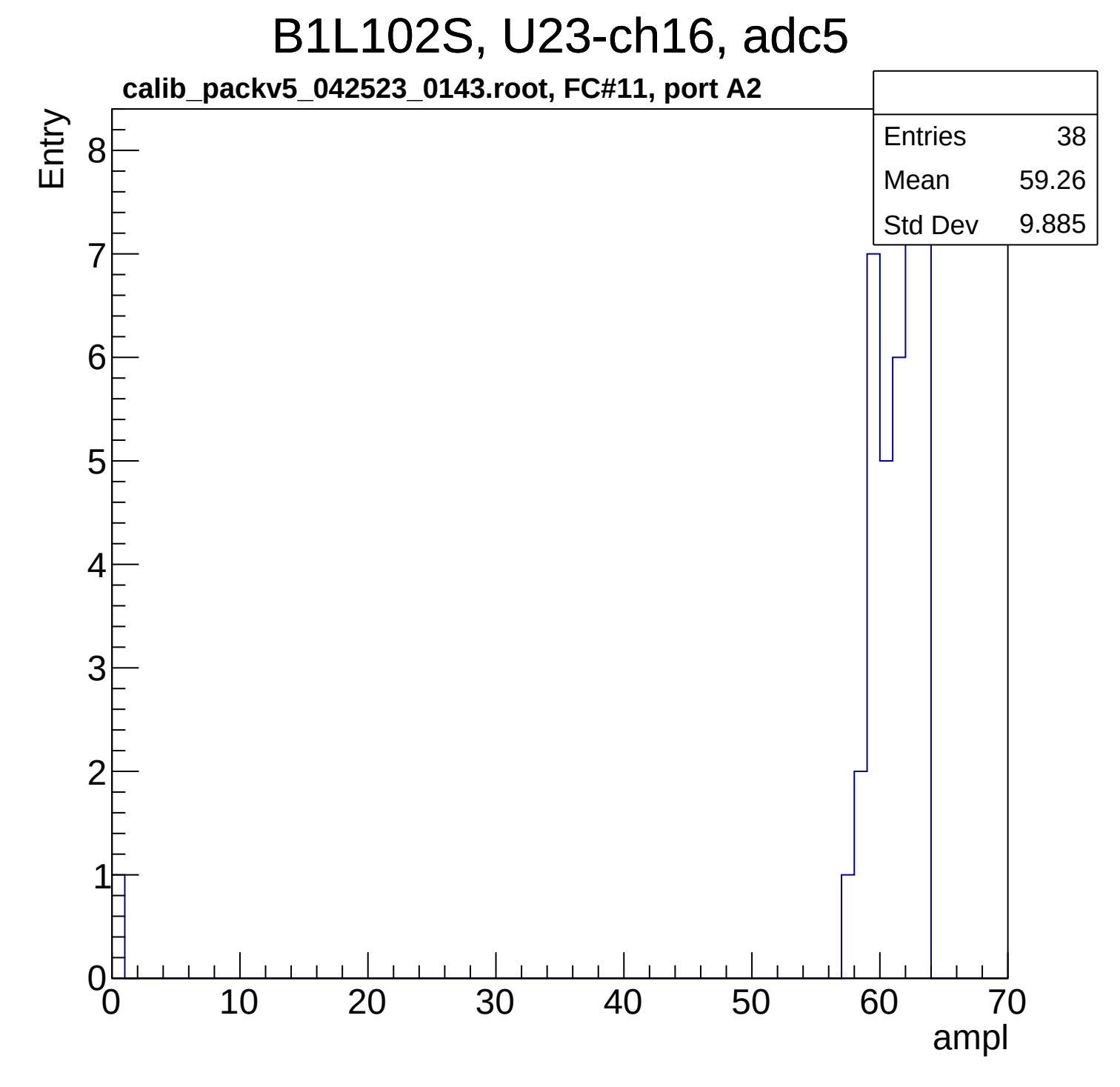

B1L102S, U23-ch16, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 30.11
Std Dev 4.912
Gaus mean : 30.7482
Gaus Width: 3.6819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

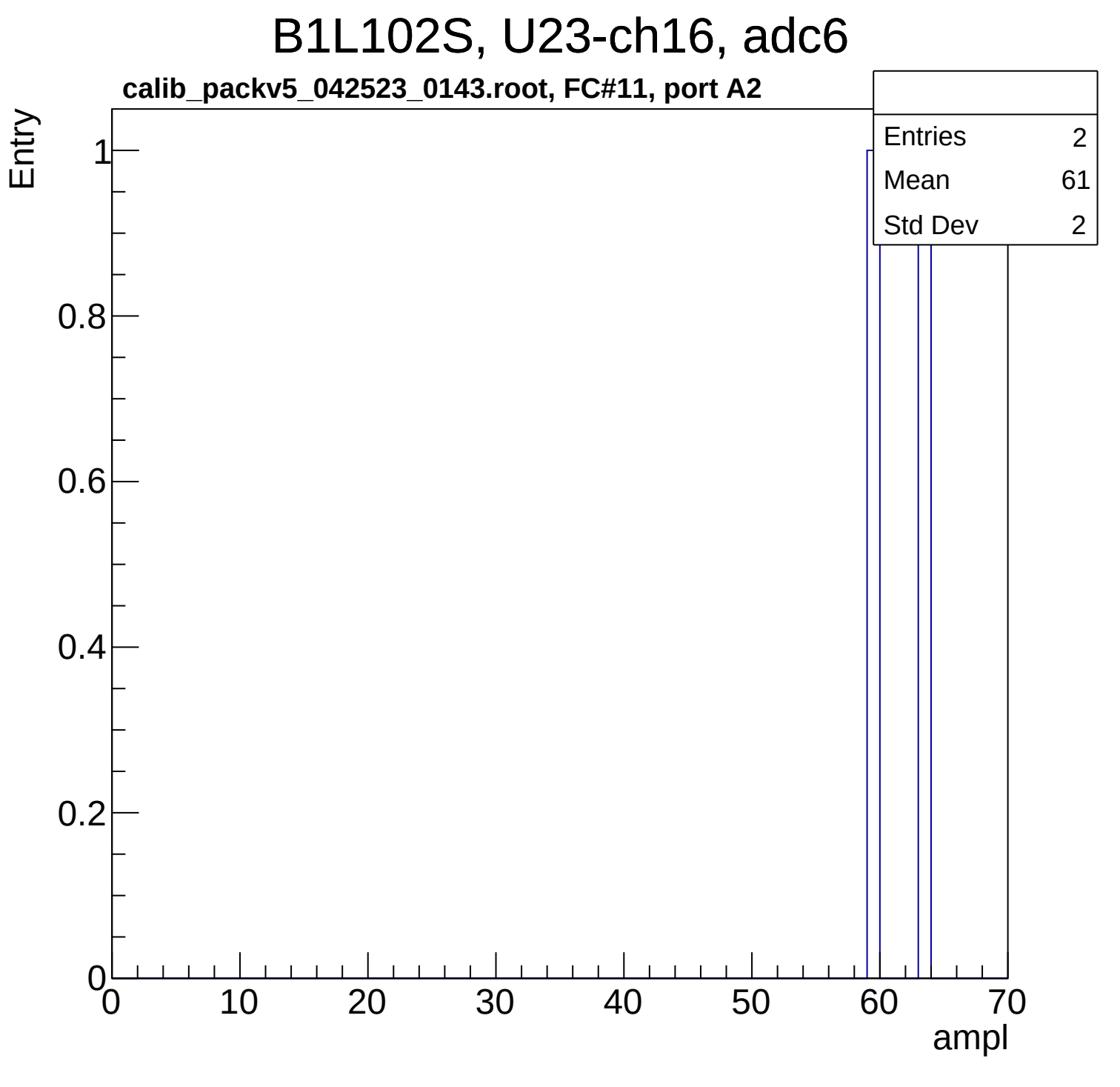

B1L102S, U23-ch16, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 37.02
Std Dev 3.094
Gaus mean : 37.5296
Gaus Width: 2.9681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

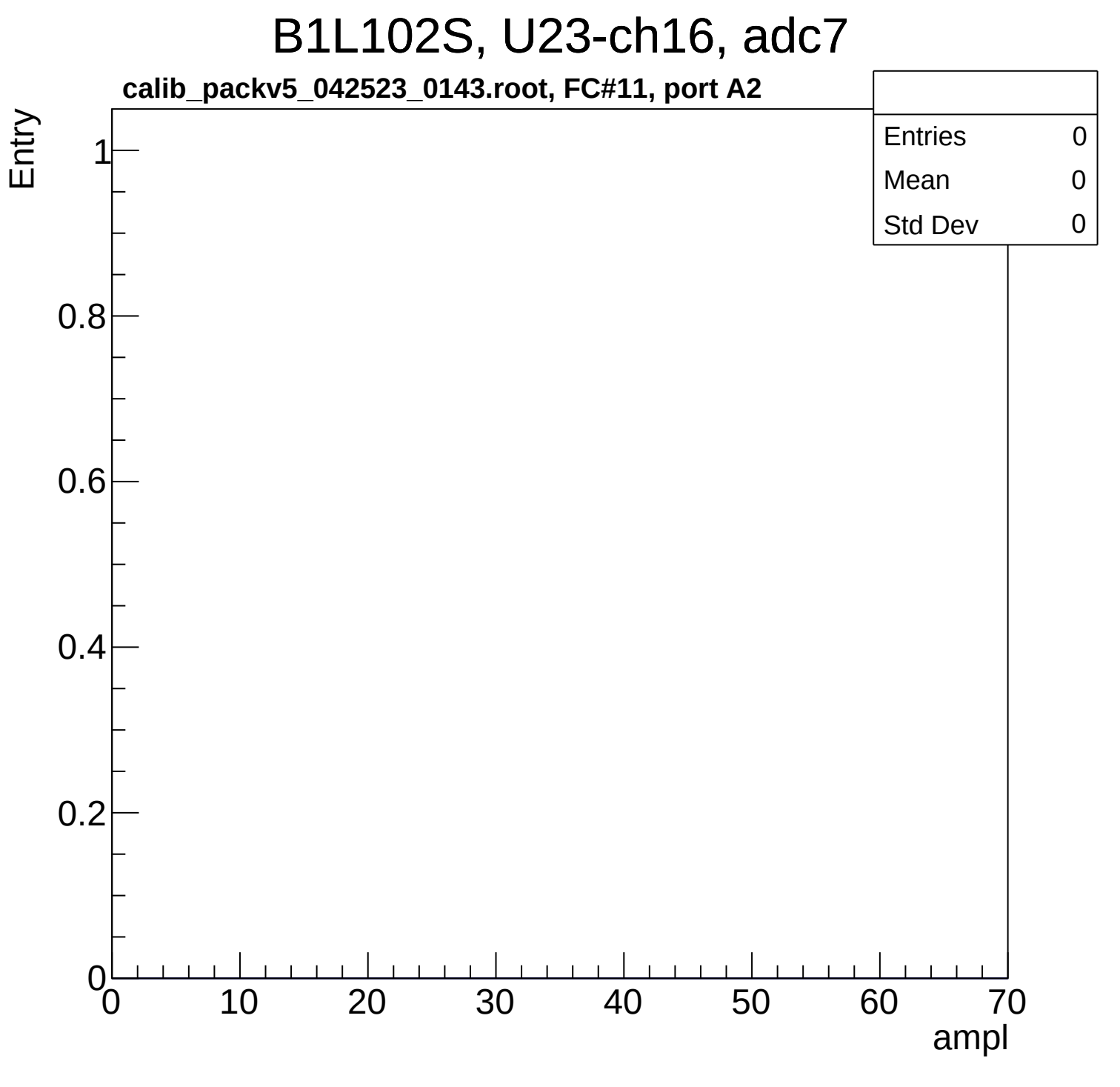

B1L102S, U23-ch16, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 43.93
Std Dev 3.029
Gaus mean : 43.7007
Gaus Width: 3.7862
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

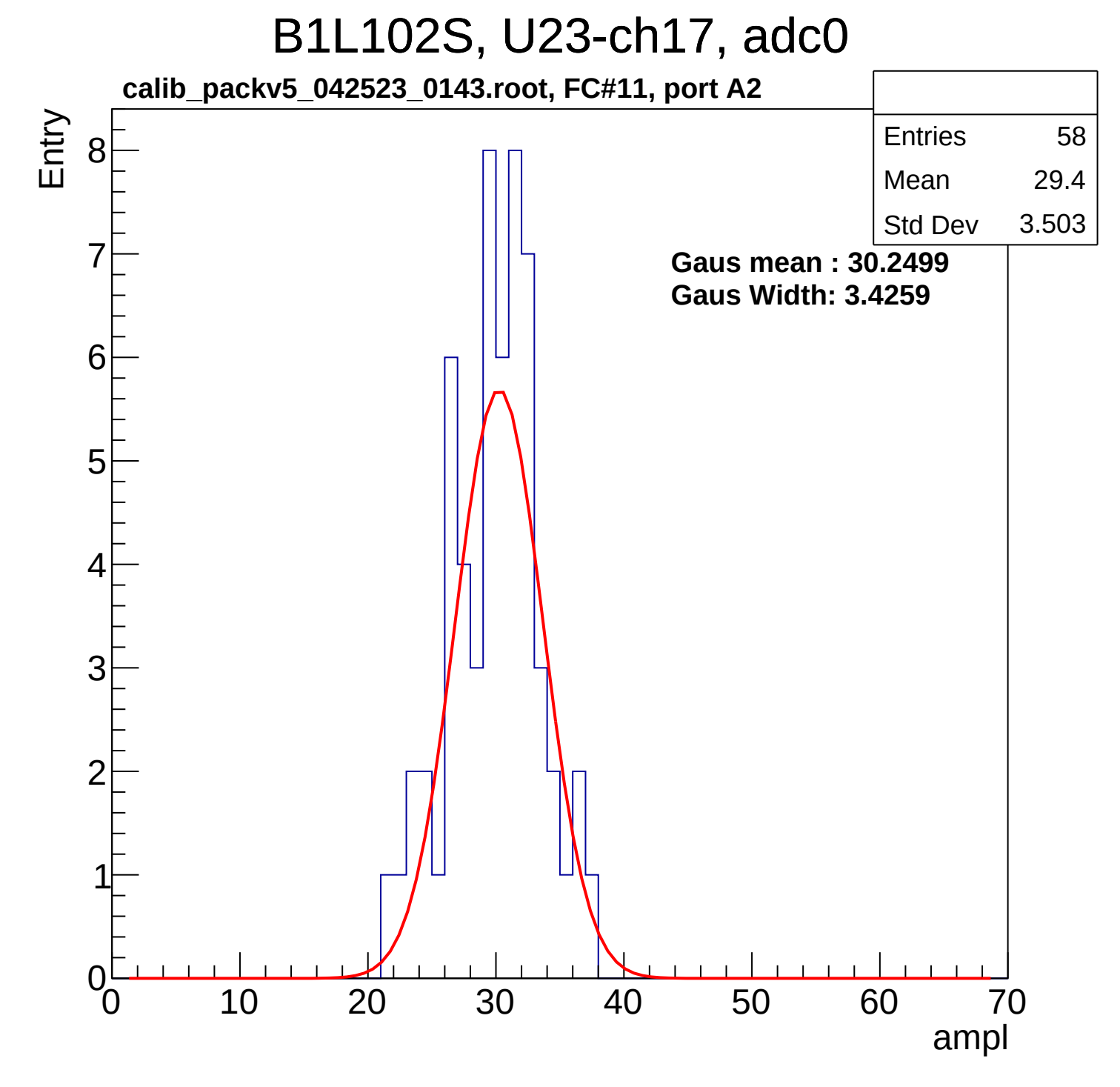

B1L102S, U23-ch16, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 50.09
Std Dev 2.637
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

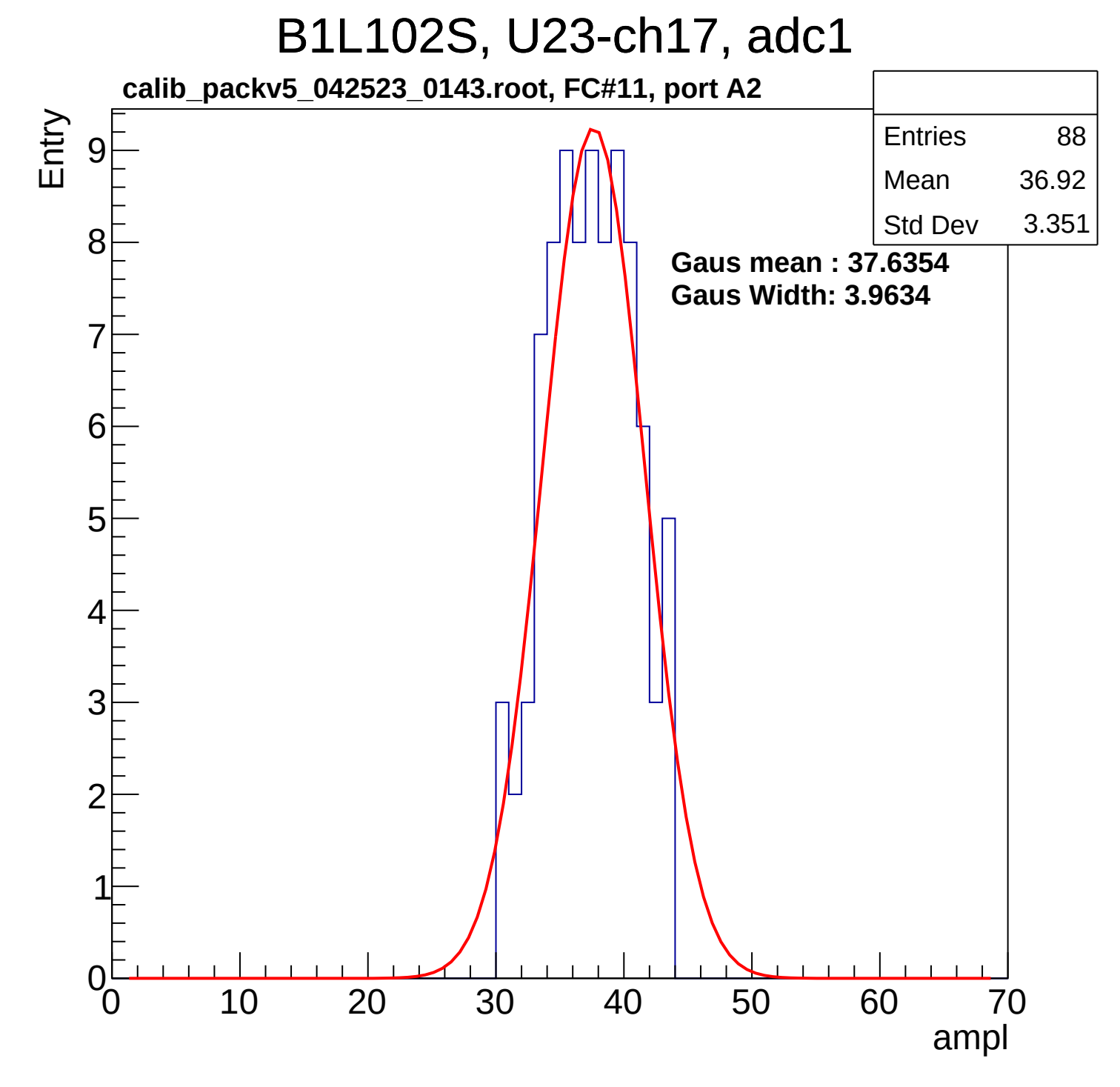

B1L102S, U23-ch16, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 56.22
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

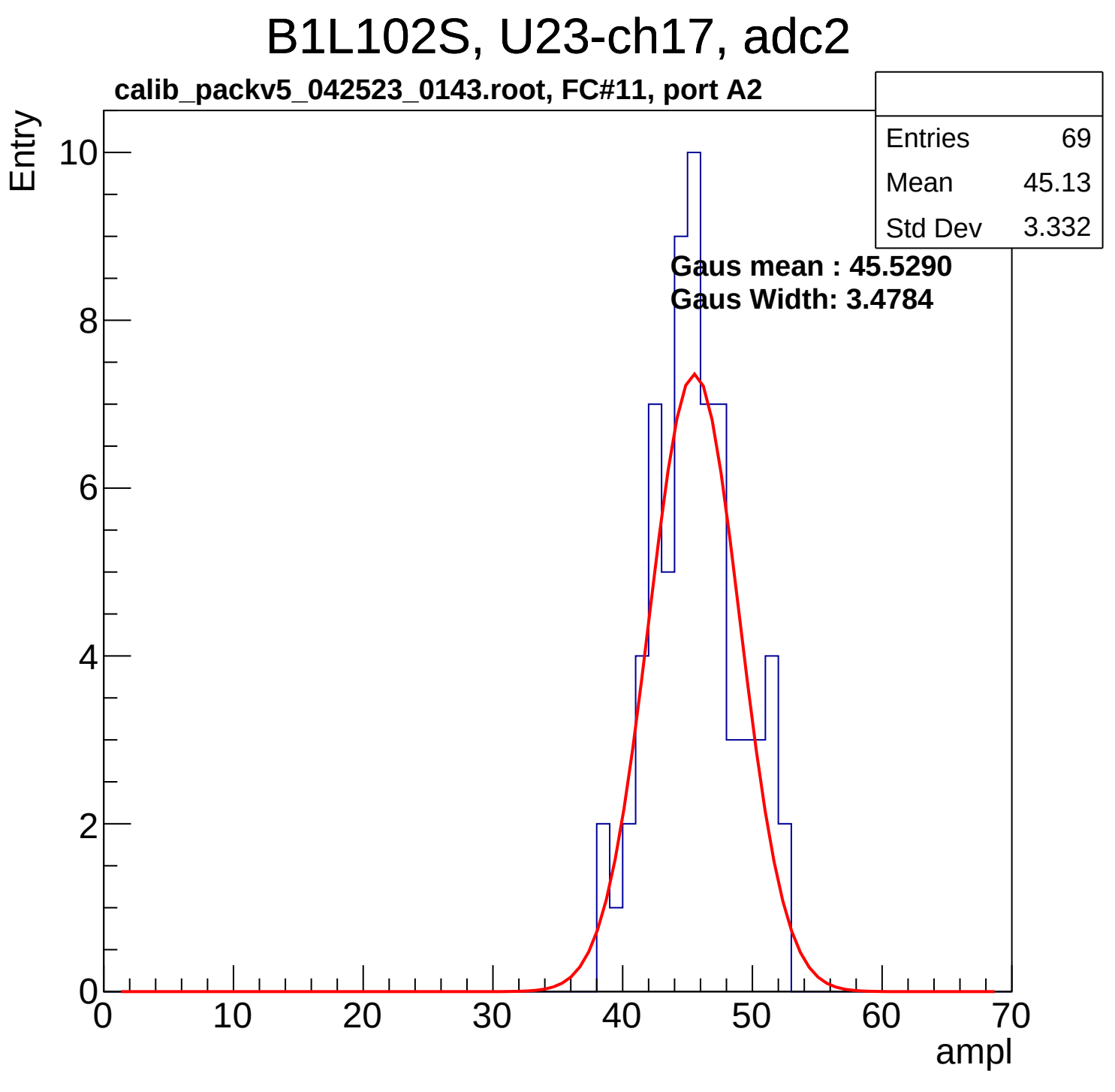

B1L102S, U23-ch16, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 59.26
Std Dev 9.885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

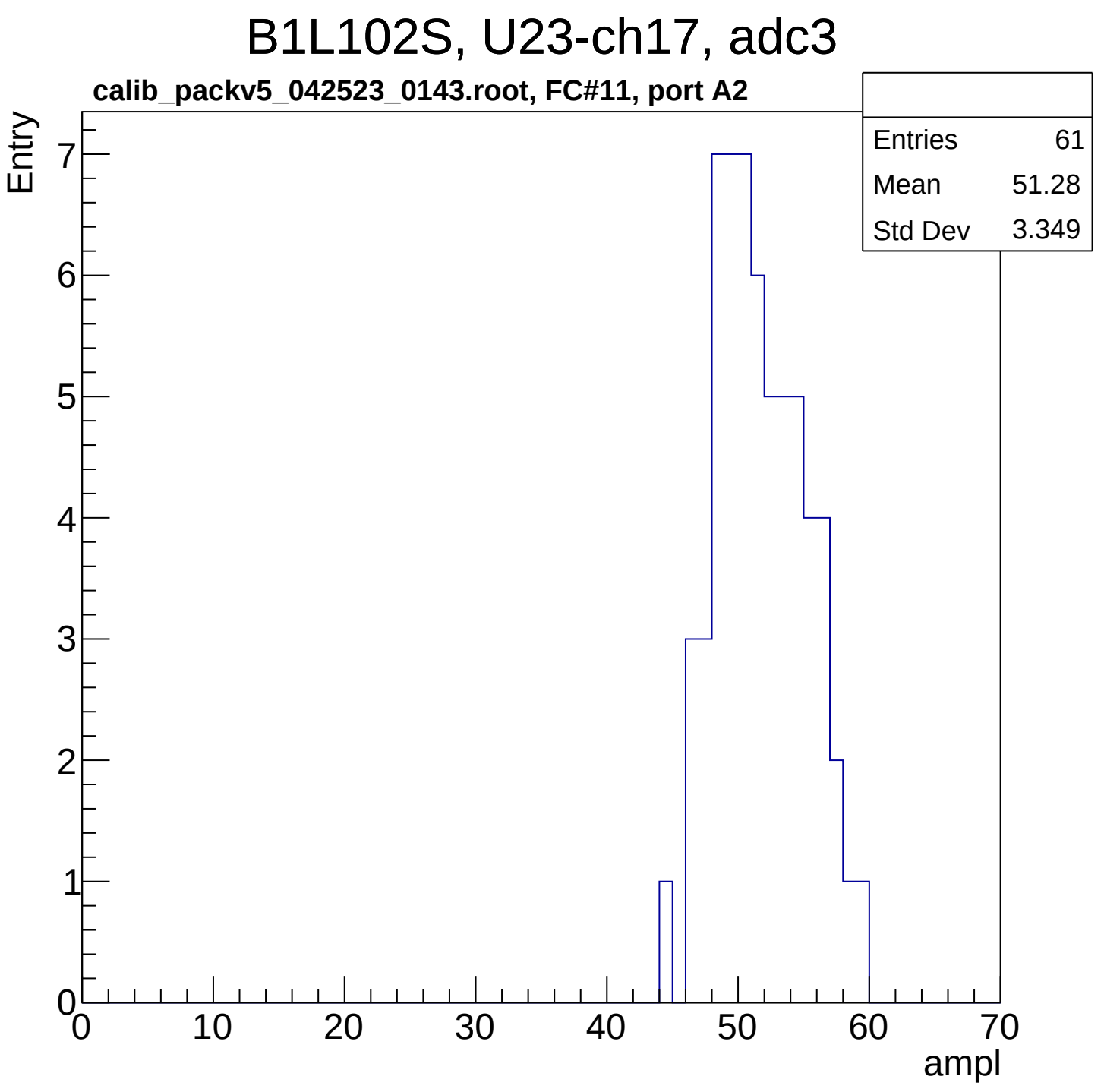

B1L102S, U23-ch16, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

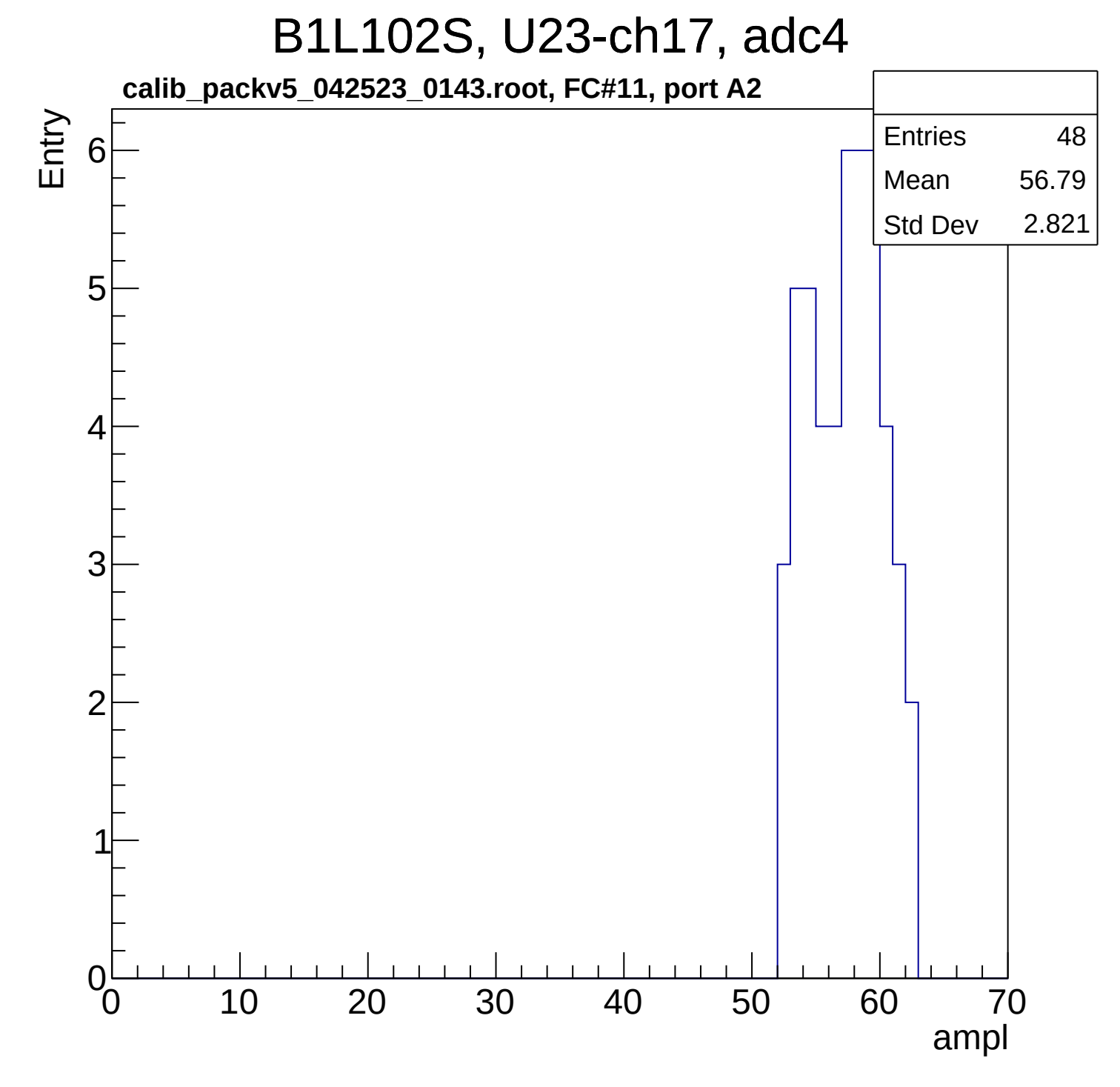

B1L102S, U23-ch16, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

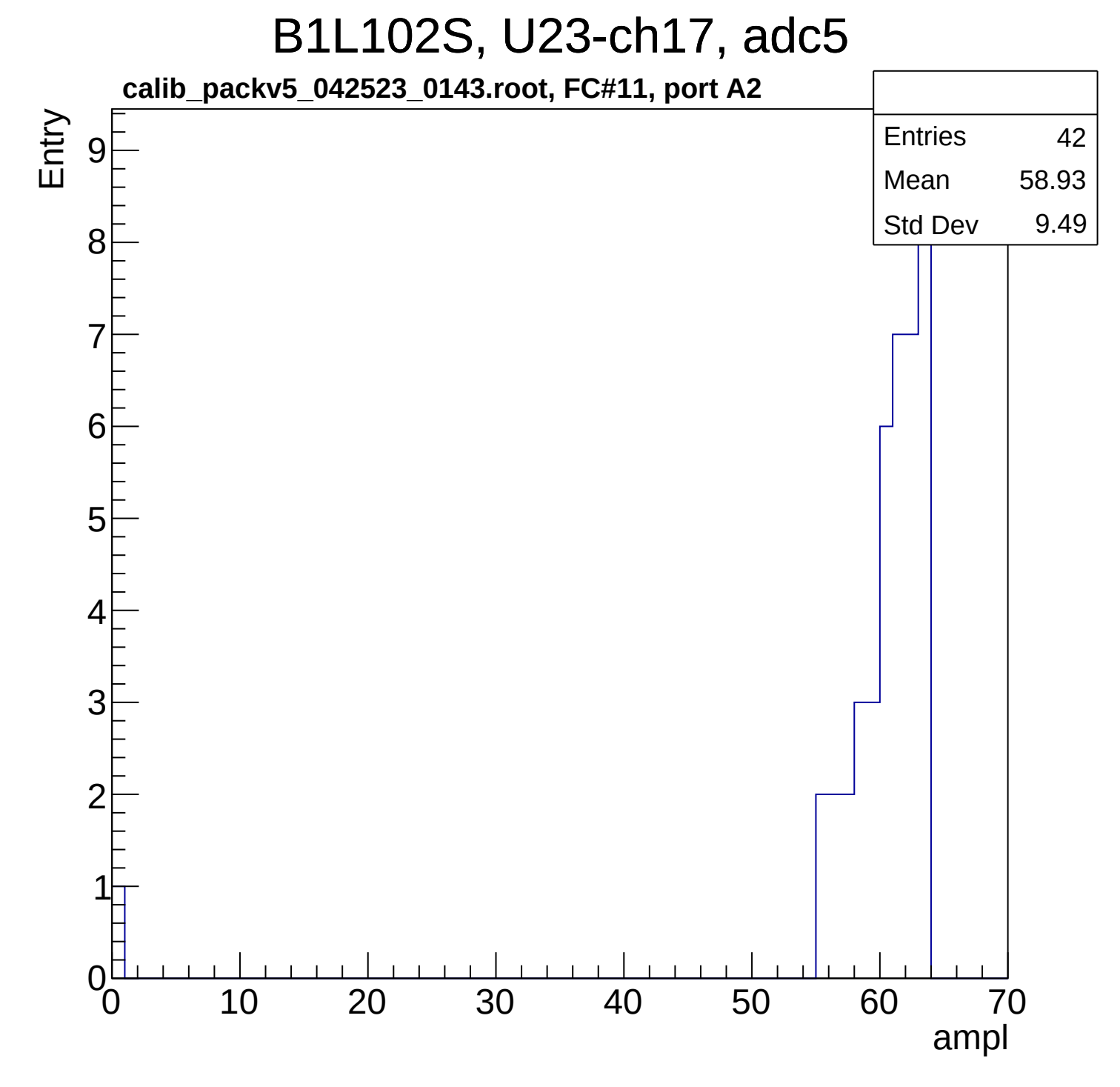

B1L102S, U23-ch17, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 29.4
Std Dev 3.503
Gaus mean : 30.2499
Gaus Width: 3.4259
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

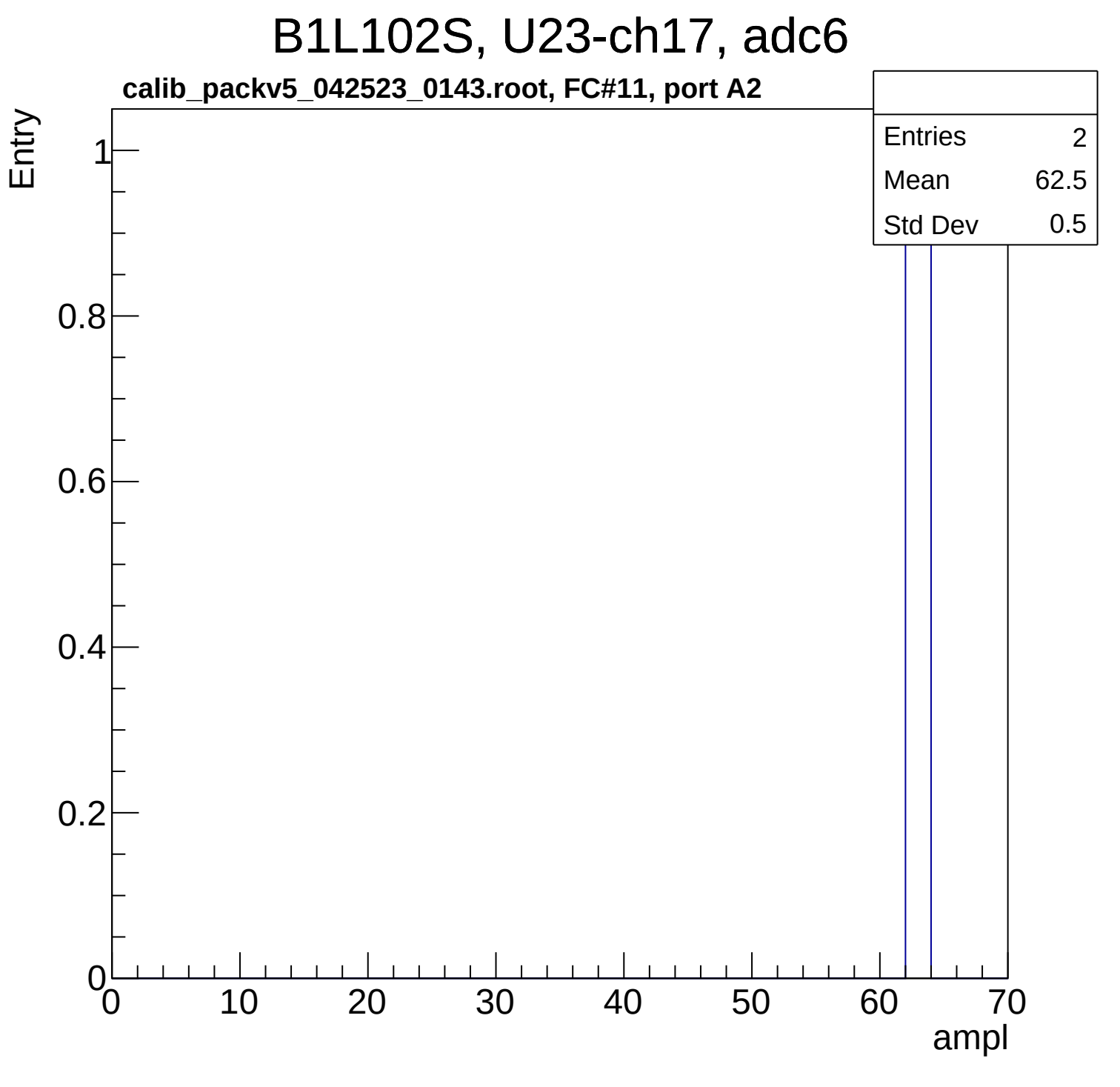

B1L102S, U23-ch17, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 36.92
Std Dev 3.351
Gaus mean : 37.6354
Gaus Width: 3.9634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

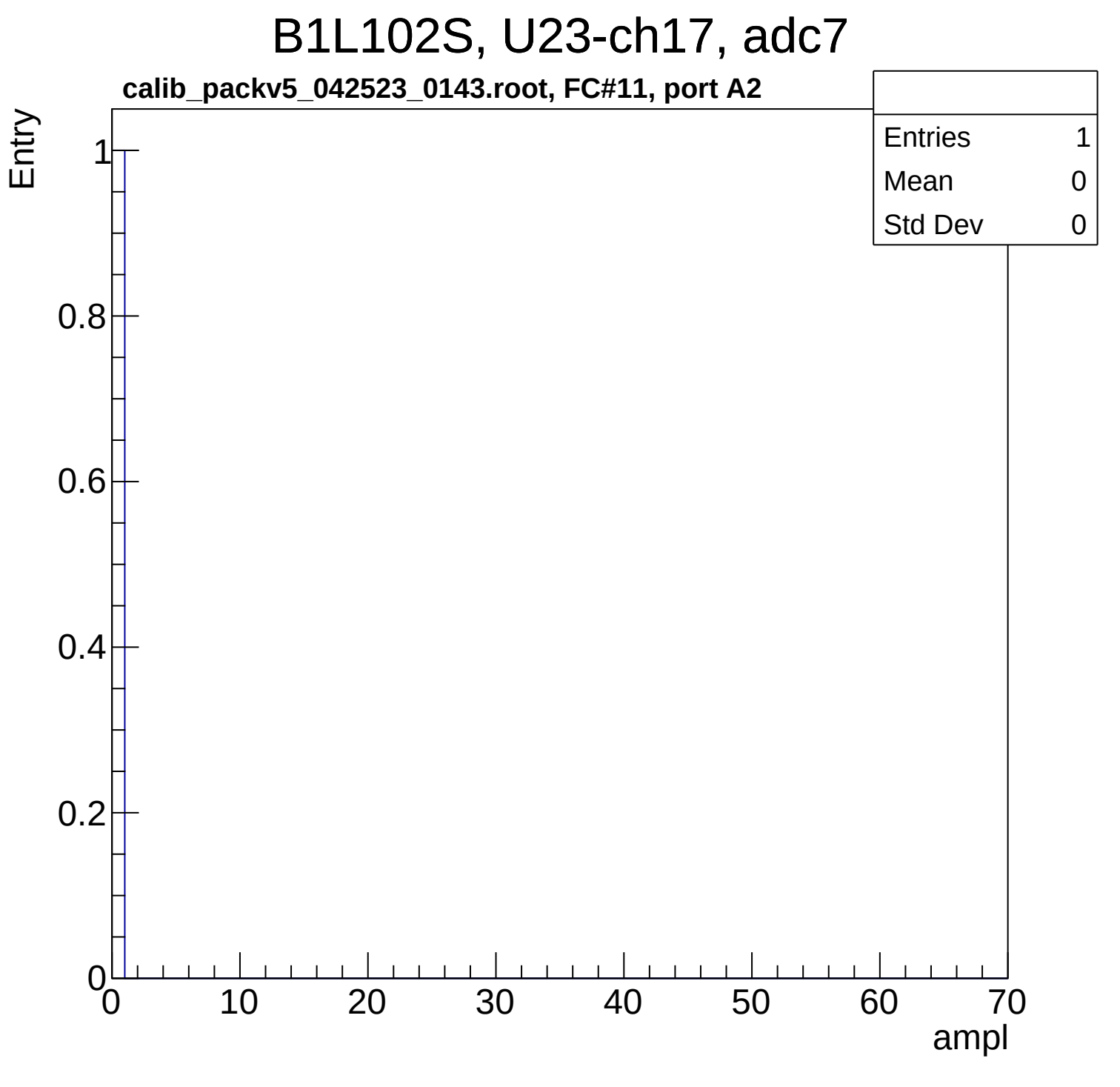

B1L102S, U23-ch17, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 45.13
Std Dev 3.332
Gaus mean : 45.5290
Gaus Width: 3.4784
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

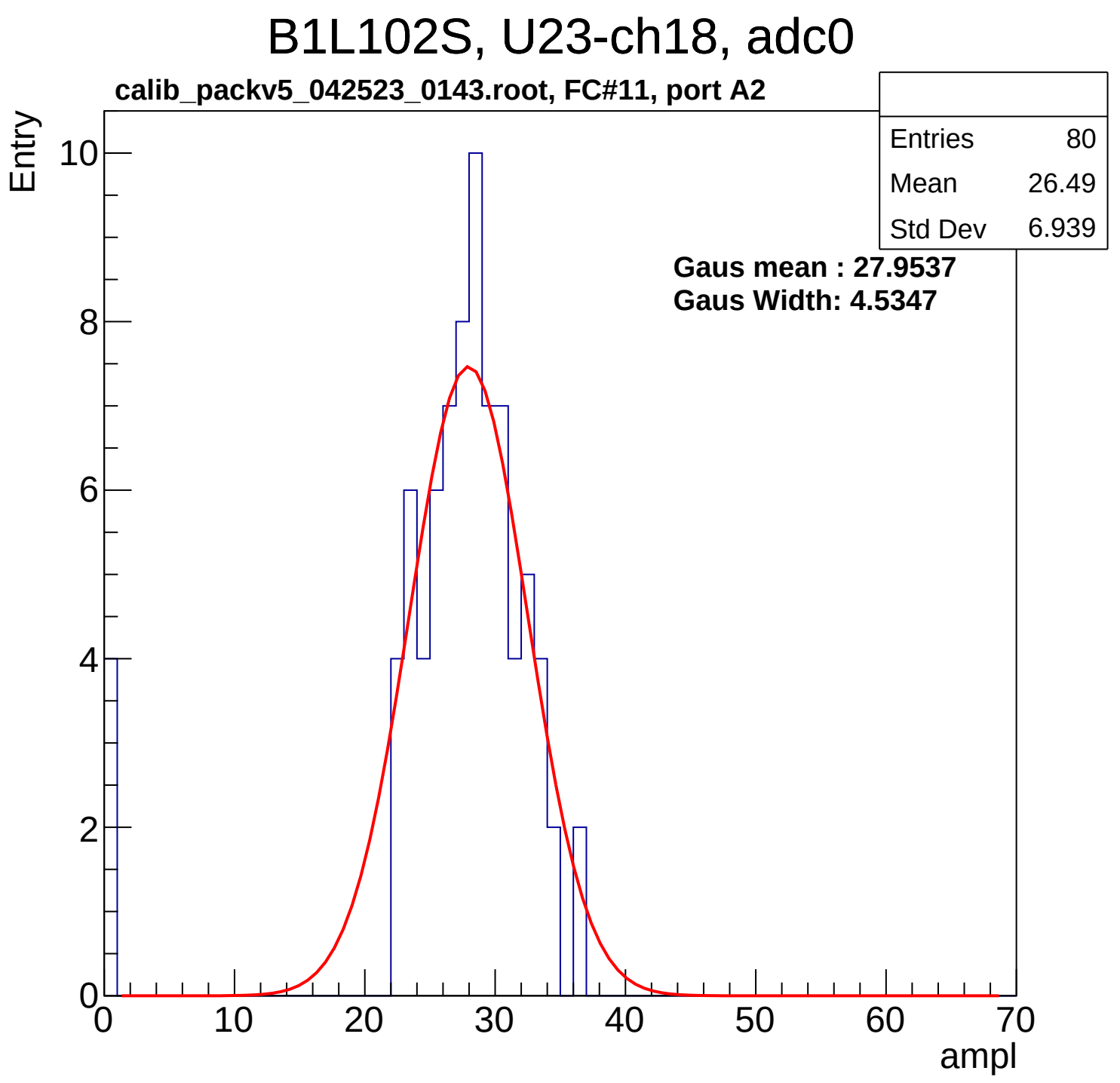

B1L102S, U23-ch17, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 51.28
Std Dev 3.349
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

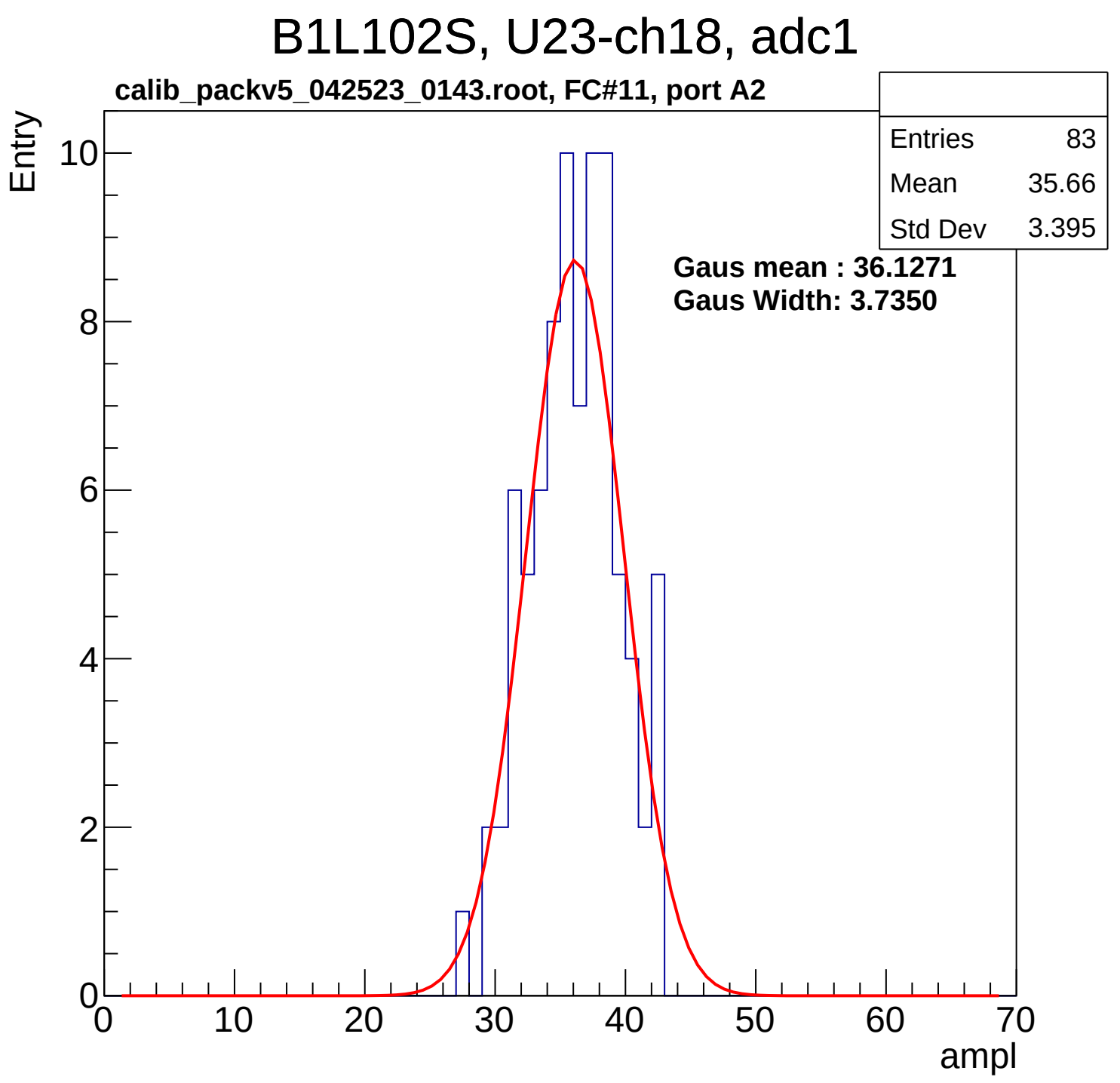

B1L102S, U23-ch17, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 56.79
Std Dev 2.821
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

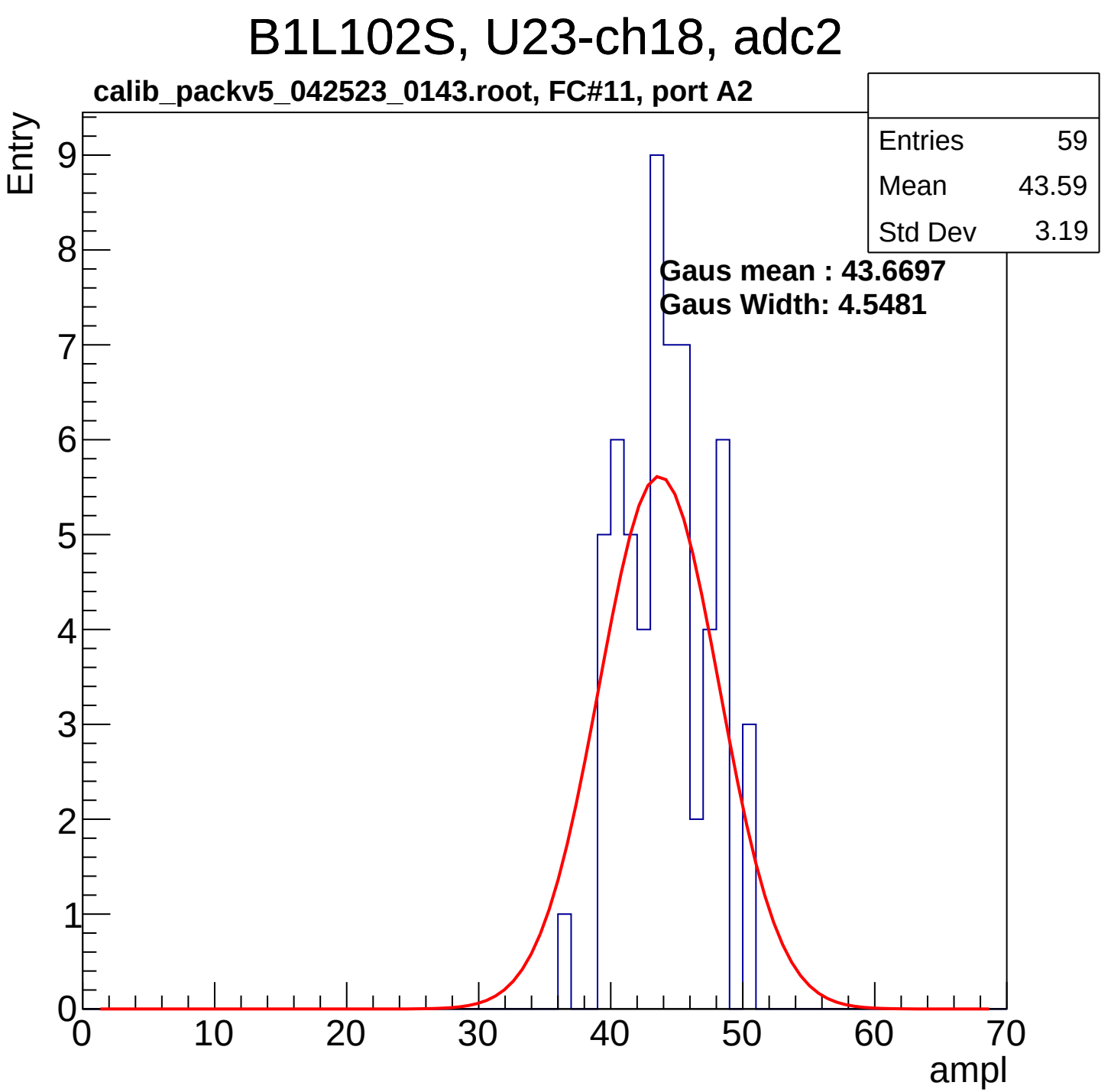

B1L102S, U23-ch17, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.93
Std Dev 9.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

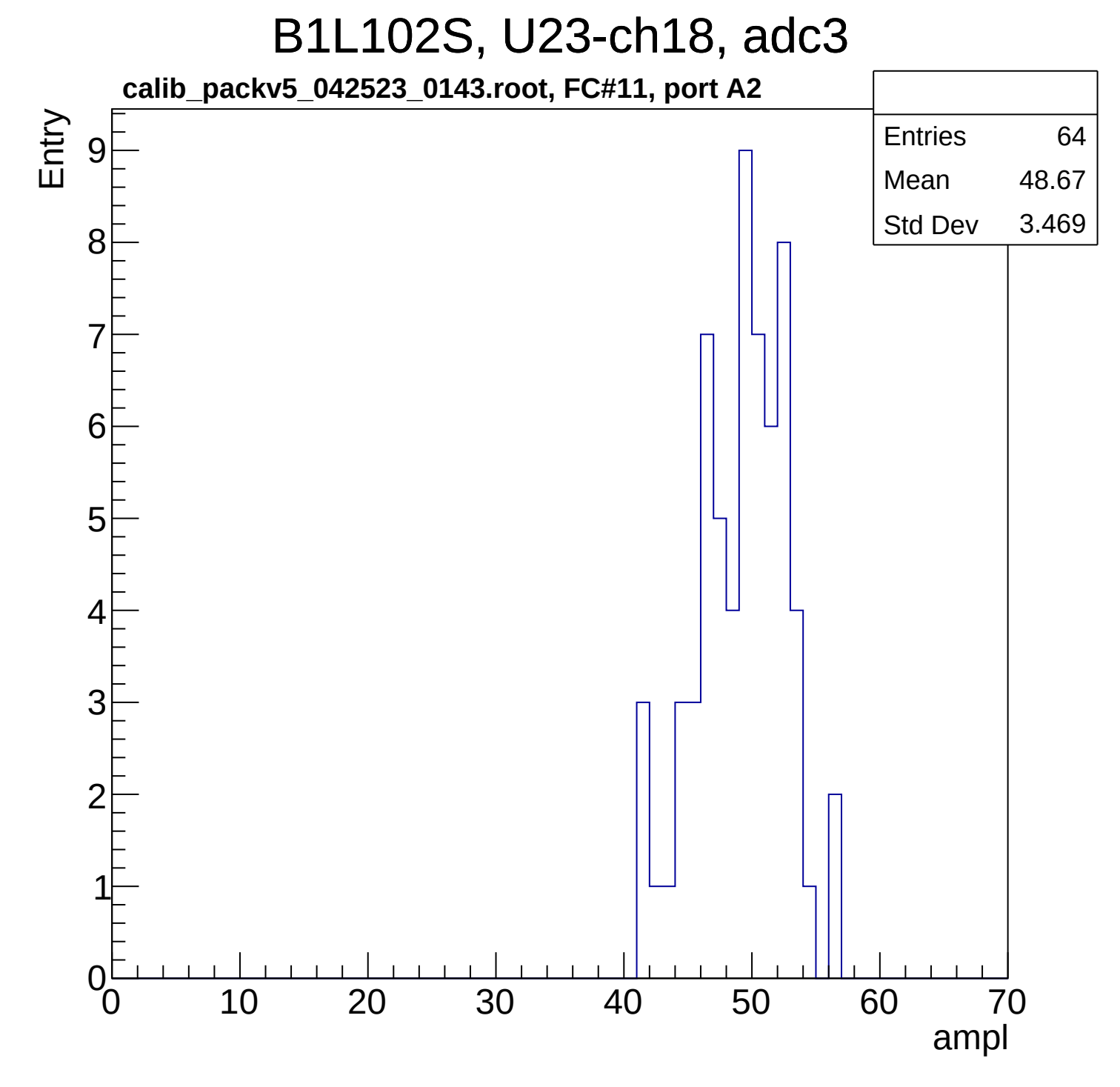

B1L102S, U23-ch17, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

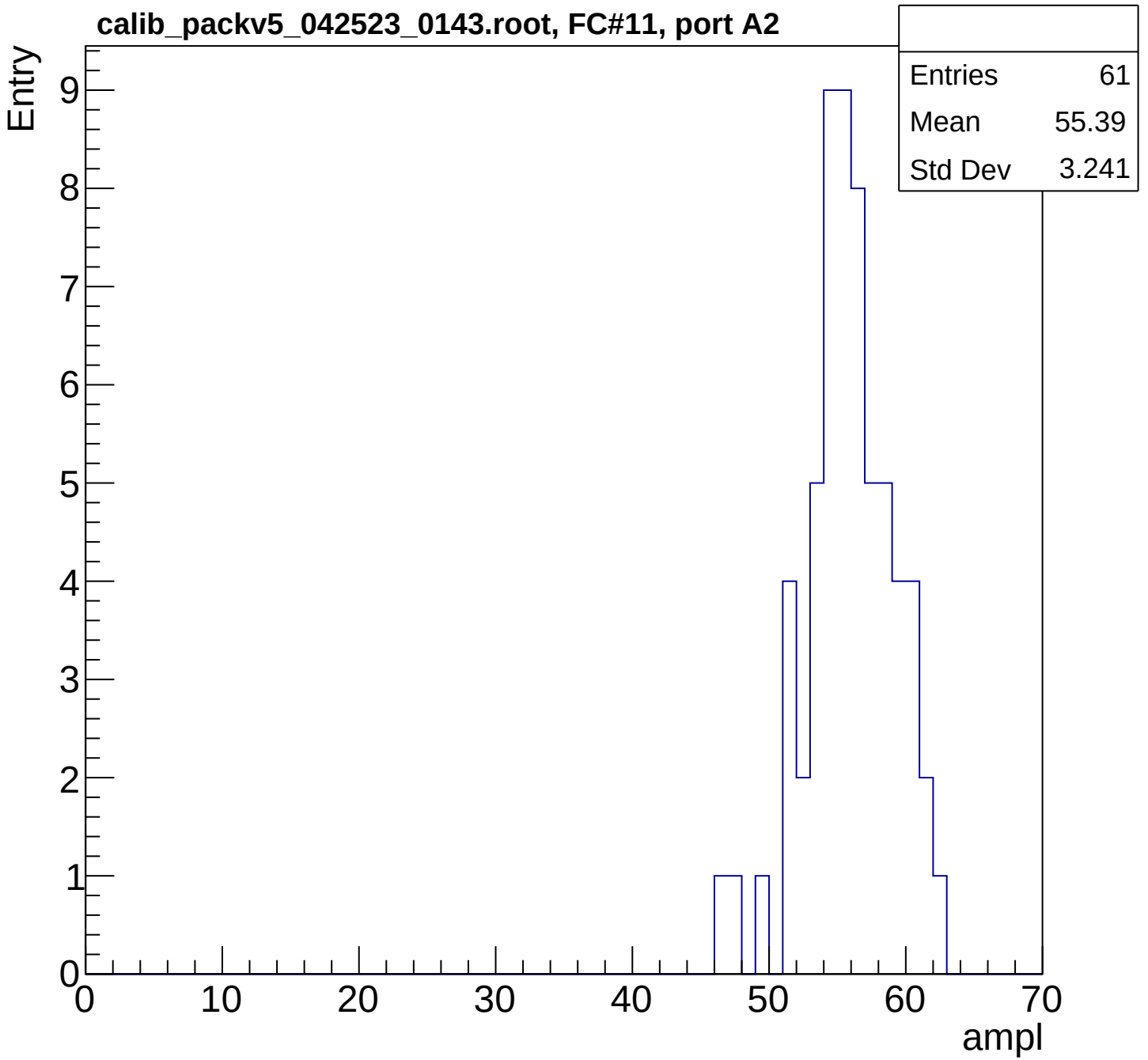

B1L102S, U23-ch17, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

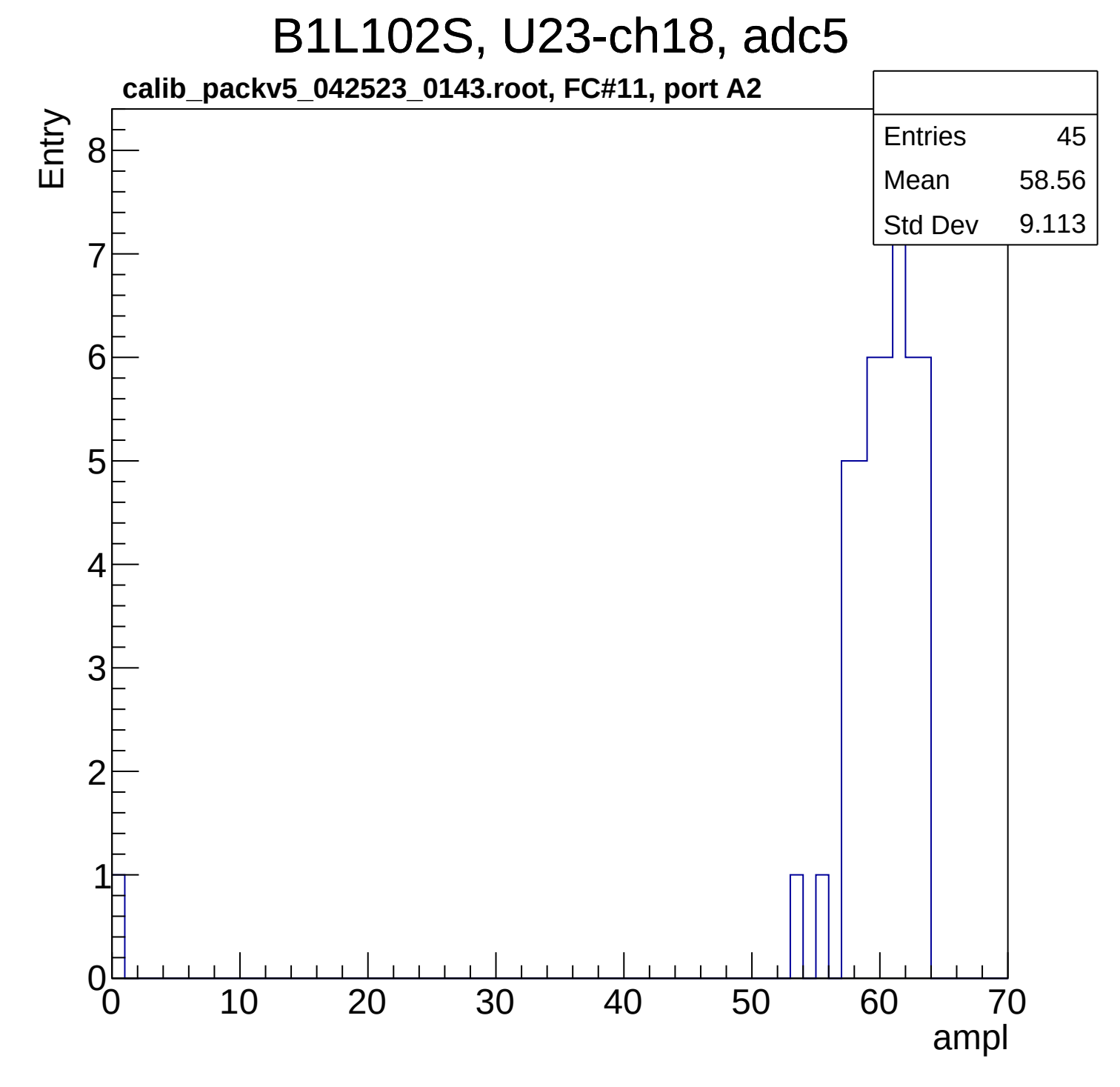

B1L102S, U23-ch18, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 26.49
Std Dev 6.939
Gaus mean : 27.9537
Gaus Width: 4.5347
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

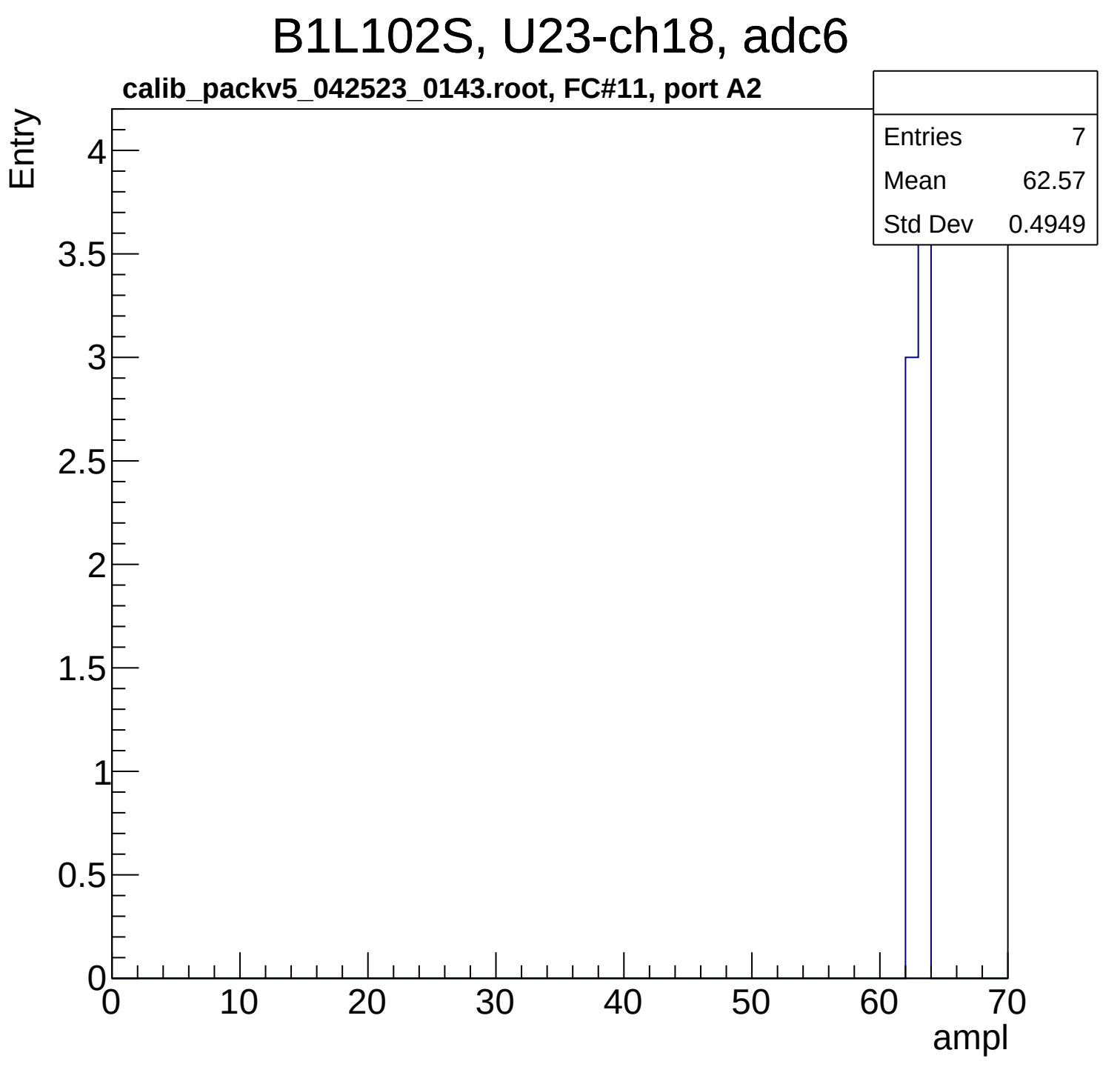

B1L102S, U23-ch18, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 35.66
Std Dev 3.395
Gaus mean : 36.1271
Gaus Width: 3.7350
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

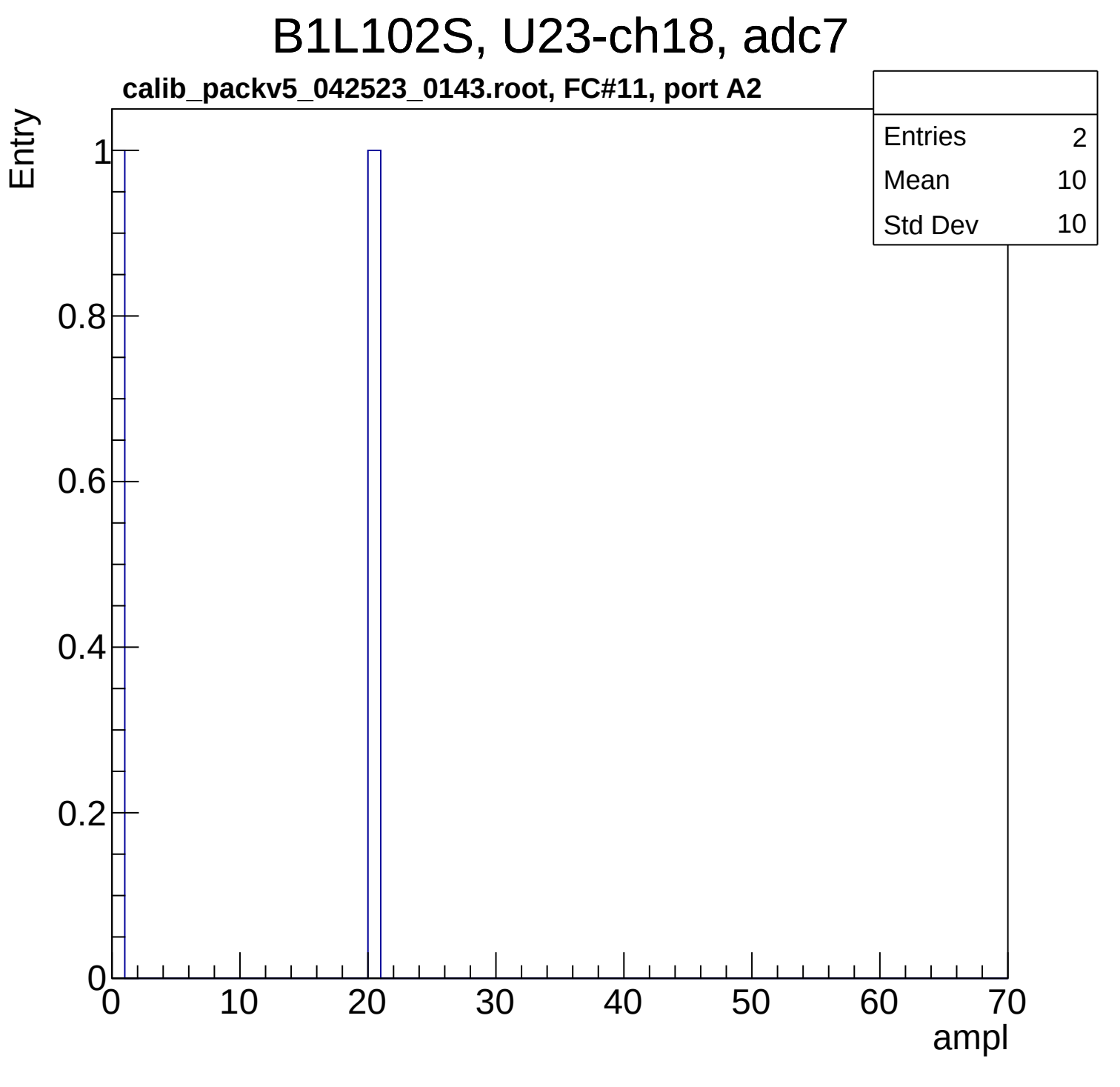

B1L102S, U23-ch18, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 43.59
Std Dev 3.19
Gaus mean : 43.6697
Gaus Width: 4.5481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

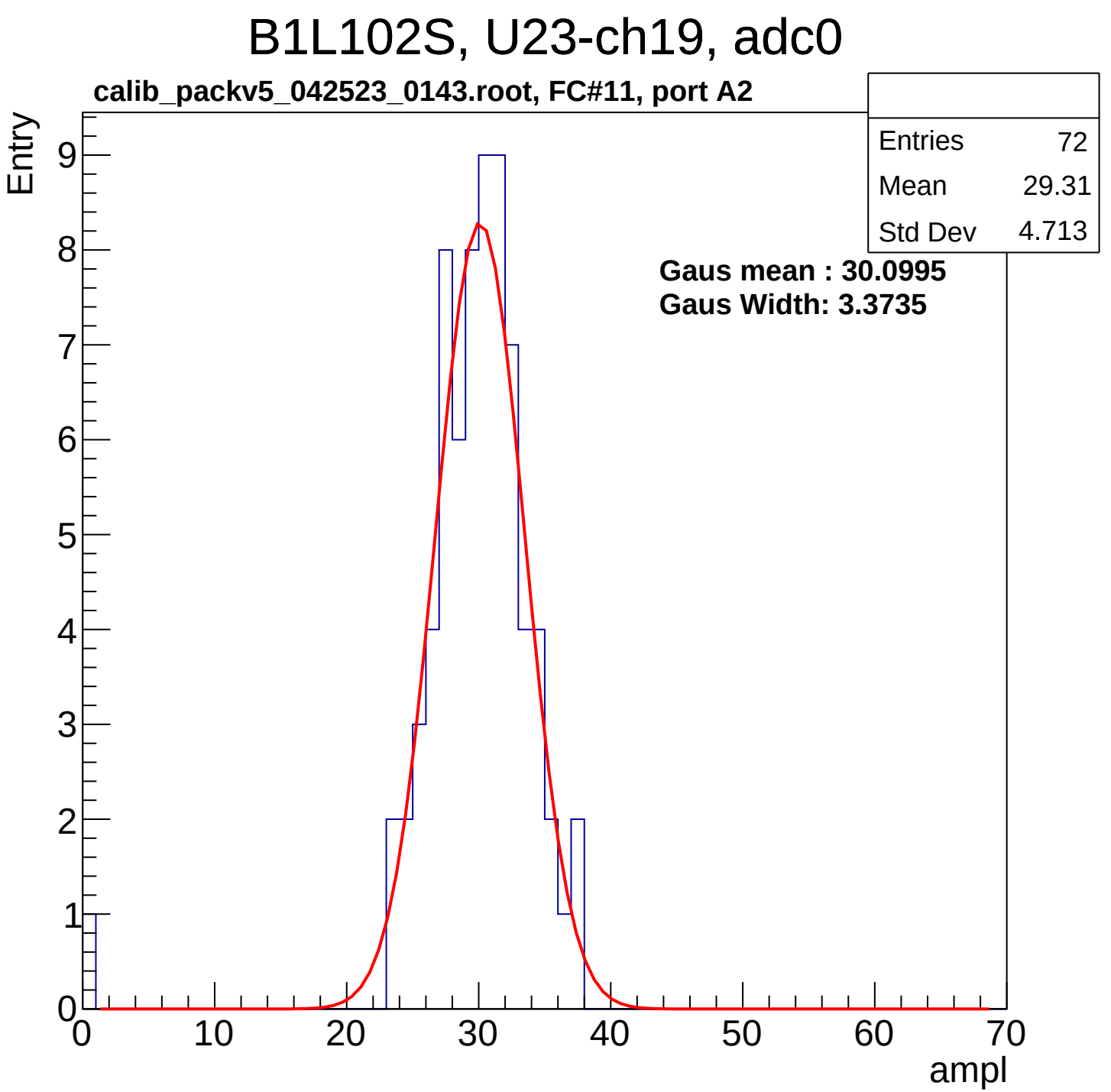

B1L102S, U23-ch18, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 48.67
Std Dev 3.469
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

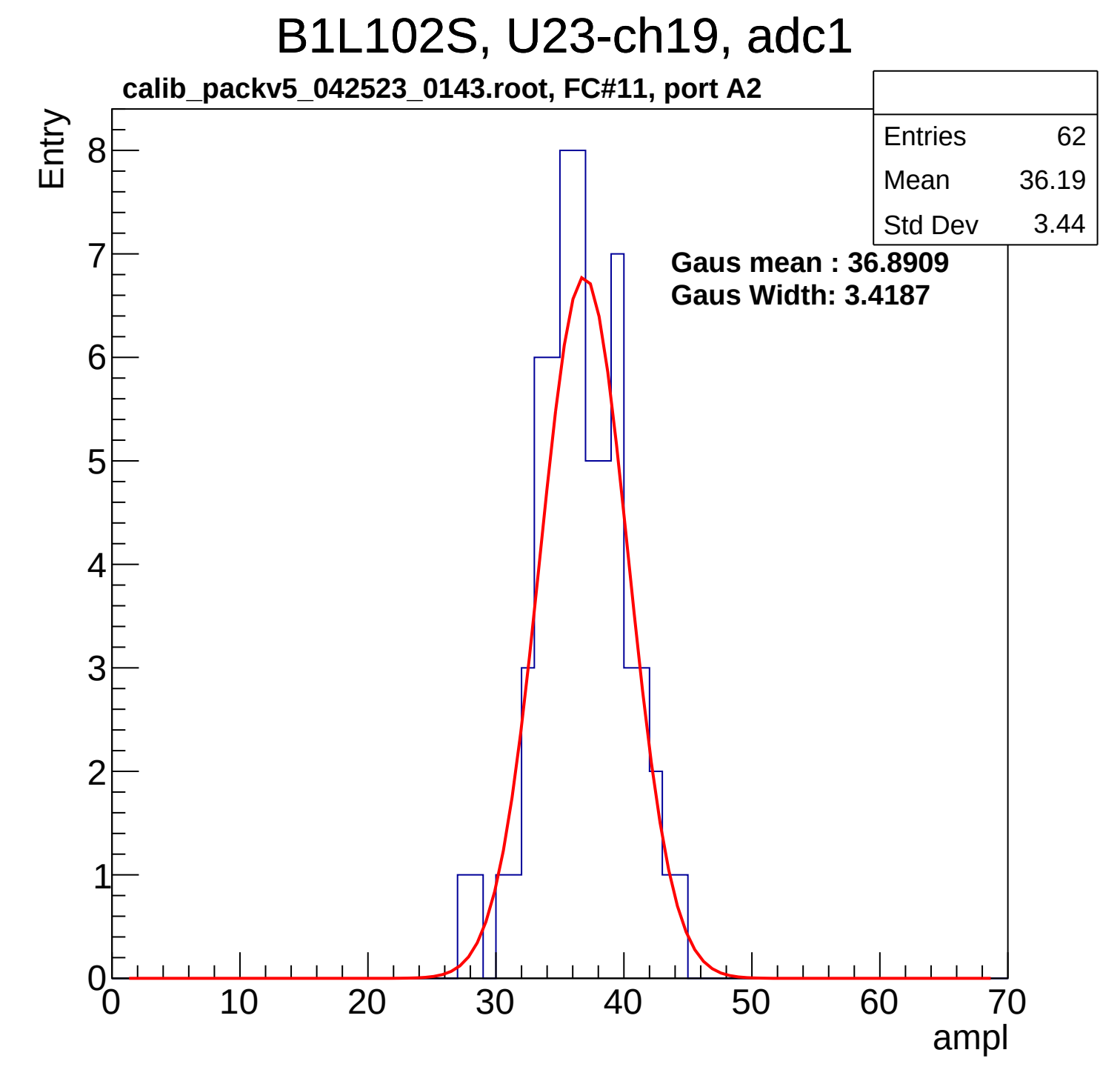

B1L102S, U23-ch18, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 55.39
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

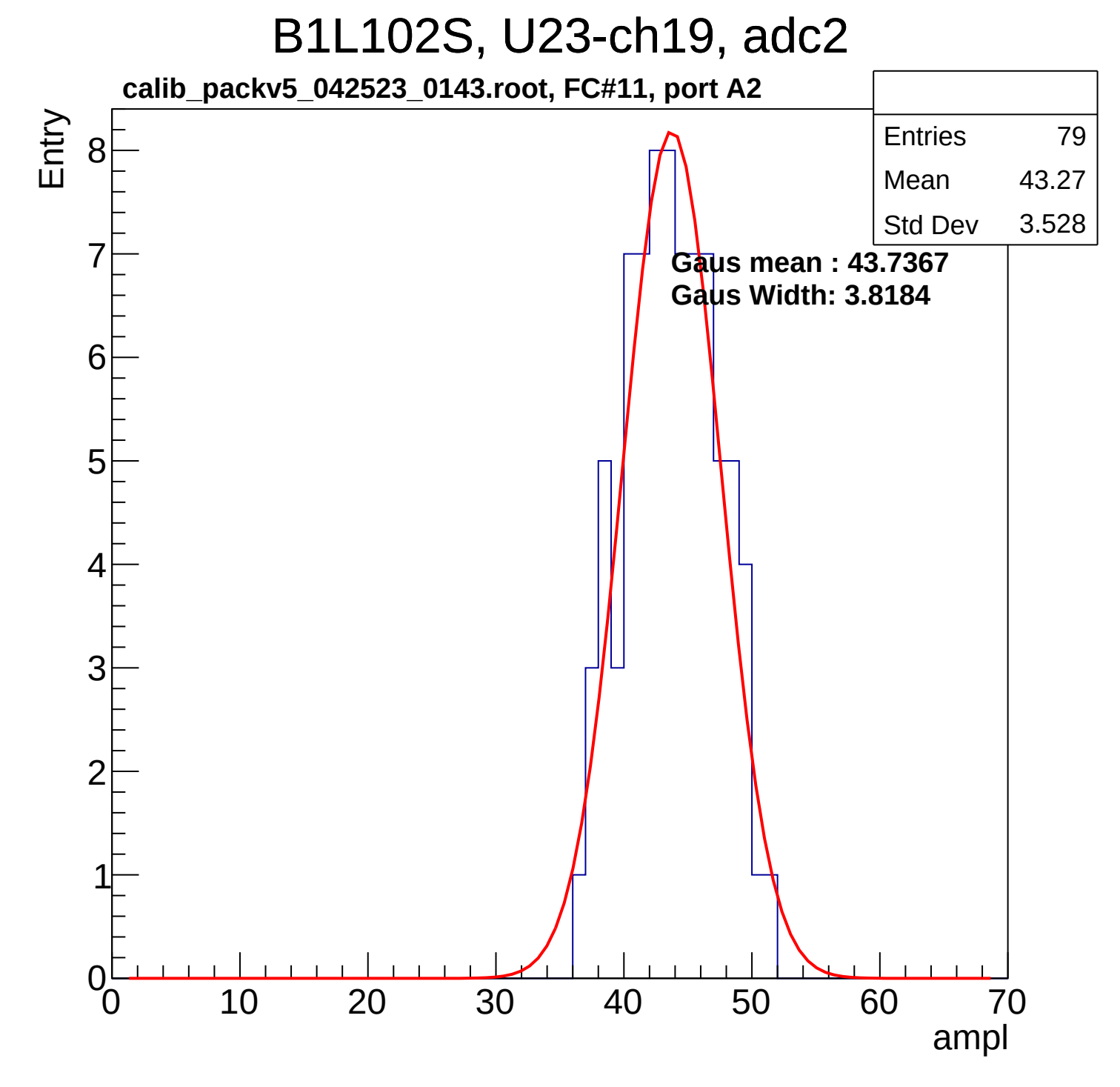

B1L102S, U23-ch18, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.56
Std Dev 9.113
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

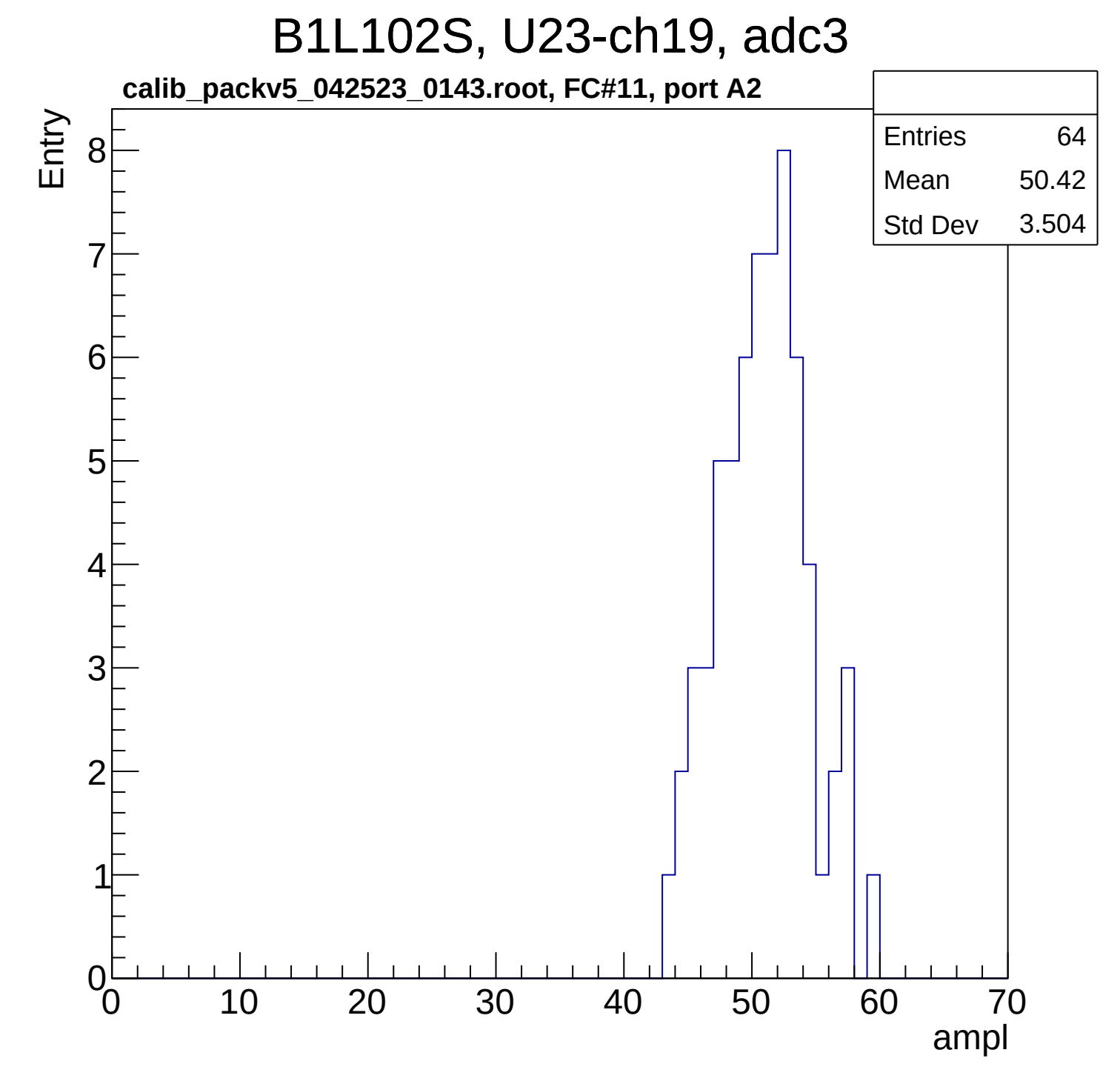

B1L102S, U23-ch18, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

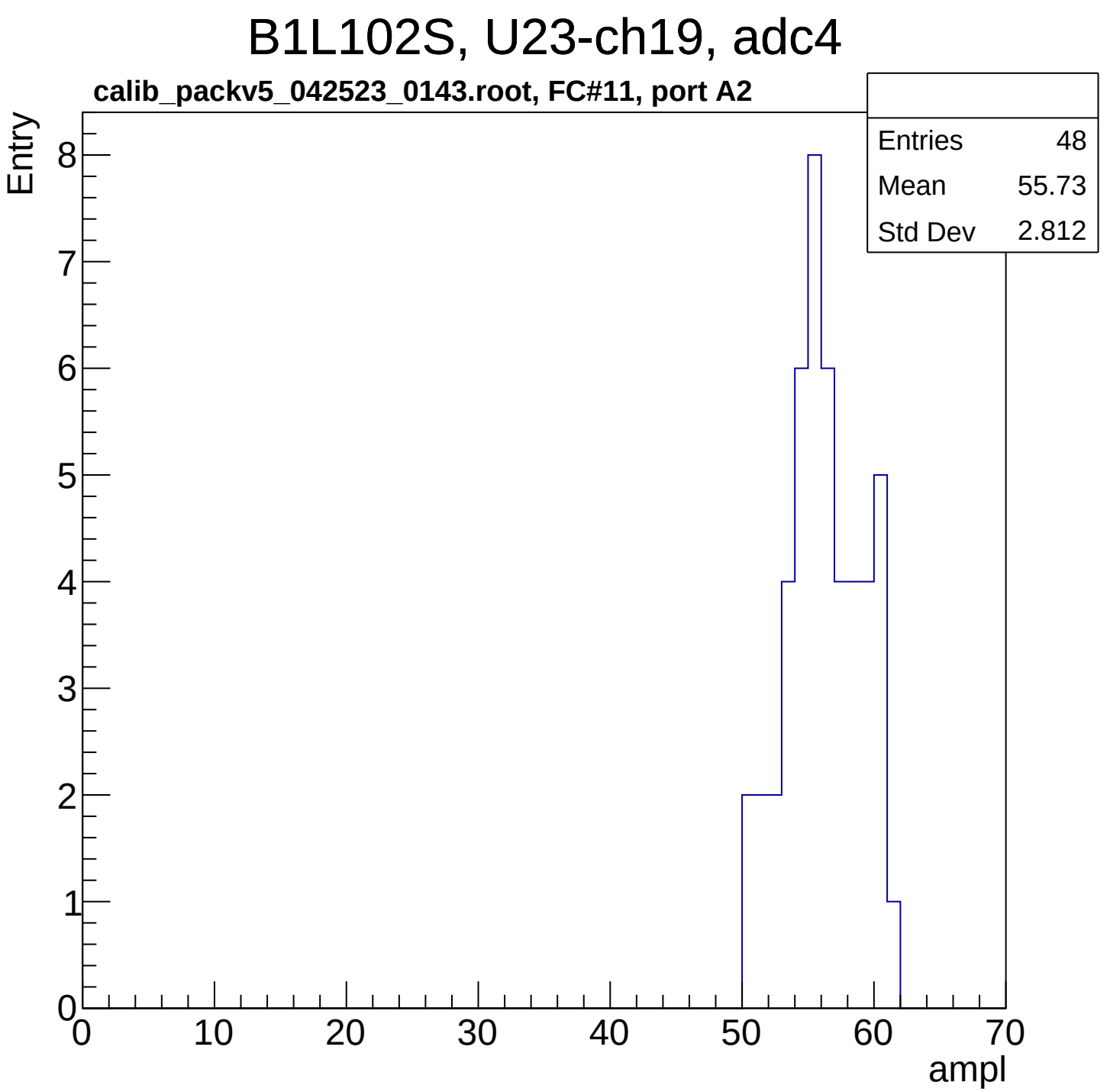

B1L102S, U23-ch18, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10
Std Dev 10
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

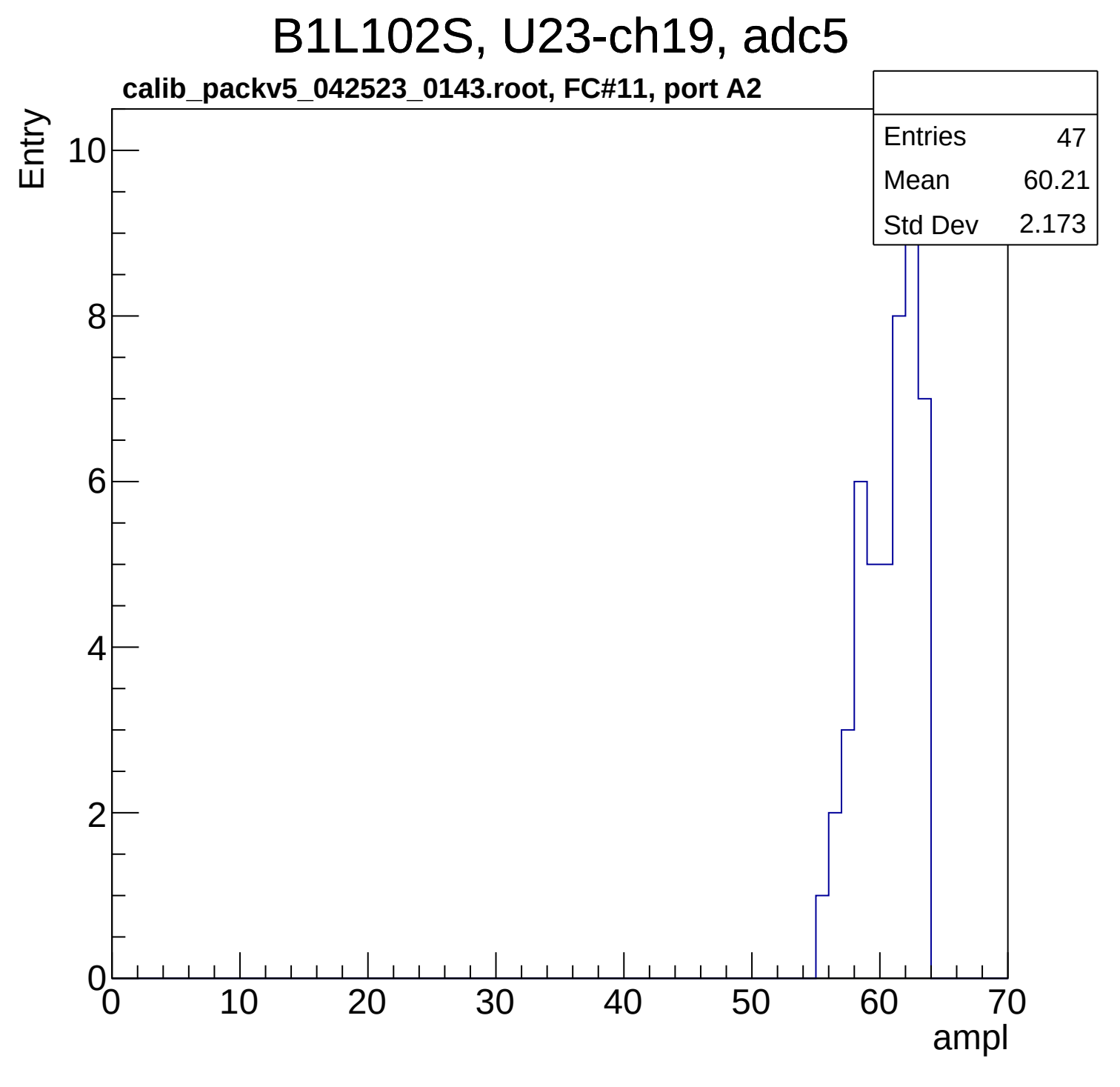

B1L102S, U23-ch19, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 29.31
Std Dev 4.713
Gaus mean : 30.0995
Gaus Width: 3.3735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

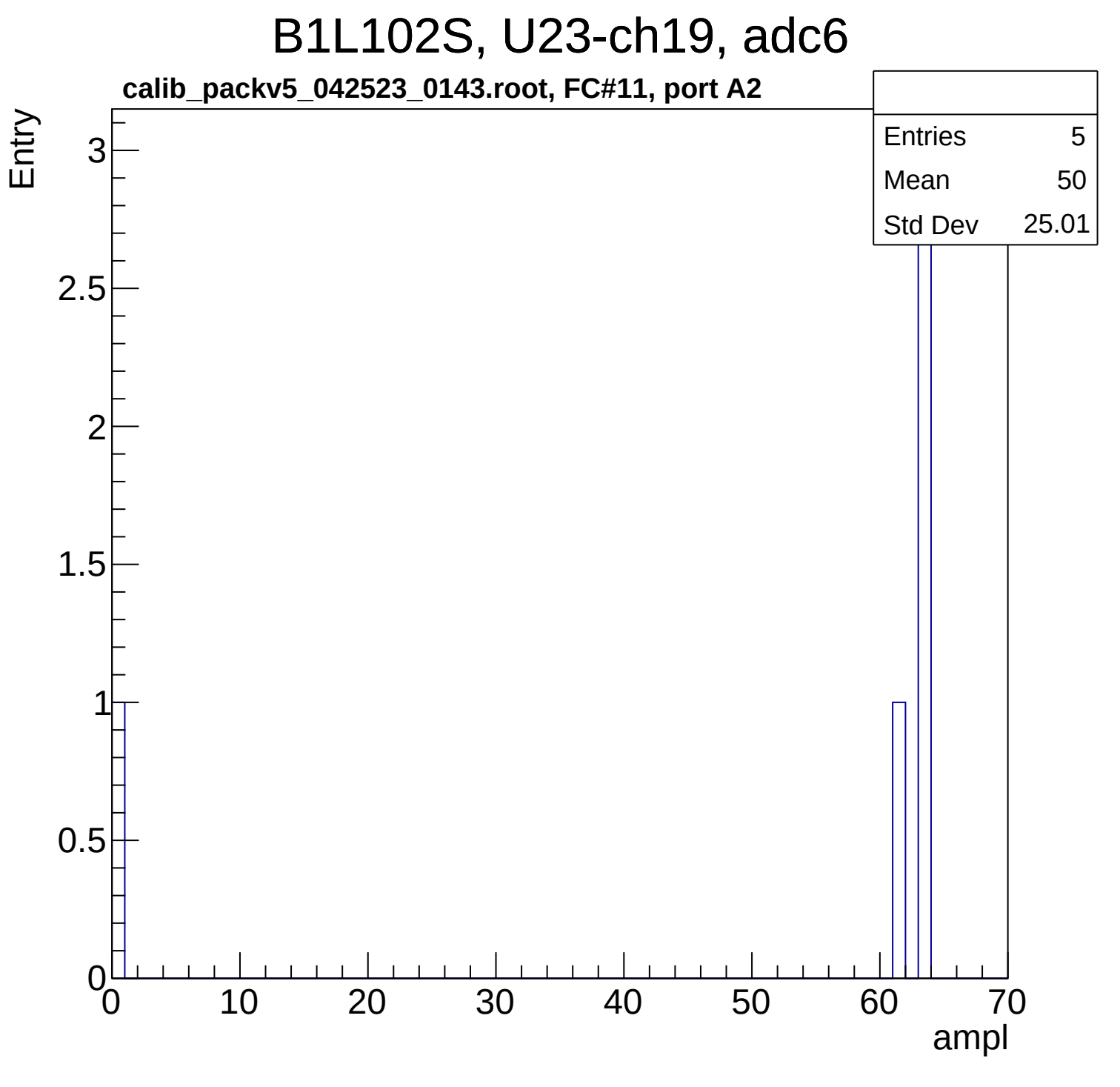

B1L102S, U23-ch19, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 36.19
Std Dev 3.44
Gaus mean : 36.8909
Gaus Width: 3.4187
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

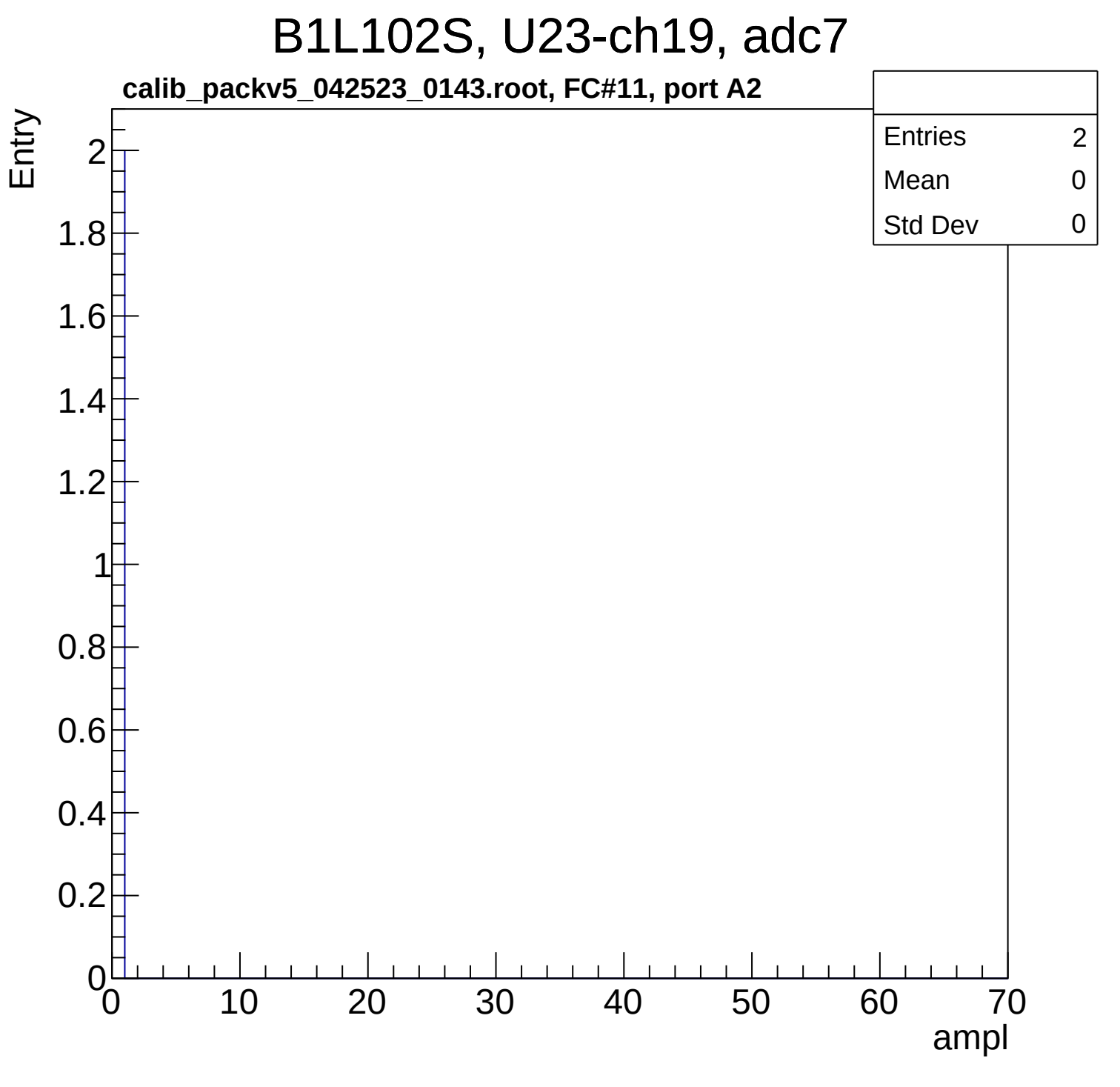

B1L102S, U23-ch19, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 43.27
Std Dev 3.528
Gaus mean : 43.7367
Gaus Width: 3.8184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

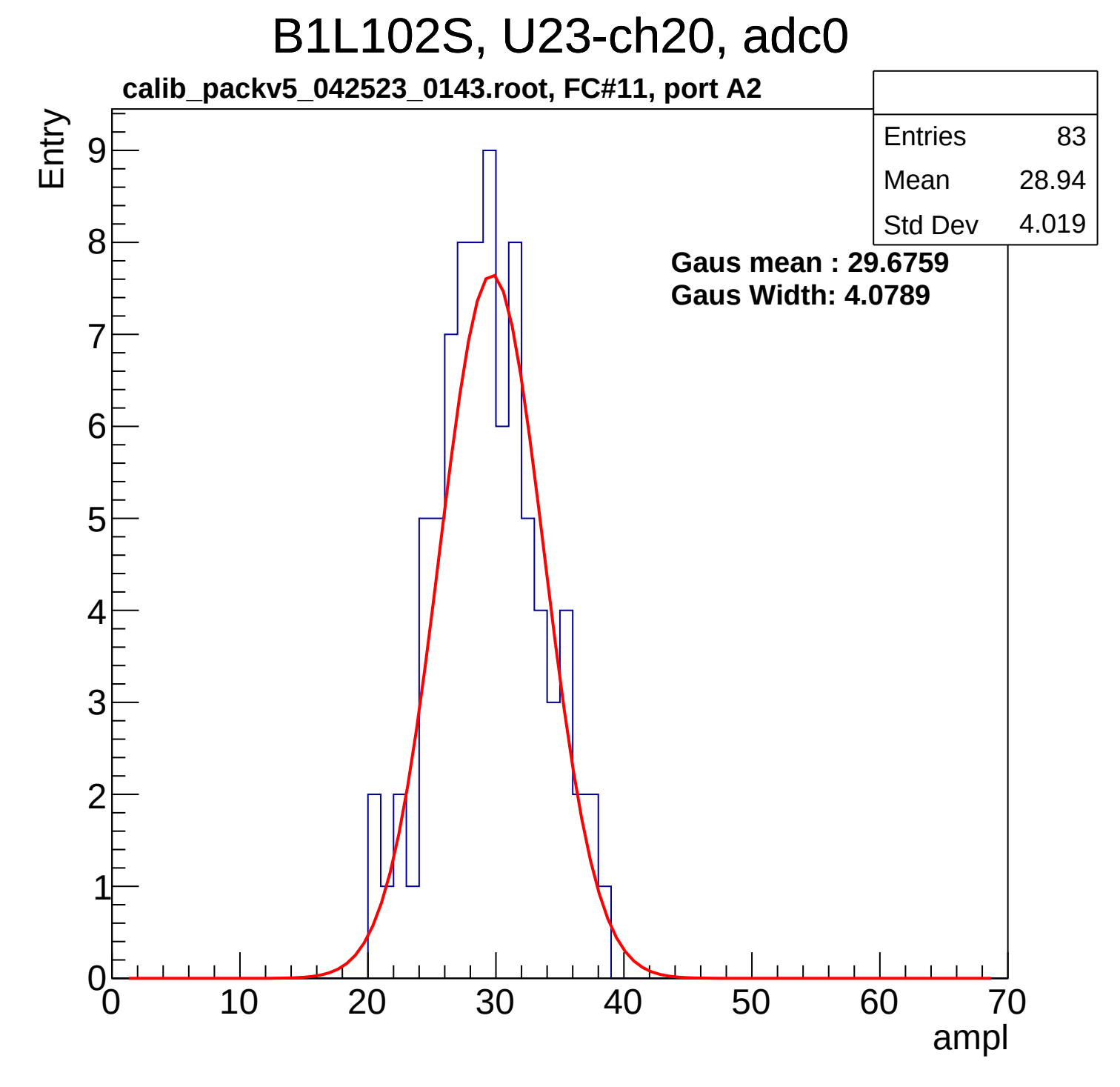

B1L102S, U23-ch19, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 50.42
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

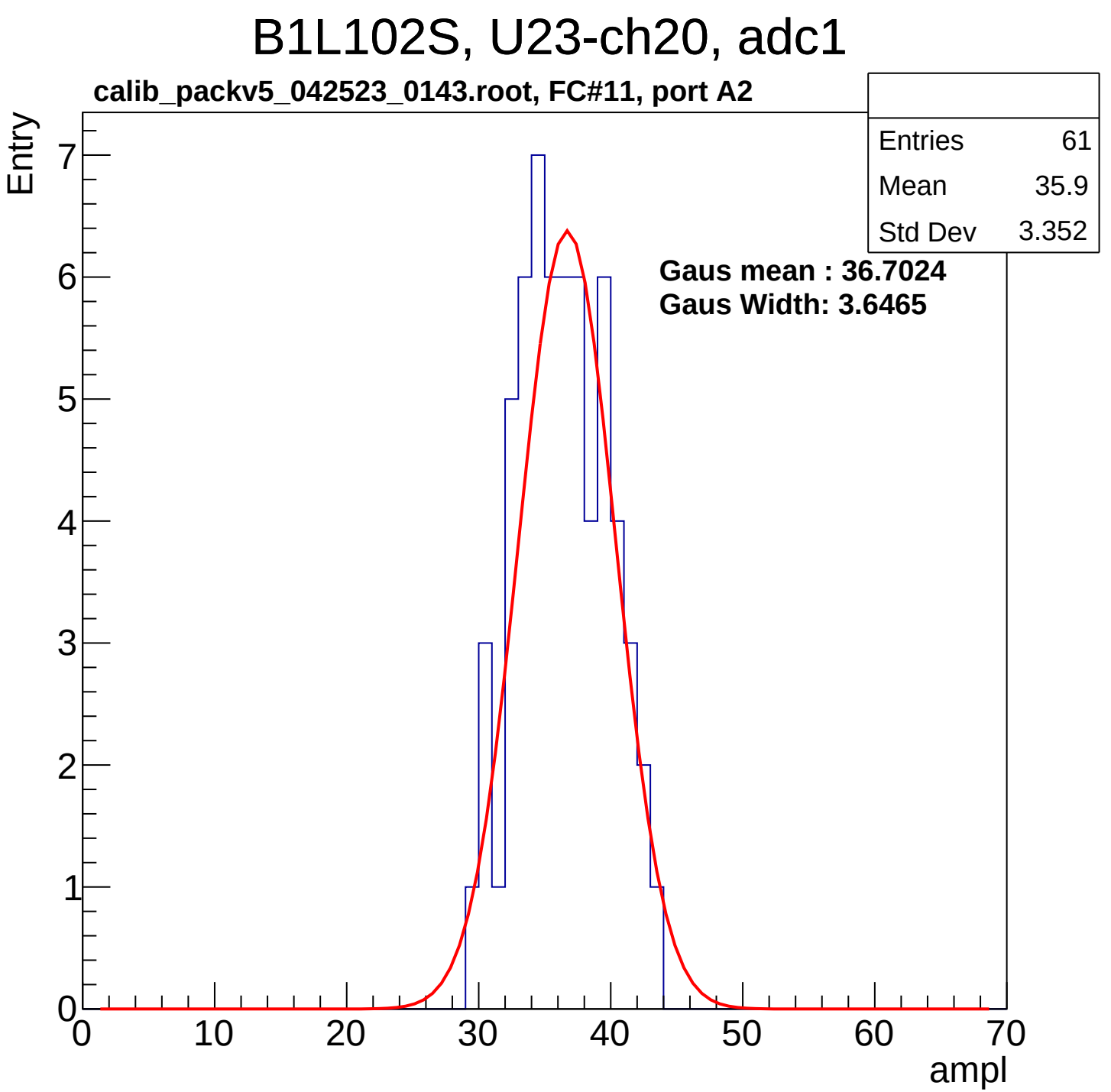

B1L102S, U23-ch19, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 55.73
Std Dev 2.812
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

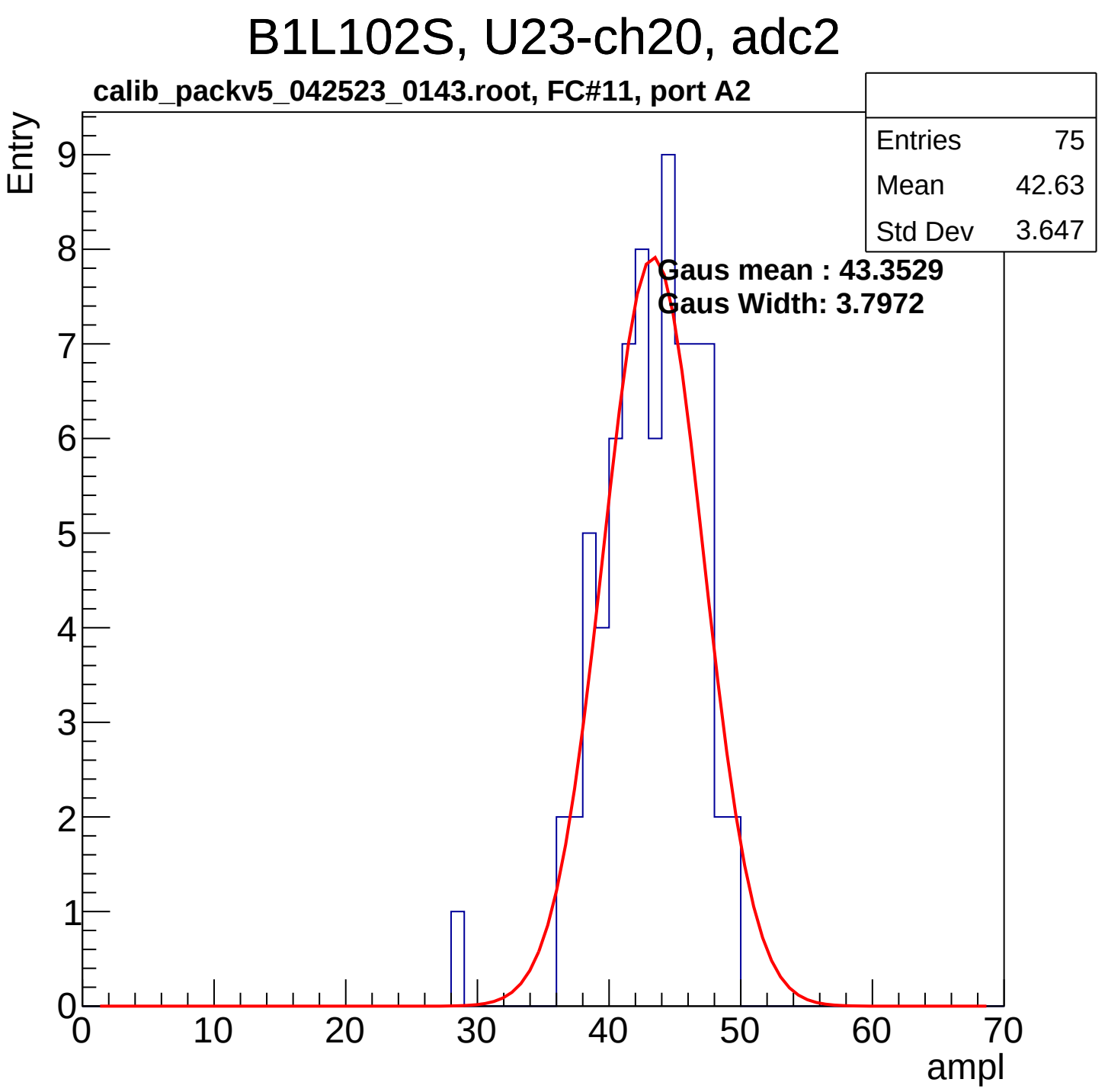

B1L102S, U23-ch19, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 60.21
Std Dev 2.173
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

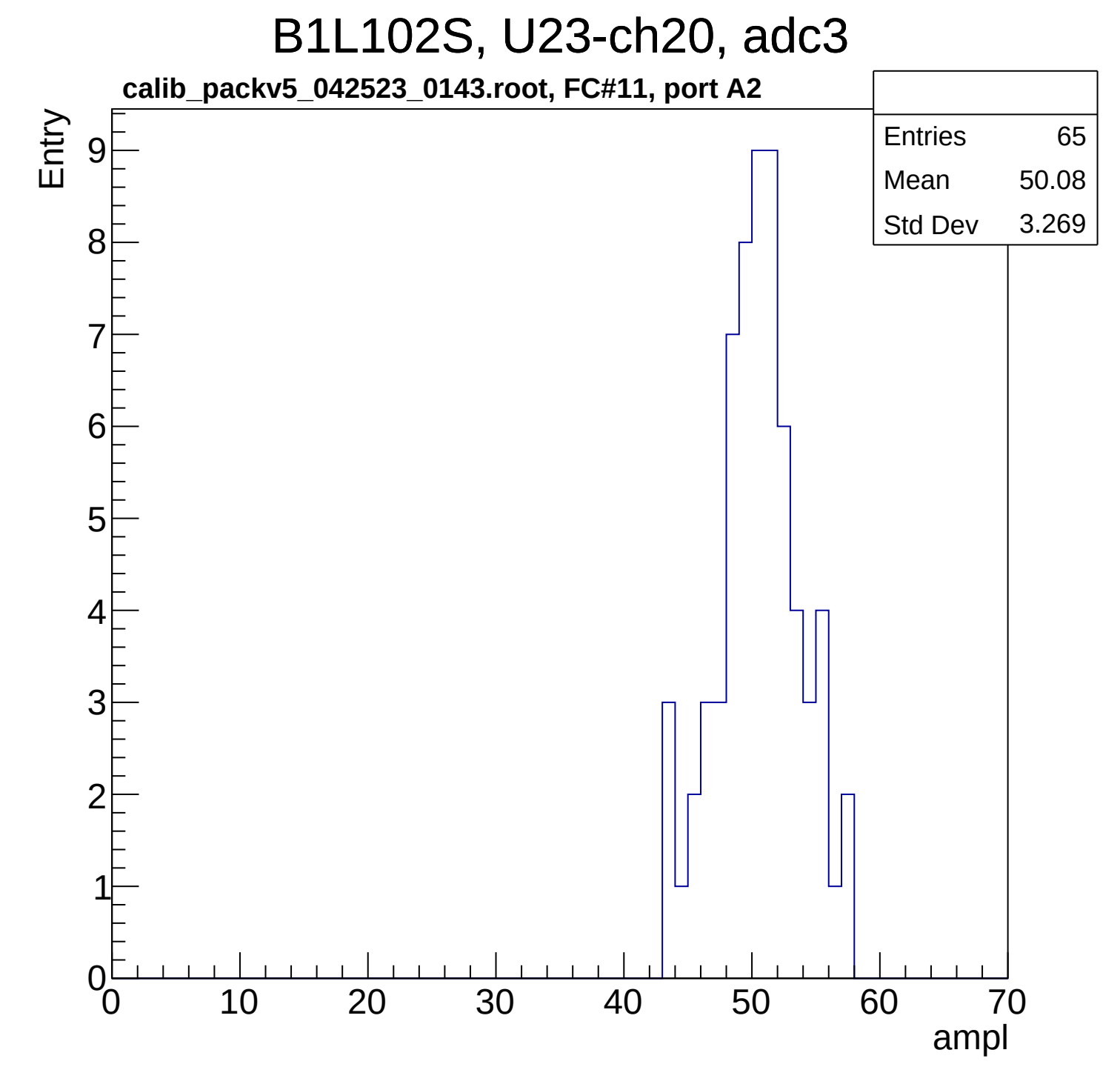

B1L102S, U23-ch19, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

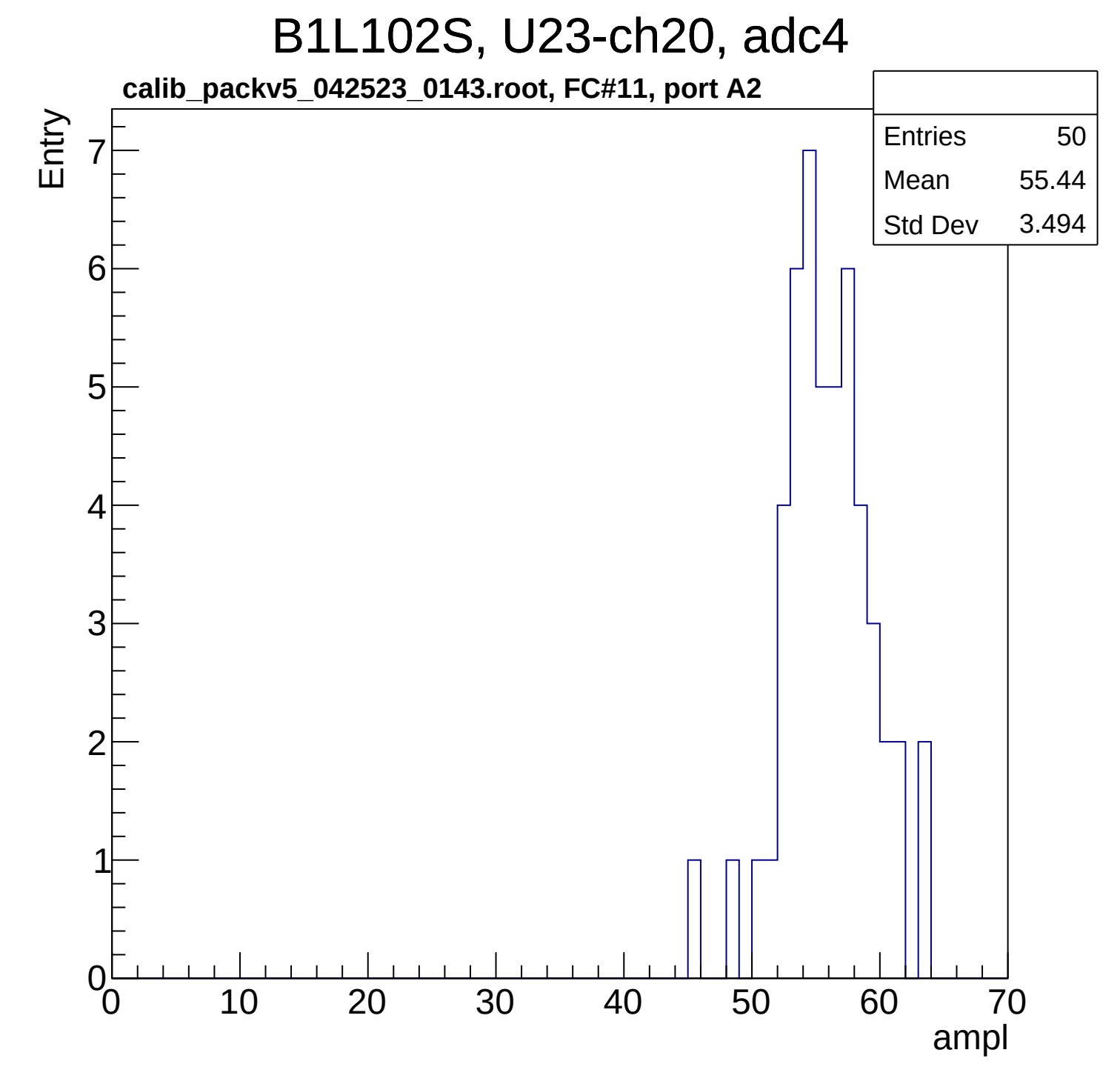

B1L102S, U23-ch19, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

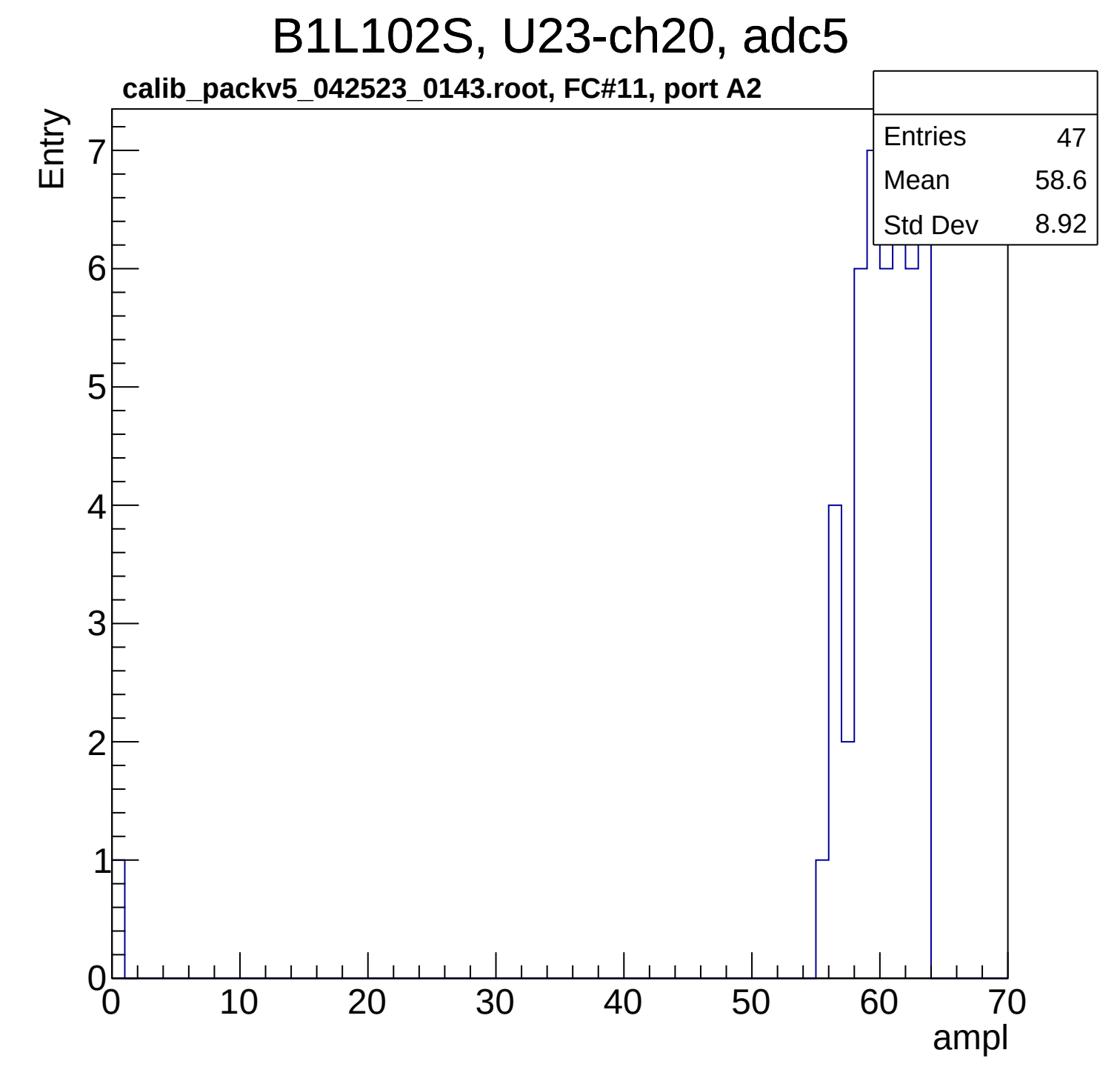

B1L102S, U23-ch20, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 28.94
Std Dev 4.019
Gaus mean : 29.6759
Gaus Width: 4.0789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

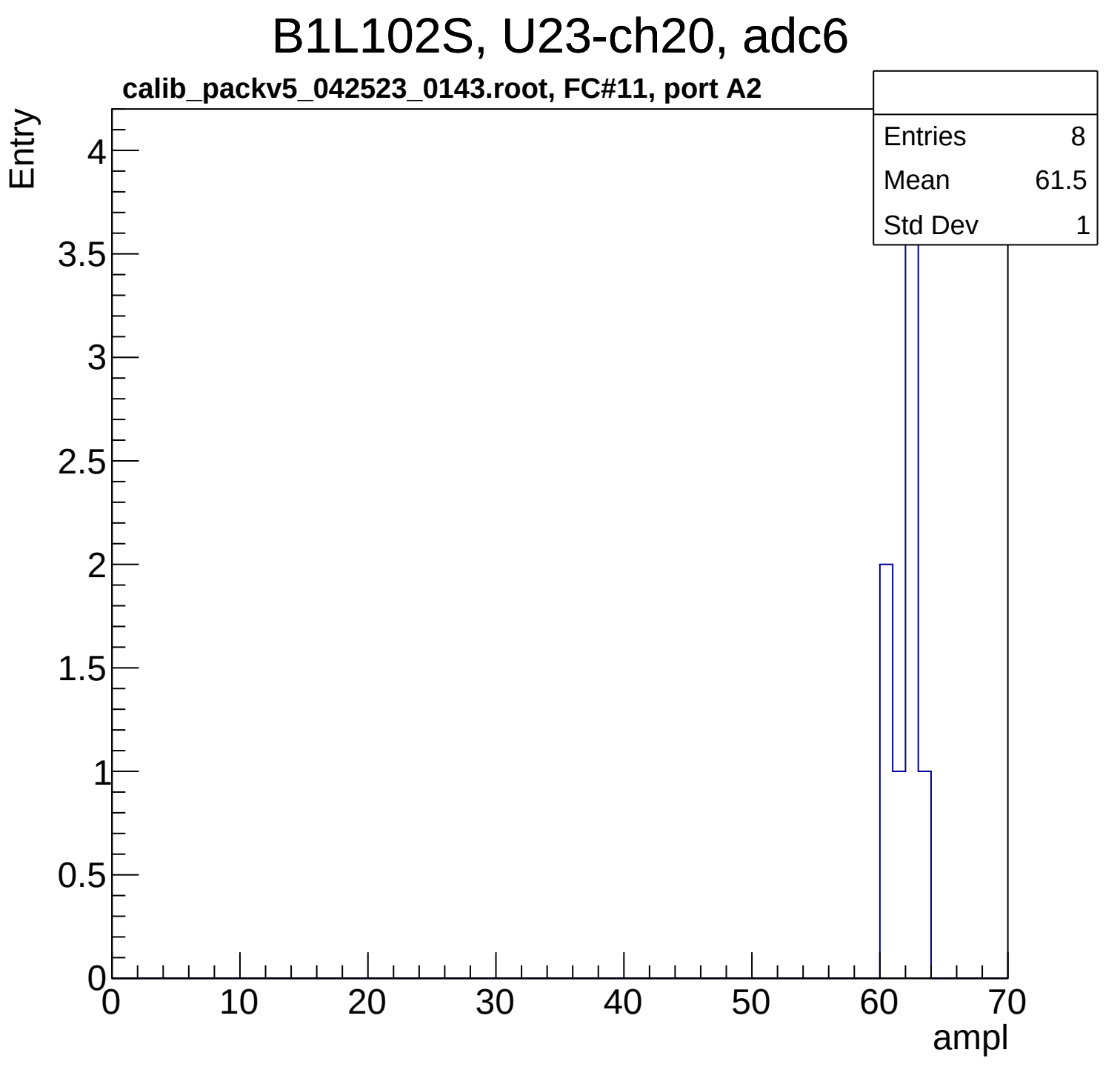

B1L102S, U23-ch20, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 35.9
Std Dev 3.352
Gaus mean : 36.7024
Gaus Width: 3.6465
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

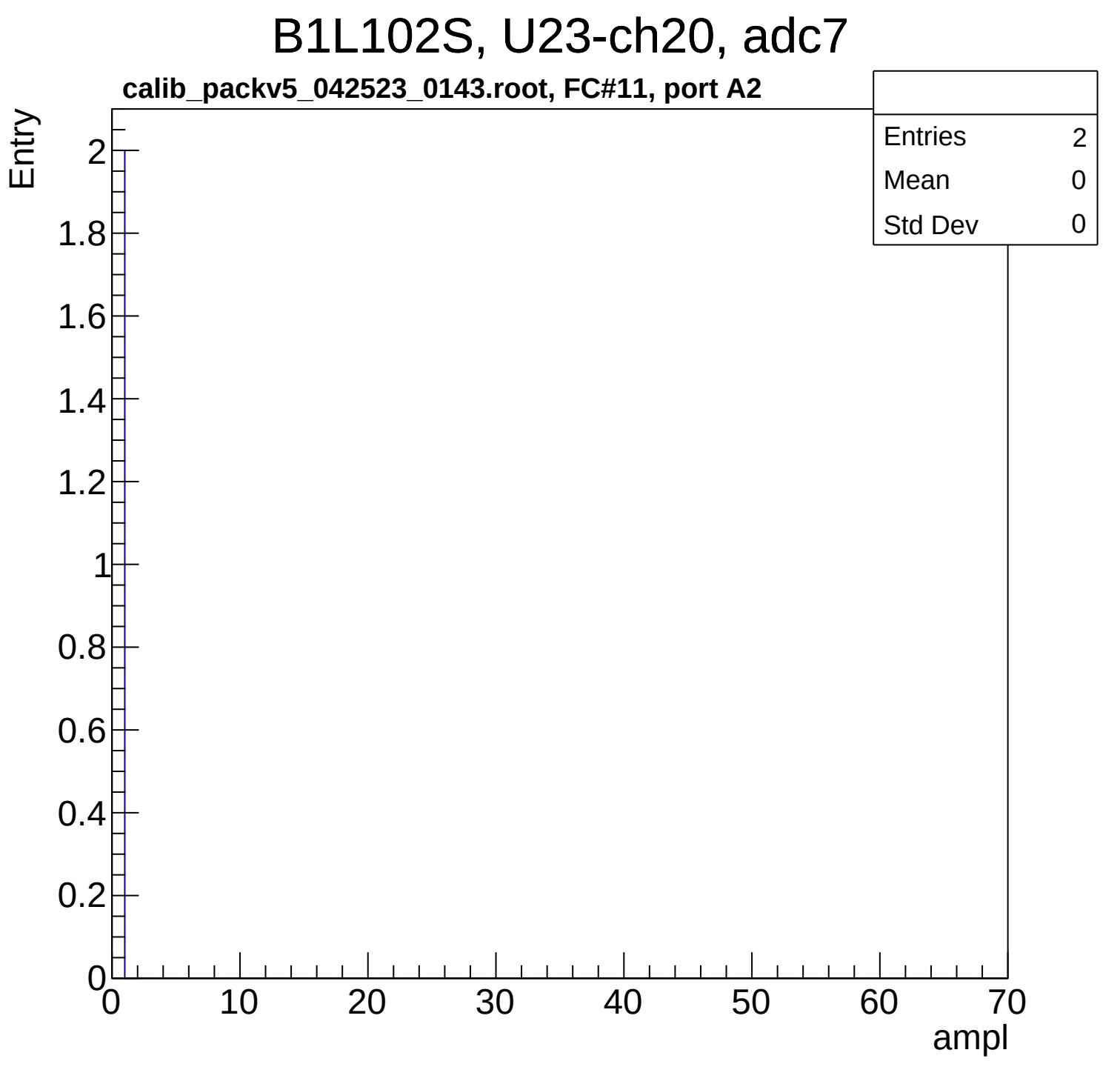

B1L102S, U23-ch20, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 42.63
Std Dev 3.647
Gaus mean : 43.3529
Gaus Width: 3.7972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

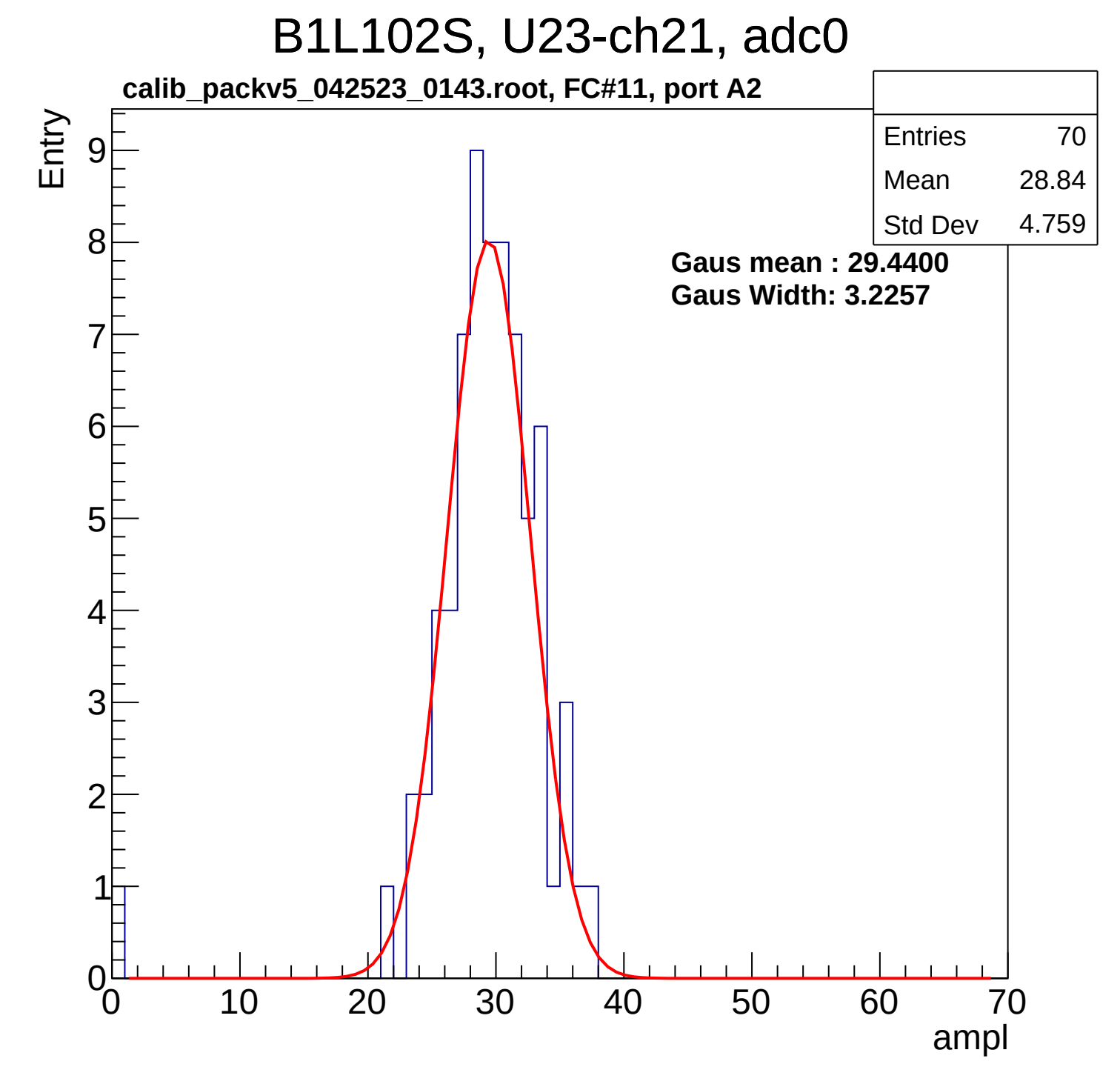

B1L102S, U23-ch20, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 50.08
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

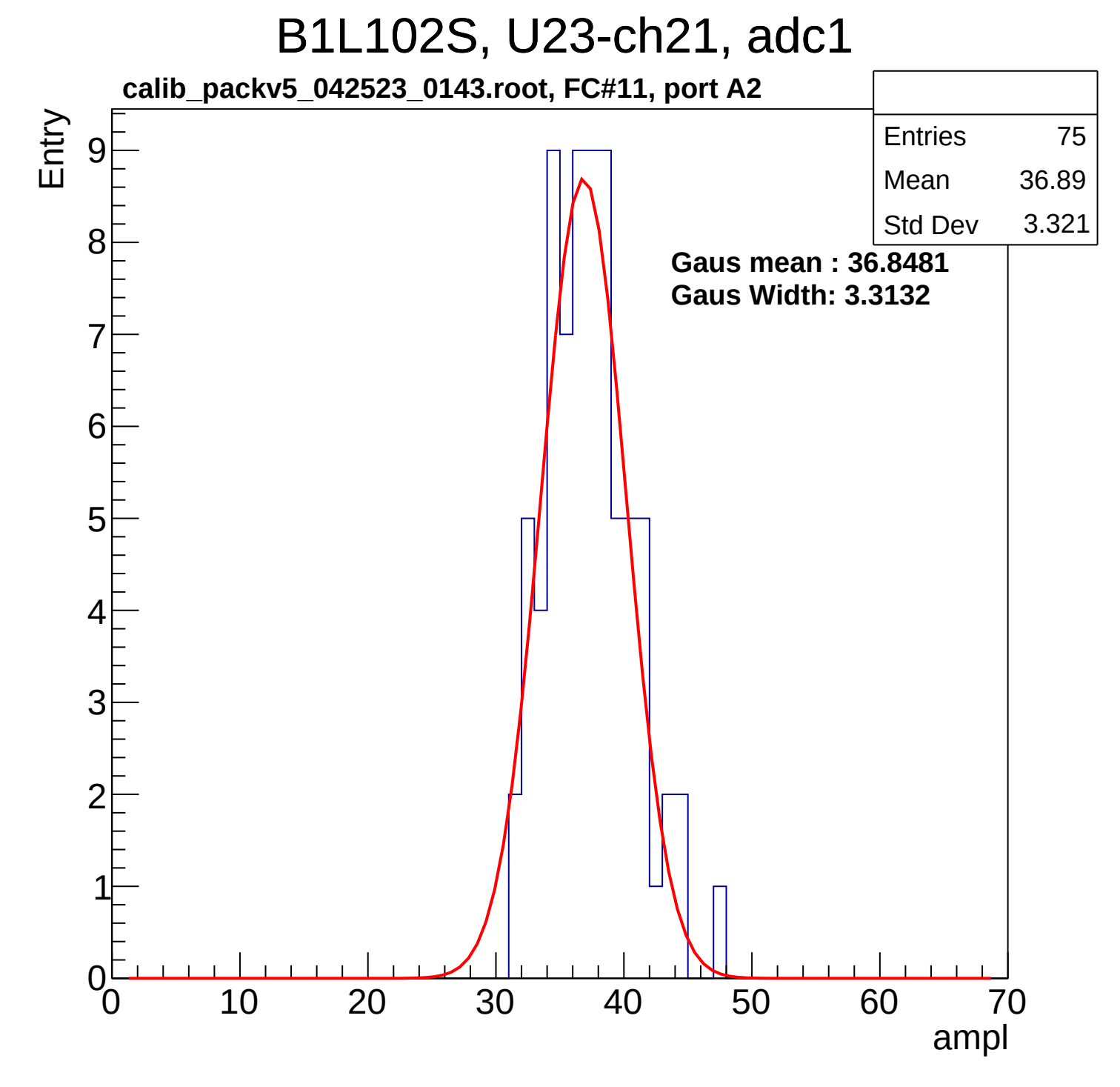

B1L102S, U23-ch20, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 55.44
Std Dev 3.494
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

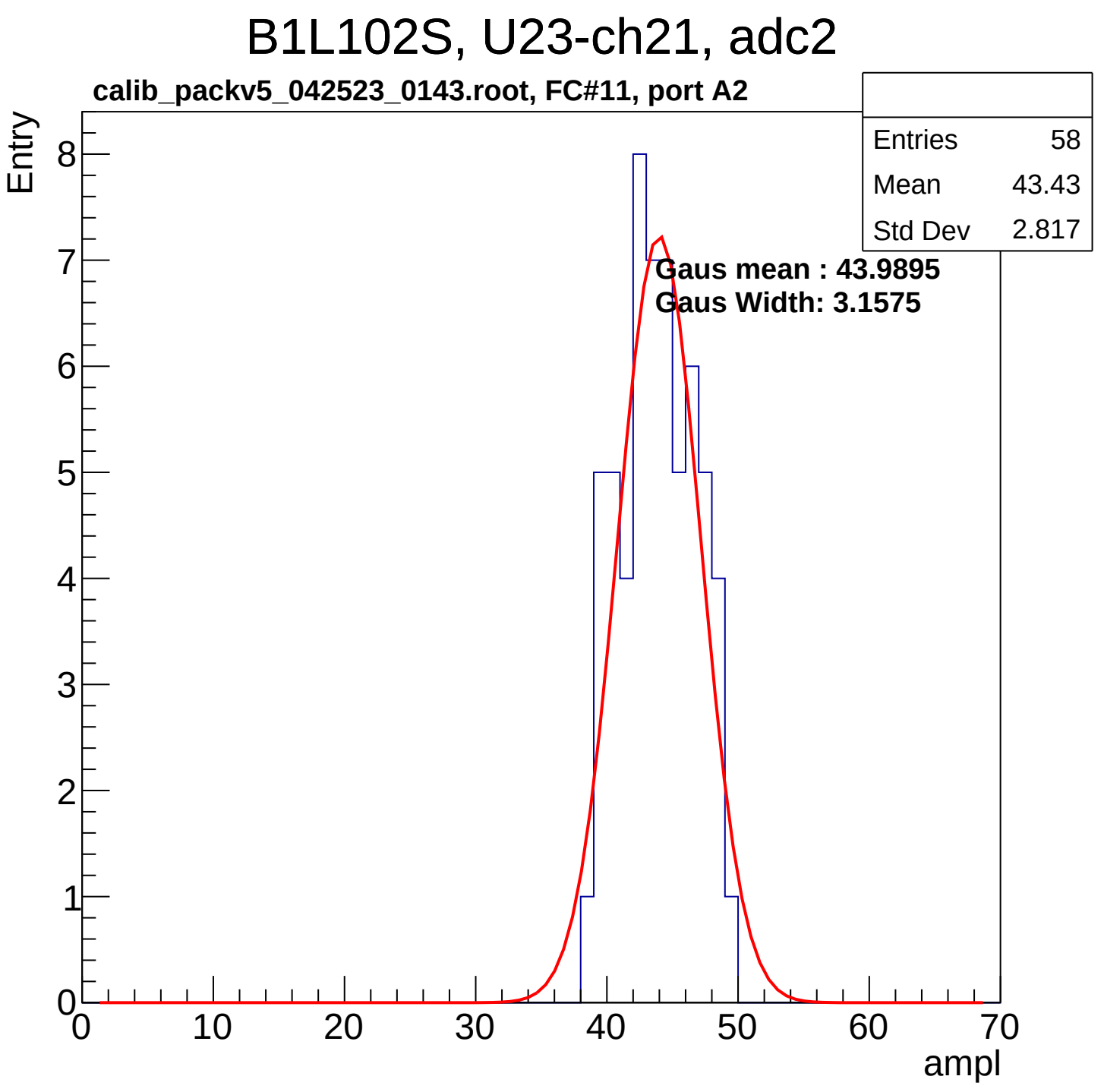

B1L102S, U23-ch20, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.6
Std Dev 8.92
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

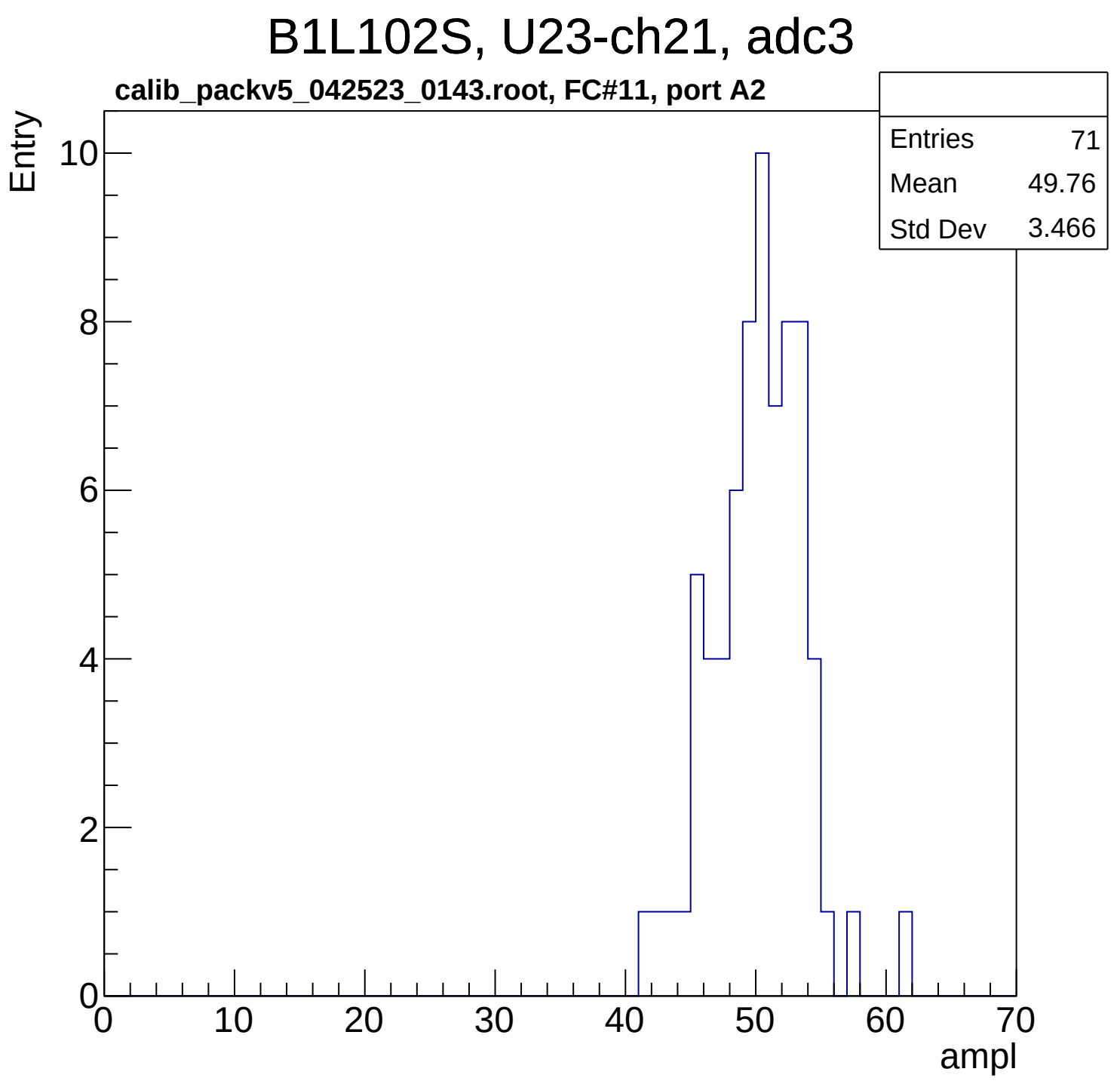

B1L102S, U23-ch20, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.5
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

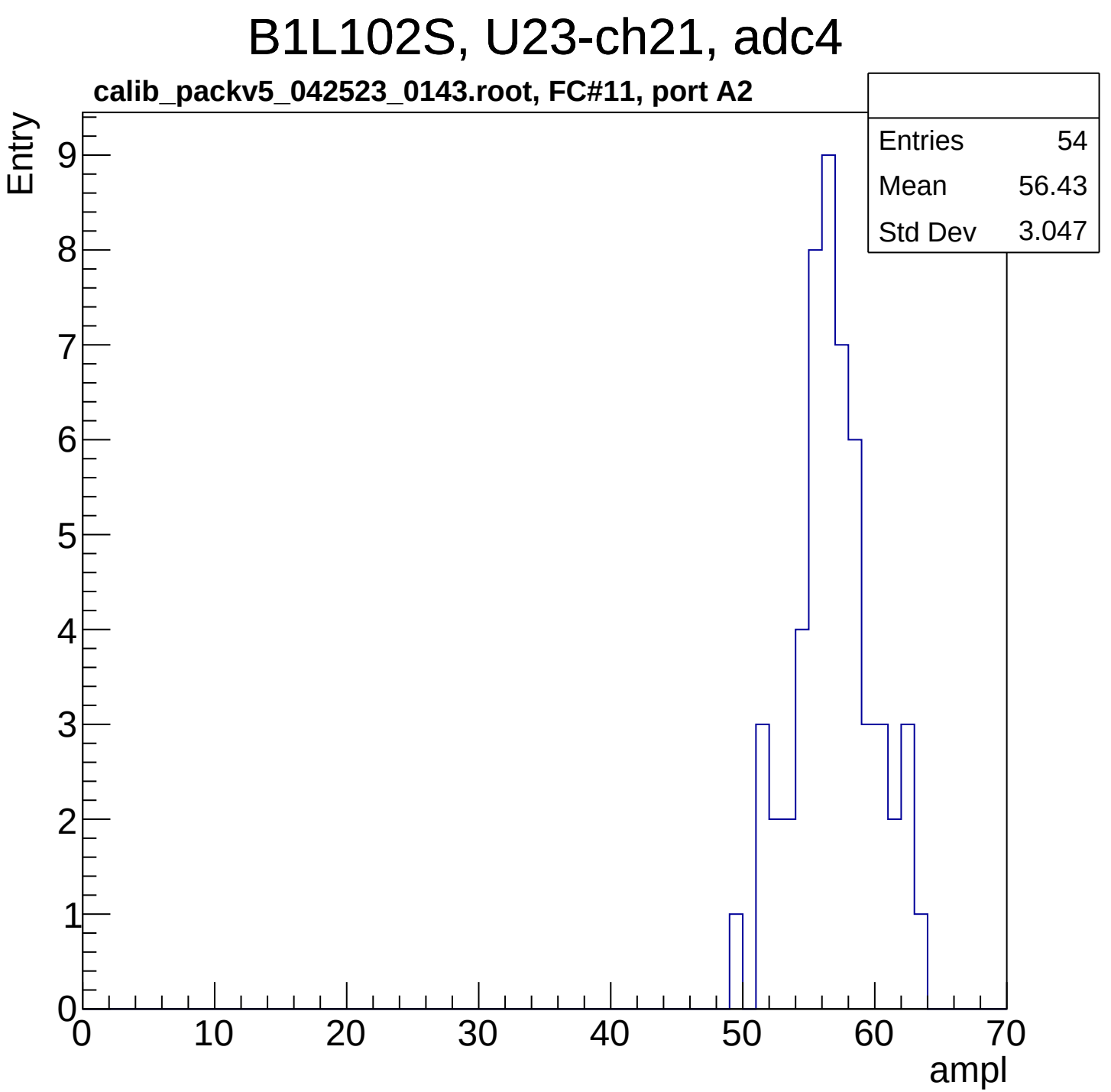

B1L102S, U23-ch20, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

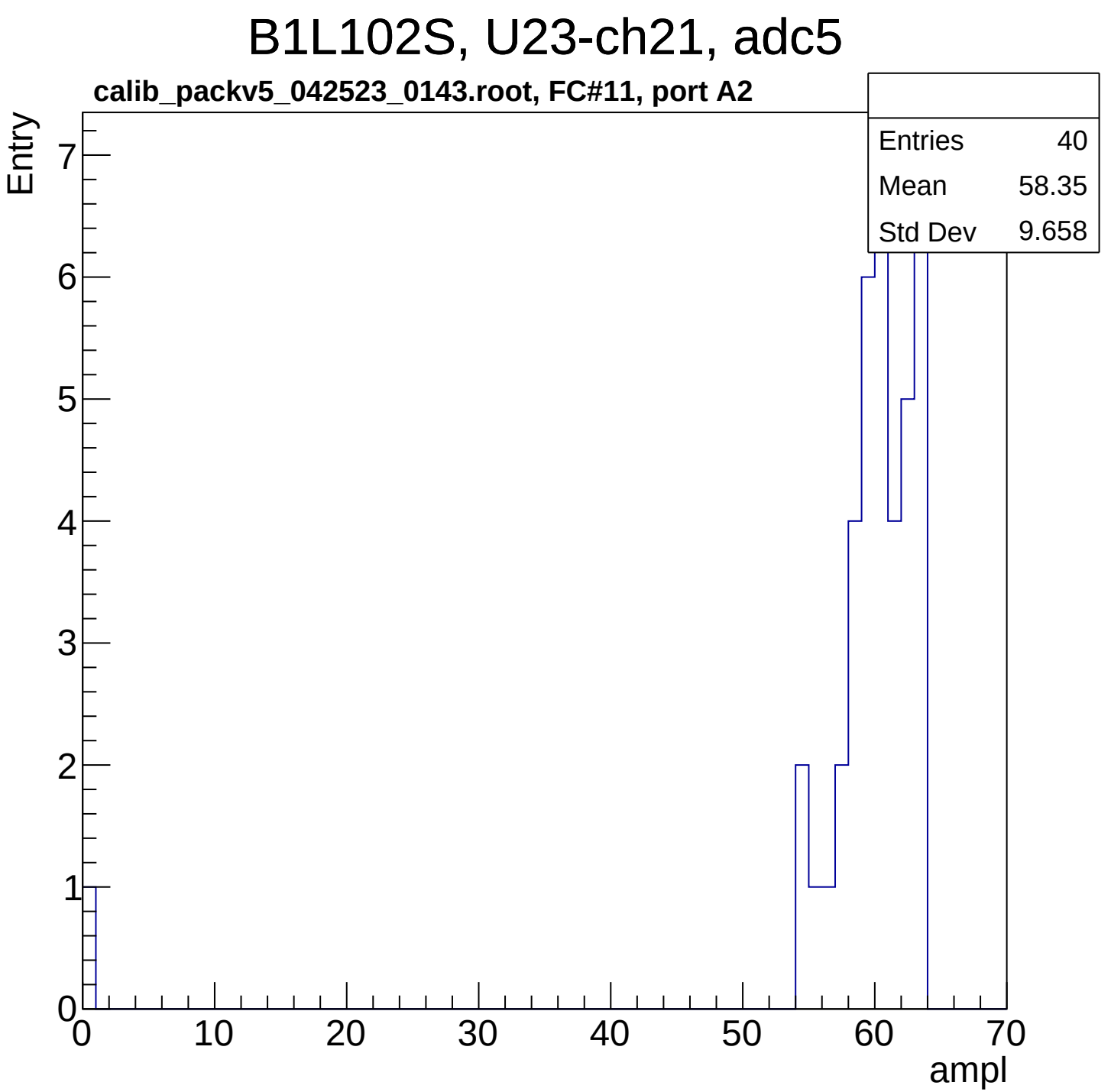

B1L102S, U23-ch21, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.84
Std Dev 4.759
Gaus mean : 29.4400
Gaus Width: 3.2257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

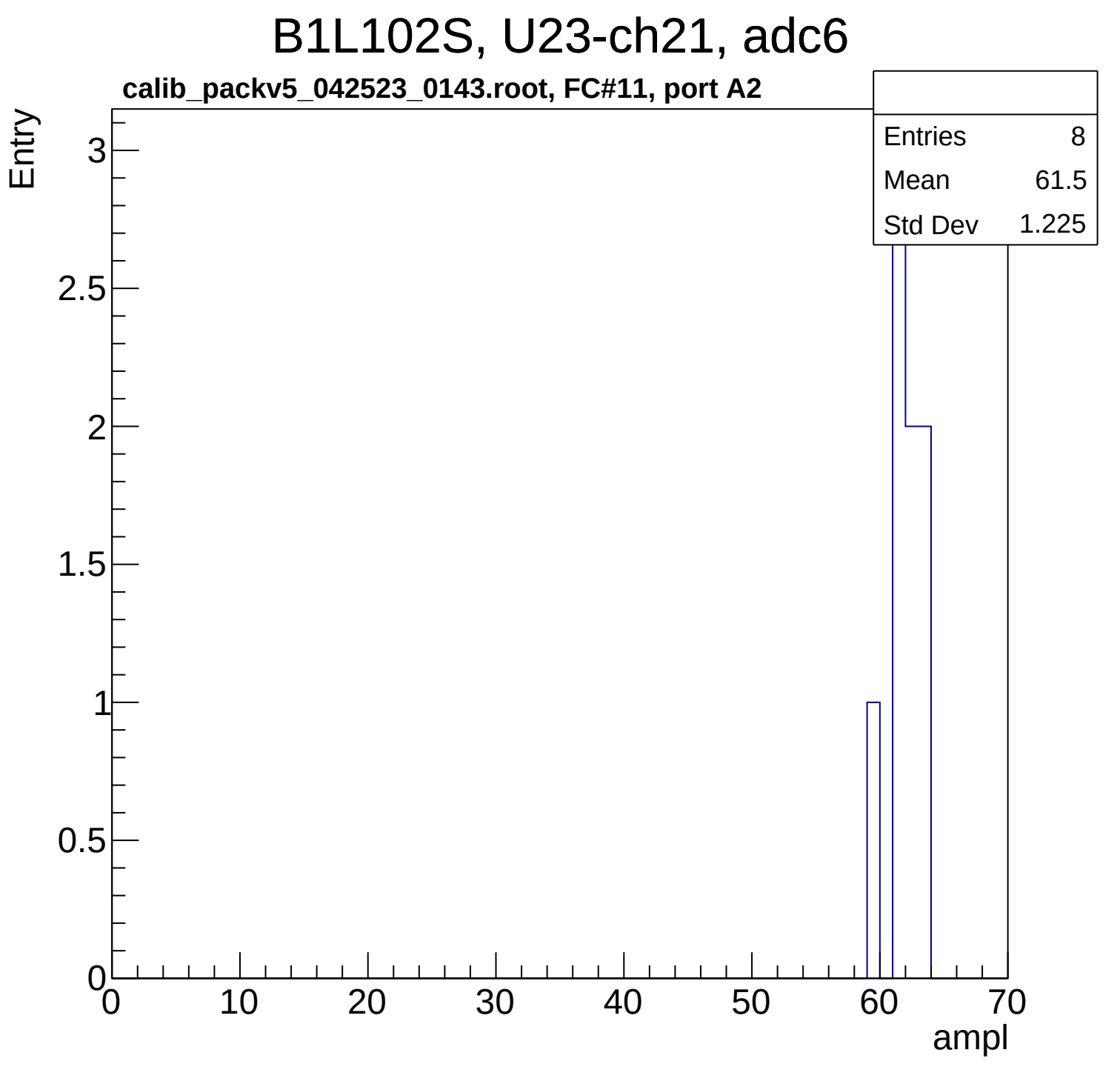

B1L102S, U23-ch21, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 36.89
Std Dev 3.321
Gaus mean : 36.8481
Gaus Width: 3.3132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

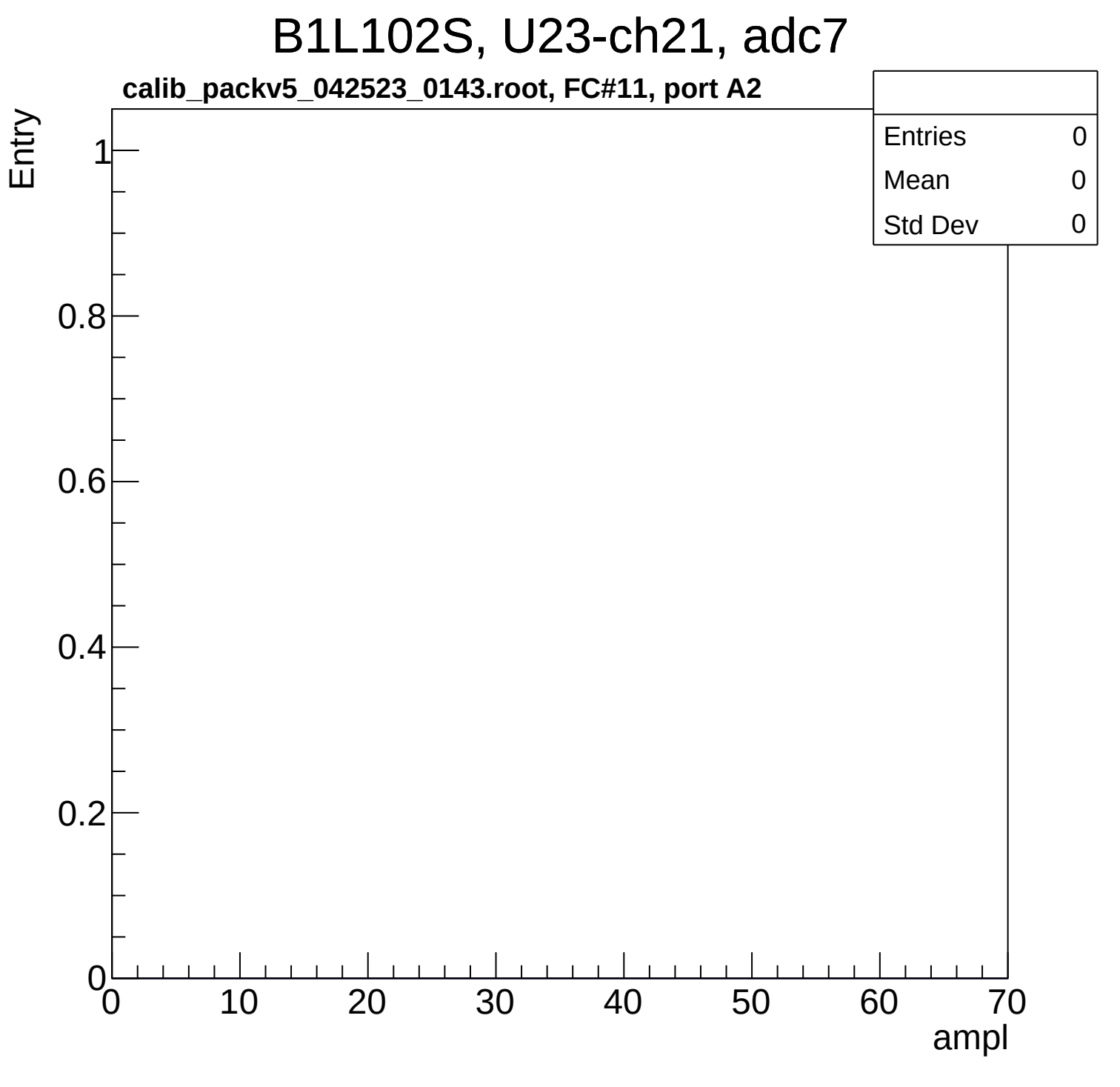

B1L102S, U23-ch21, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 43.43
Std Dev 2.817
Gaus mean : 43.9895
Gaus Width: 3.1575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

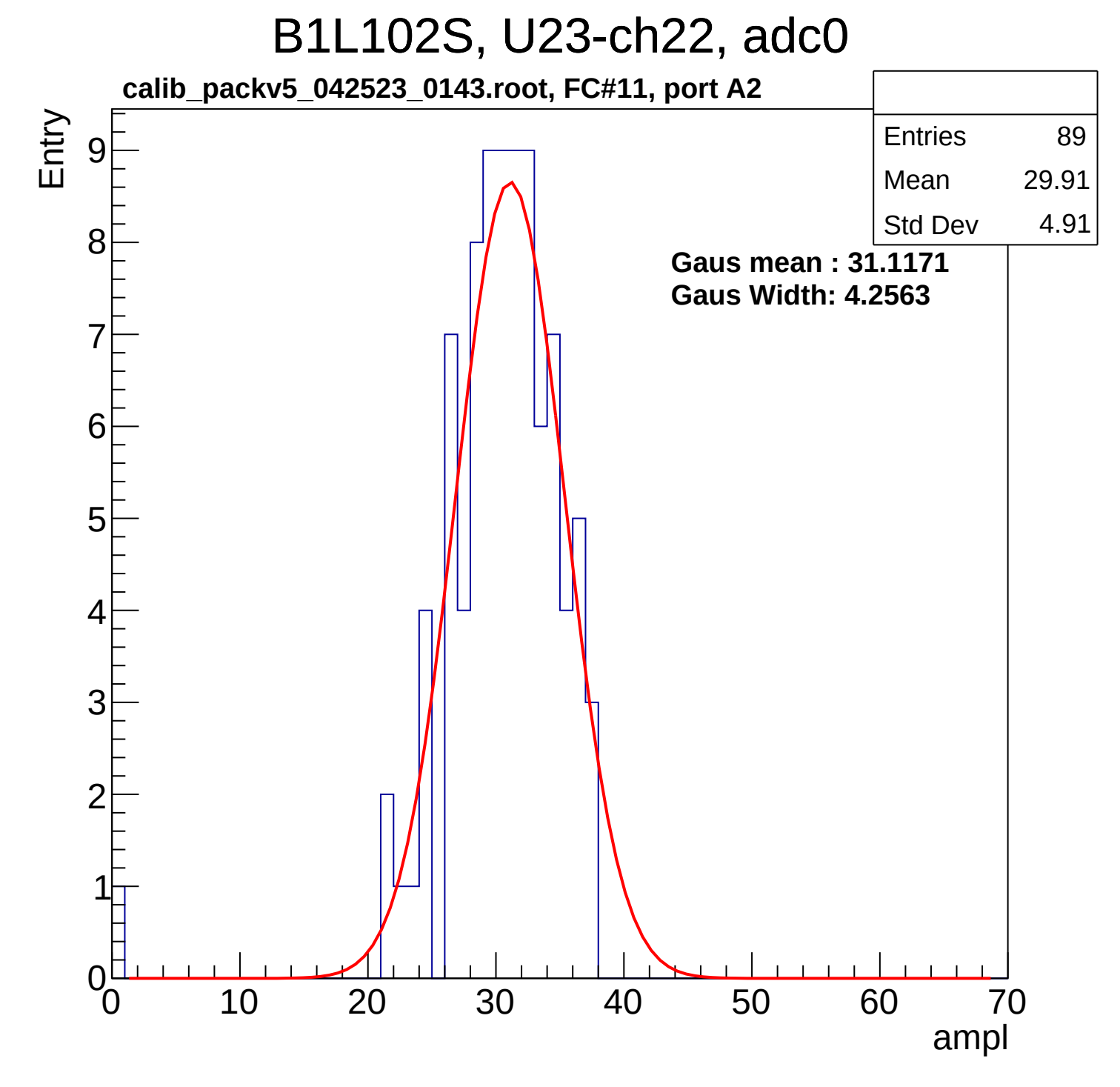

B1L102S, U23-ch21, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 49.76
Std Dev 3.466
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

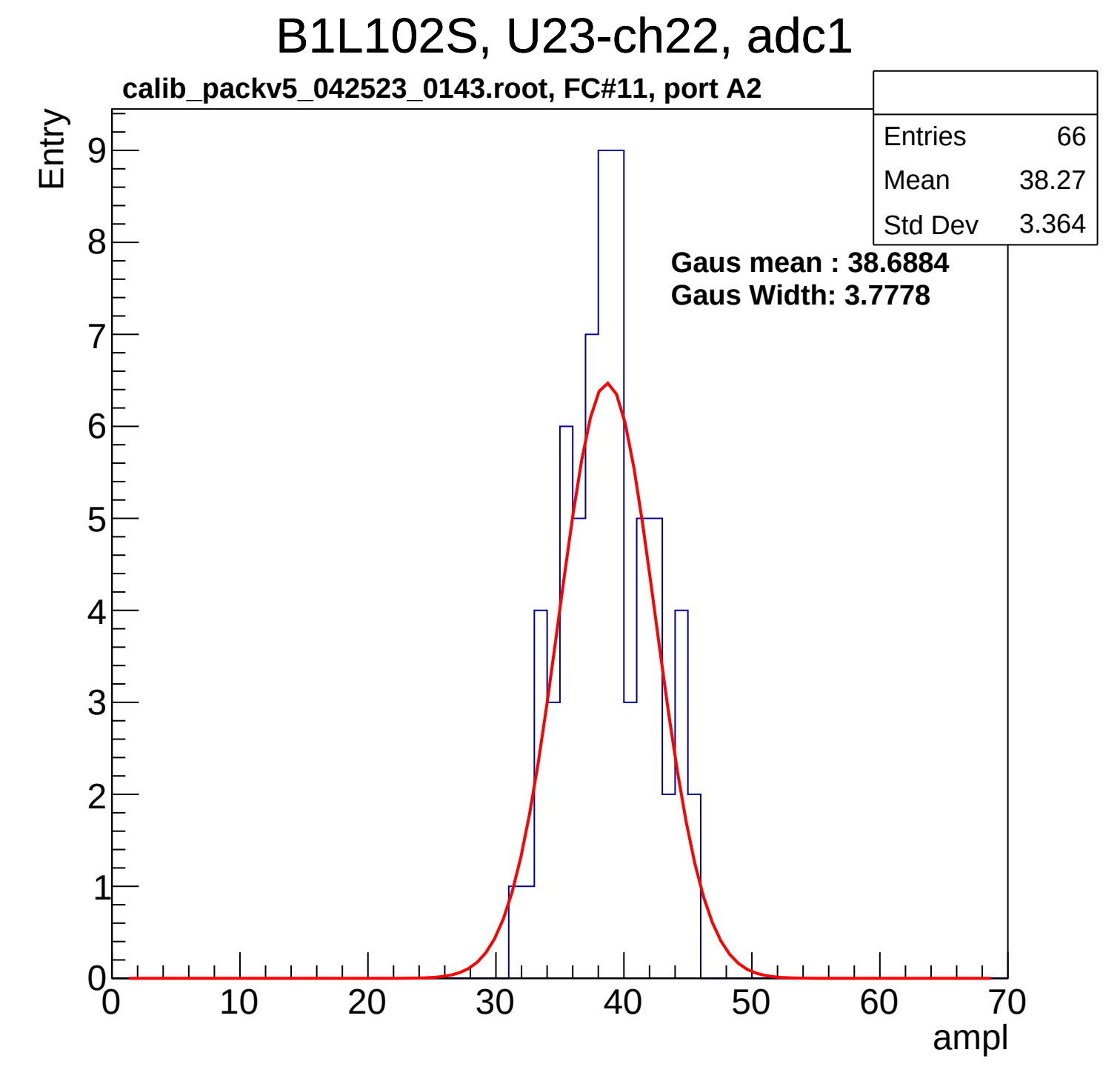

B1L102S, U23-ch21, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 56.43
Std Dev 3.047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

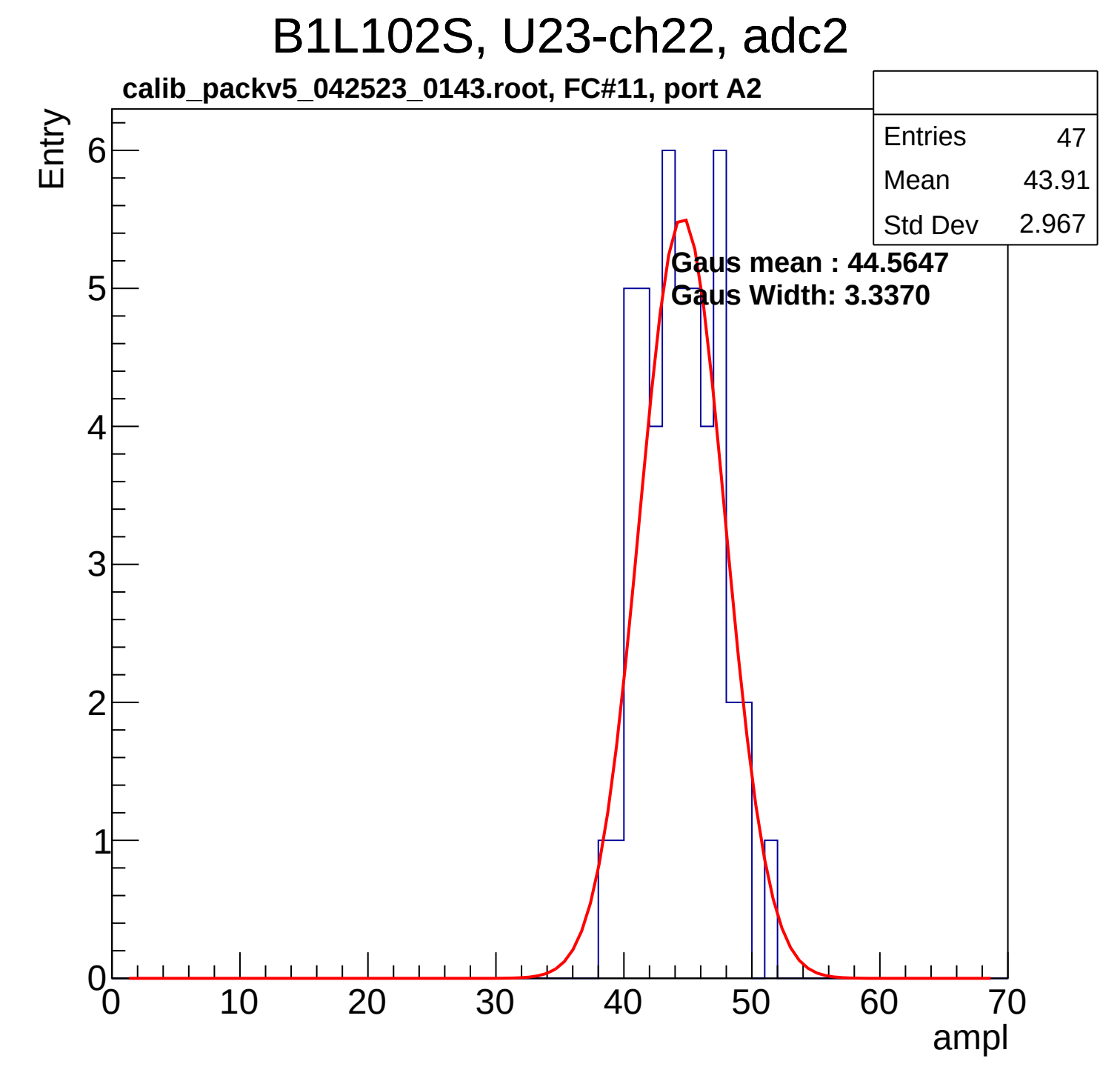

B1L102S, U23-ch21, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.35
Std Dev 9.658
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

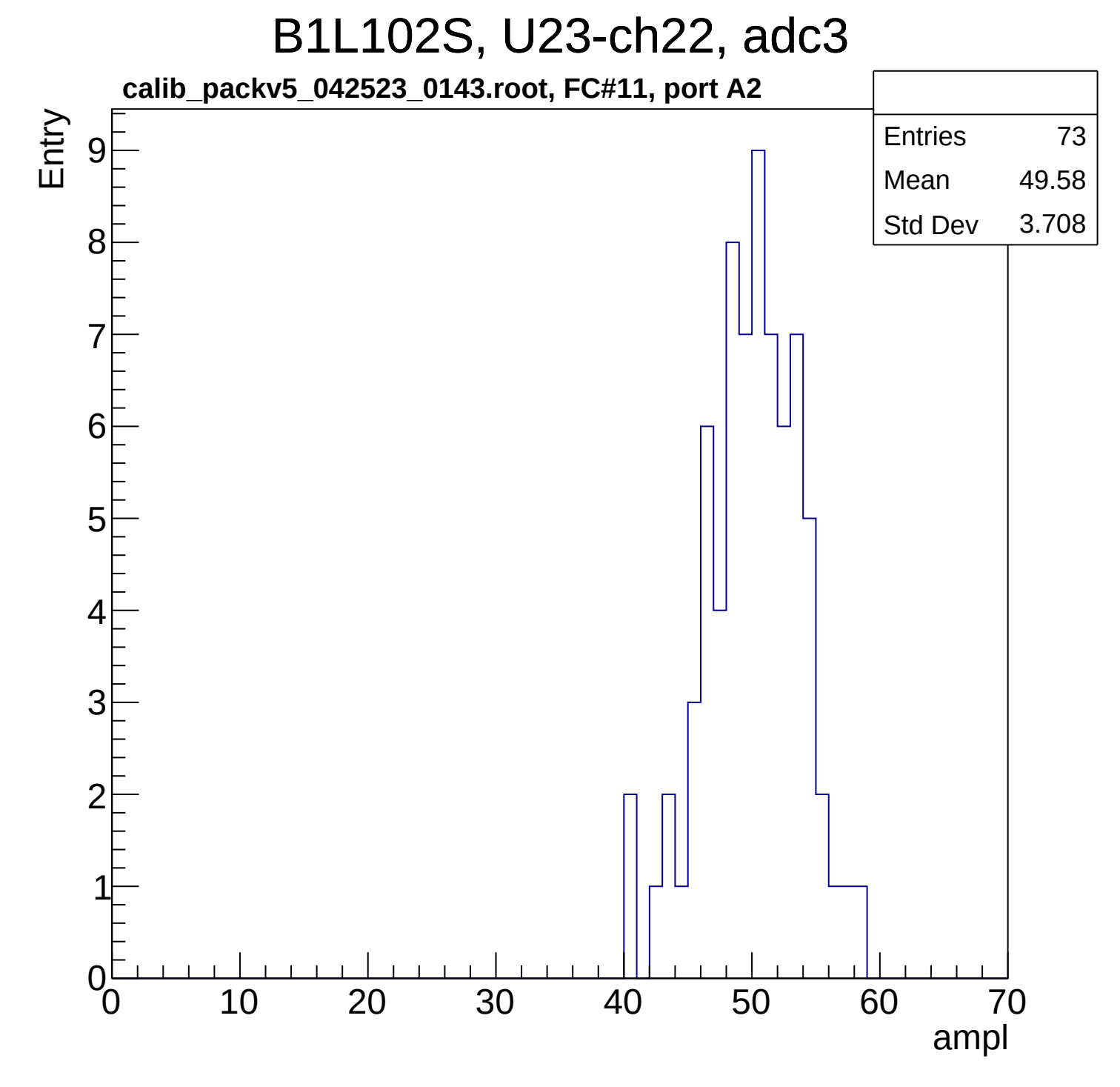

B1L102S, U23-ch21, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.5
Std Dev 1.225
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

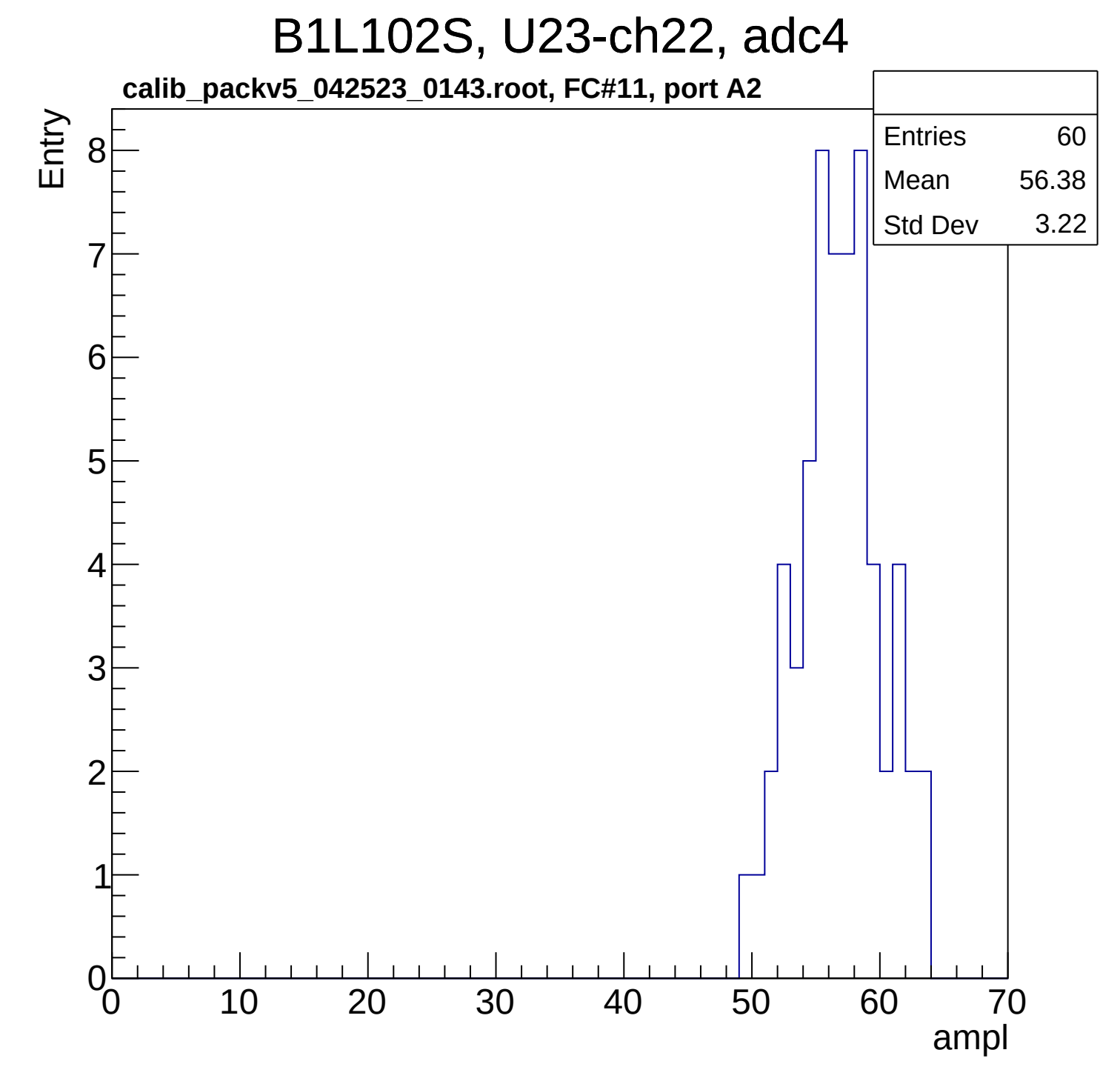

B1L102S, U23-ch21, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

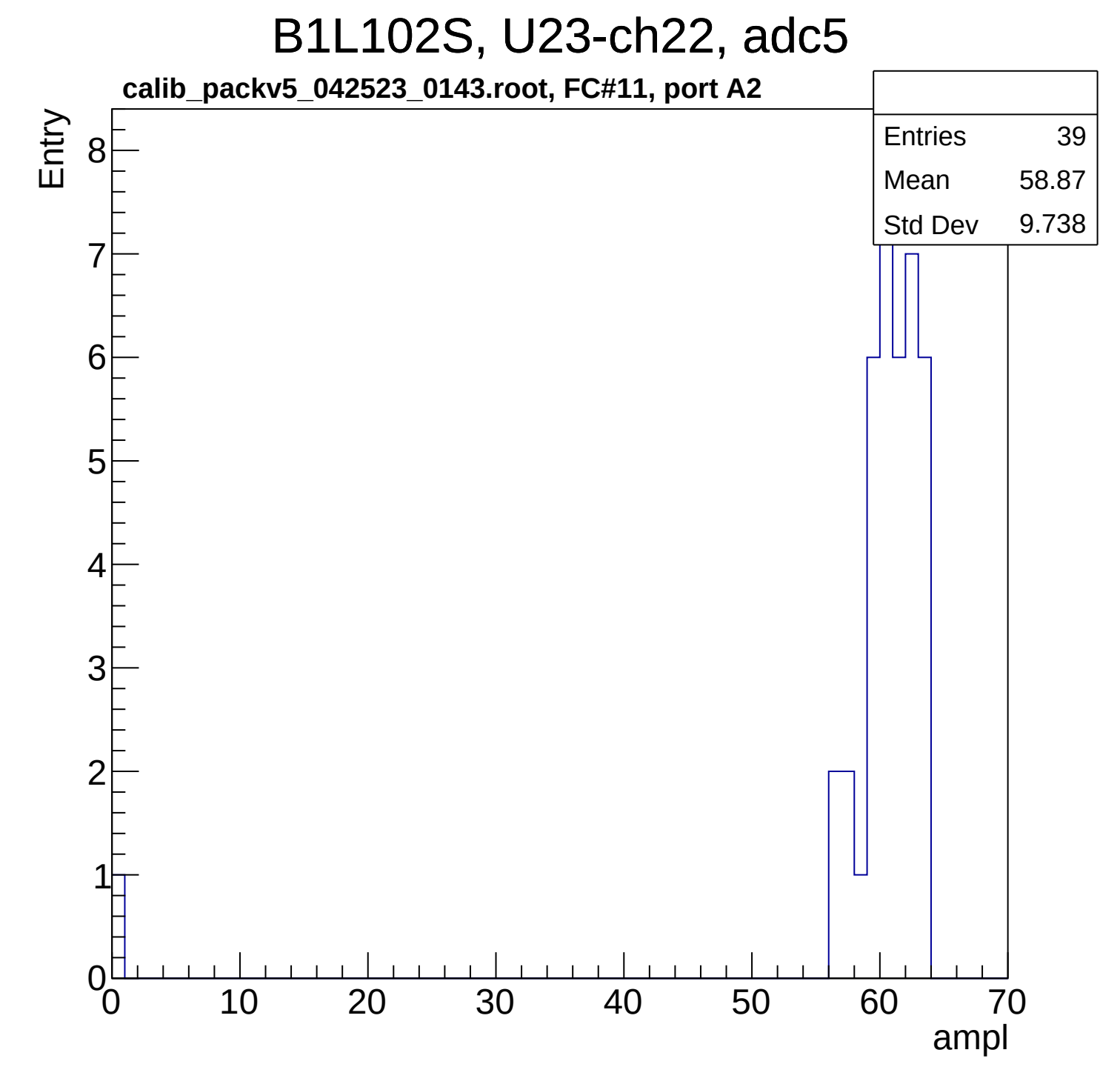

B1L102S, U23-ch22, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 29.91
Std Dev 4.91
Gaus mean : 31.1171
Gaus Width: 4.2563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

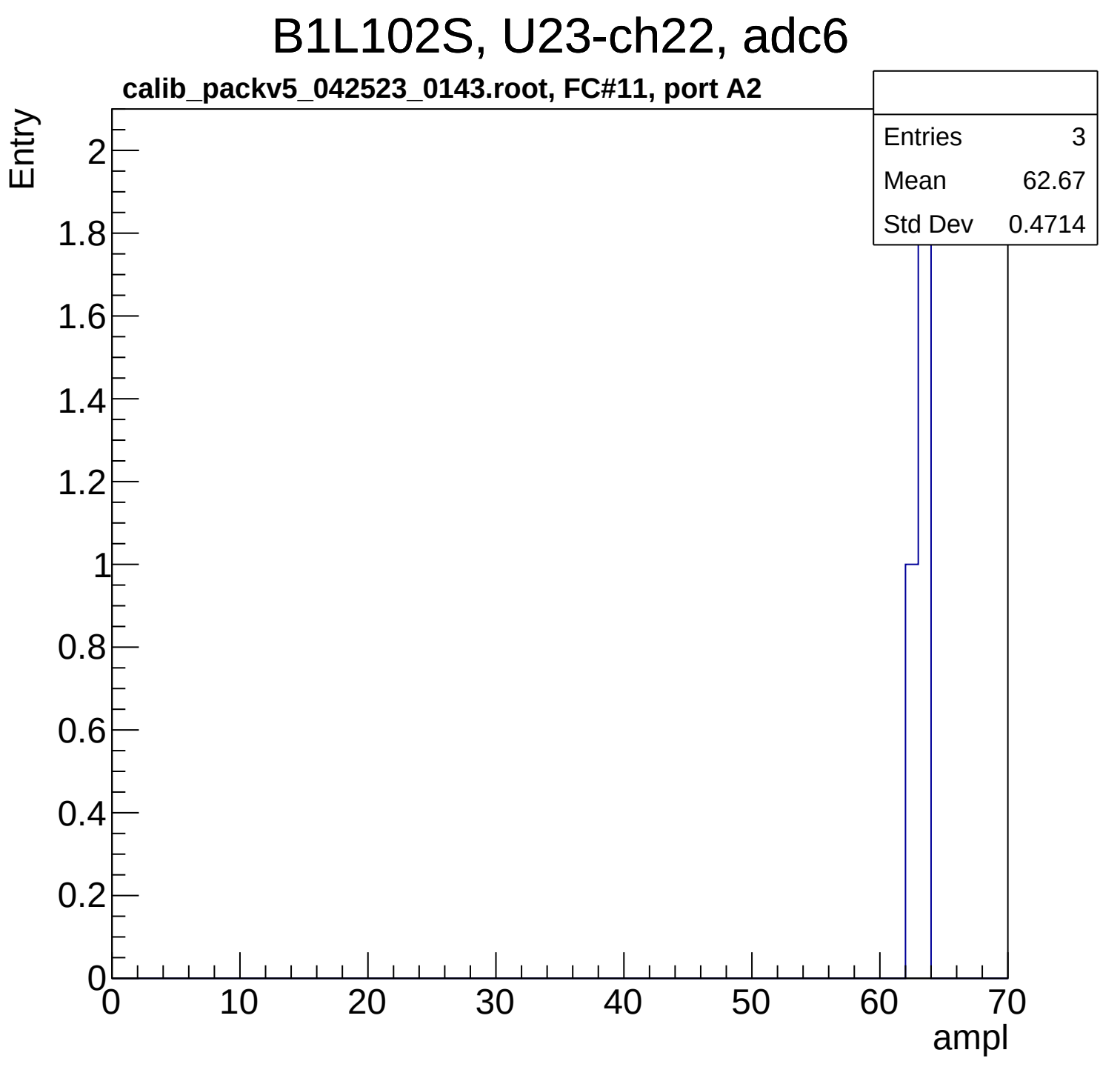

B1L102S, U23-ch22, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 38.27
Std Dev 3.364
Gaus mean : 38.6884
Gaus Width: 3.7778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

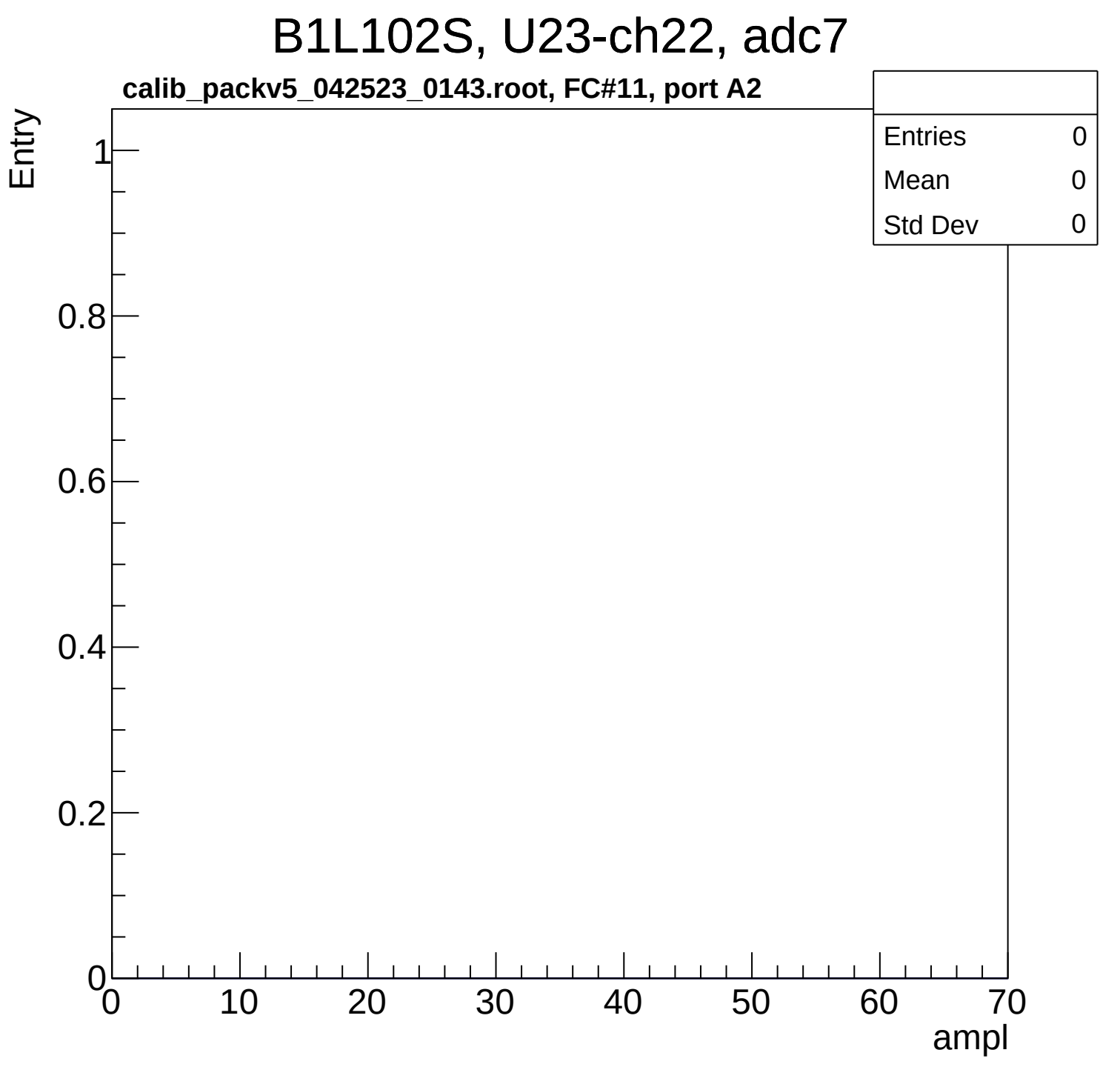

B1L102S, U23-ch22, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 43.91
Std Dev 2.967
Gaus mean : 44.5647
Gaus Width: 3.3370
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

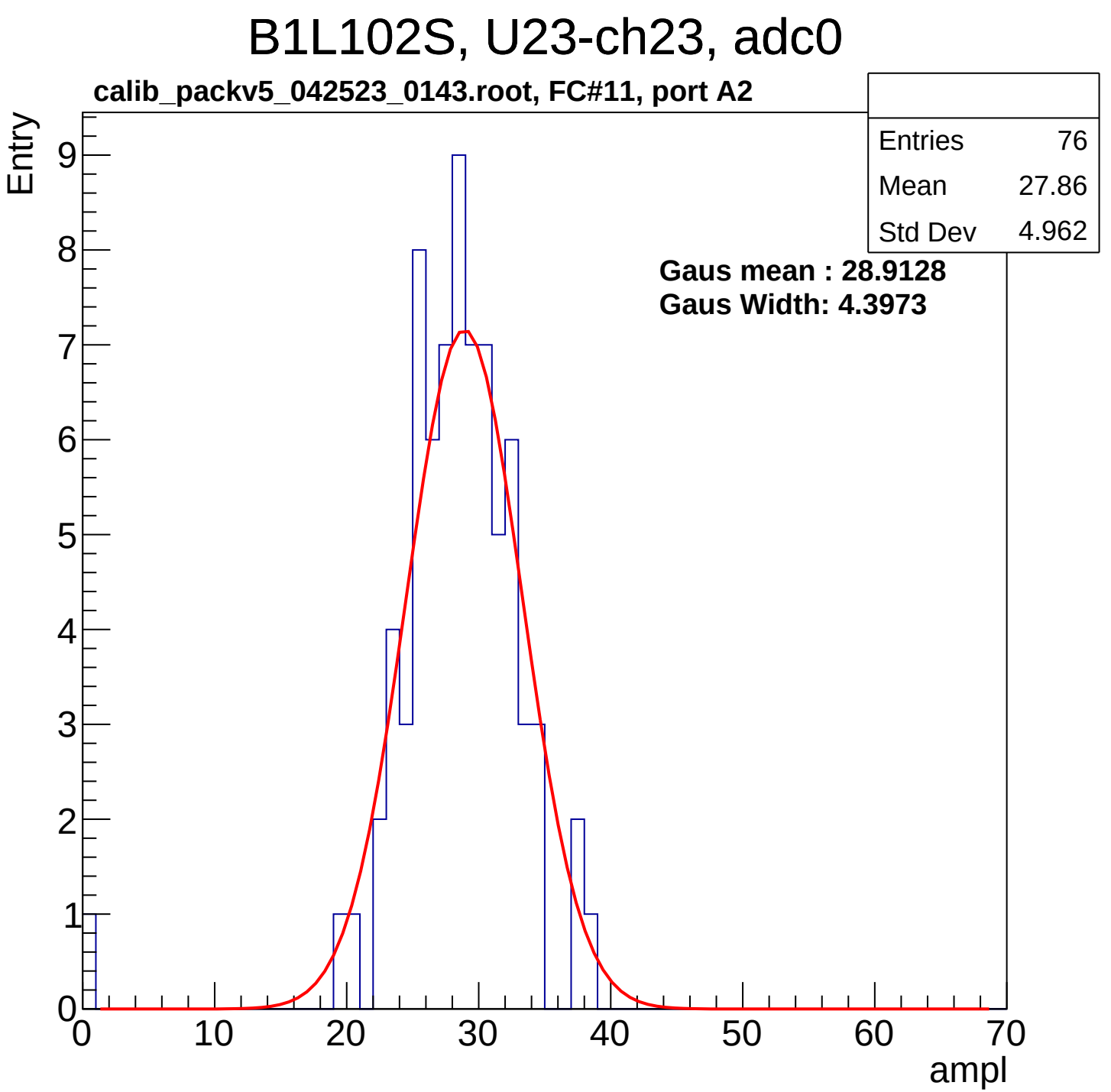

B1L102S, U23-ch22, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 49.58
Std Dev 3.708
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

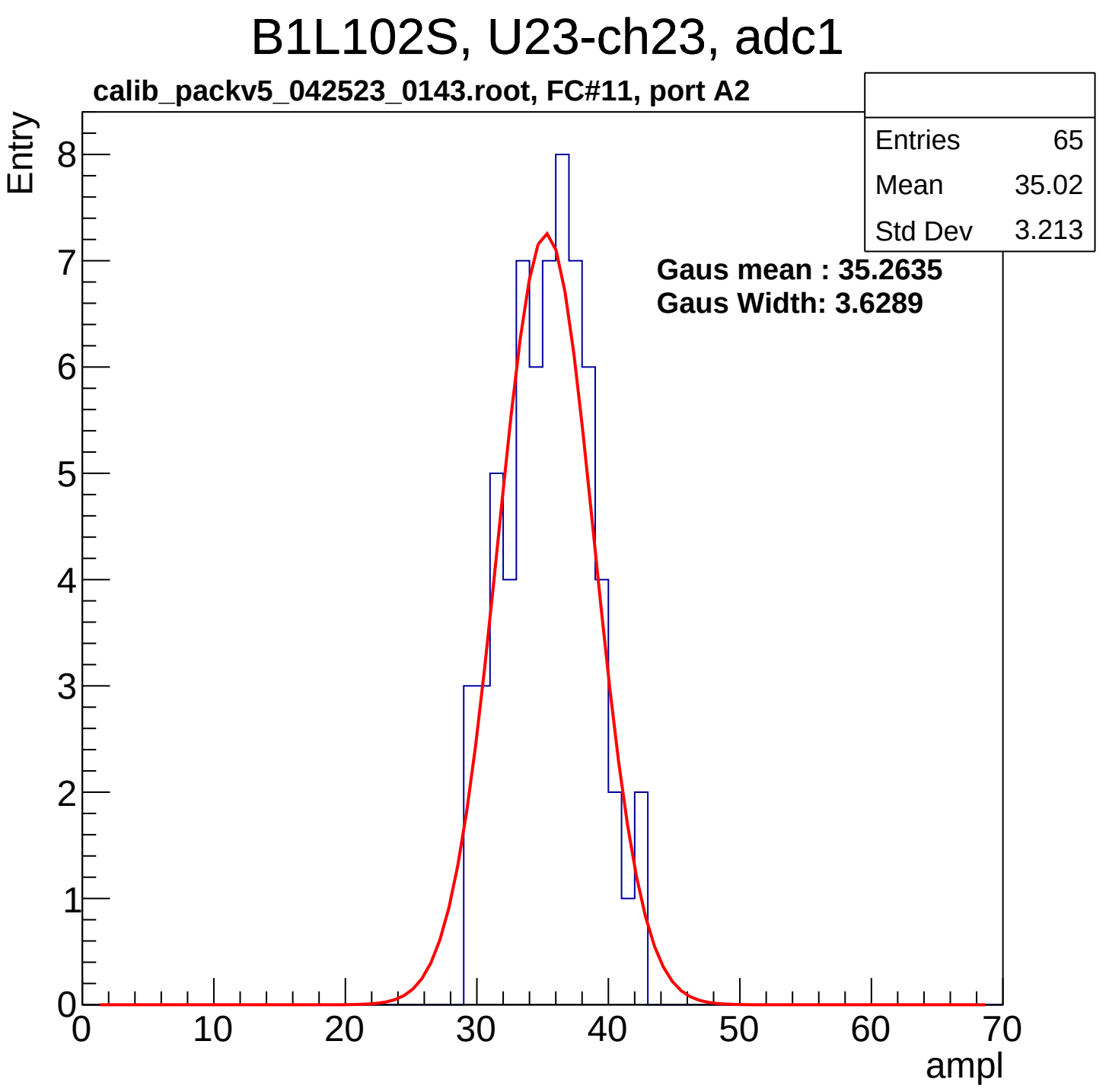

B1L102S, U23-ch22, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 56.38
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

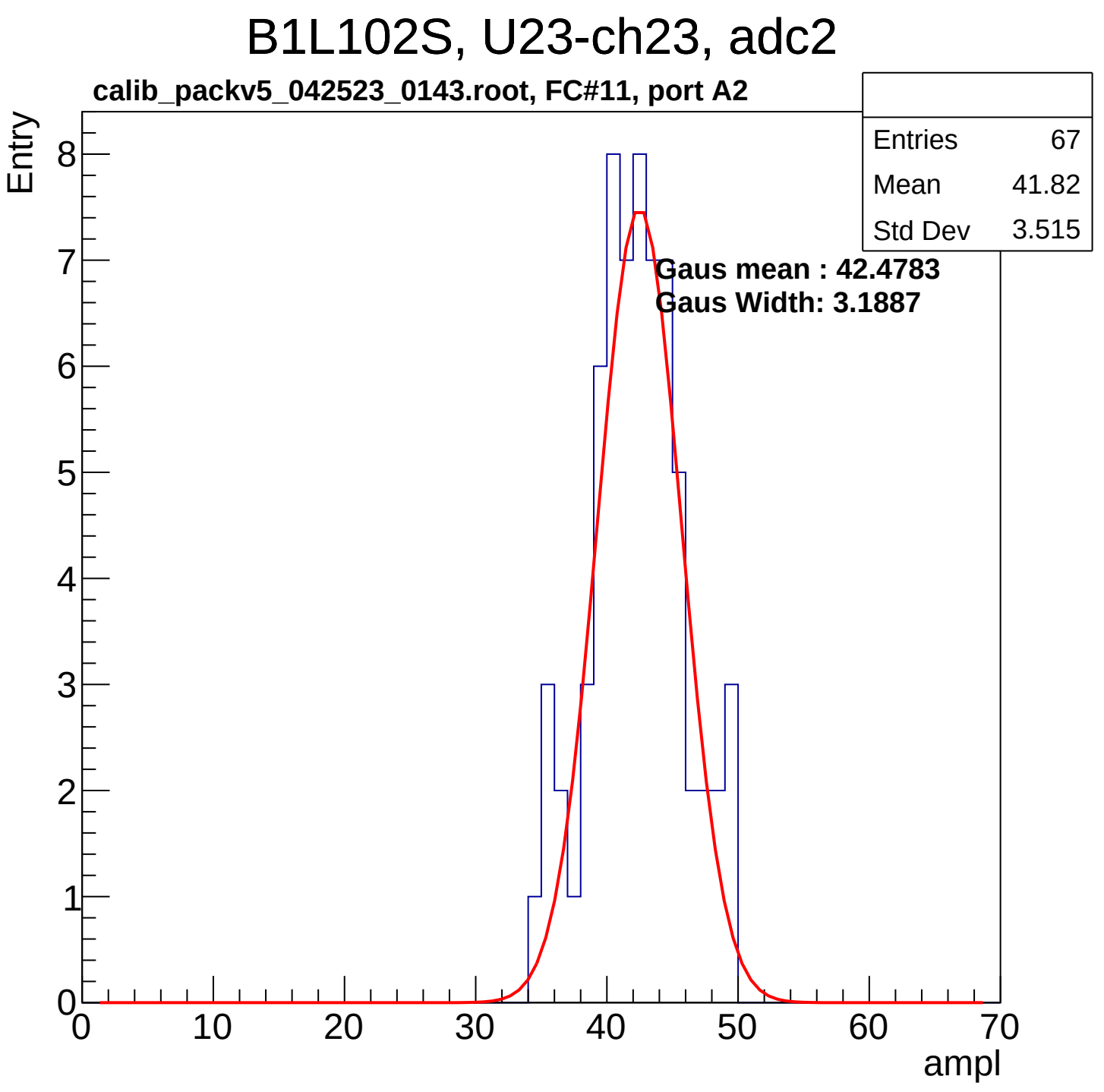

B1L102S, U23-ch22, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 58.87
Std Dev 9.738
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

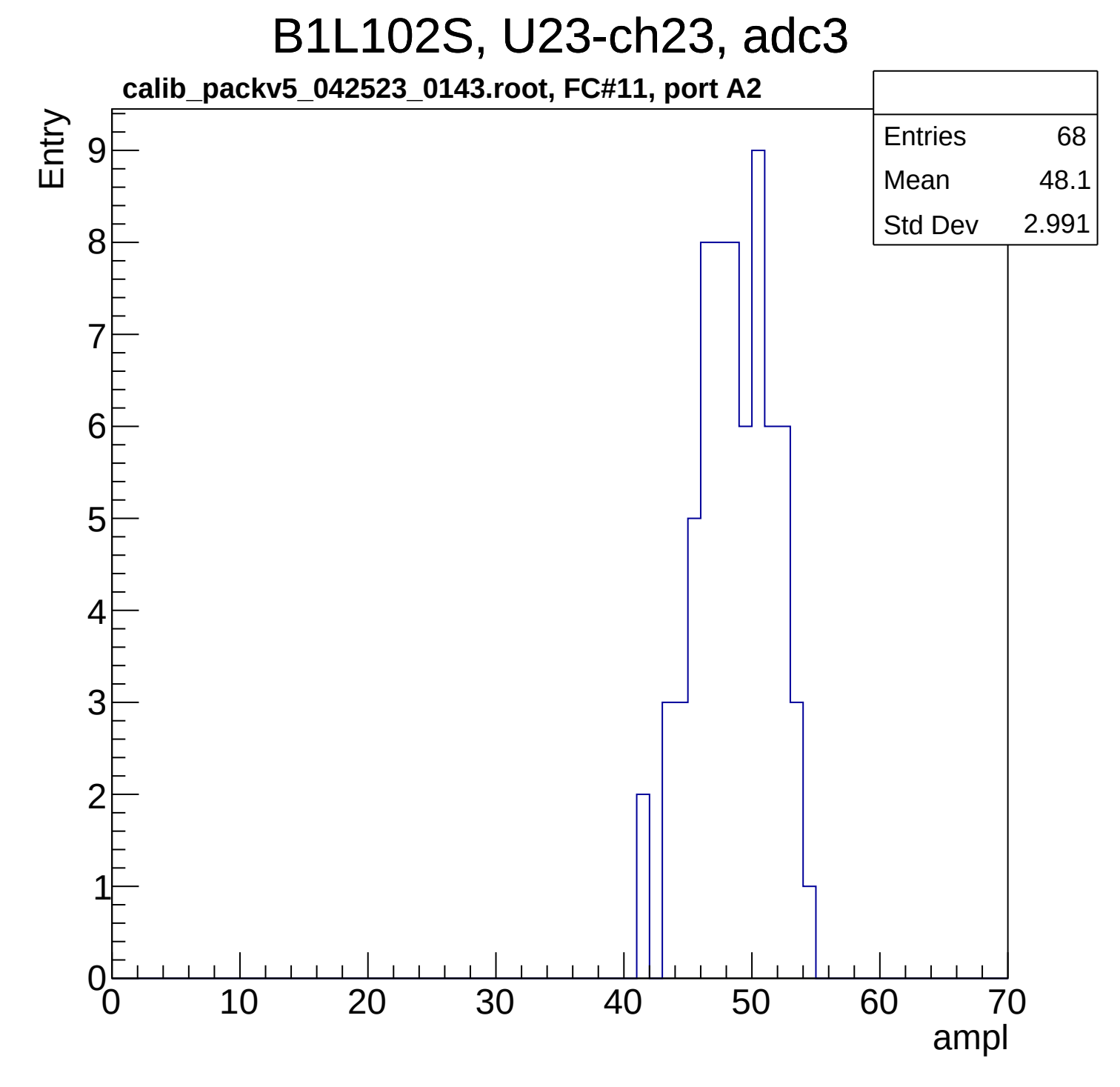

B1L102S, U23-ch22, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

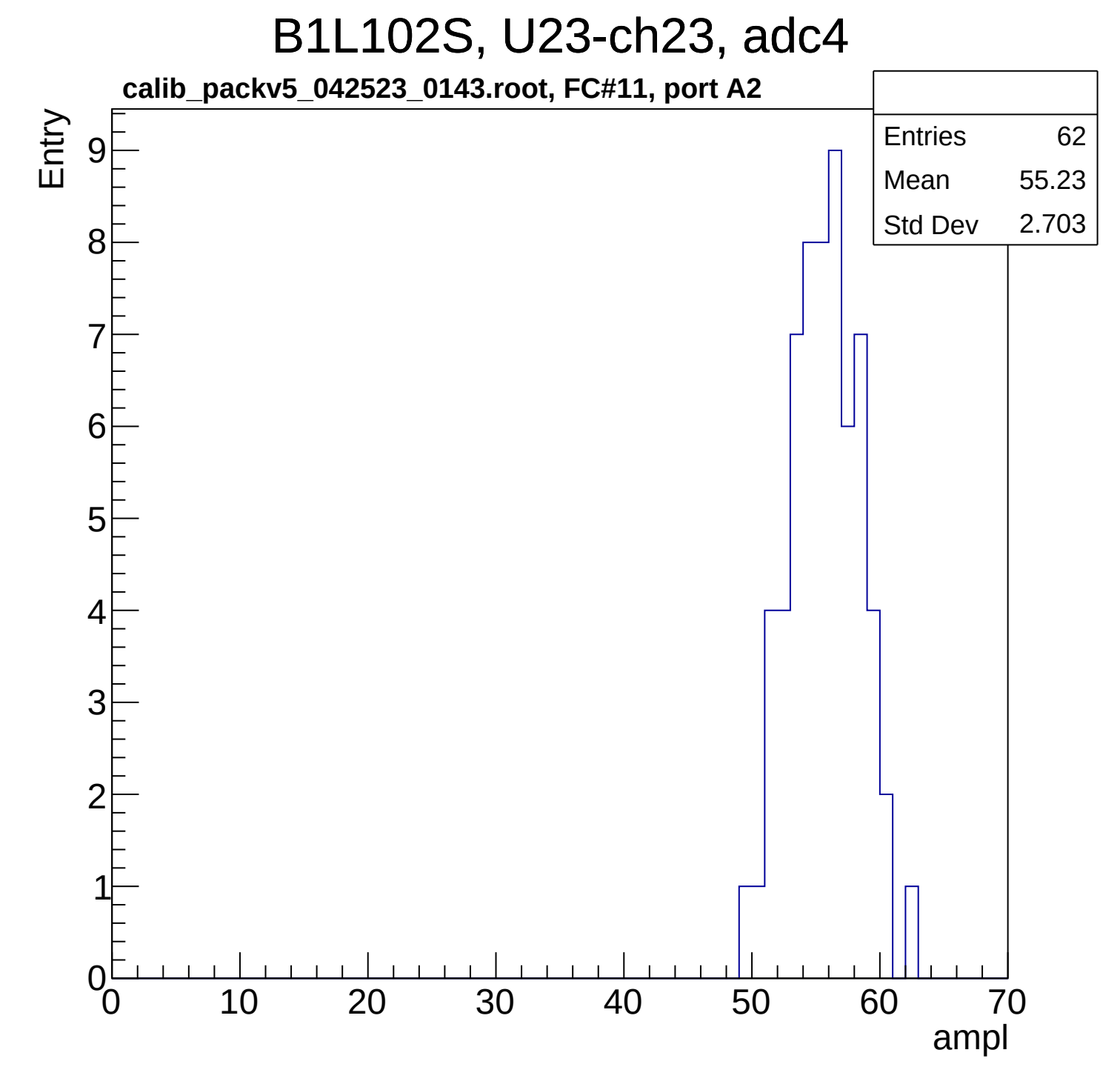

B1L102S, U23-ch22, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

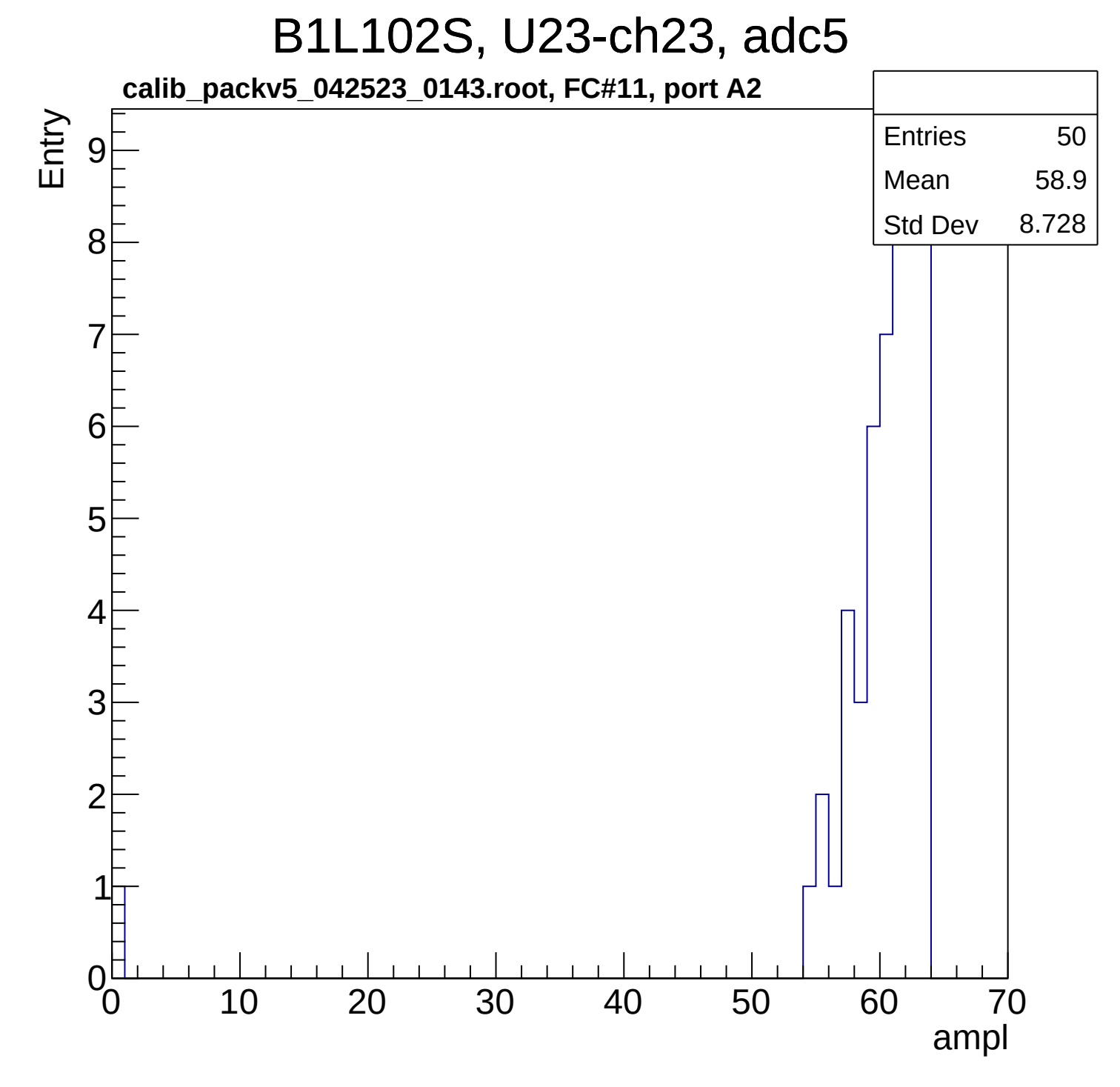

B1L102S, U23-ch23, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 27.86
Std Dev 4.962
Gaus mean : 28.9128
Gaus Width: 4.3973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

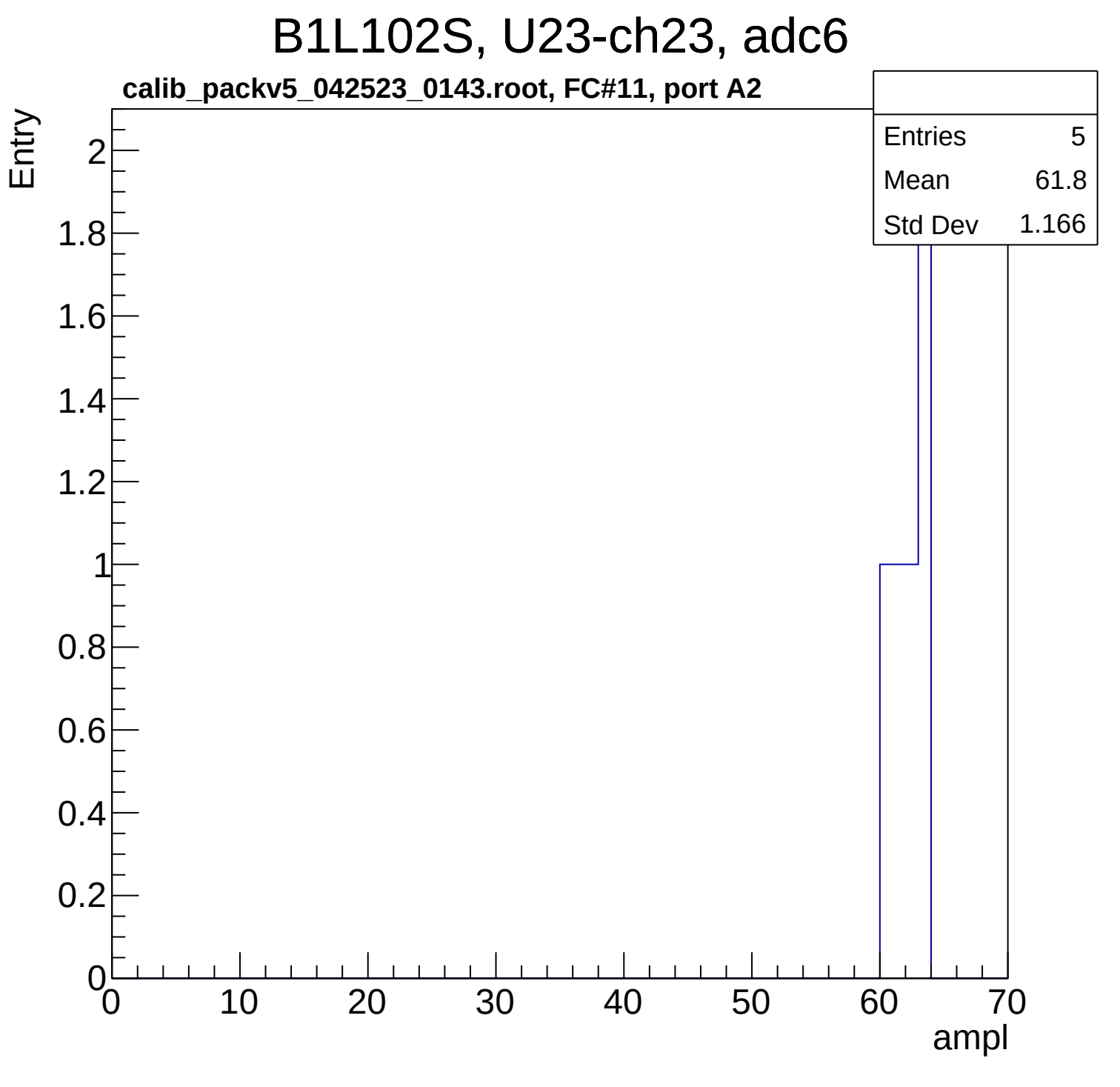

B1L102S, U23-ch23, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.02
Std Dev 3.213
Gaus mean : 35.2635
Gaus Width: 3.6289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

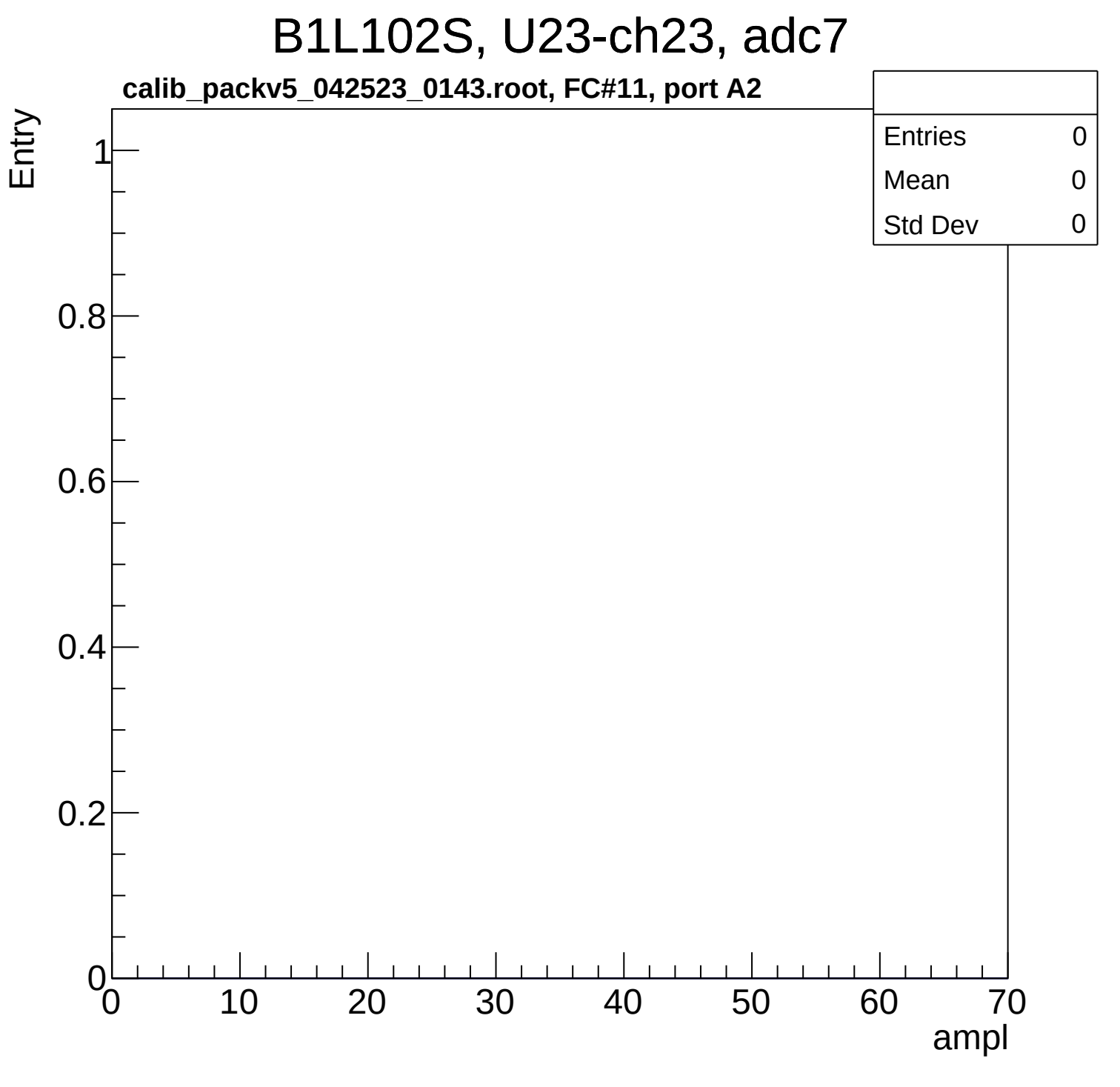

B1L102S, U23-ch23, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 41.82
Std Dev 3.515
Gaus mean : 42.4783
Gaus Width: 3.1887
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

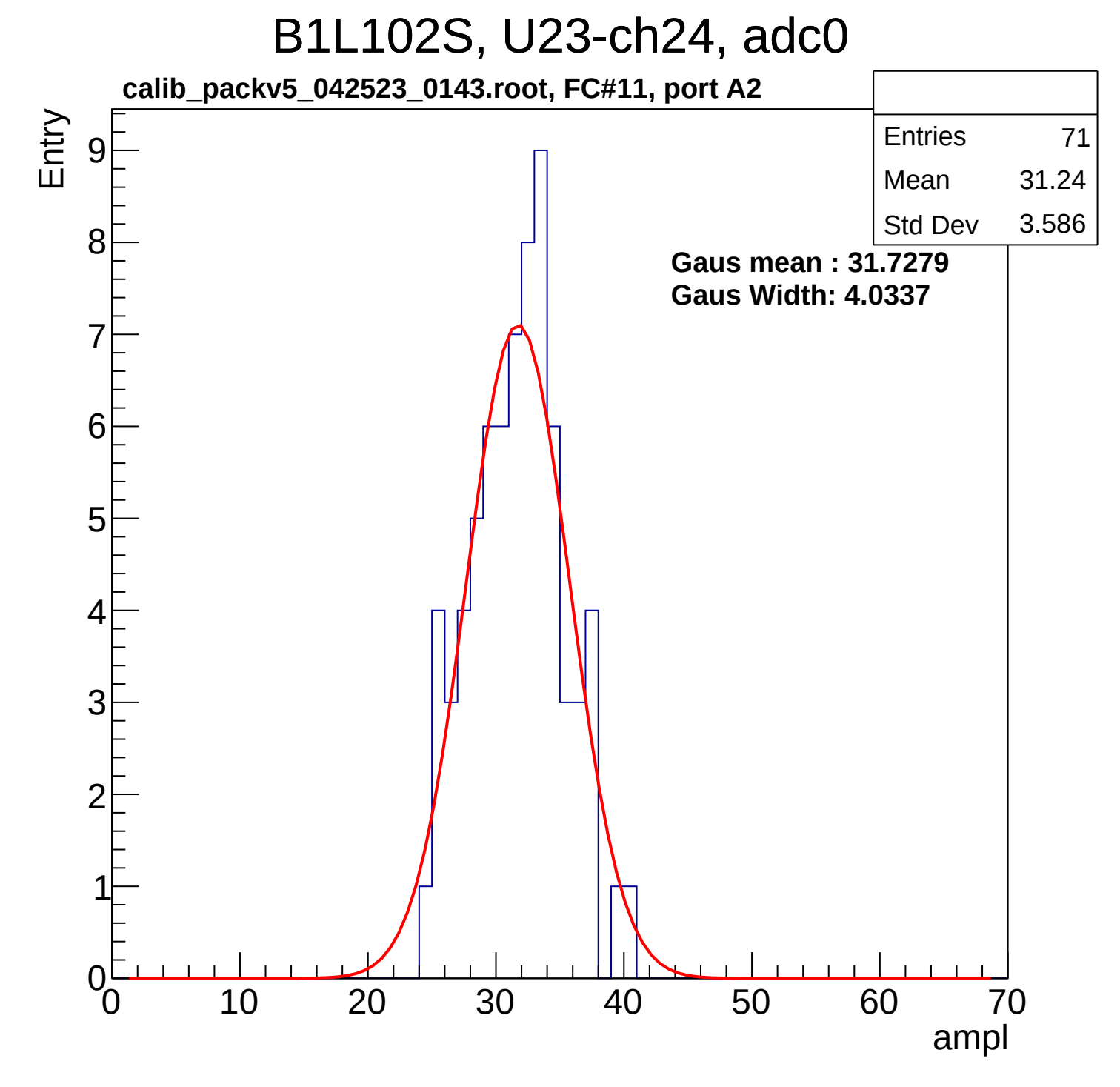

B1L102S, U23-ch23, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 48.1
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

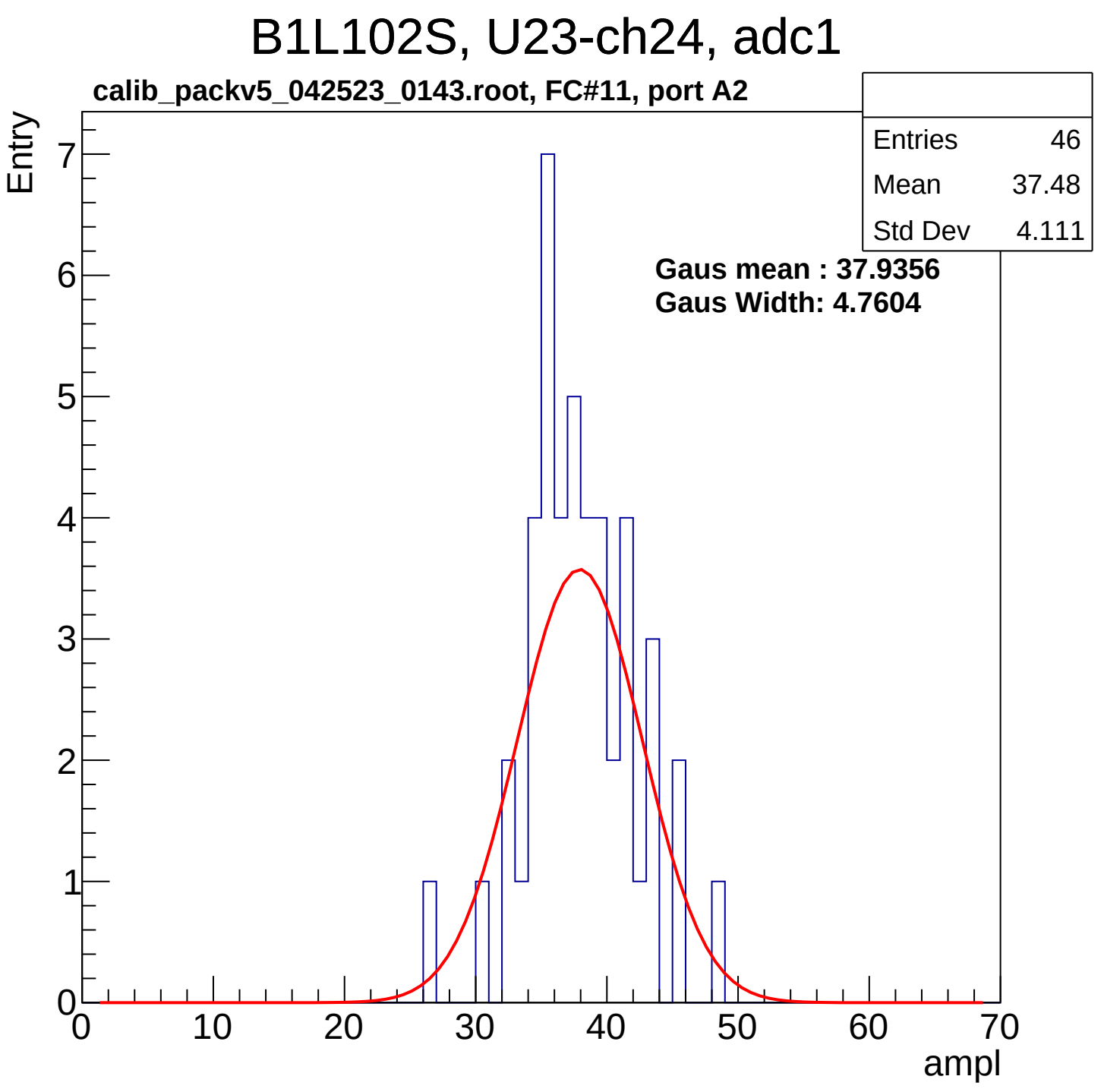

B1L102S, U23-ch23, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 55.23
Std Dev 2.703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

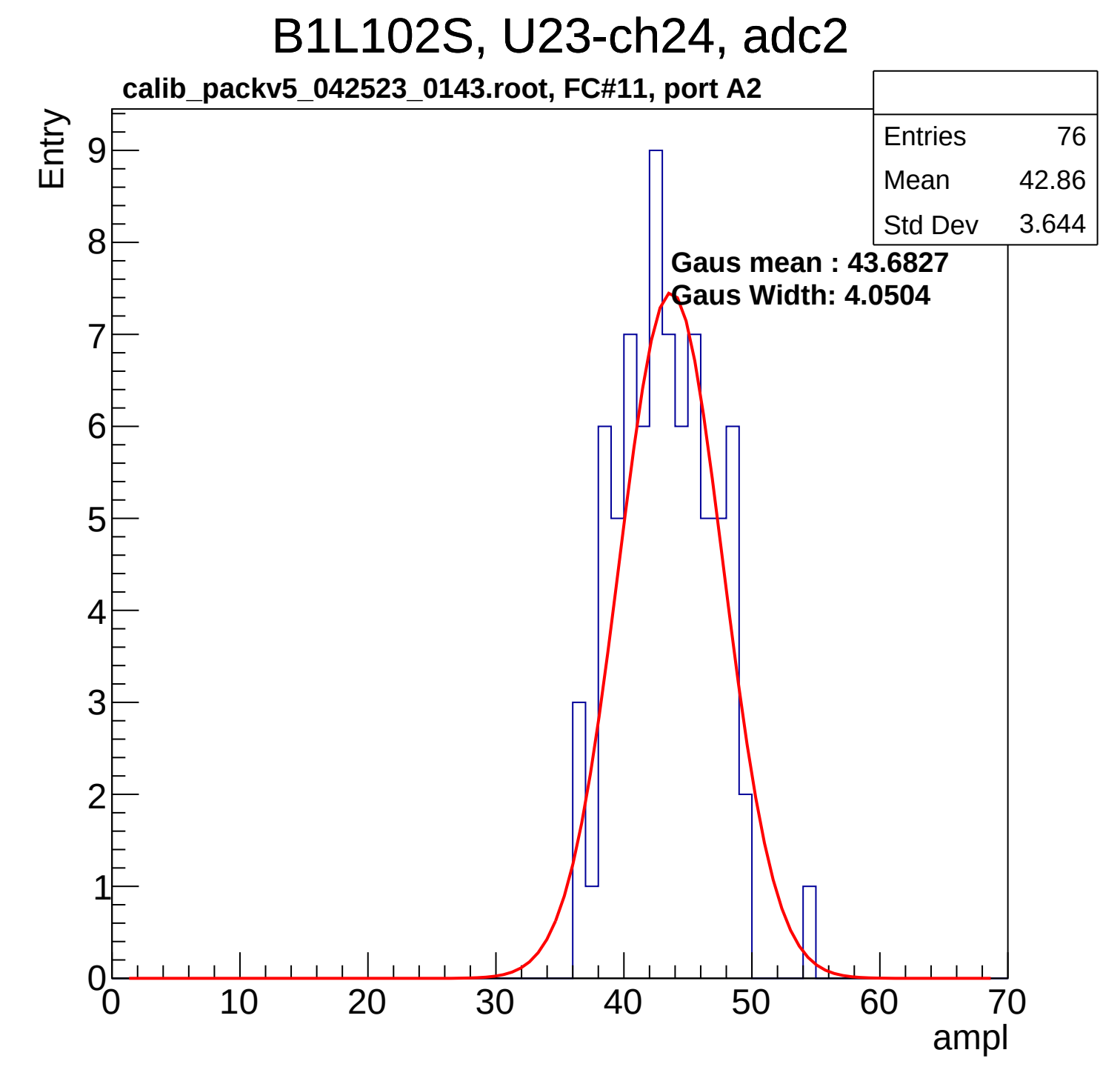

B1L102S, U23-ch23, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 58.9
Std Dev 8.728
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

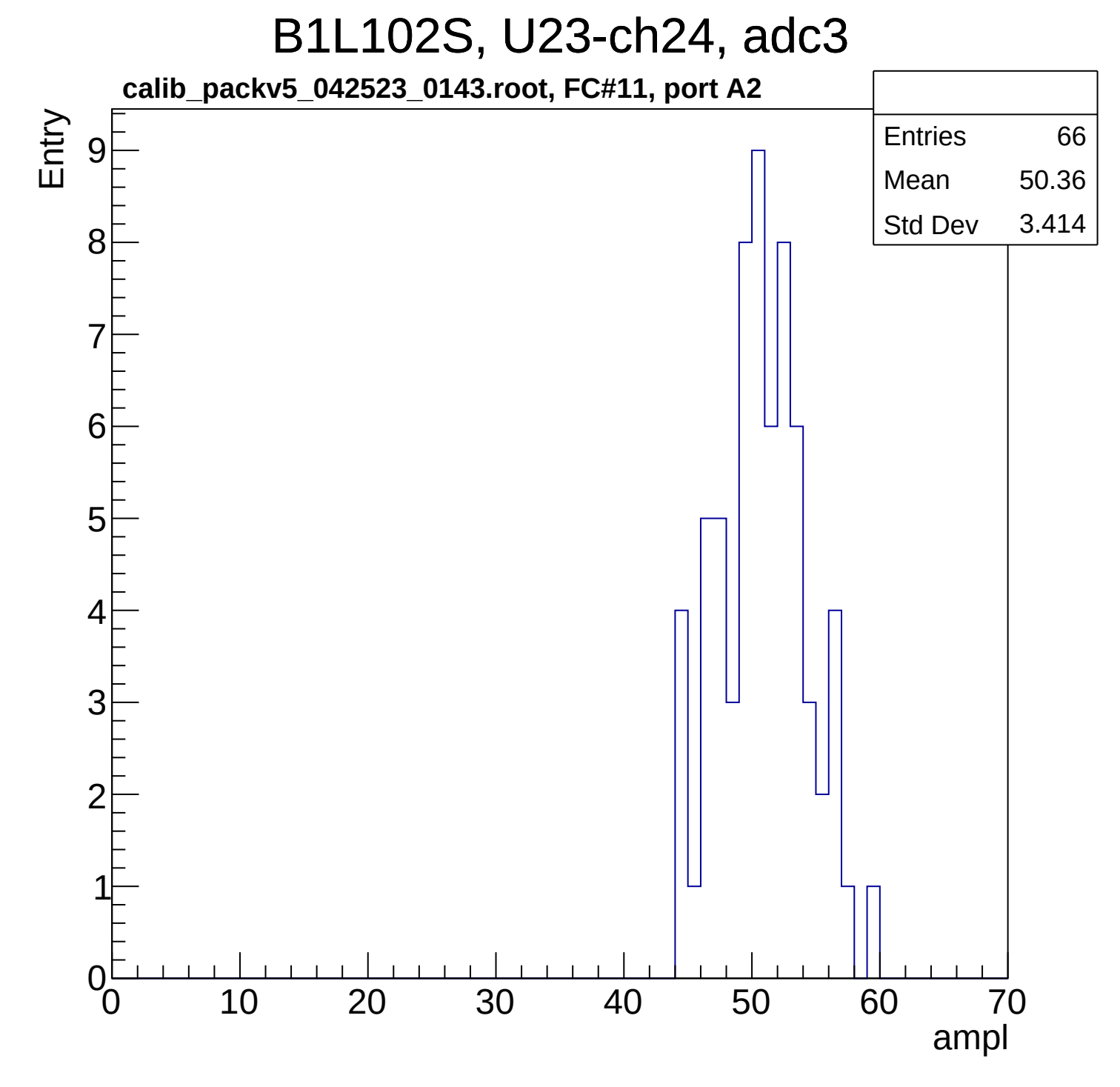

B1L102S, U23-ch23, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

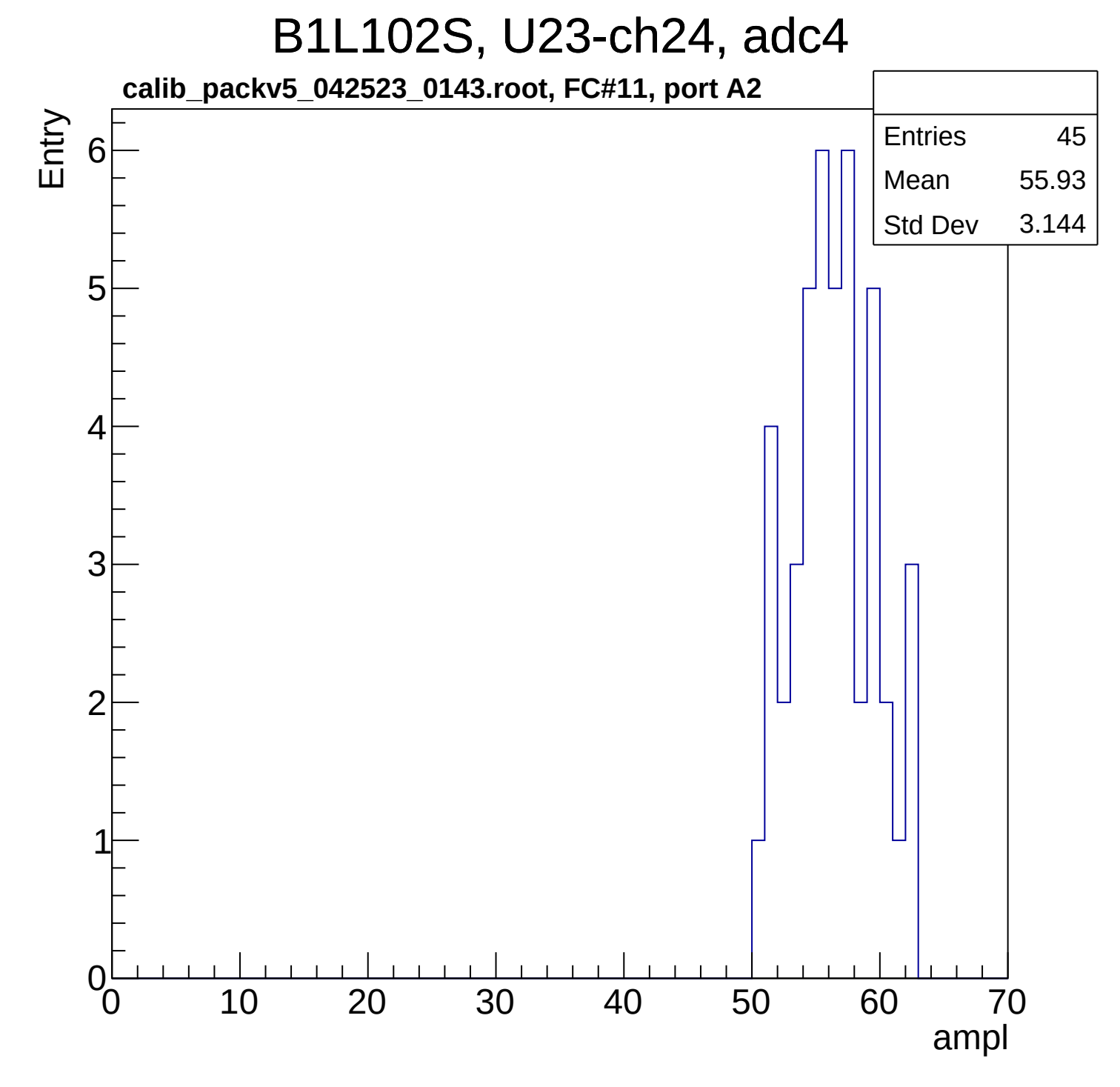

B1L102S, U23-ch23, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

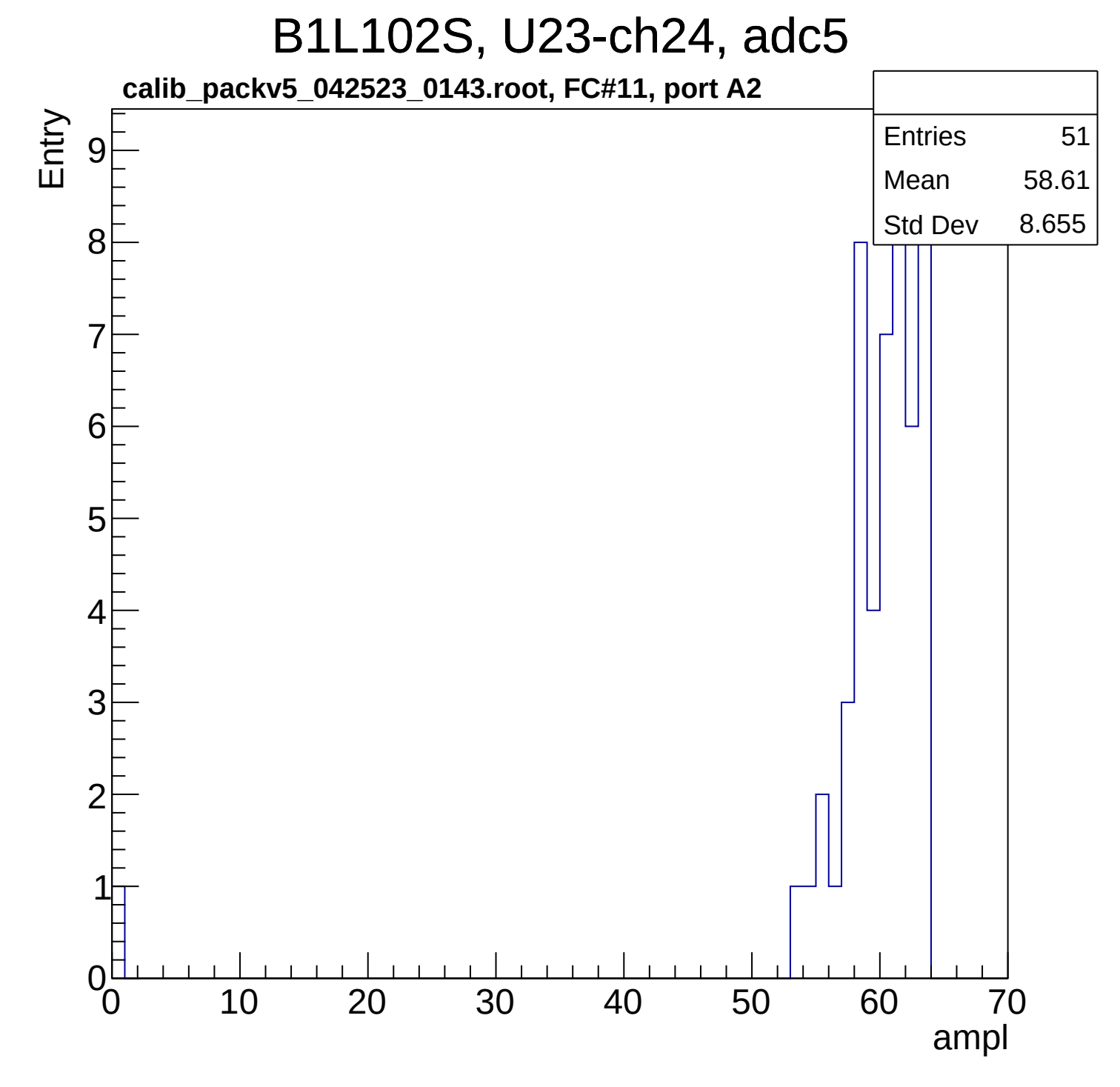

B1L102S, U23-ch24, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 31.24
Std Dev 3.586
Gaus mean : 31.7279
Gaus Width: 4.0337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

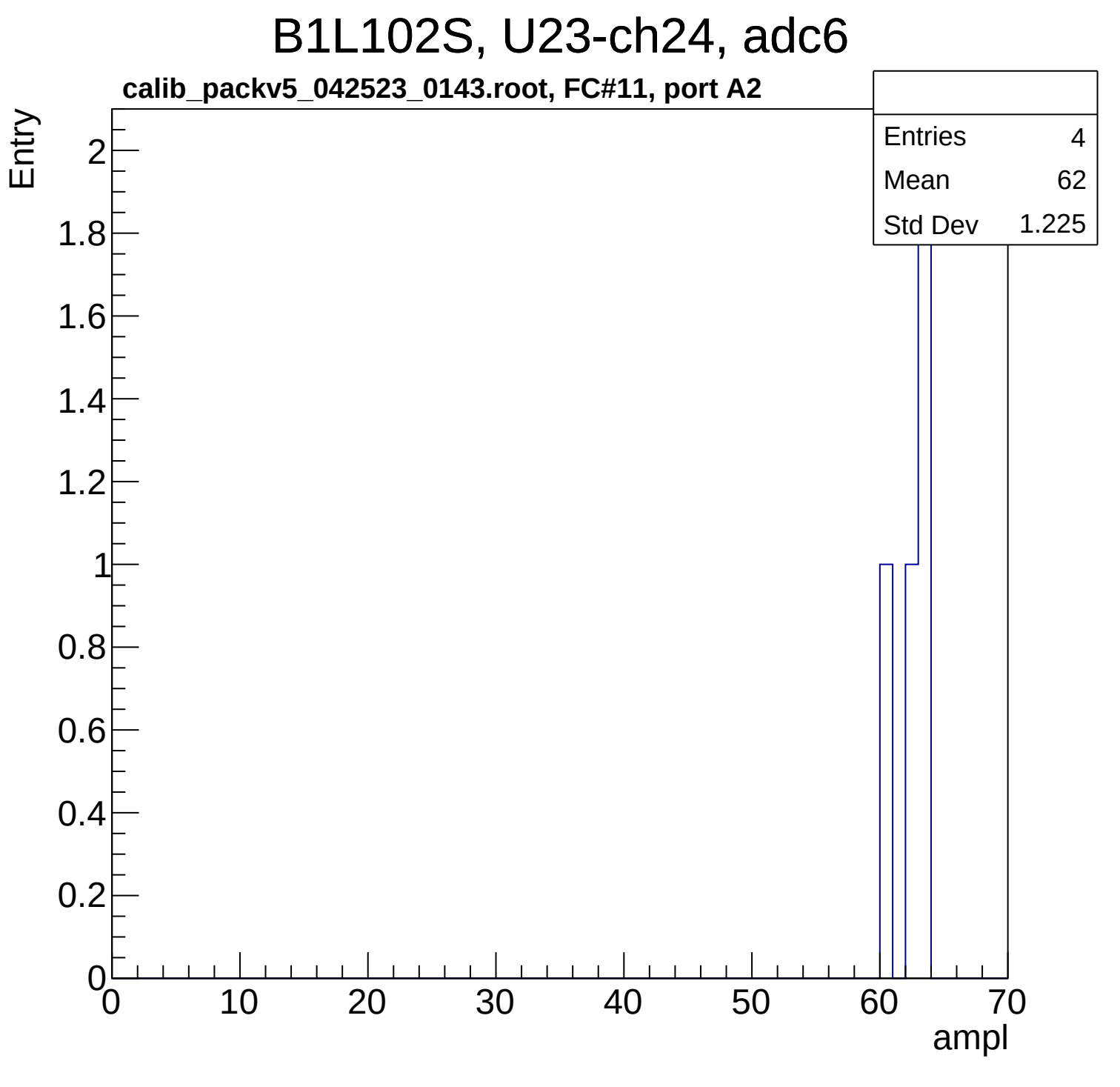

B1L102S, U23-ch24, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 37.48
Std Dev 4.111
Gaus mean : 37.9356
Gaus Width: 4.7604
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

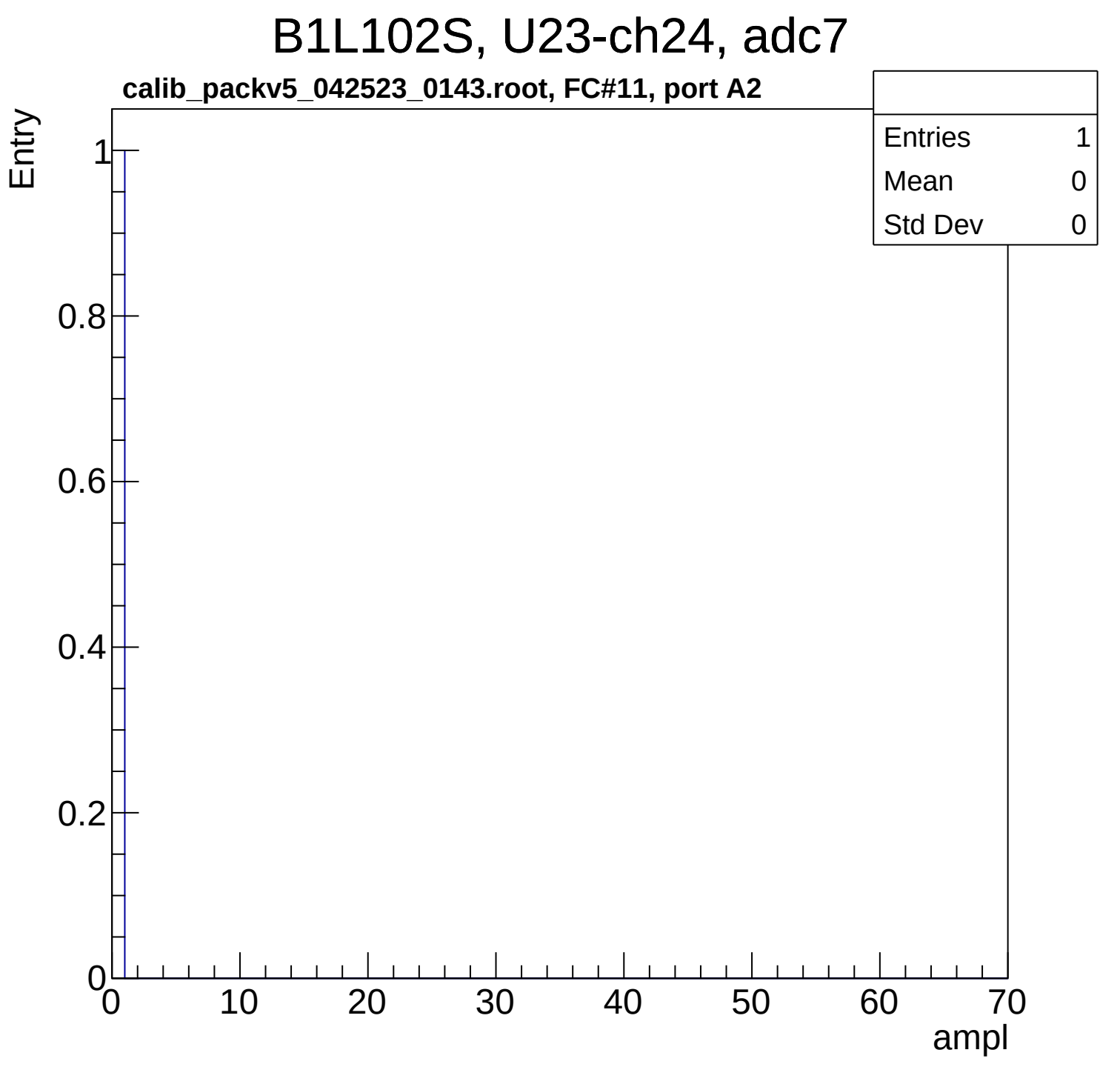

B1L102S, U23-ch24, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 42.86
Std Dev 3.644
Gaus mean : 43.6827
Gaus Width: 4.0504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

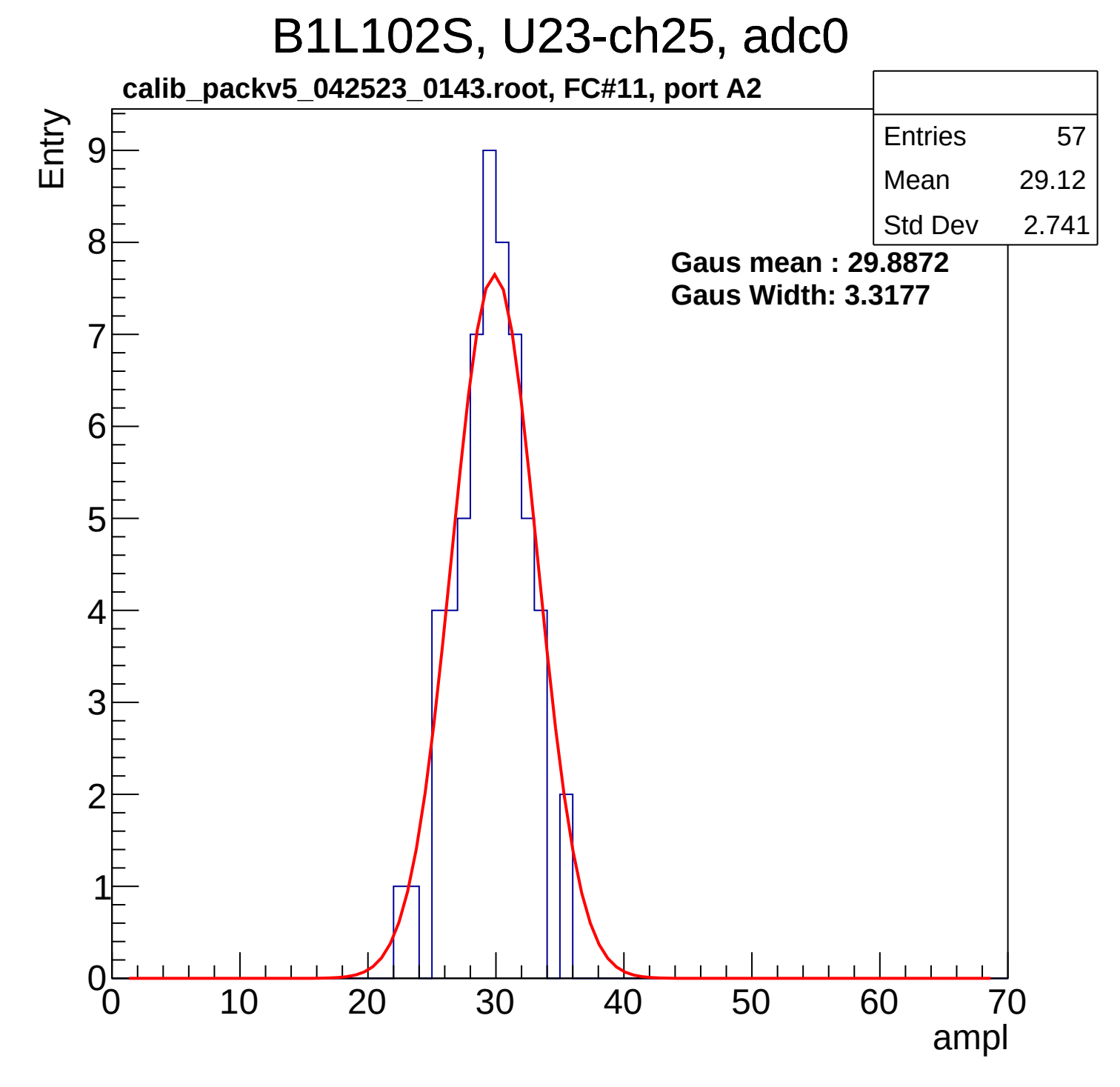

B1L102S, U23-ch24, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 50.36
Std Dev 3.414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

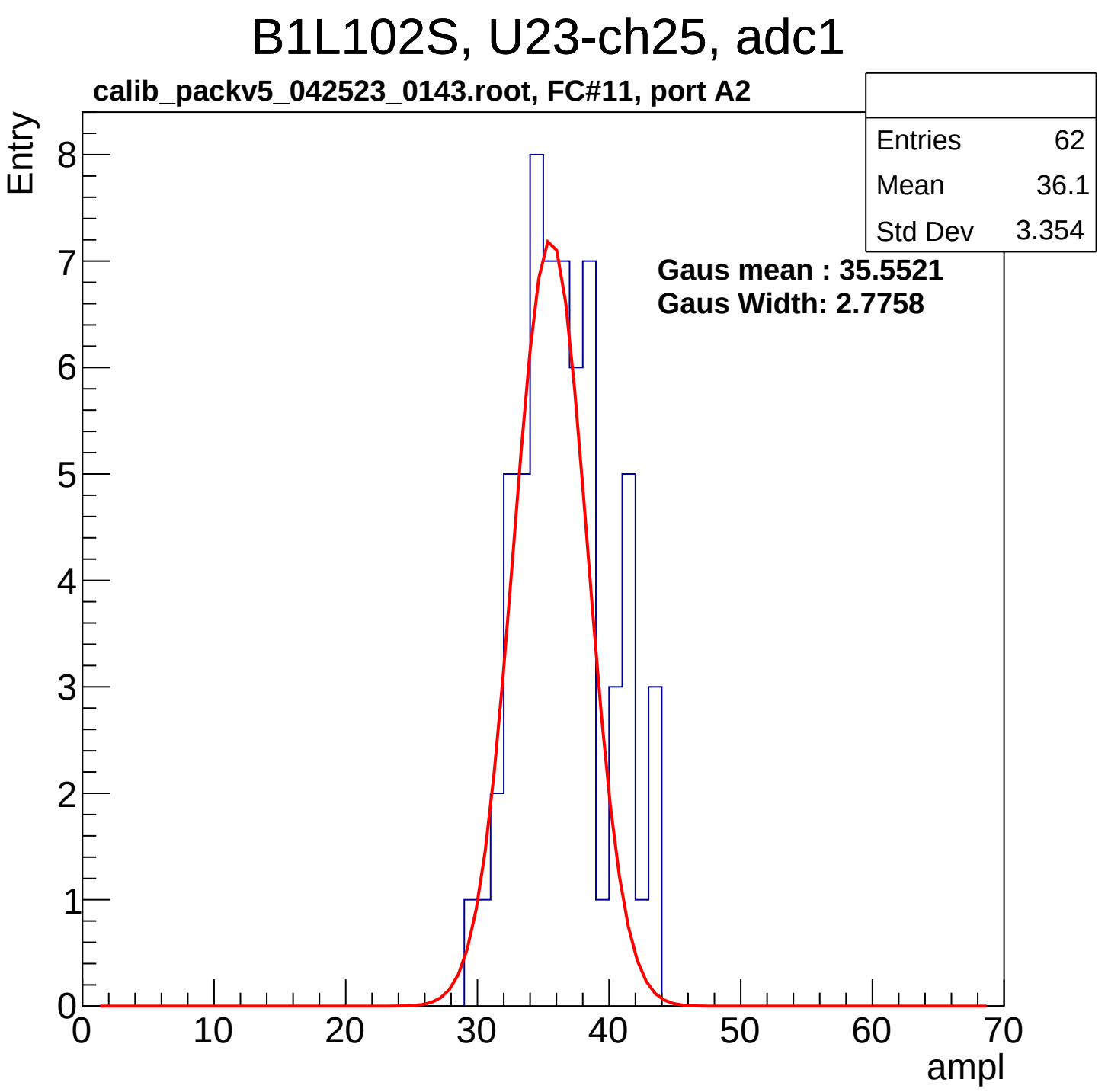

B1L102S, U23-ch24, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 55.93
Std Dev 3.144
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

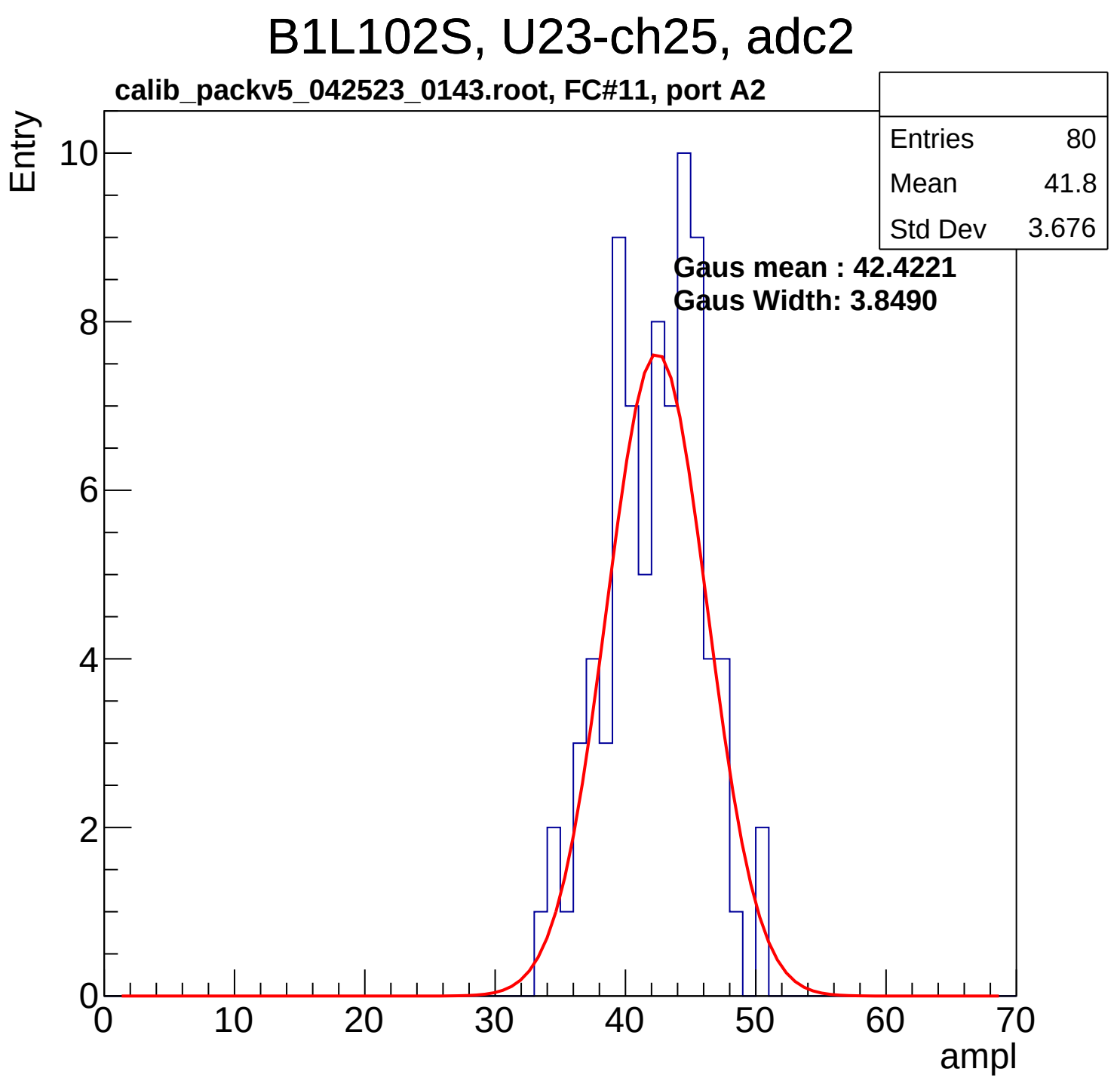

B1L102S, U23-ch24, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58.61
Std Dev 8.655
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

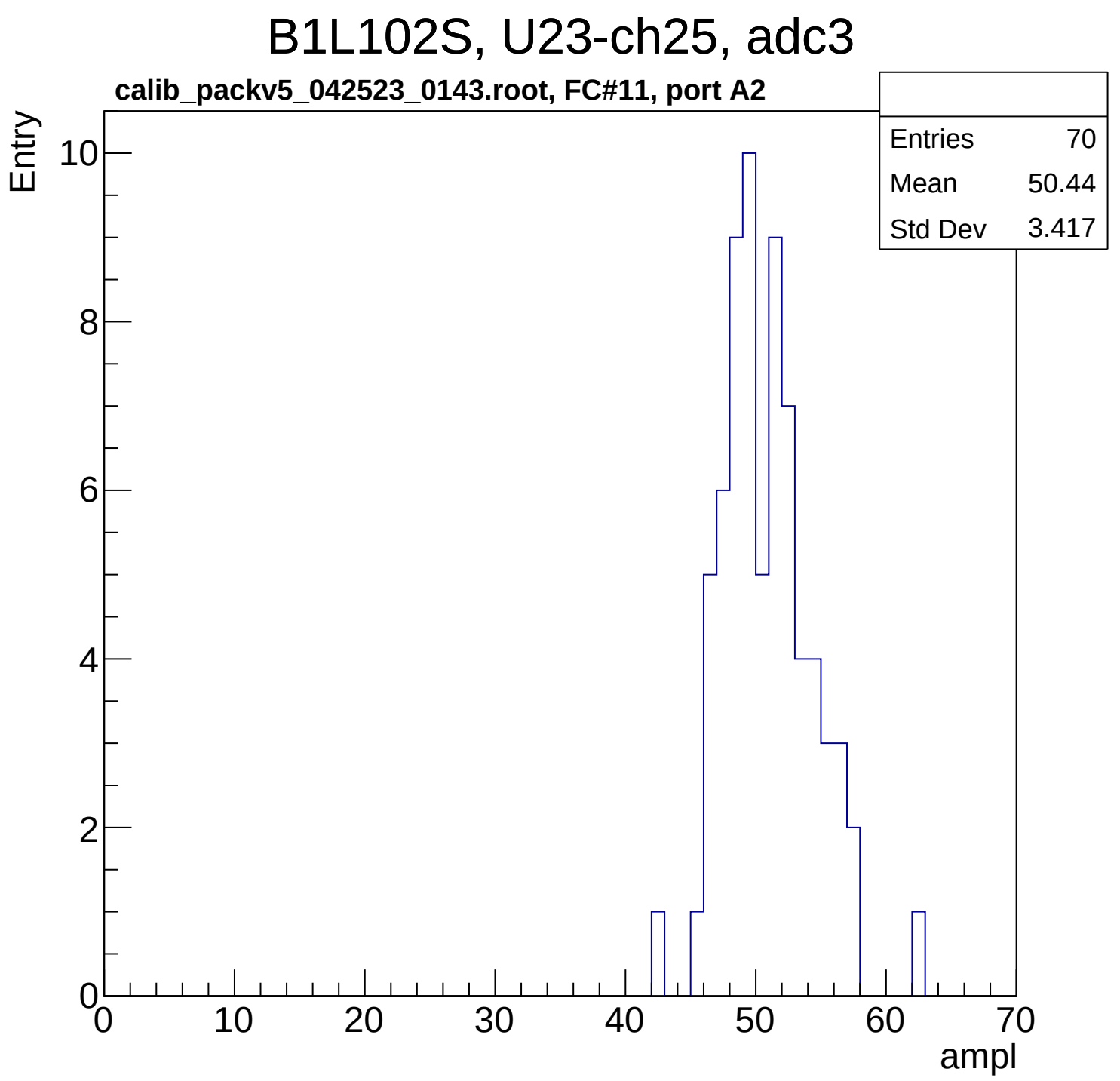

B1L102S, U23-ch24, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

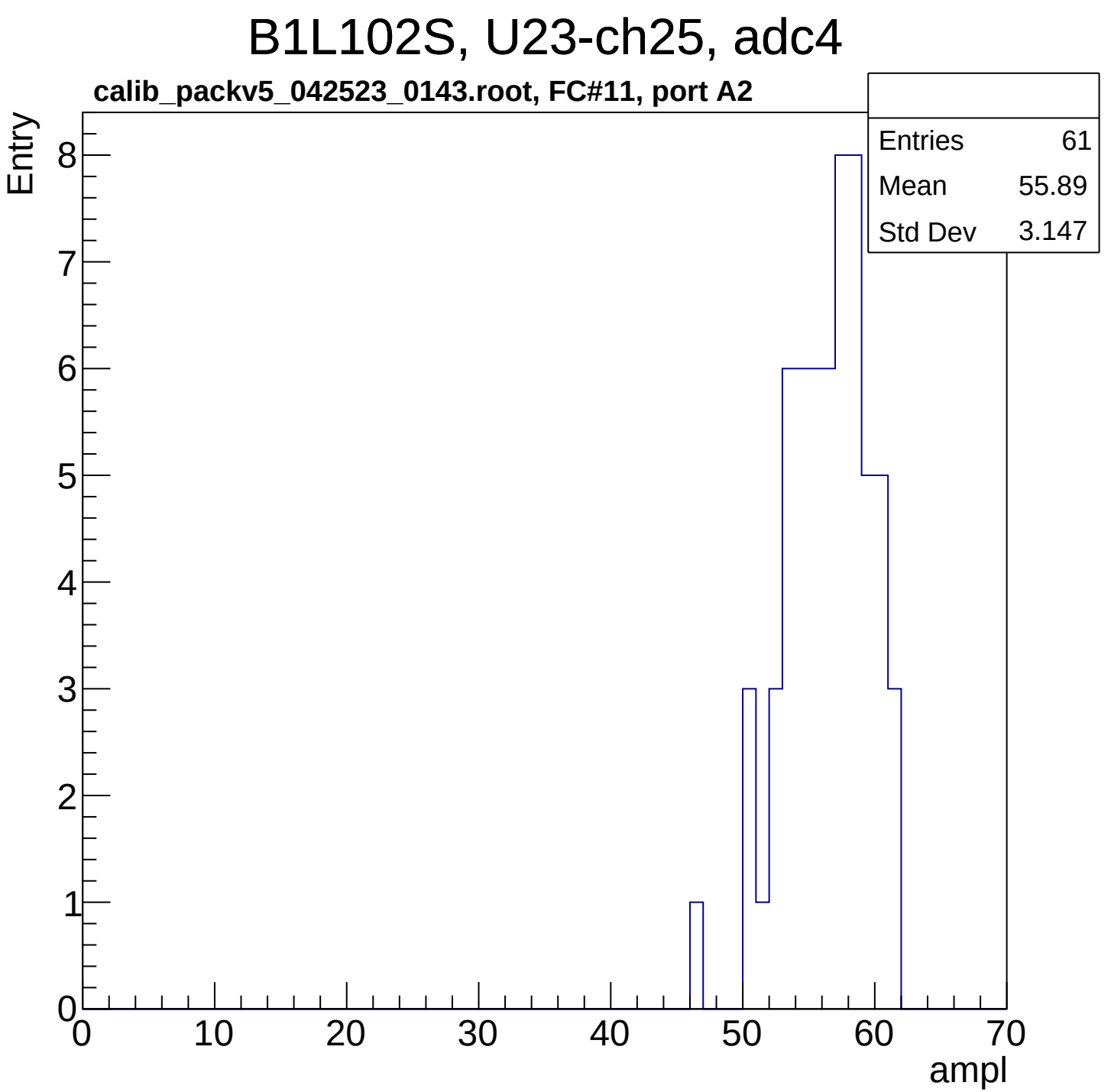

B1L102S, U23-ch24, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

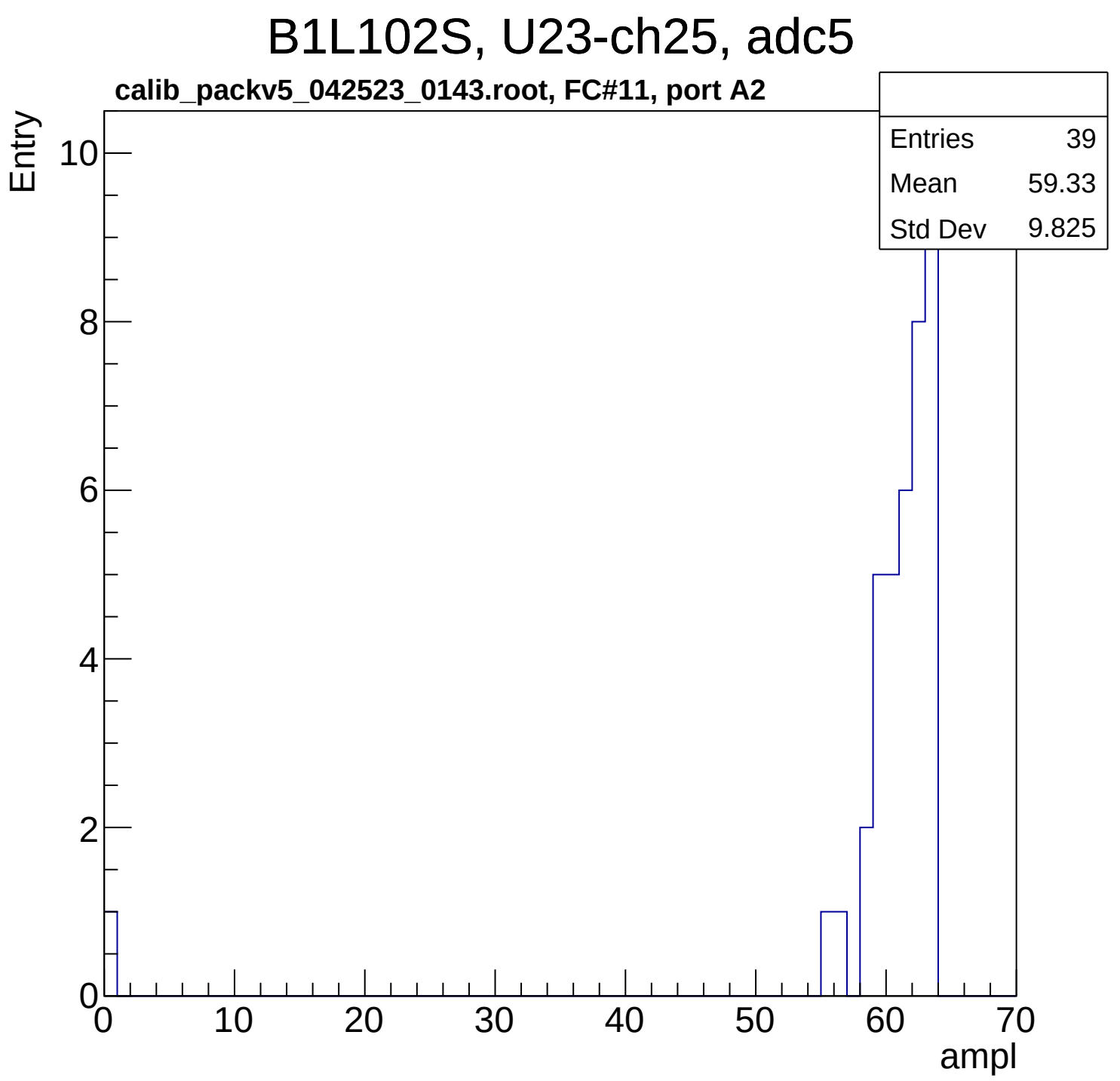

B1L102S, U23-ch25, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 29.12
Std Dev 2.741
Gaus mean : 29.8872
Gaus Width: 3.3177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

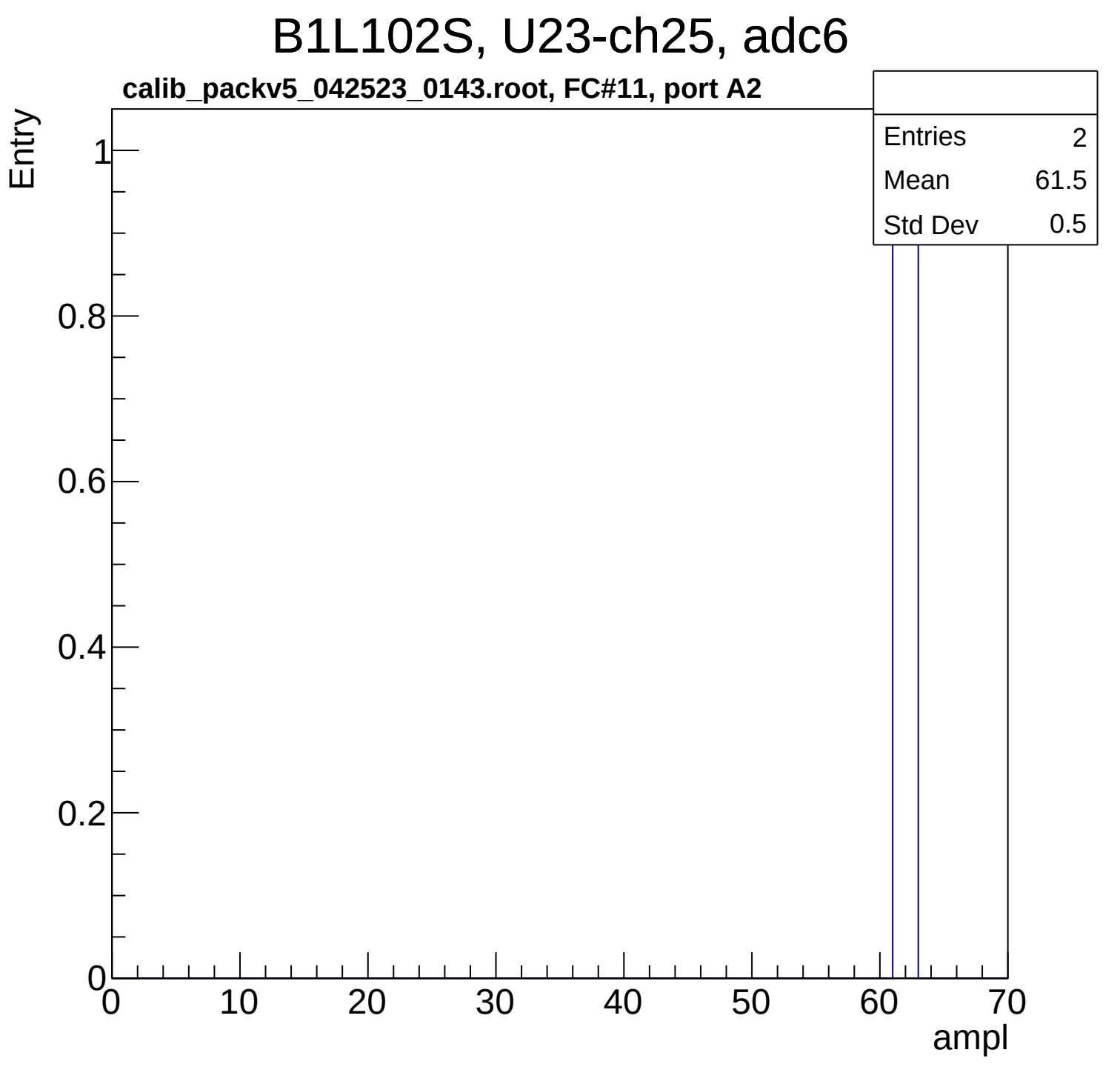

B1L102S, U23-ch25, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 36.1
Std Dev 3.354
Gaus mean : 35.5521
Gaus Width: 2.7758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

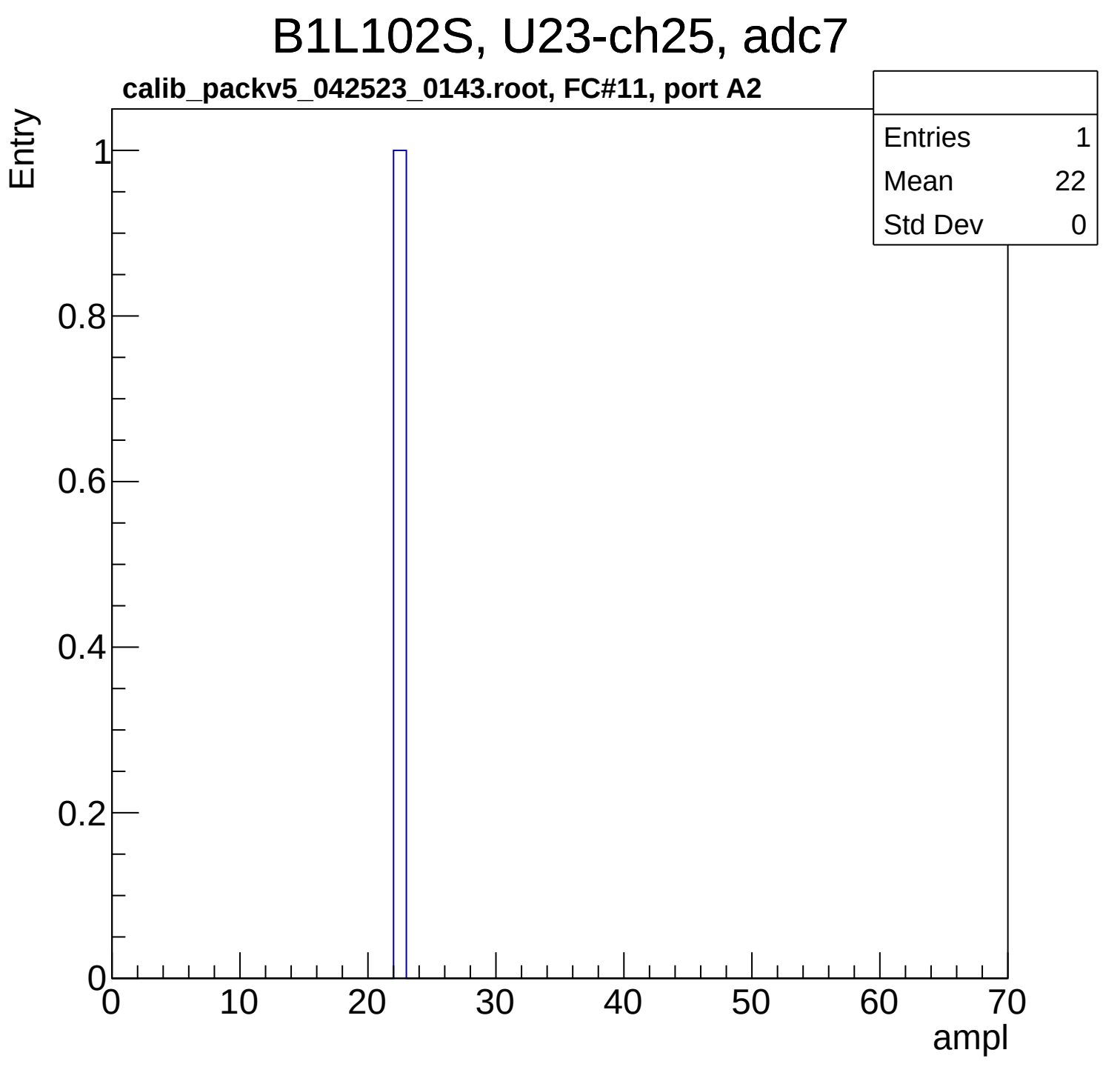

B1L102S, U23-ch25, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 41.8
Std Dev 3.676
Gaus mean : 42.4221
Gaus Width: 3.8490
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

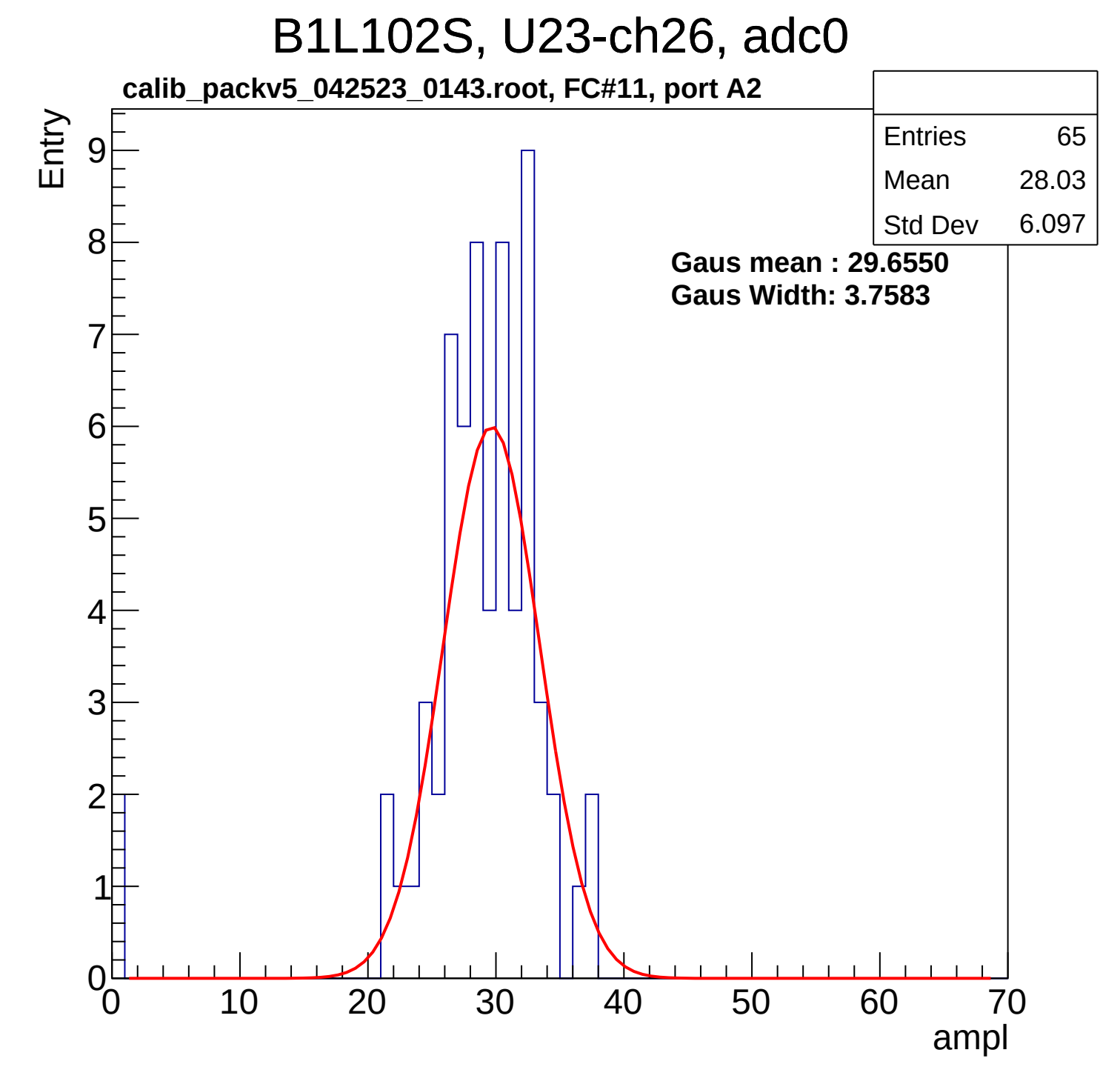

B1L102S, U23-ch25, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 50.44
Std Dev 3.417
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

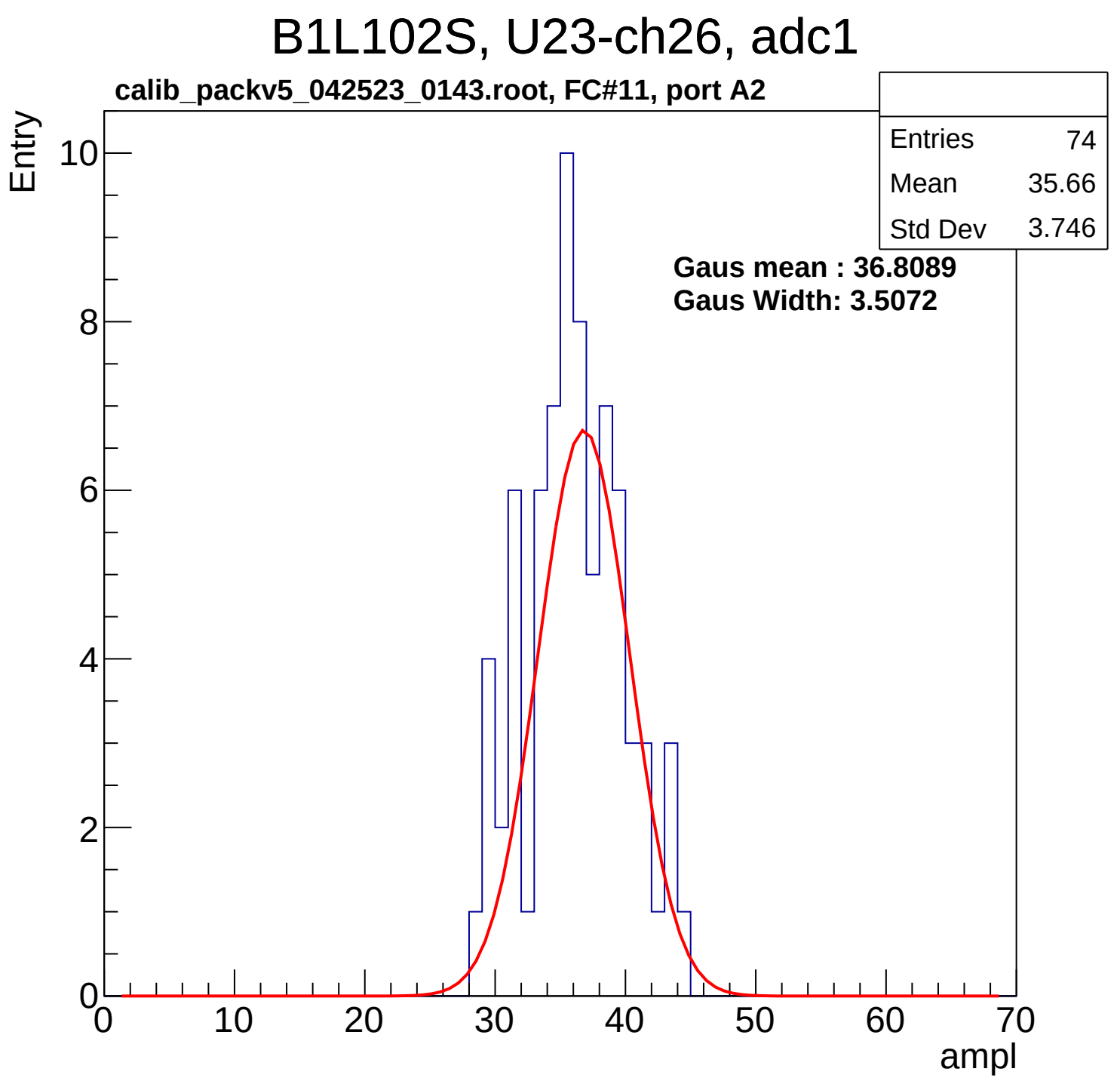

B1L102S, U23-ch25, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 55.89
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

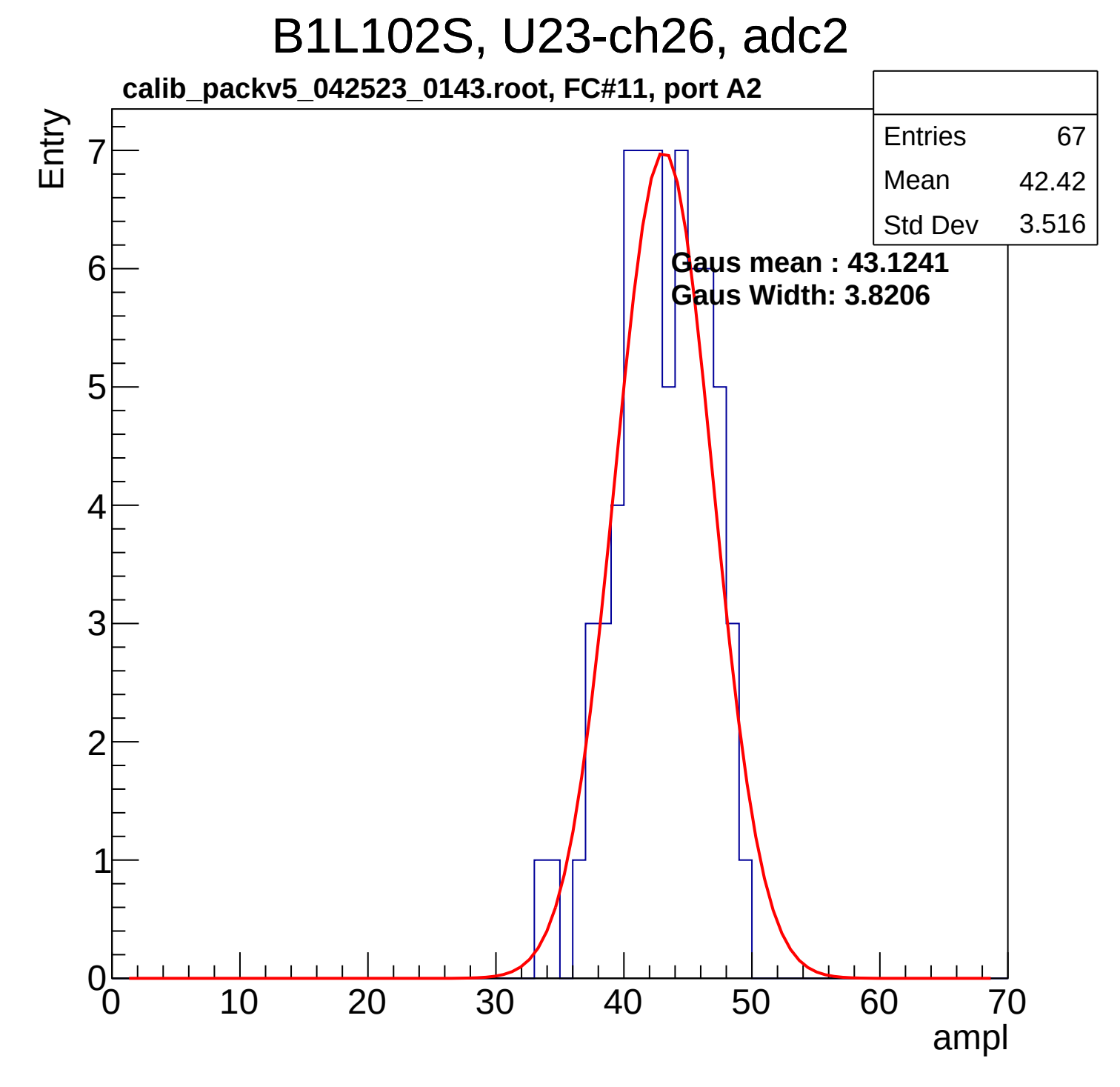

B1L102S, U23-ch25, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 59.33
Std Dev 9.825
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

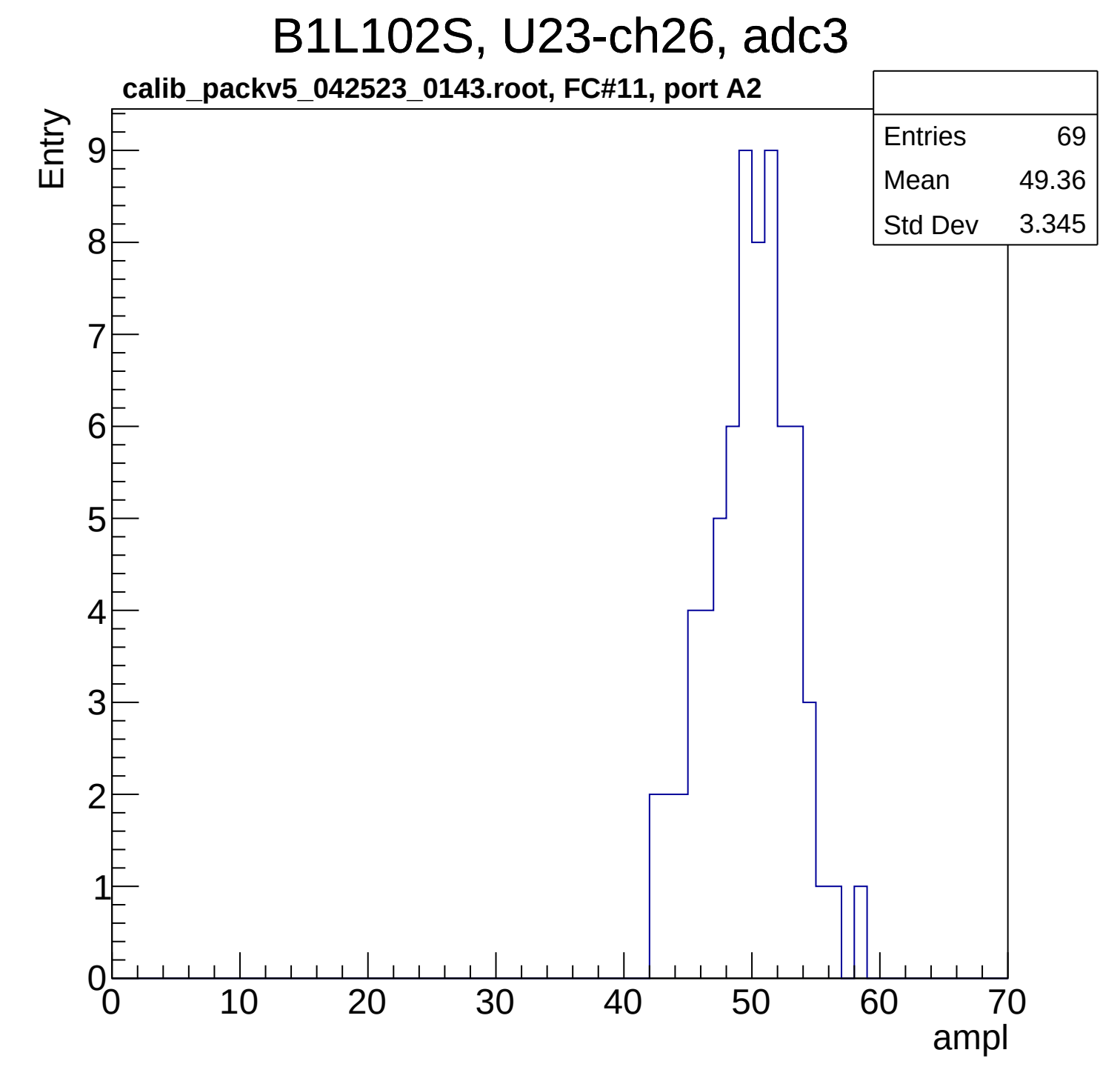

B1L102S, U23-ch25, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

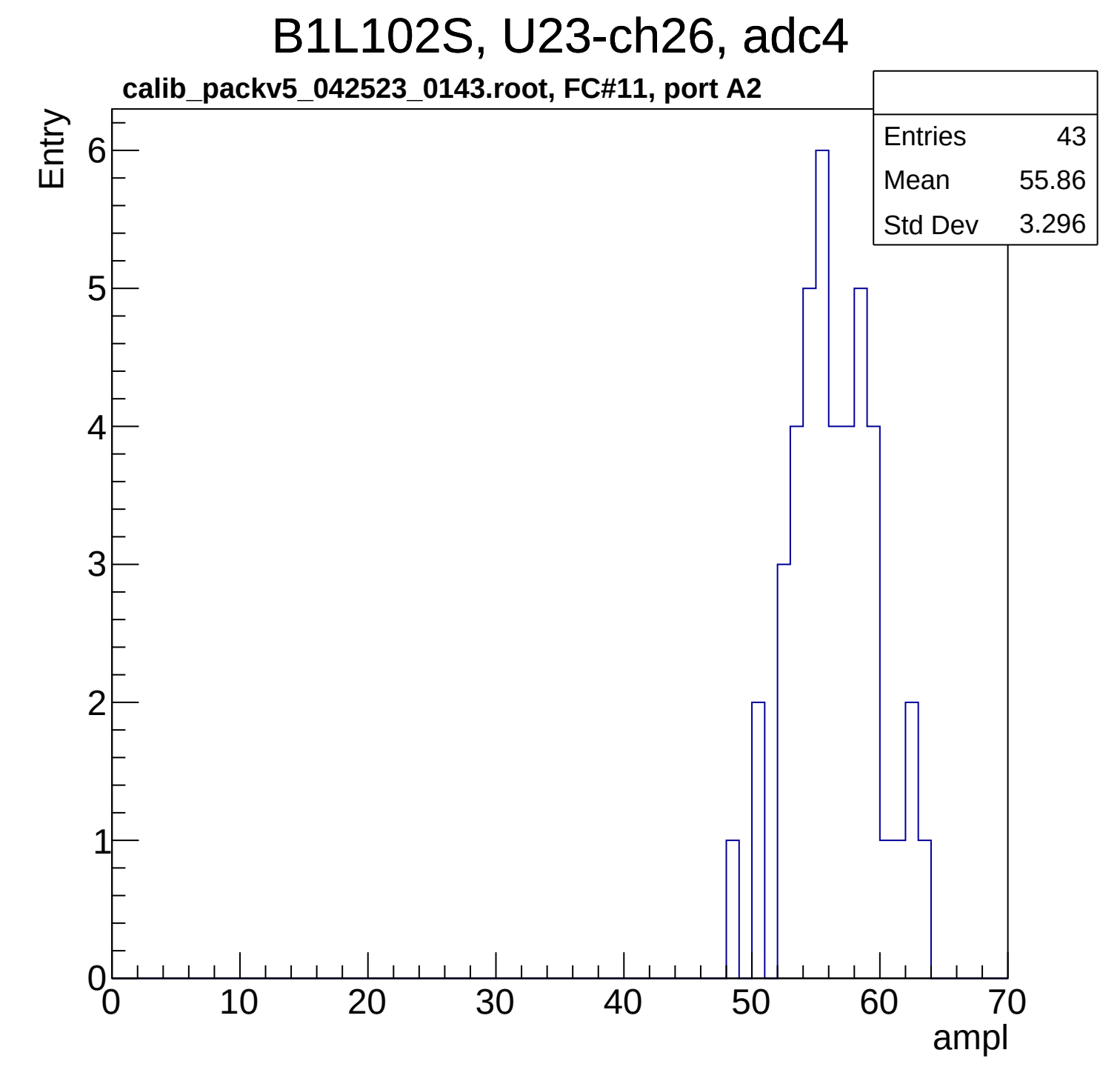

B1L102S, U23-ch25, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

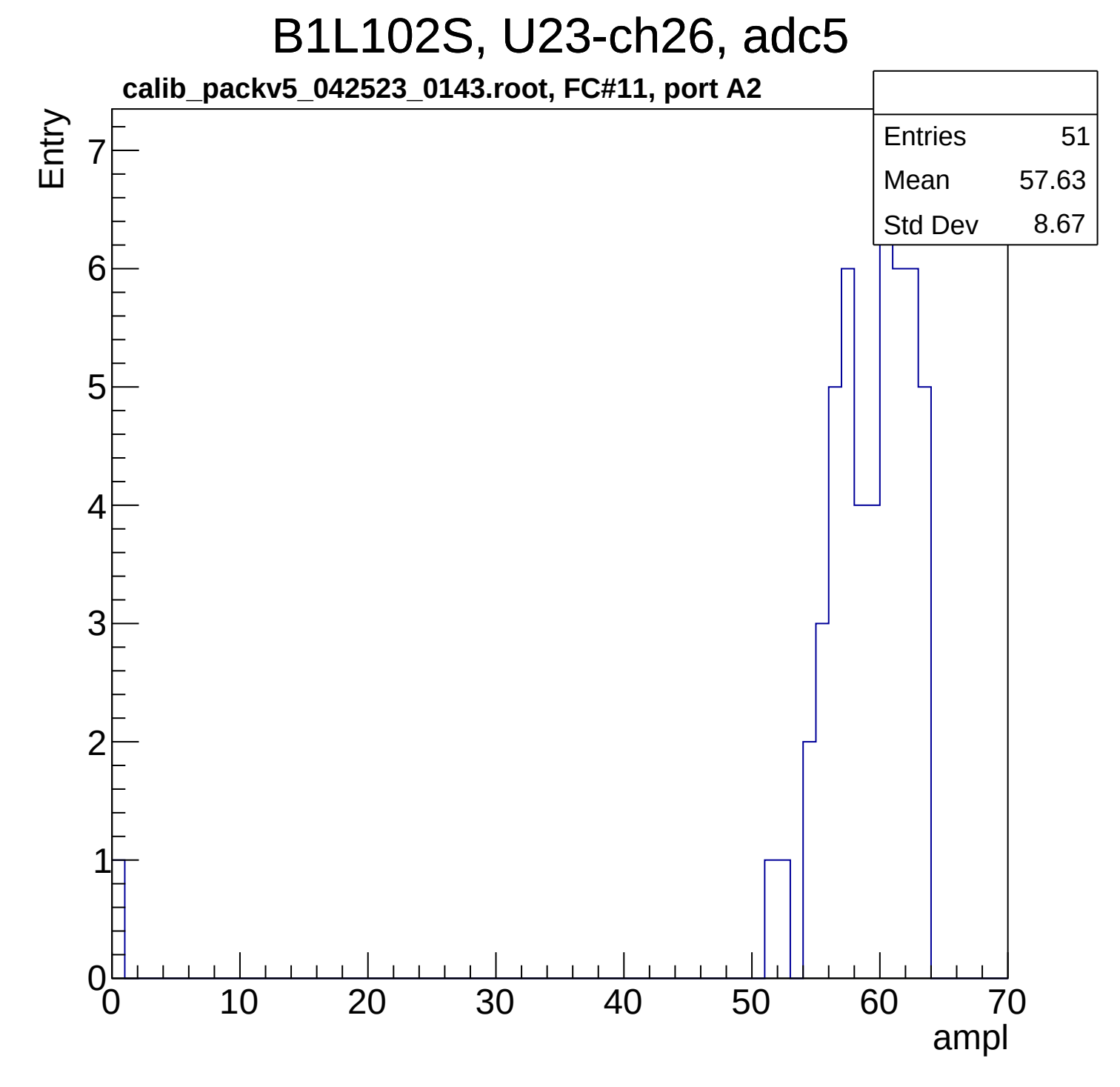

B1L102S, U23-ch26, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 28.03
Std Dev 6.097
Gaus mean : 29.6550
Gaus Width: 3.7583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

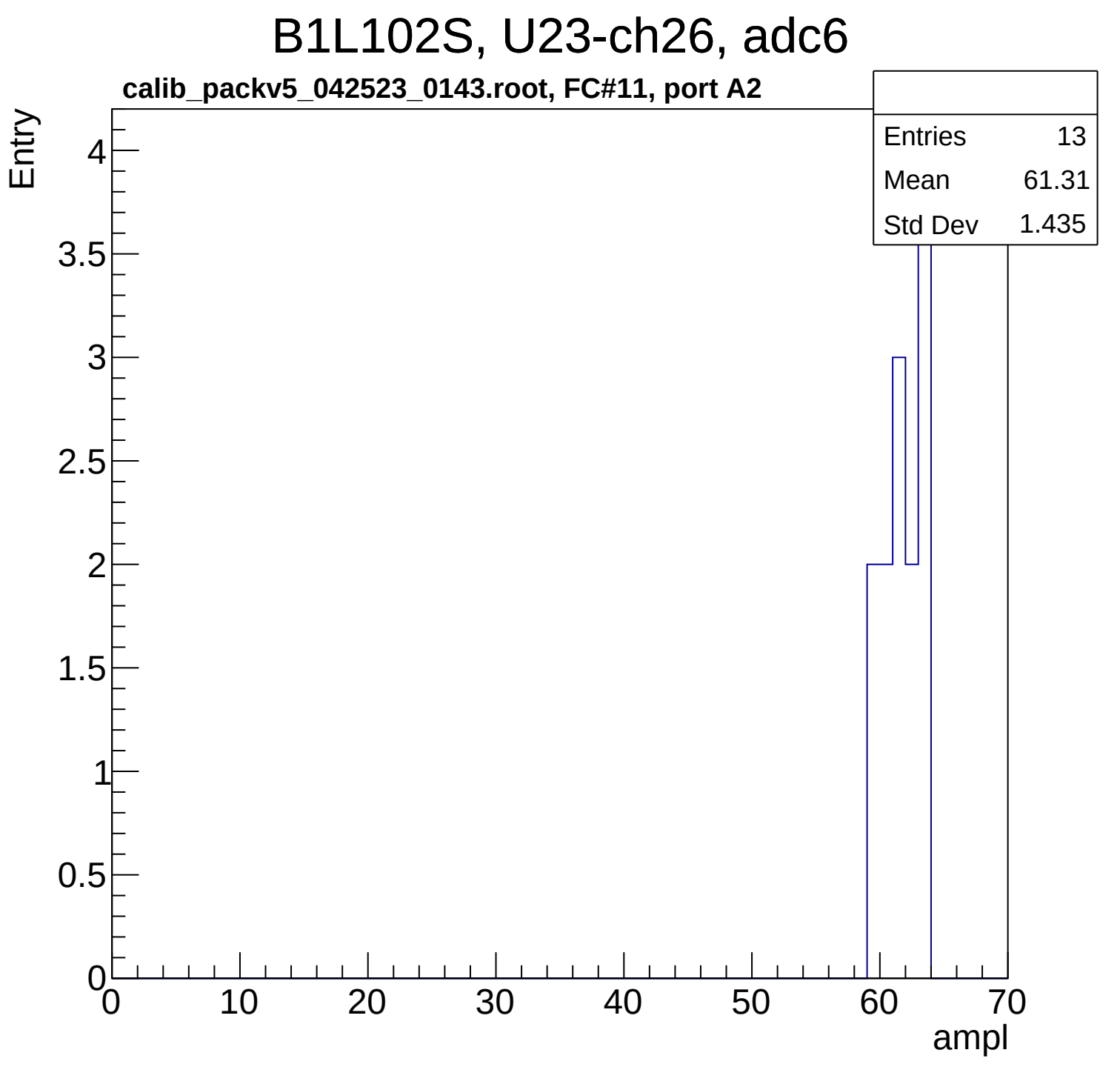

B1L102S, U23-ch26, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 35.66
Std Dev 3.746
Gaus mean : 36.8089
Gaus Width: 3.5072
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

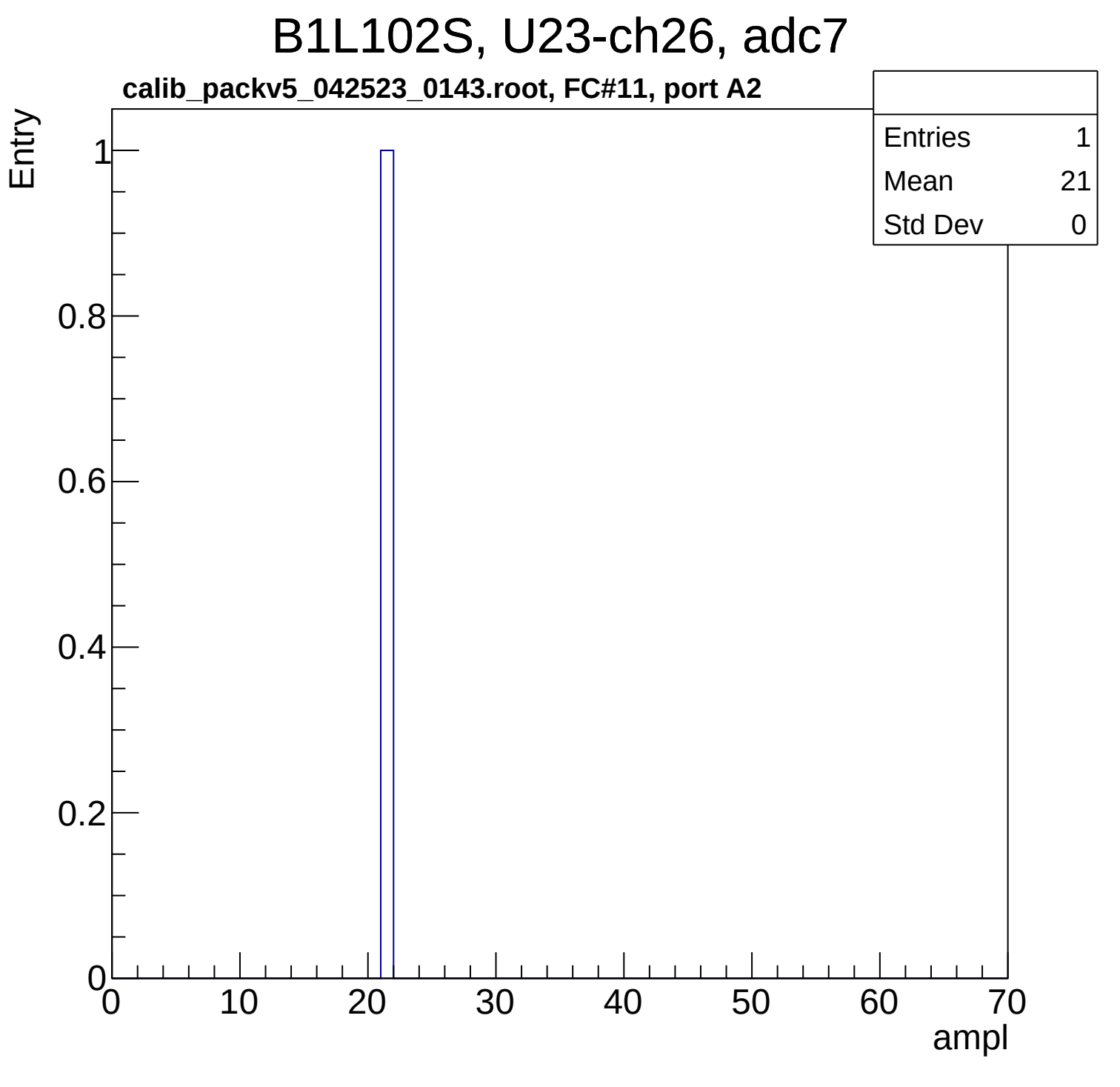

B1L102S, U23-ch26, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 42.42
Std Dev 3.516
Gaus mean : 43.1241
Gaus Width: 3.8206
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

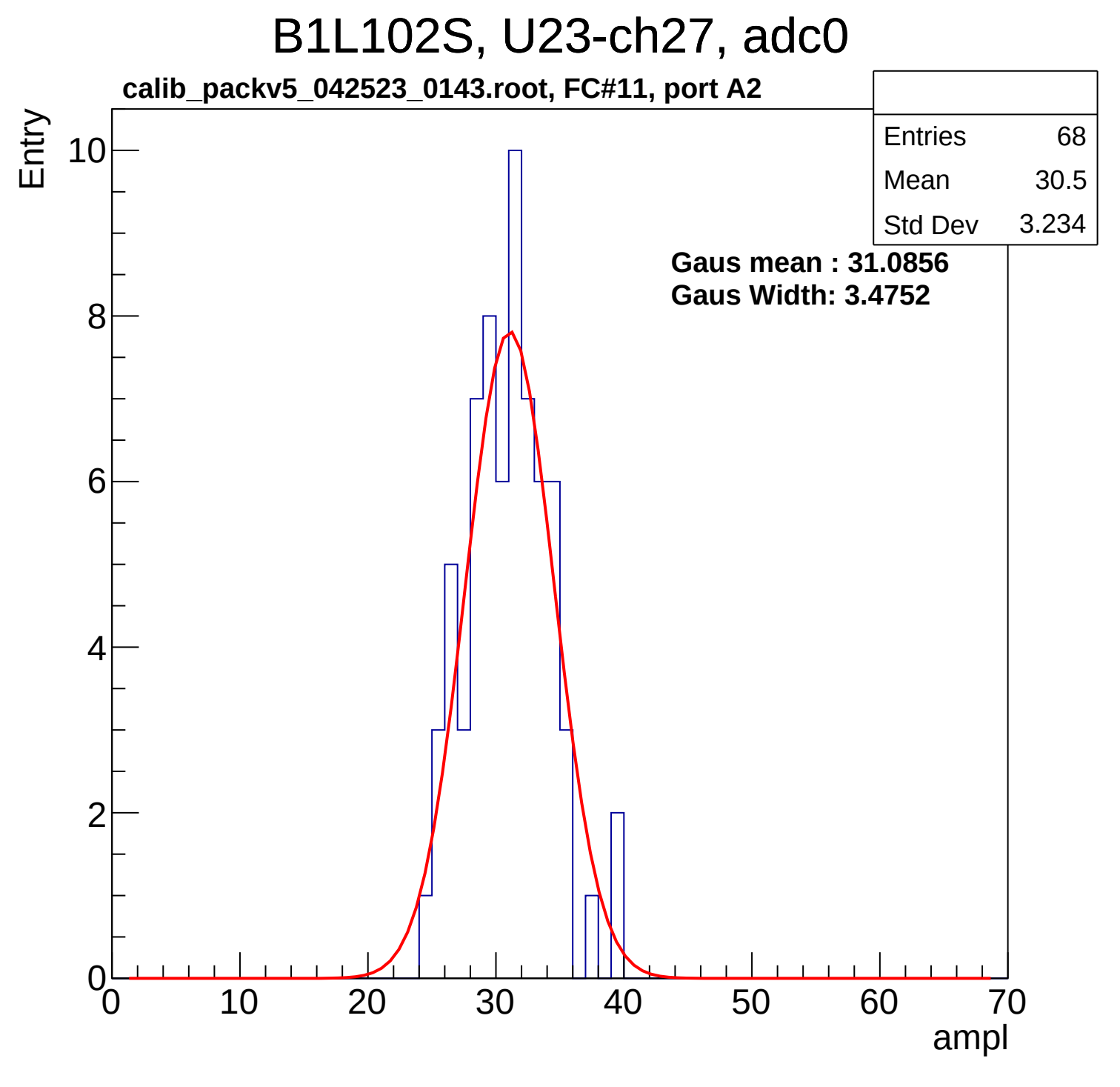

B1L102S, U23-ch26, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 49.36
Std Dev 3.345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

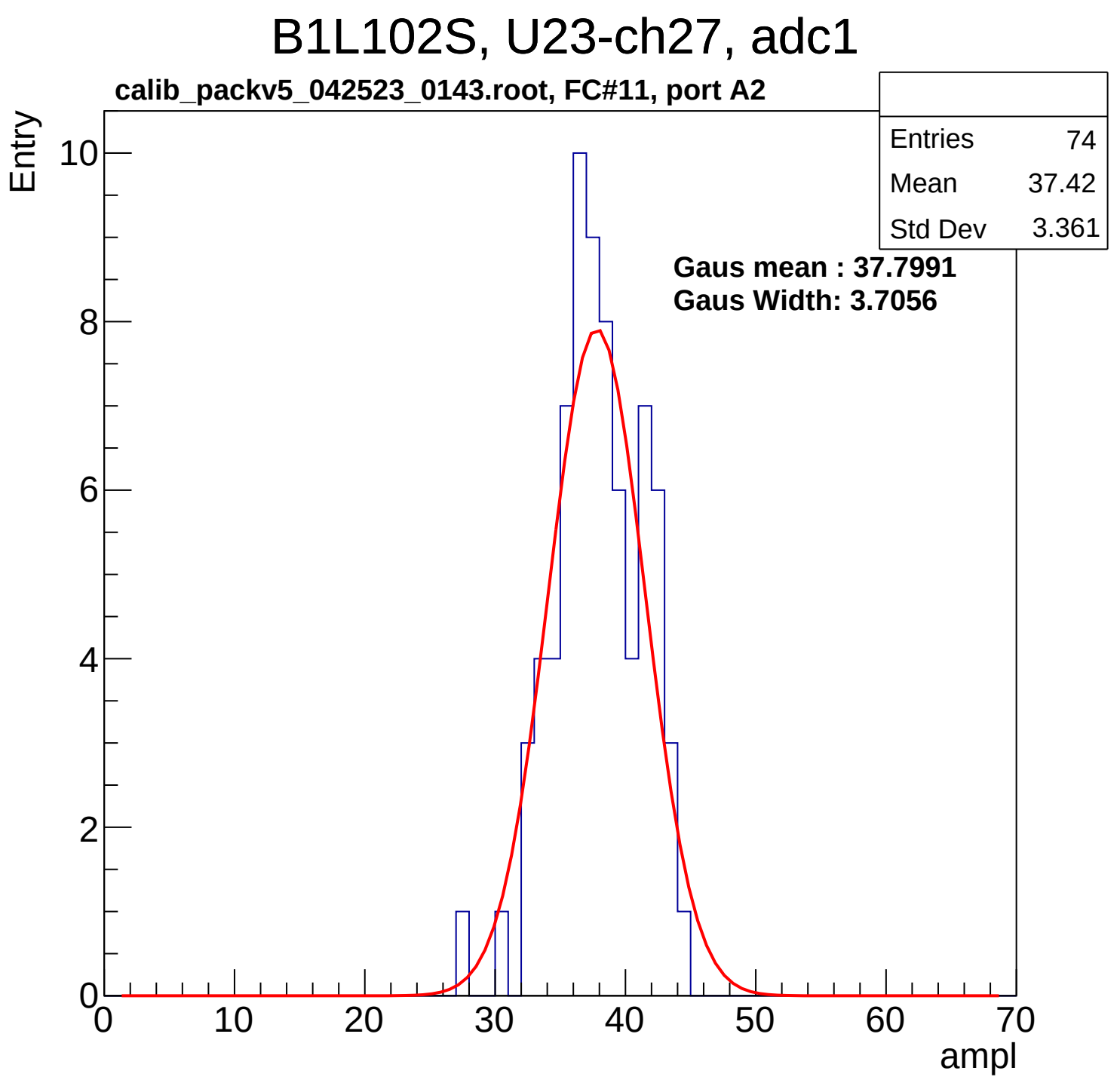

B1L102S, U23-ch26, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 55.86
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

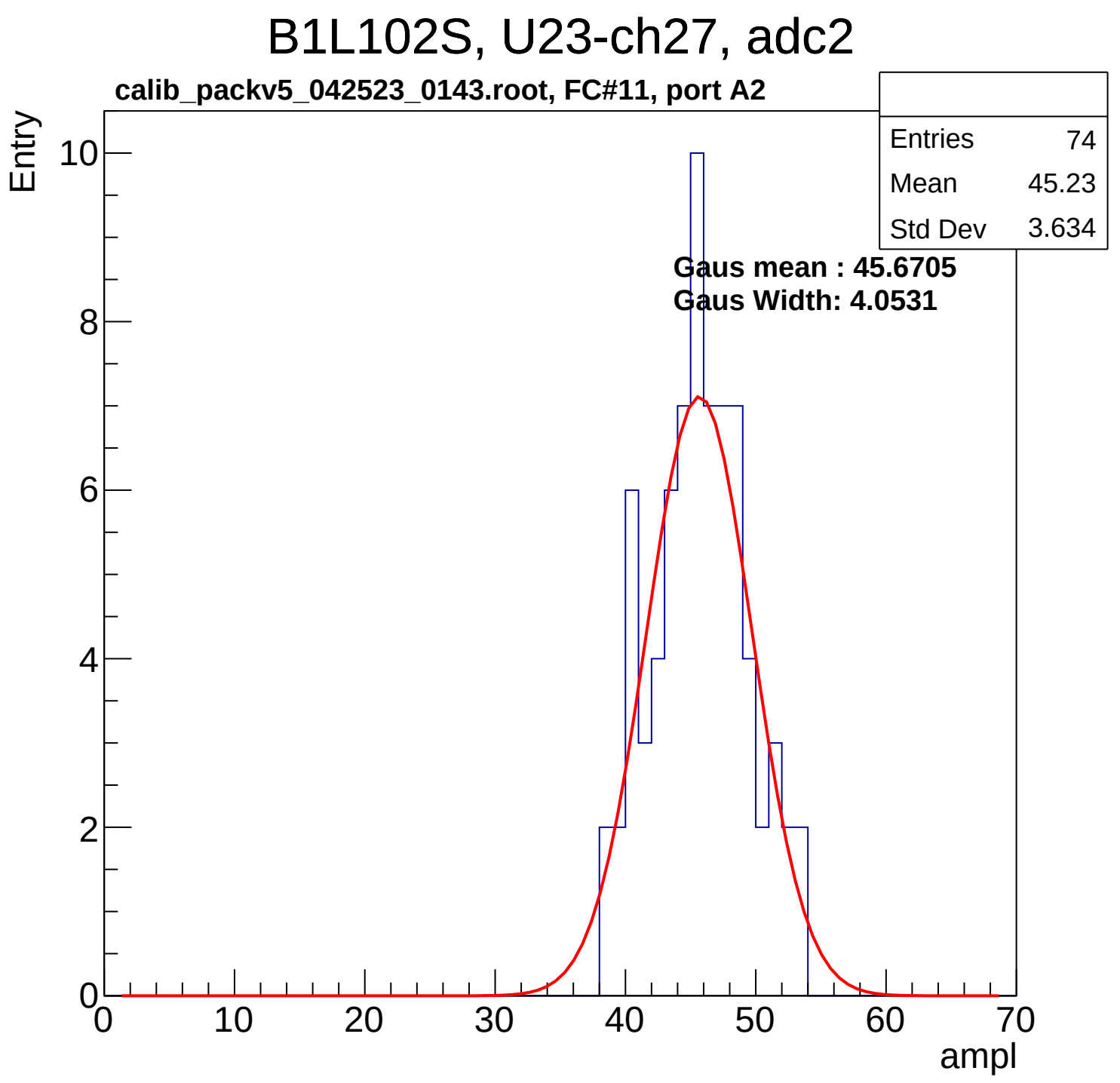

B1L102S, U23-ch26, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 57.63
Std Dev 8.67
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

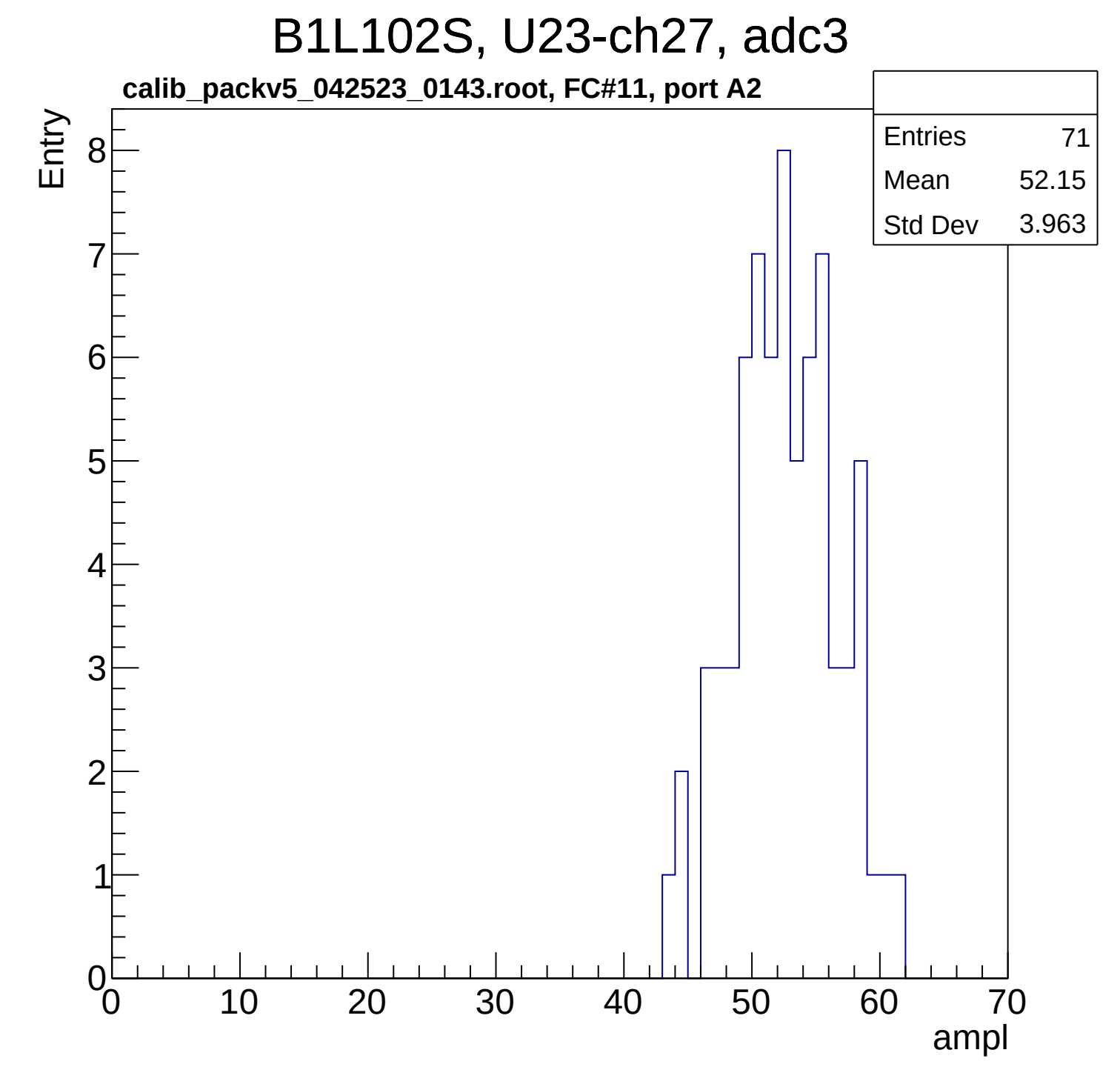

B1L102S, U23-ch26, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 61.31
Std Dev 1.435
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

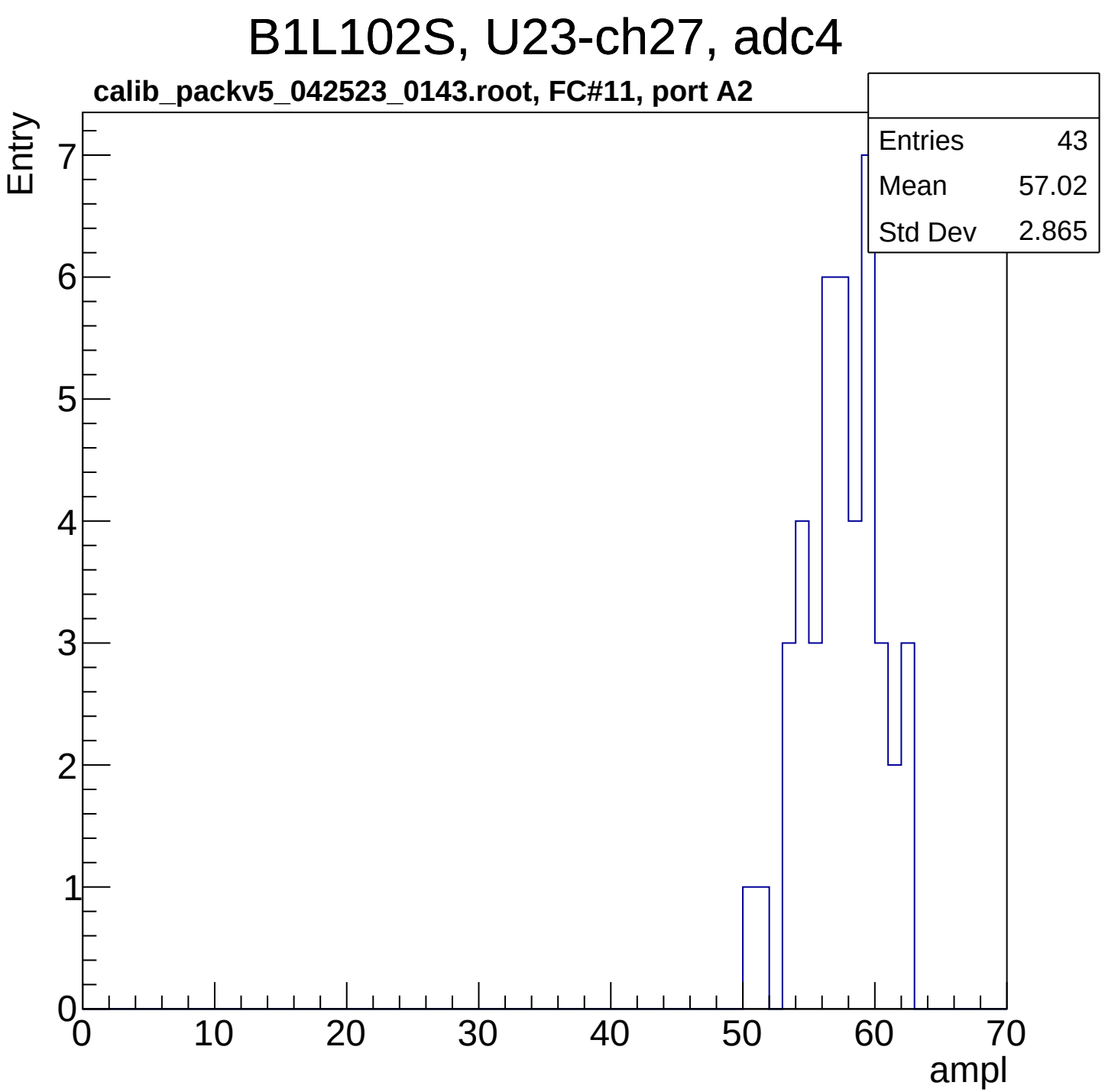

B1L102S, U23-ch26, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

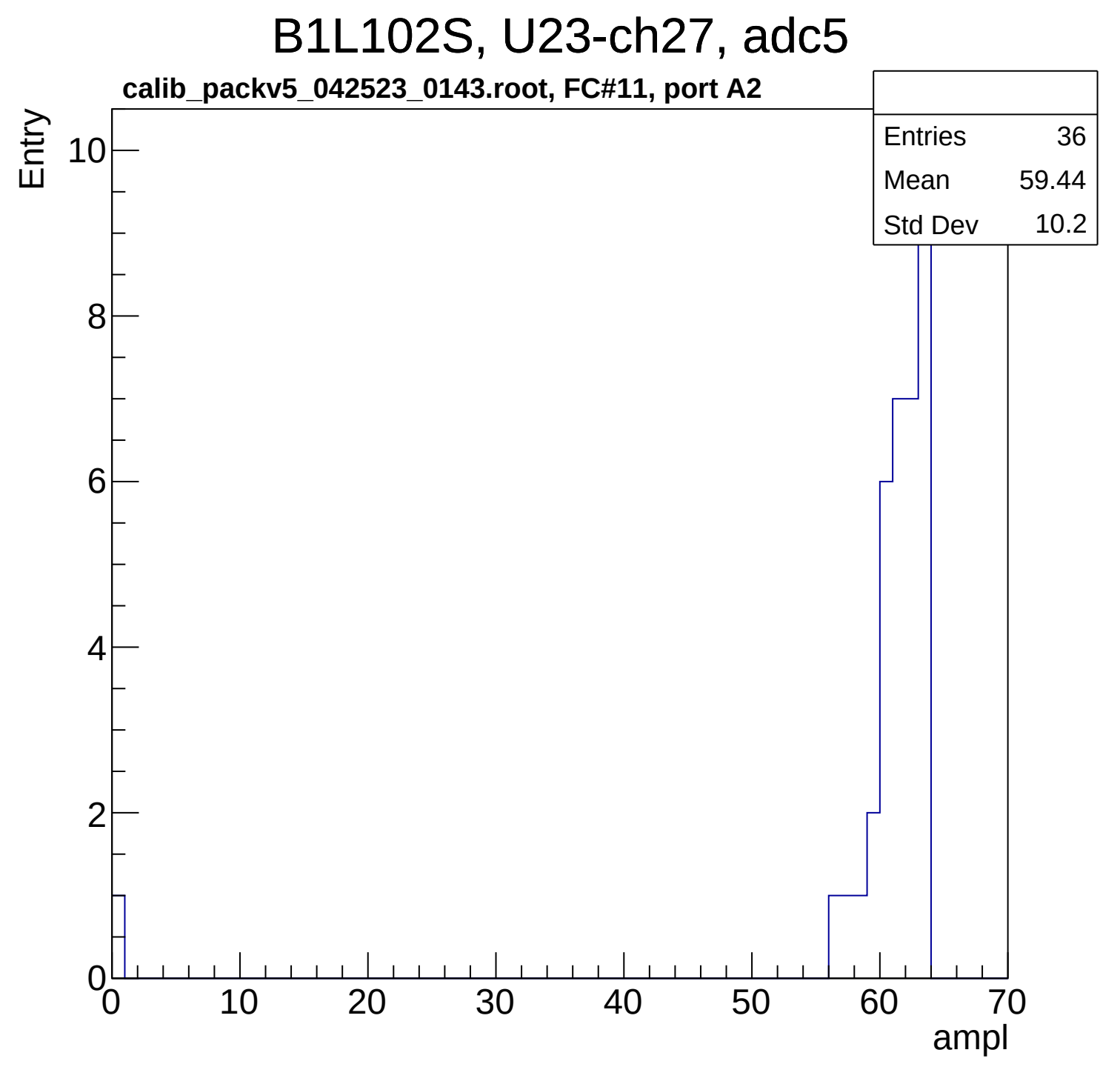

B1L102S, U23-ch27, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 30.5
Std Dev 3.234
Gaus mean : 31.0856
Gaus Width: 3.4752
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

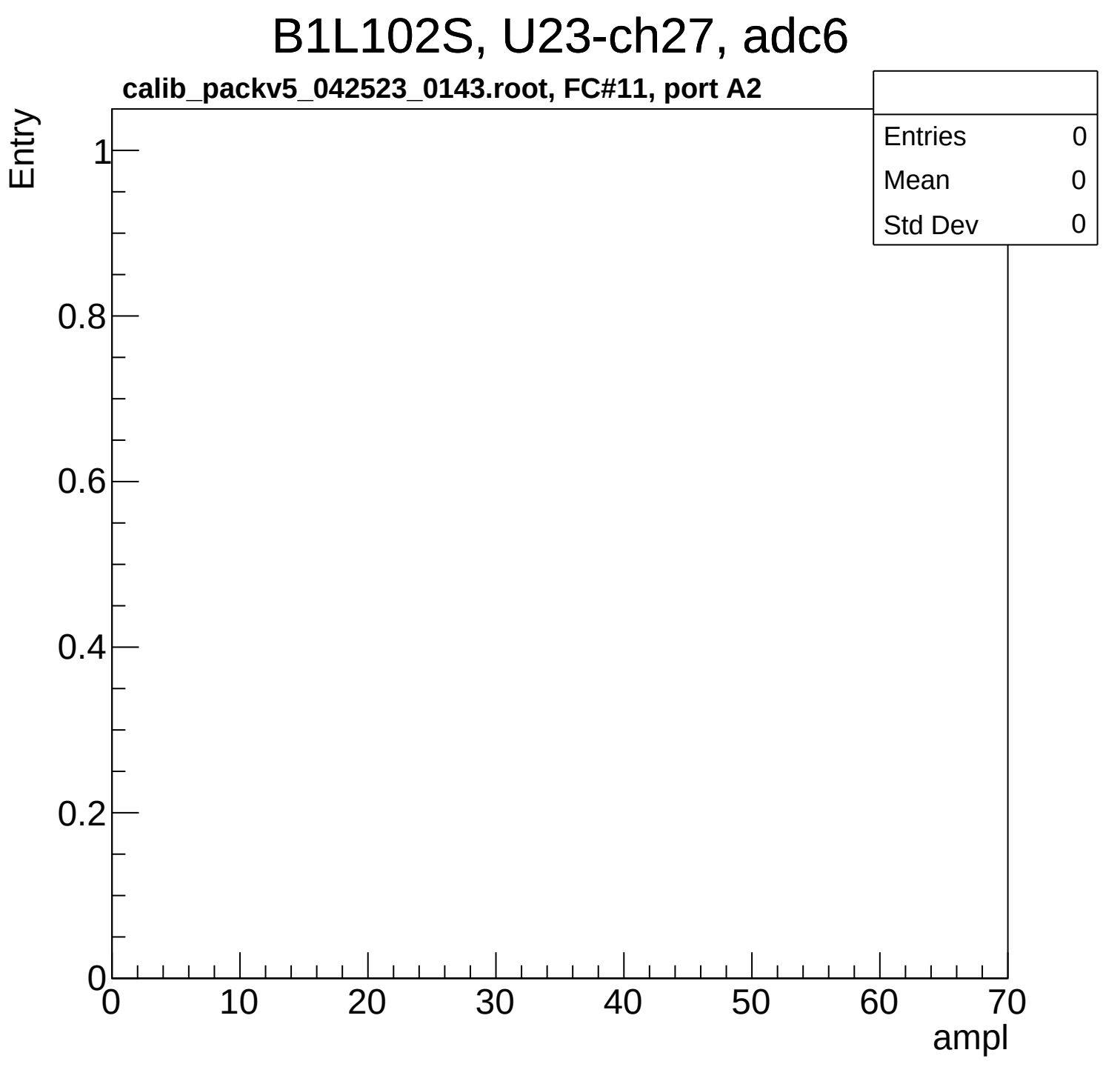

B1L102S, U23-ch27, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 37.42
Std Dev 3.361
Gaus mean : 37.7991
Gaus Width: 3.7056
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

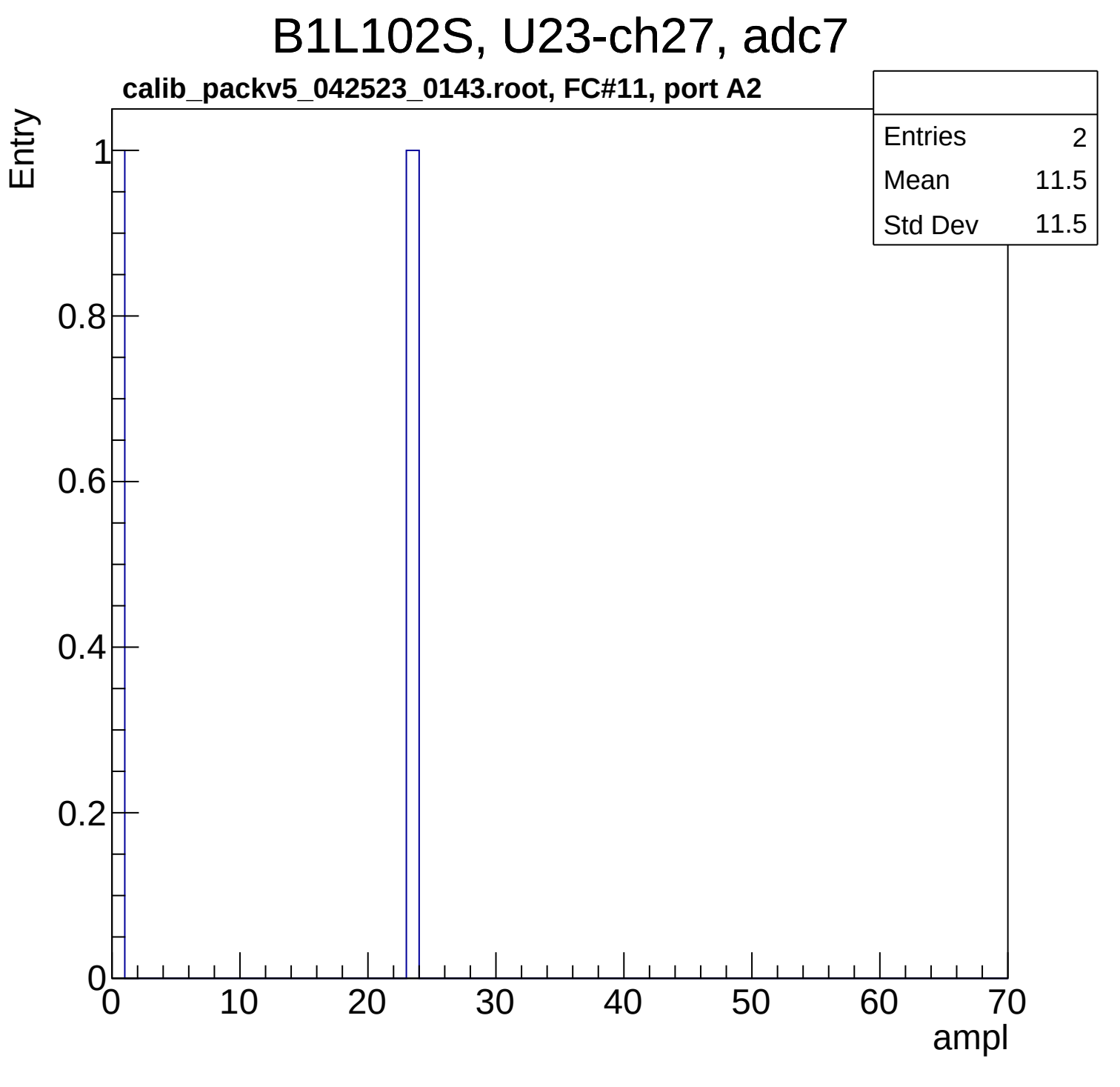

B1L102S, U23-ch27, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 45.23
Std Dev 3.634
Gaus mean : 45.6705
Gaus Width: 4.0531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

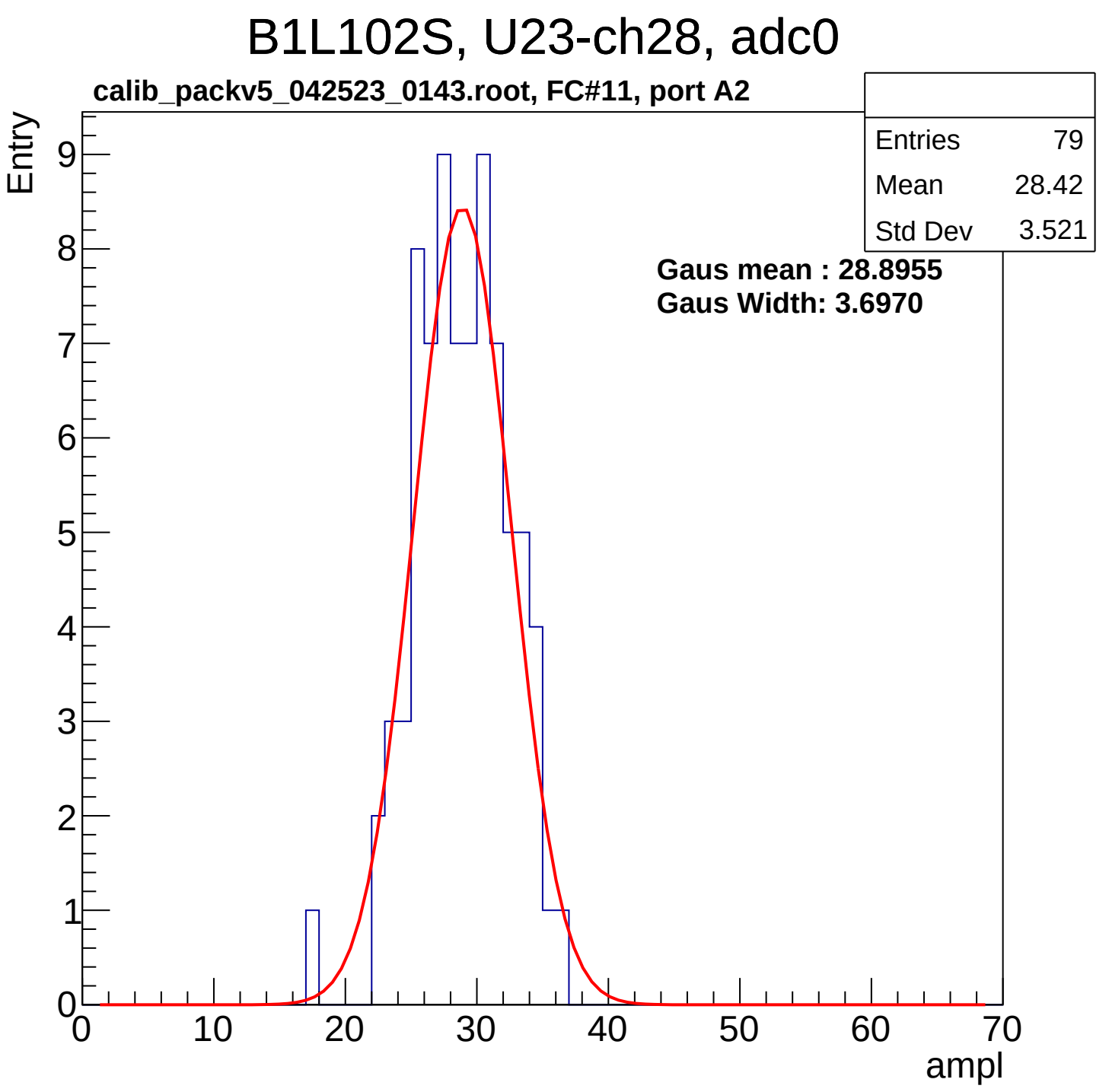

B1L102S, U23-ch27, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 52.15
Std Dev 3.963
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

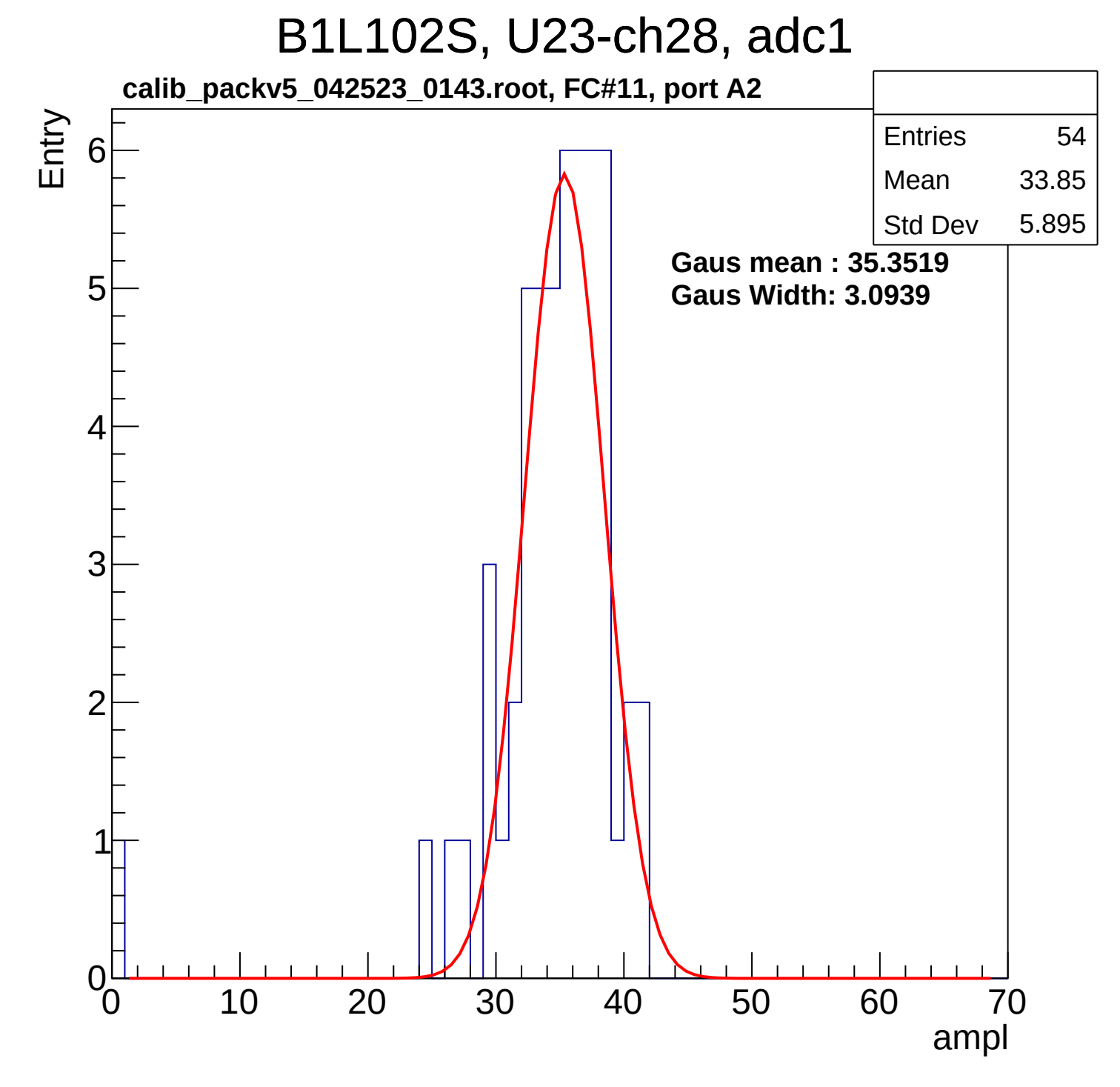

B1L102S, U23-ch27, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 57.02
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

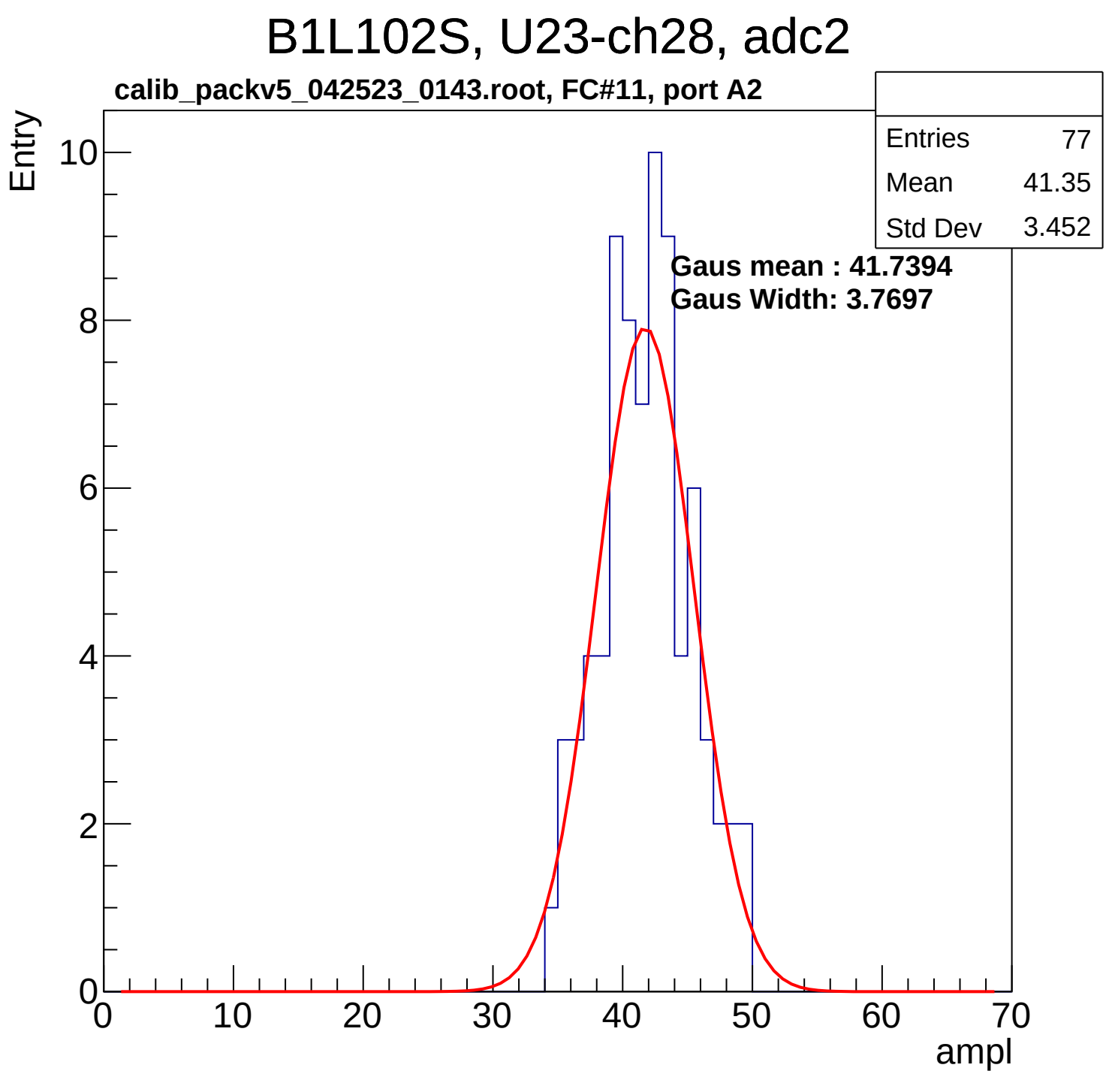

B1L102S, U23-ch27, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 36
Mean 59.44
Std Dev 10.2
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

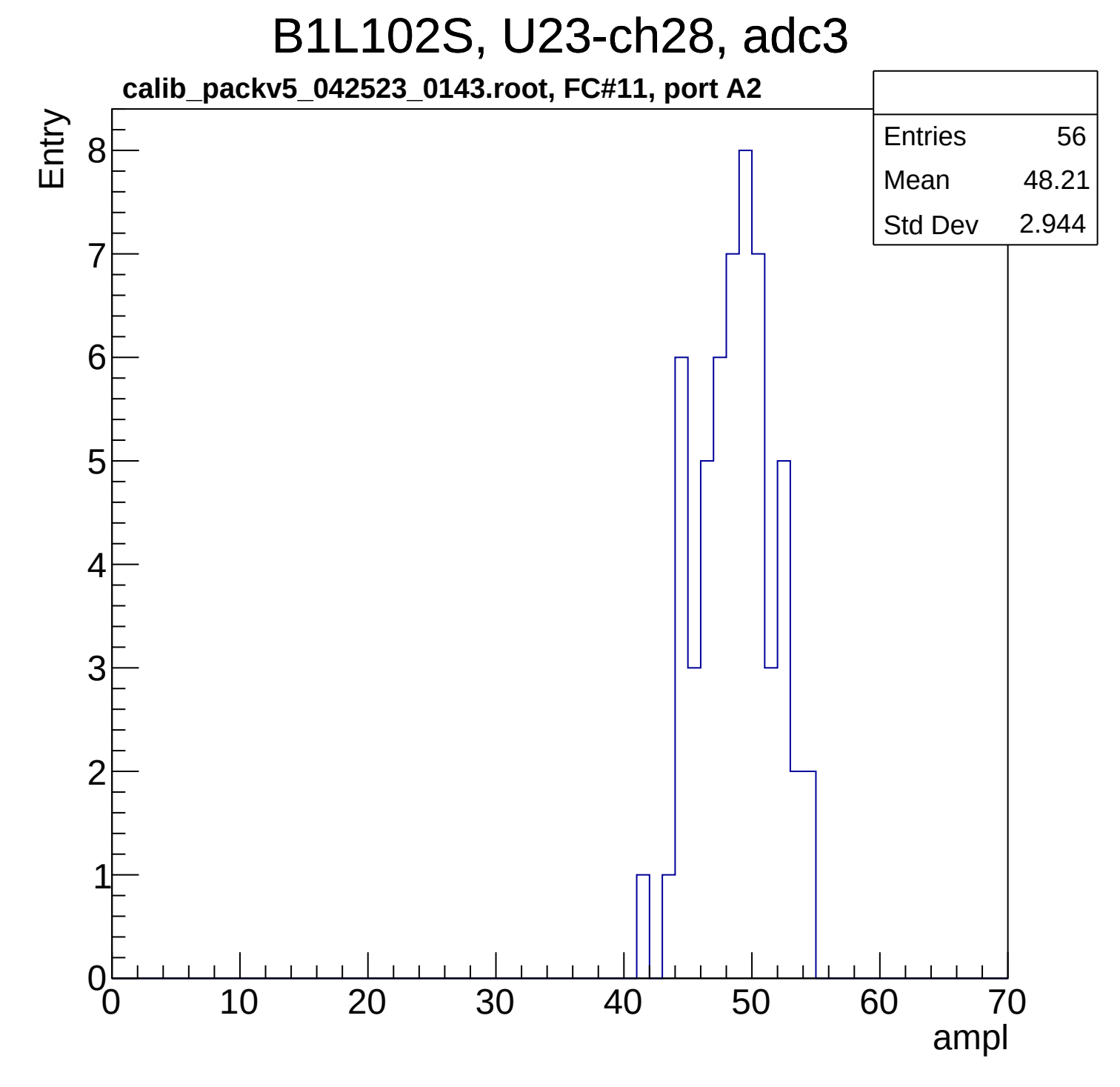

B1L102S, U23-ch27, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

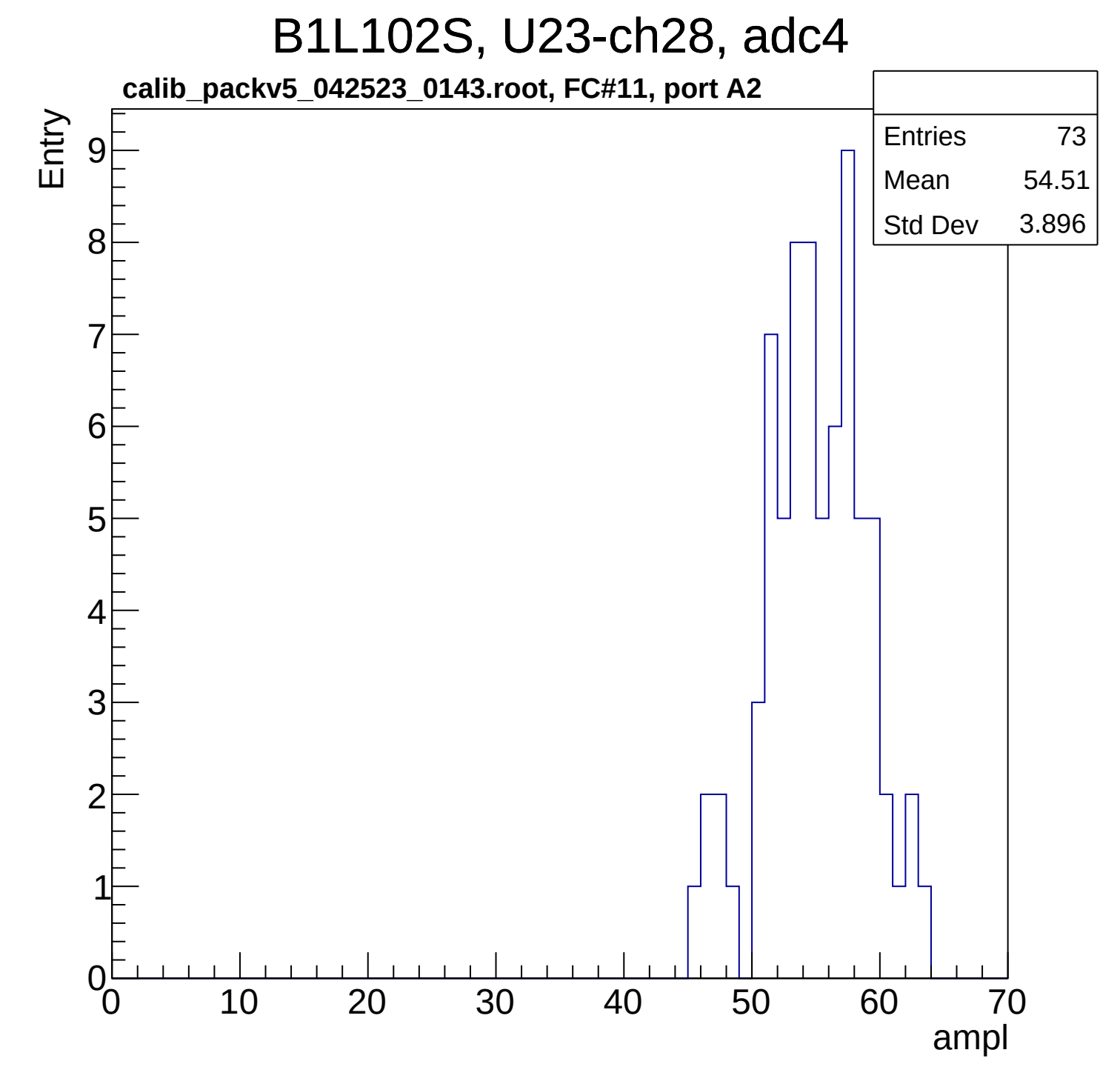

B1L102S, U23-ch27, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

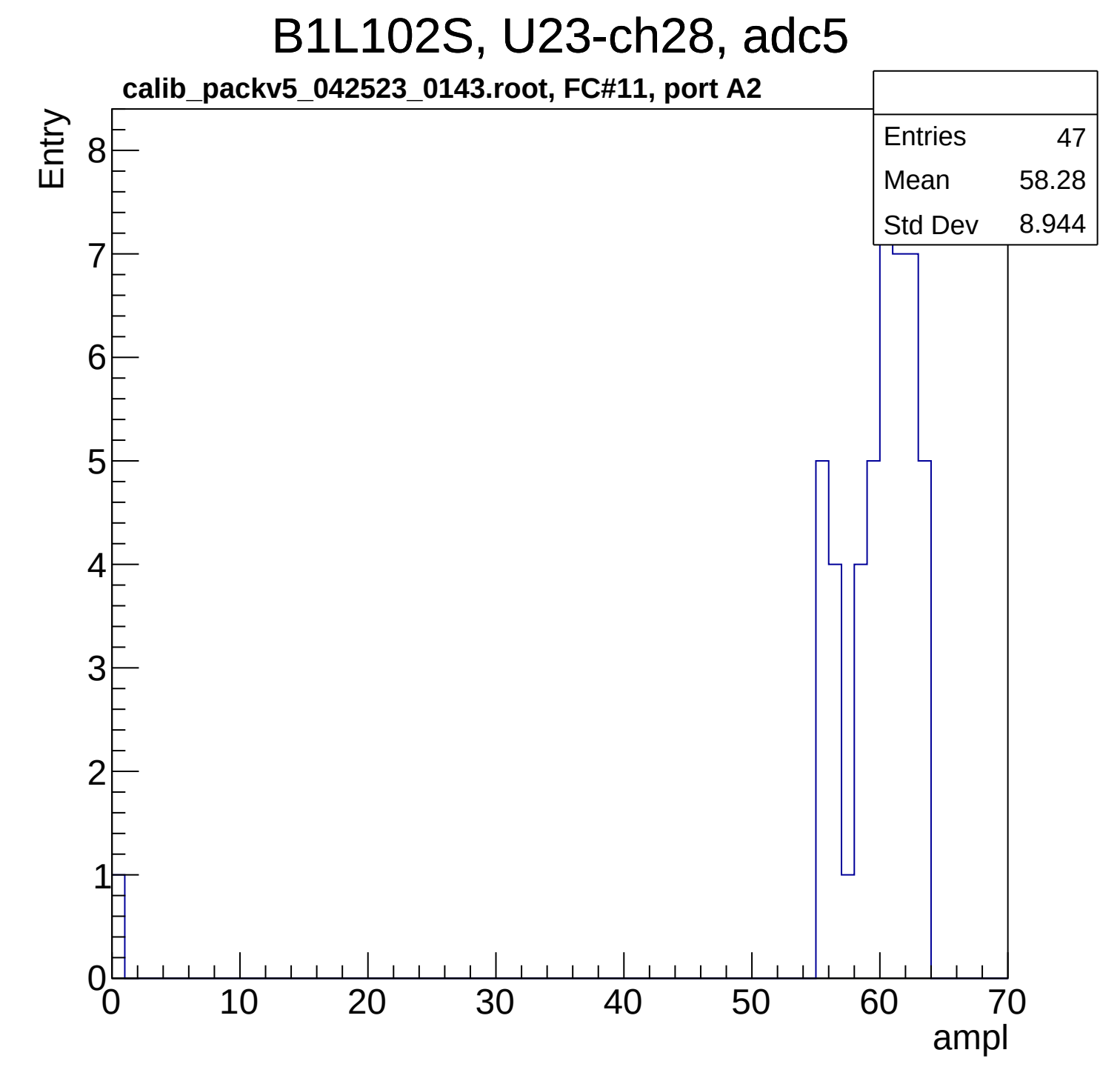

B1L102S, U23-ch28, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 28.42
Std Dev 3.521
Gaus mean : 28.8955
Gaus Width: 3.6970
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

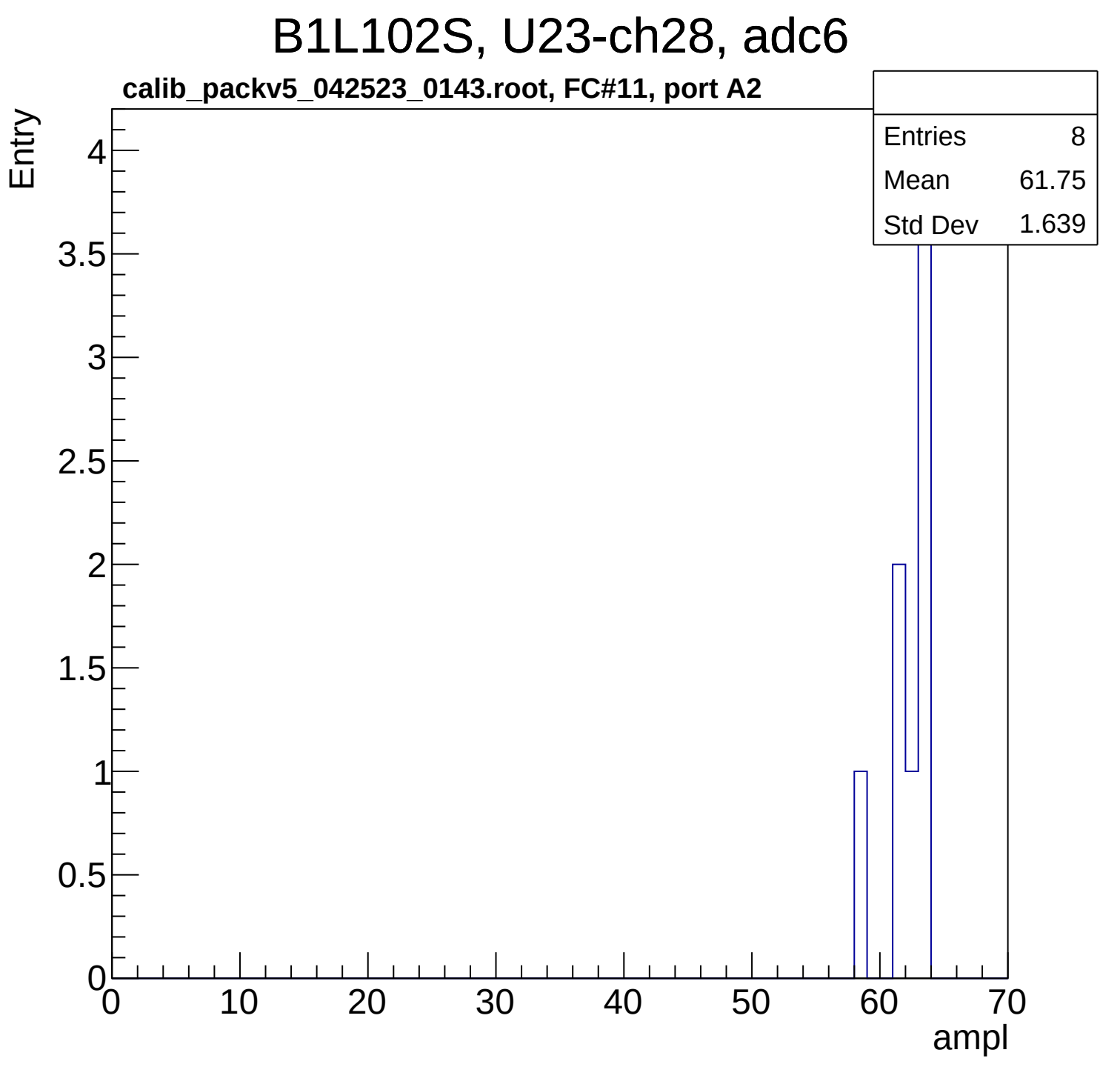

B1L102S, U23-ch28, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 33.85
Std Dev 5.895
Gaus mean : 35.3519
Gaus Width: 3.0939
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

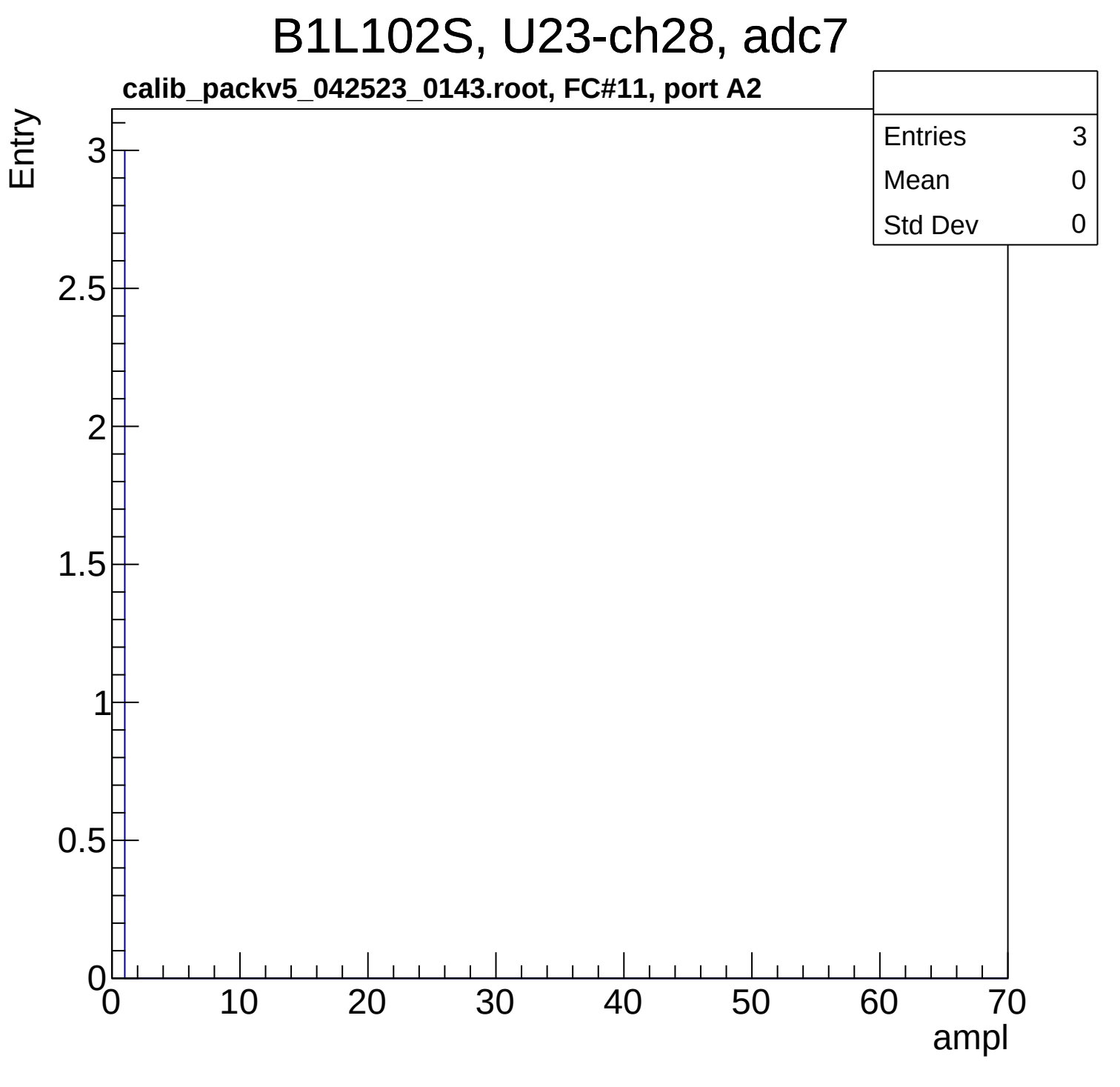

B1L102S, U23-ch28, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 41.35
Std Dev 3.452
Gaus mean : 41.7394
Gaus Width: 3.7697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

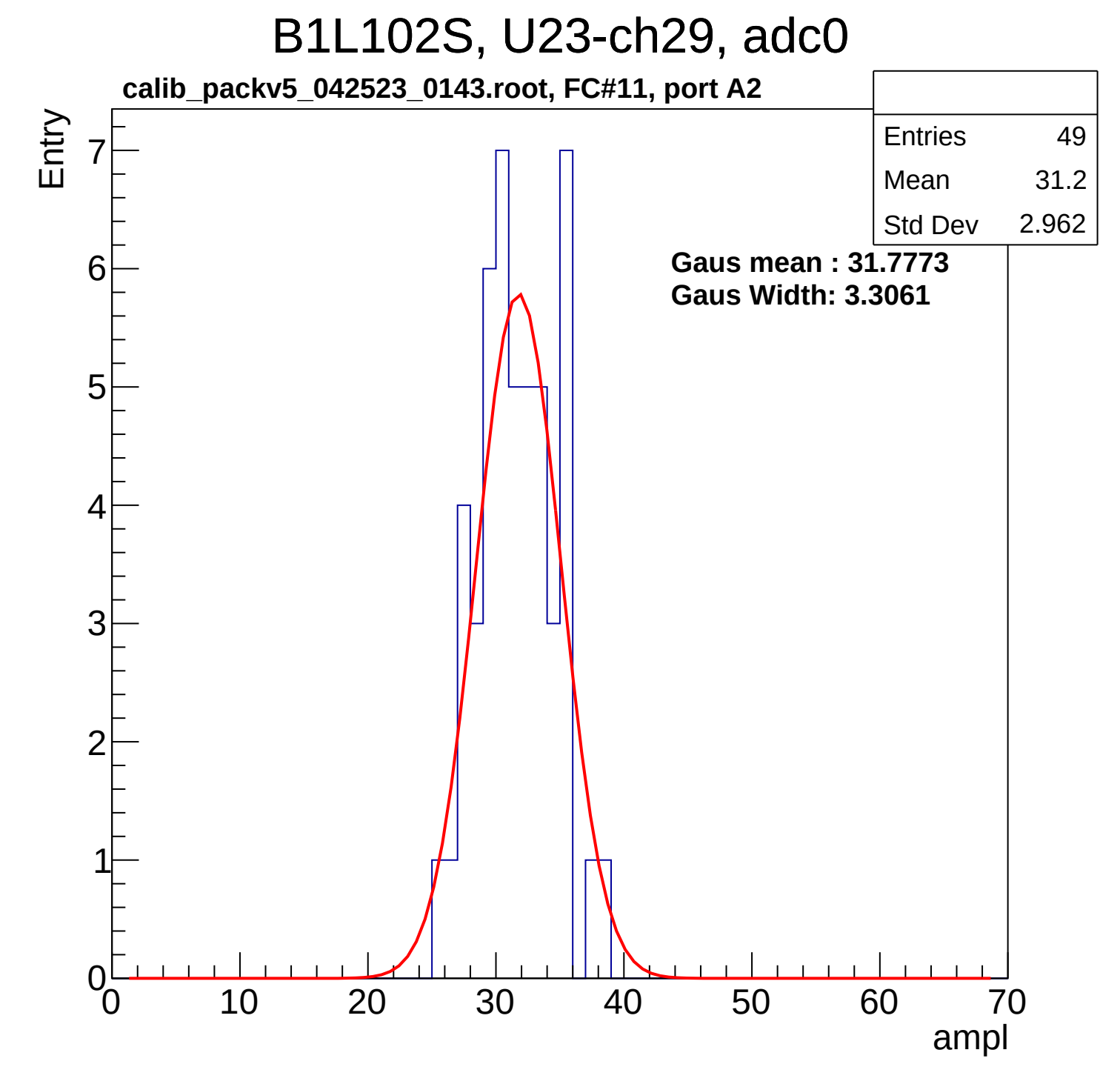

B1L102S, U23-ch28, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 48.21
Std Dev 2.944
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

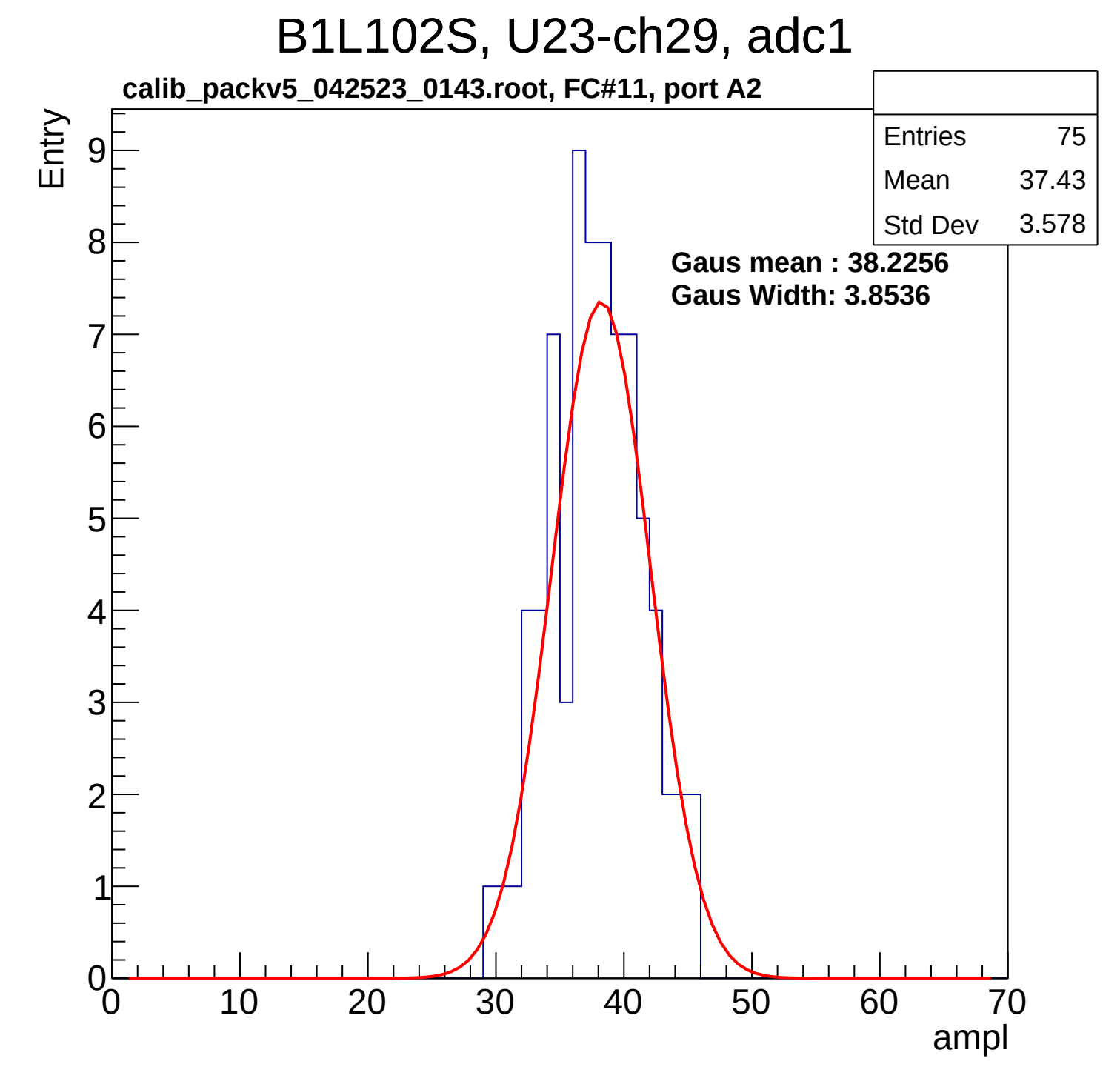

B1L102S, U23-ch28, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 54.51
Std Dev 3.896
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

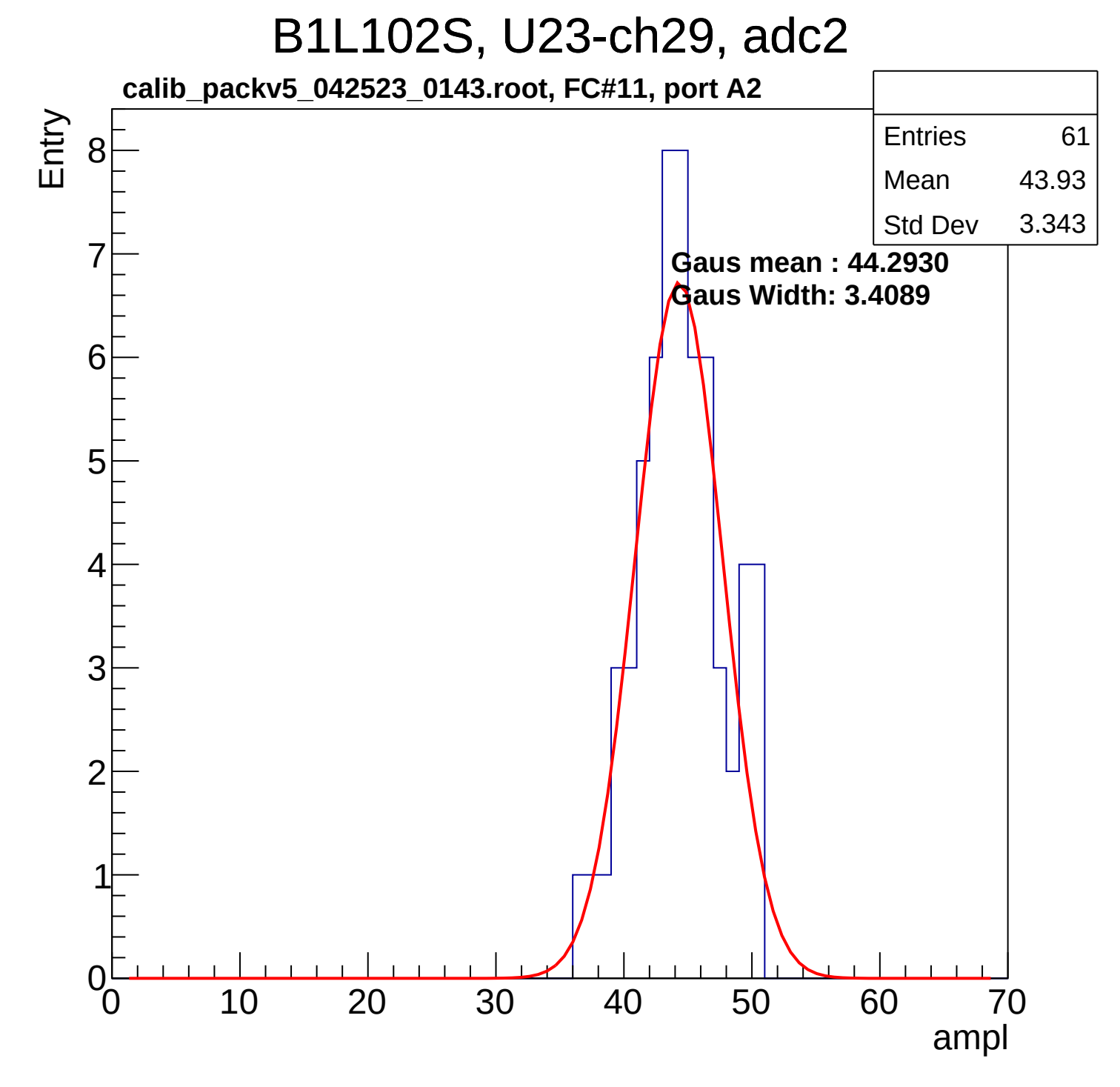

B1L102S, U23-ch28, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.28
Std Dev 8.944
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

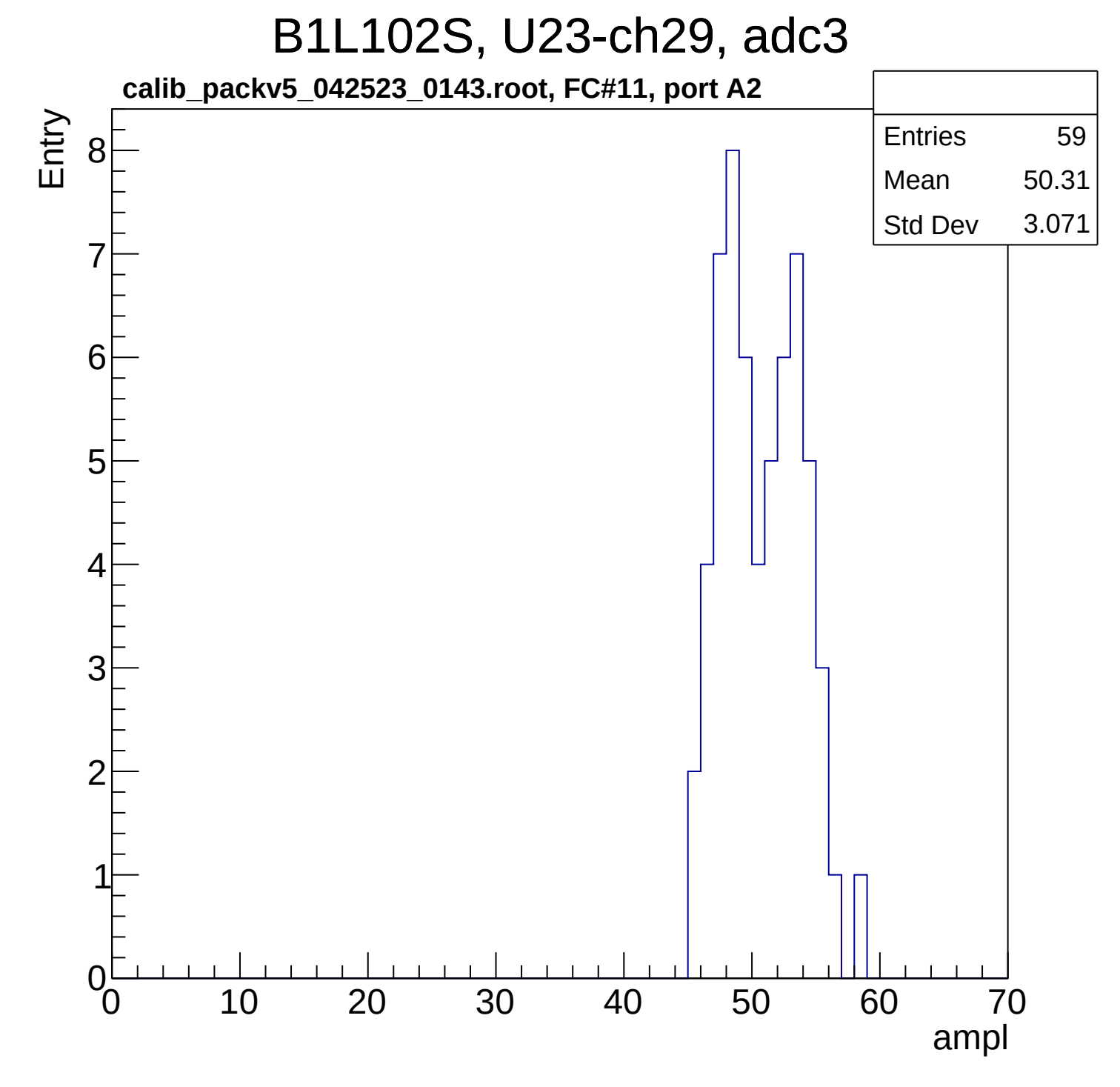

B1L102S, U23-ch28, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

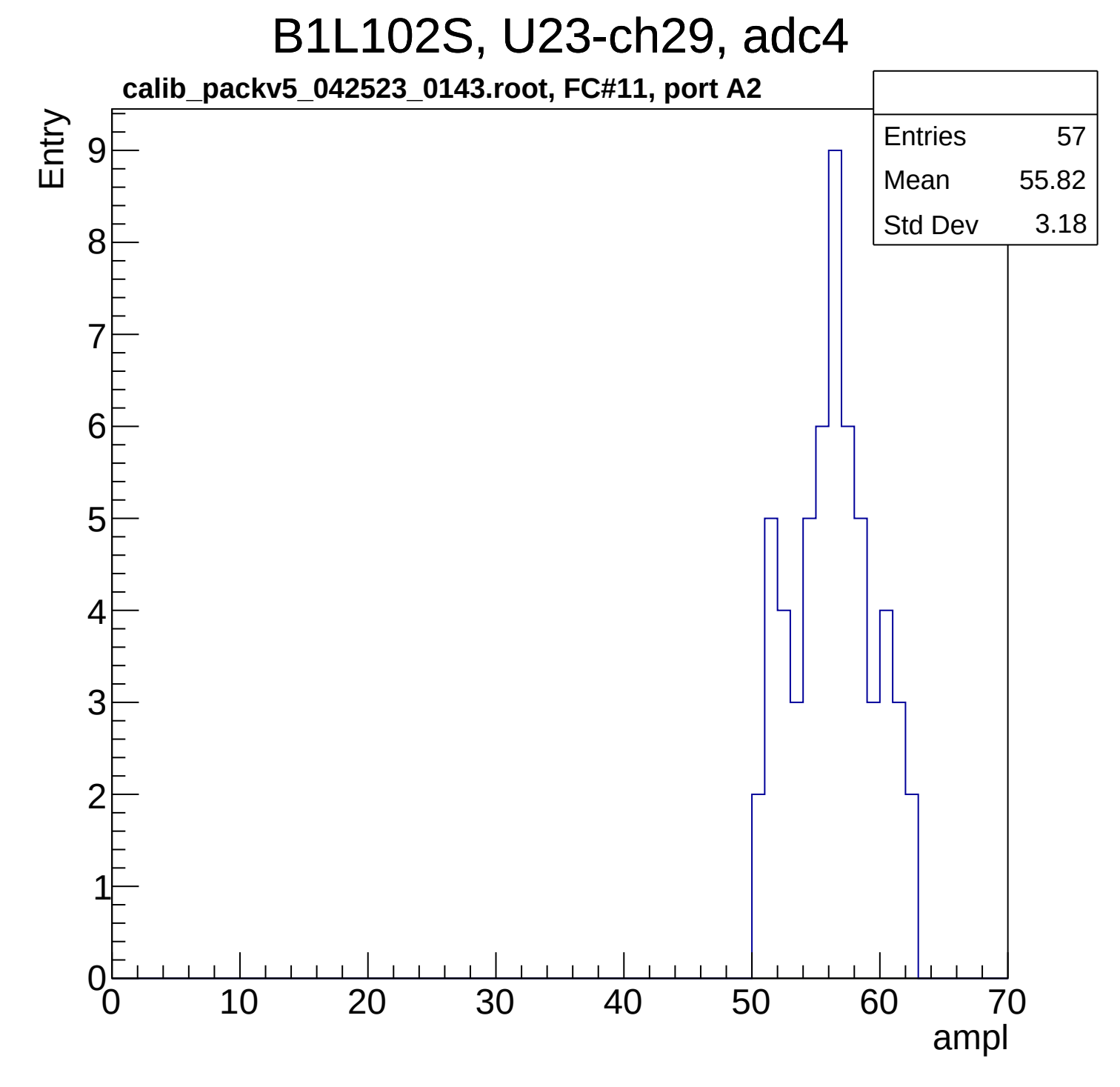

B1L102S, U23-ch28, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

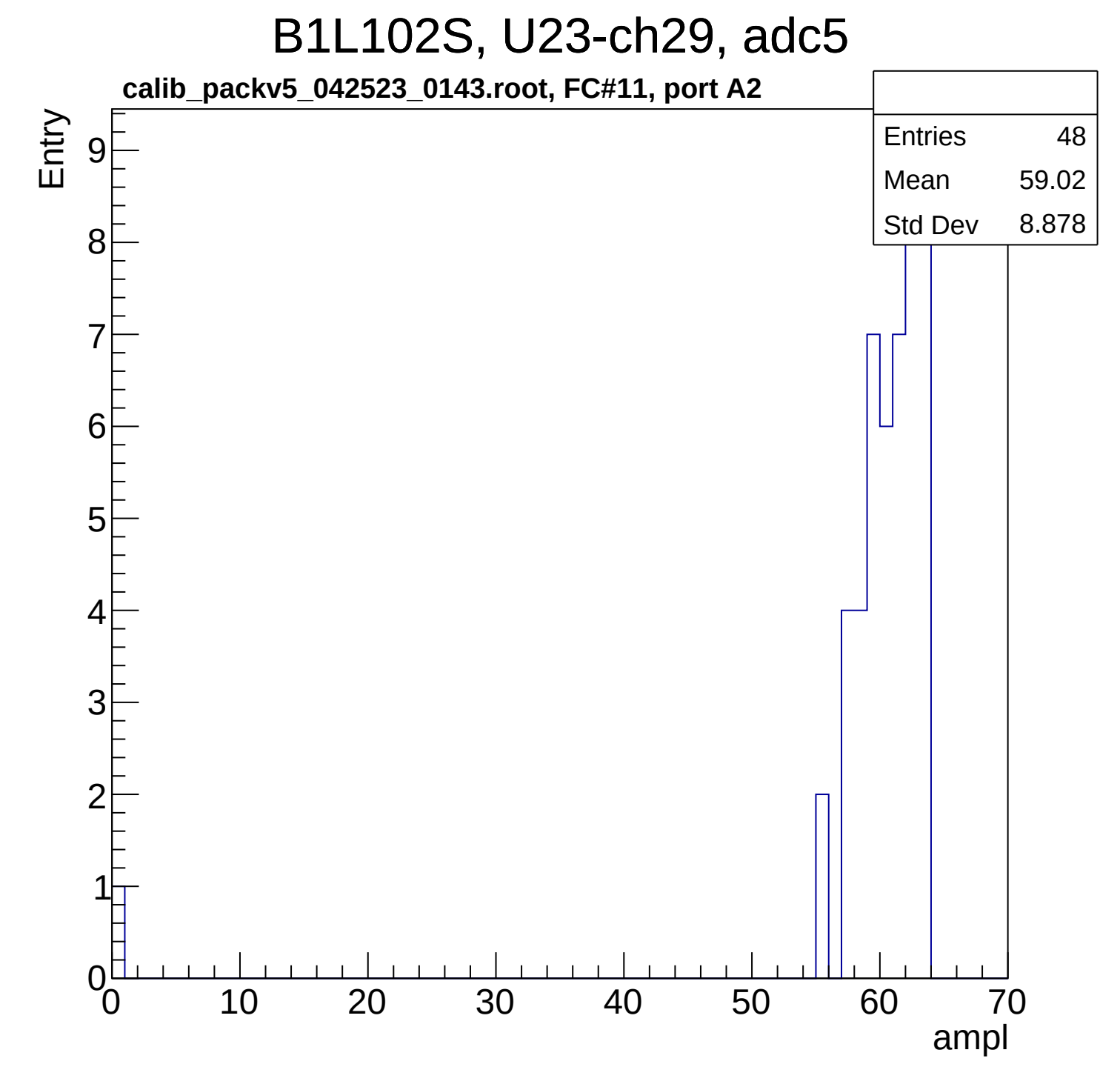

B1L102S, U23-ch29, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 31.2
Std Dev 2.962
Gaus mean : 31.7773
Gaus Width: 3.3061
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

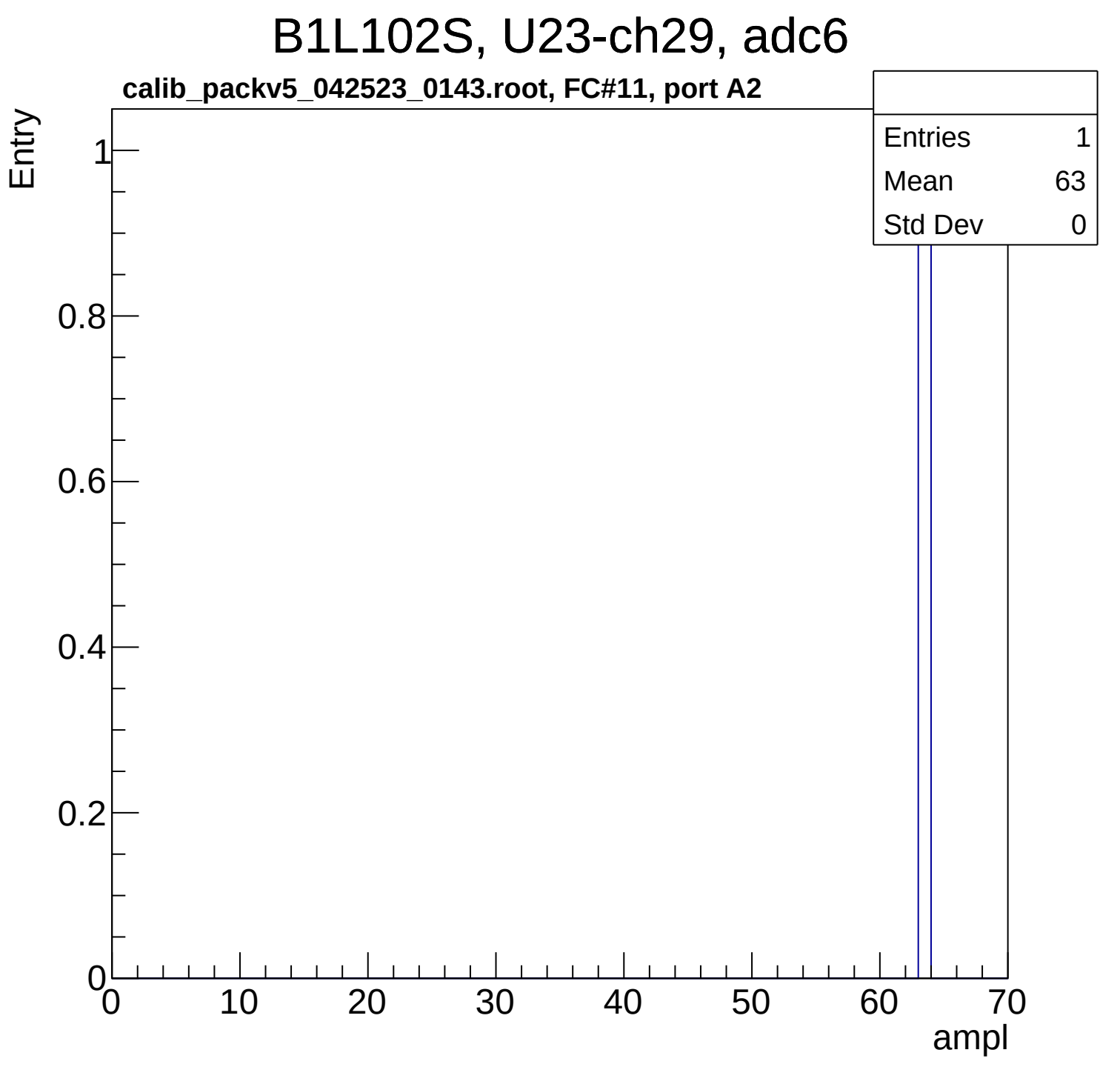

B1L102S, U23-ch29, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 37.43
Std Dev 3.578
Gaus mean : 38.2256
Gaus Width: 3.8536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

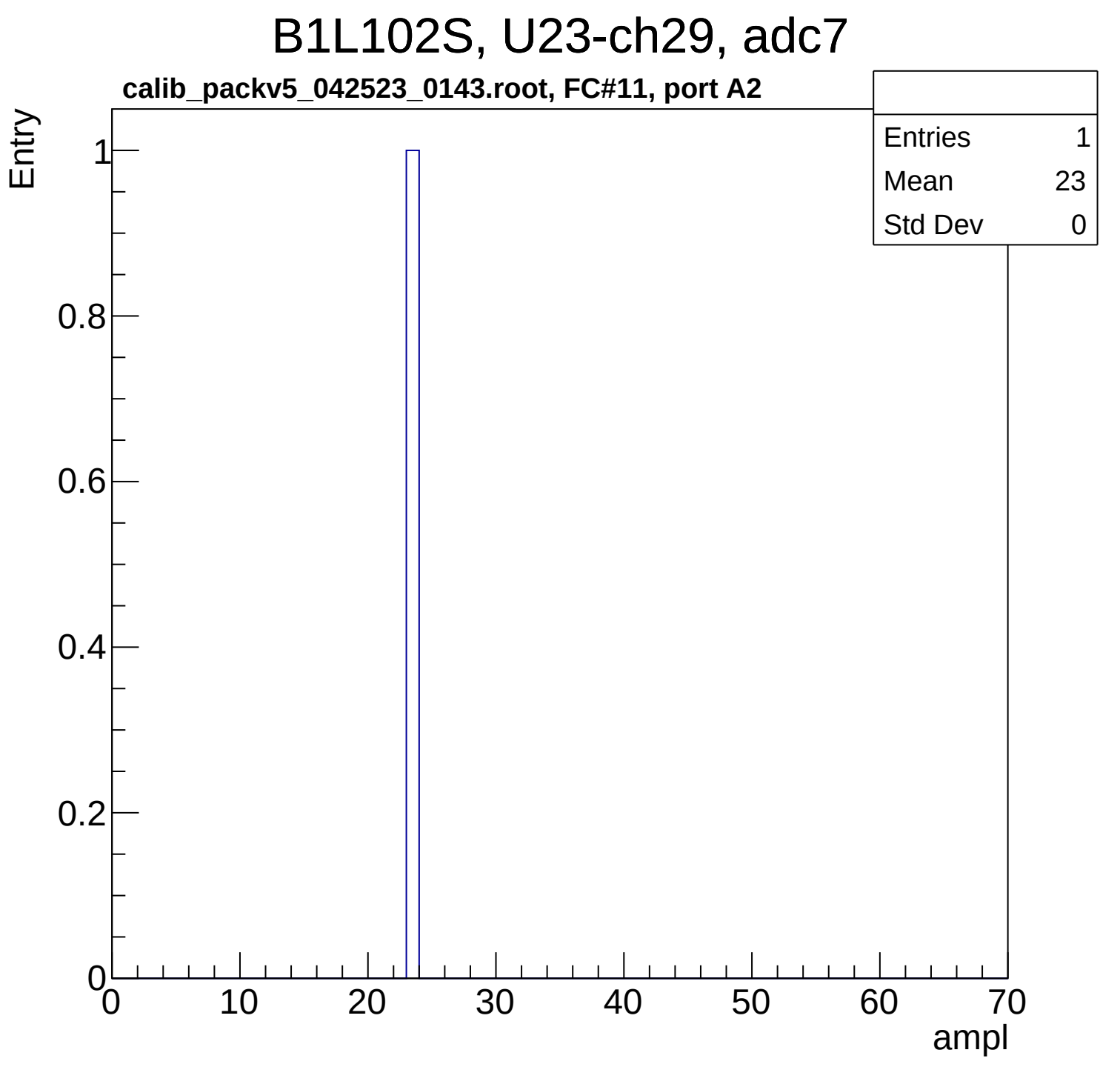

B1L102S, U23-ch29, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 43.93
Std Dev 3.343
Gaus mean : 44.2930
Gaus Width: 3.4089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

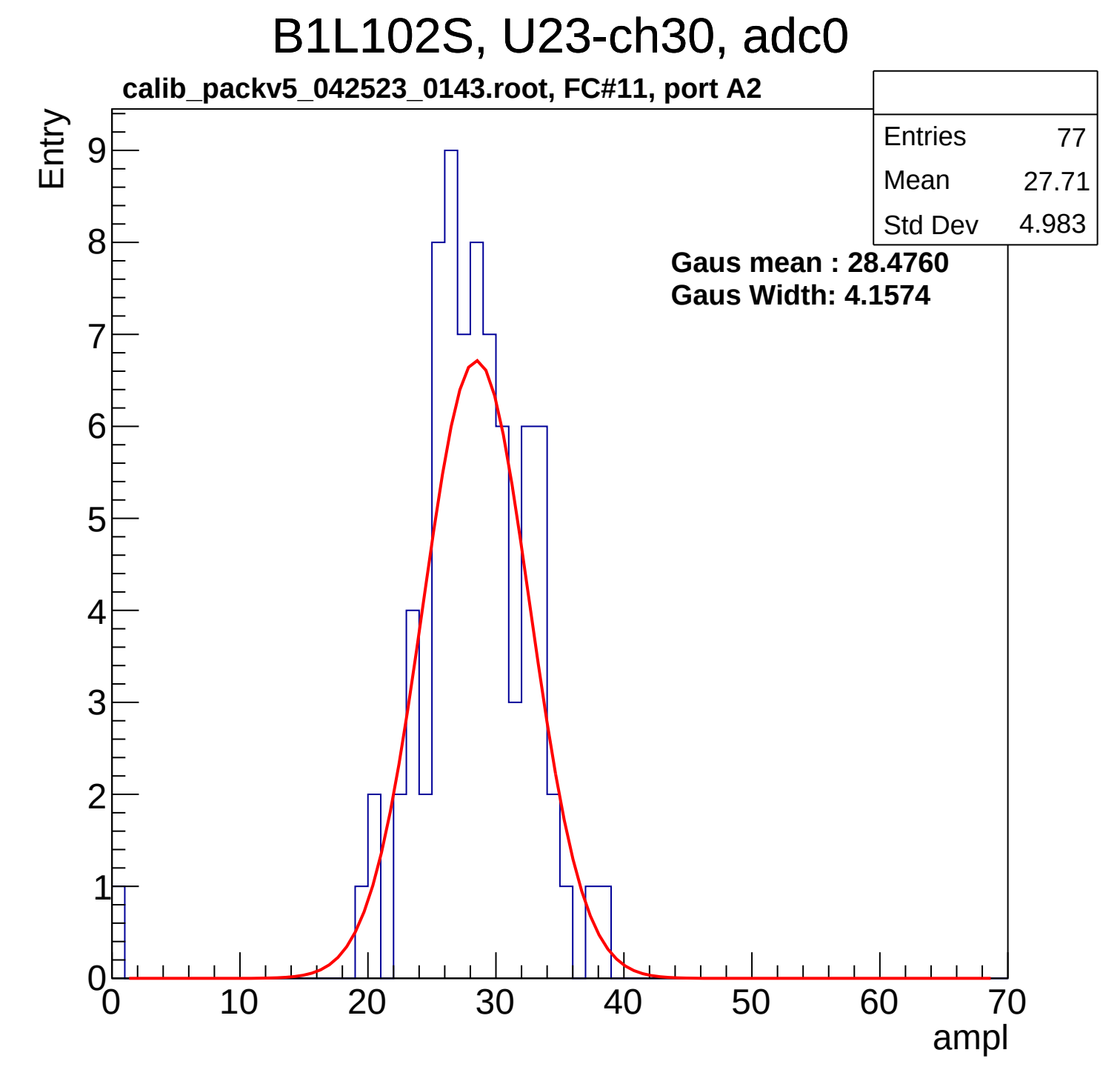

B1L102S, U23-ch29, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 50.31
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

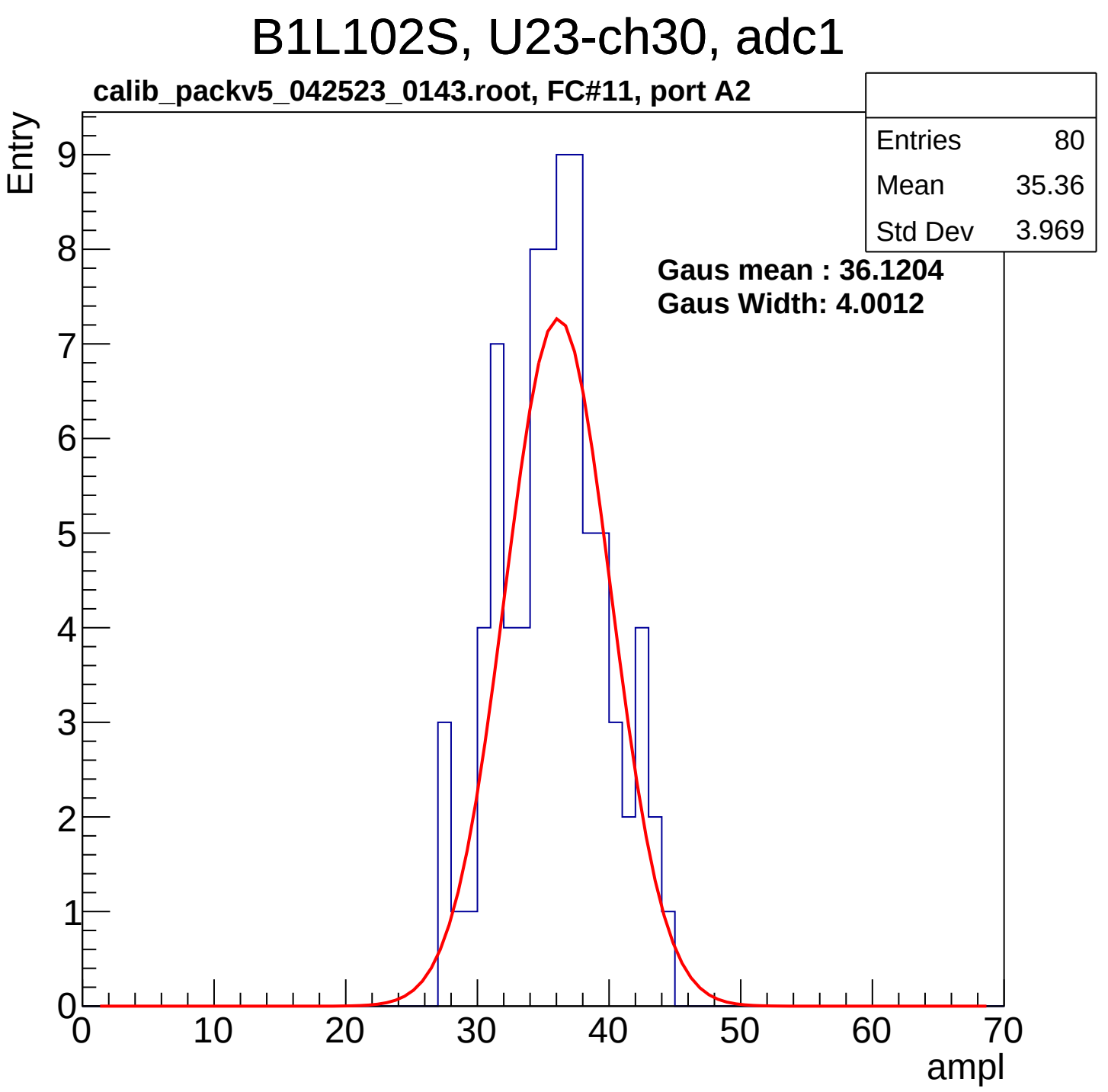

B1L102S, U23-ch29, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.82
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

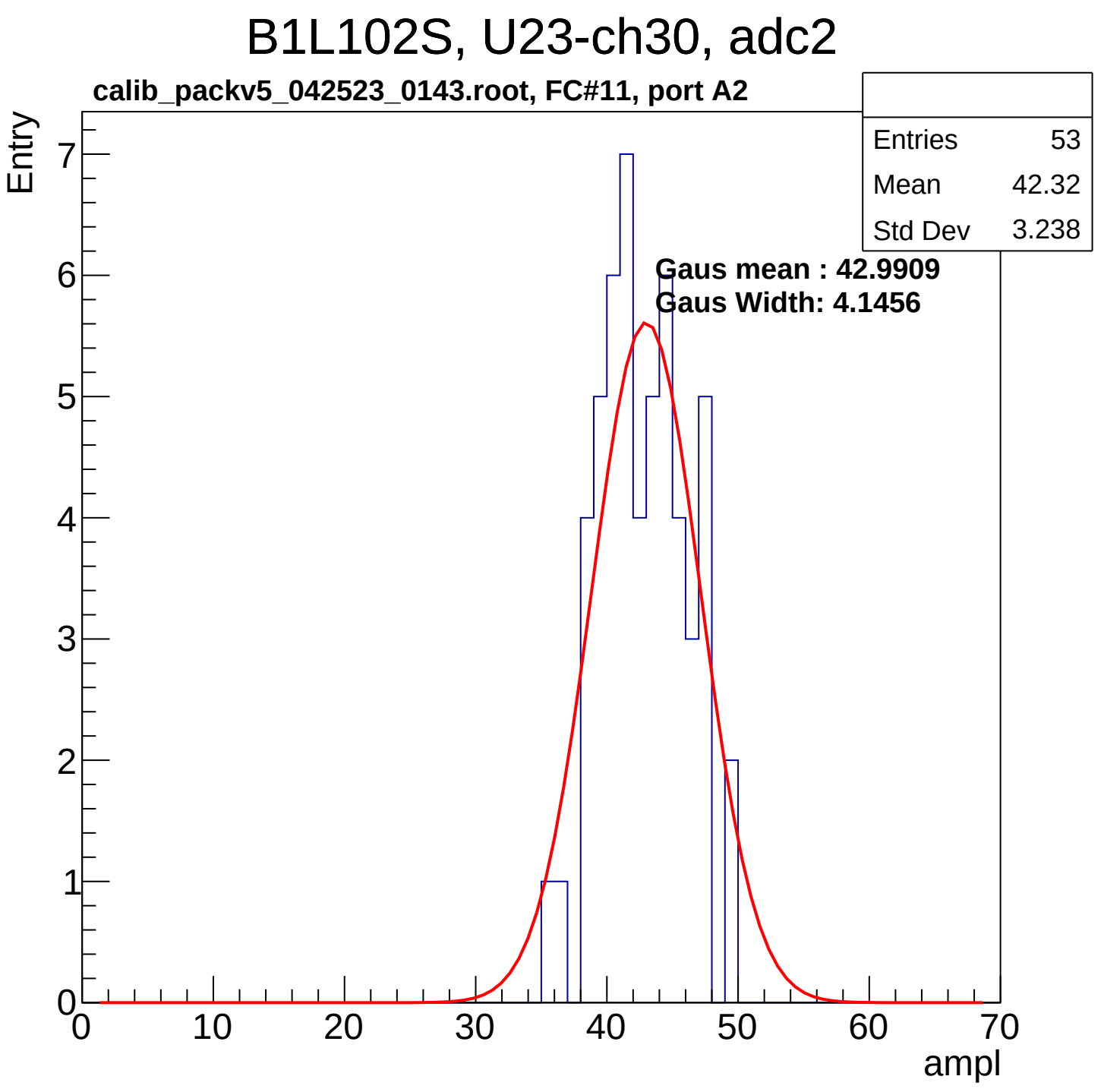

B1L102S, U23-ch29, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 59.02
Std Dev 8.878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

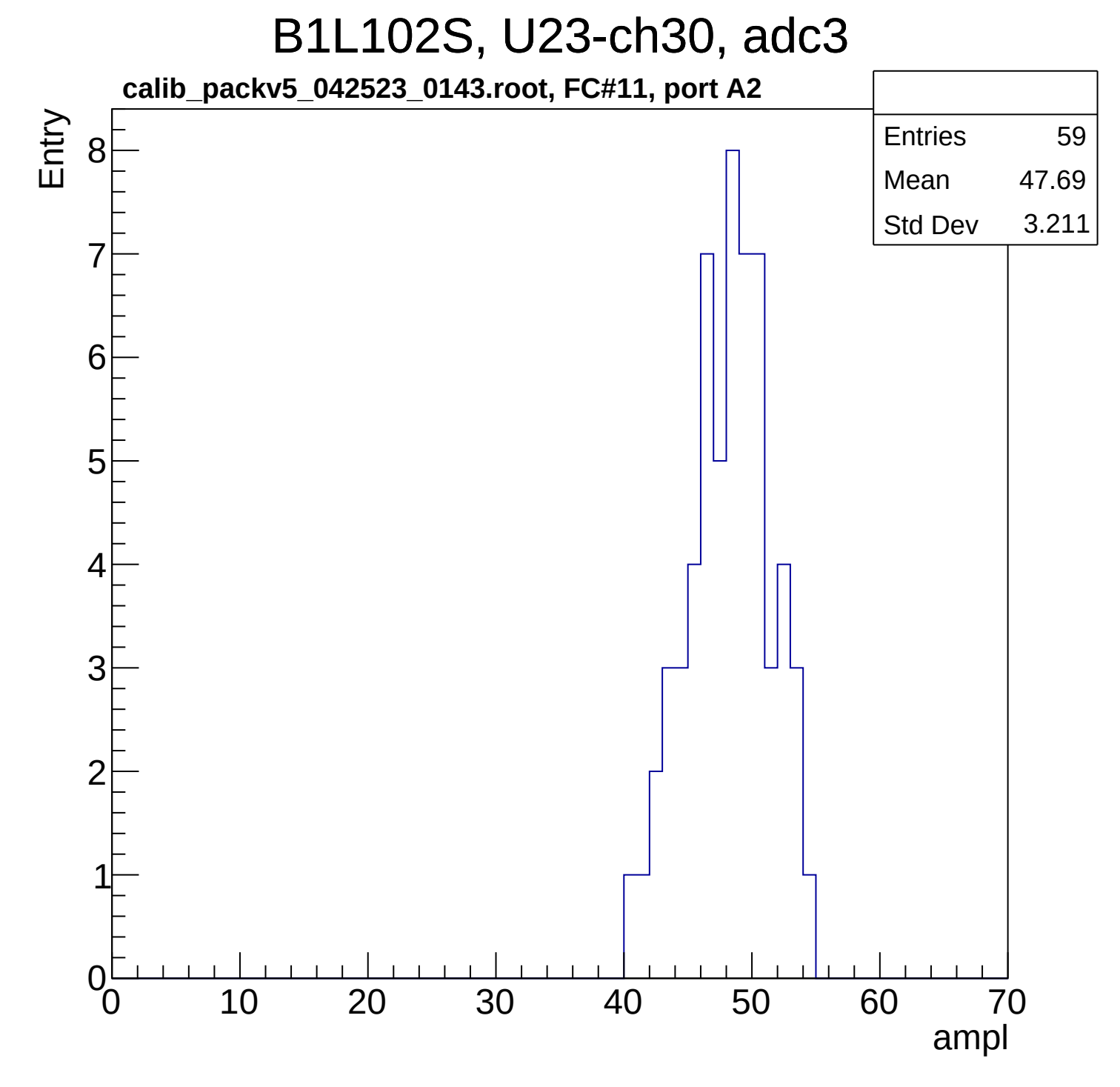

B1L102S, U23-ch29, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

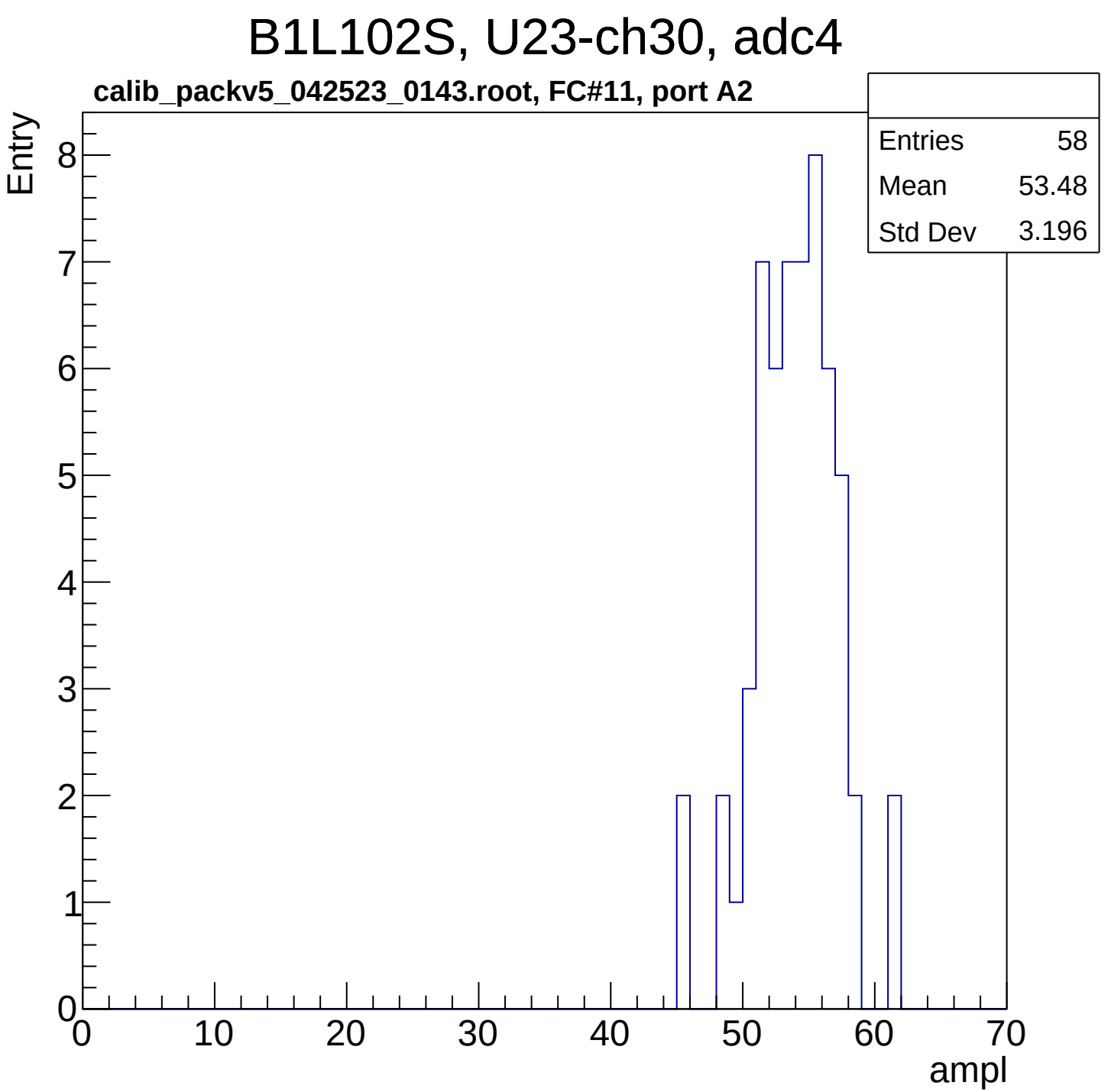

B1L102S, U23-ch29, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

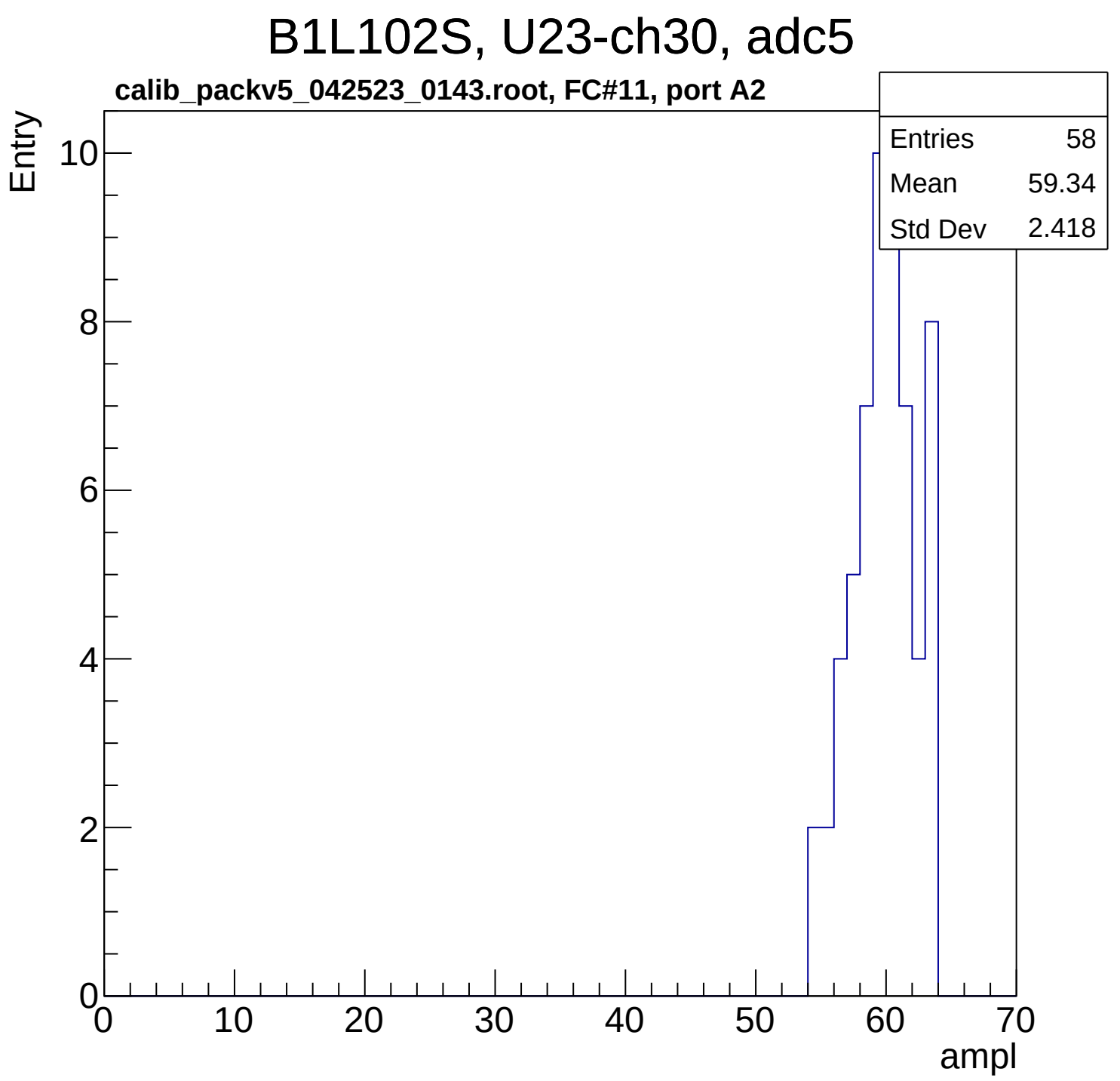

B1L102S, U23-ch30, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 27.71
Std Dev 4.983
Gaus mean : 28.4760
Gaus Width: 4.1574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

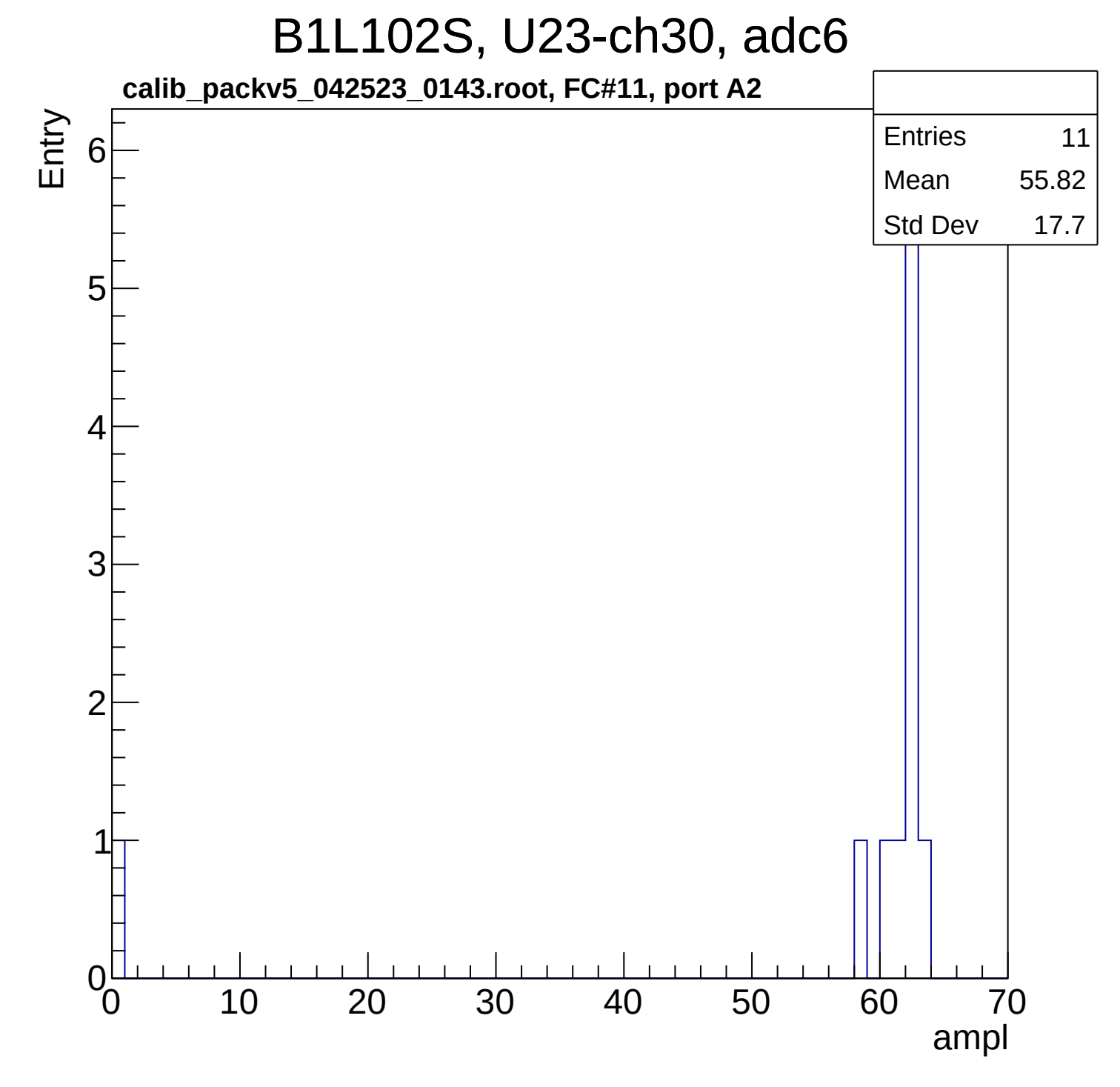

B1L102S, U23-ch30, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 35.36
Std Dev 3.969
Gaus mean : 36.1204
Gaus Width: 4.0012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

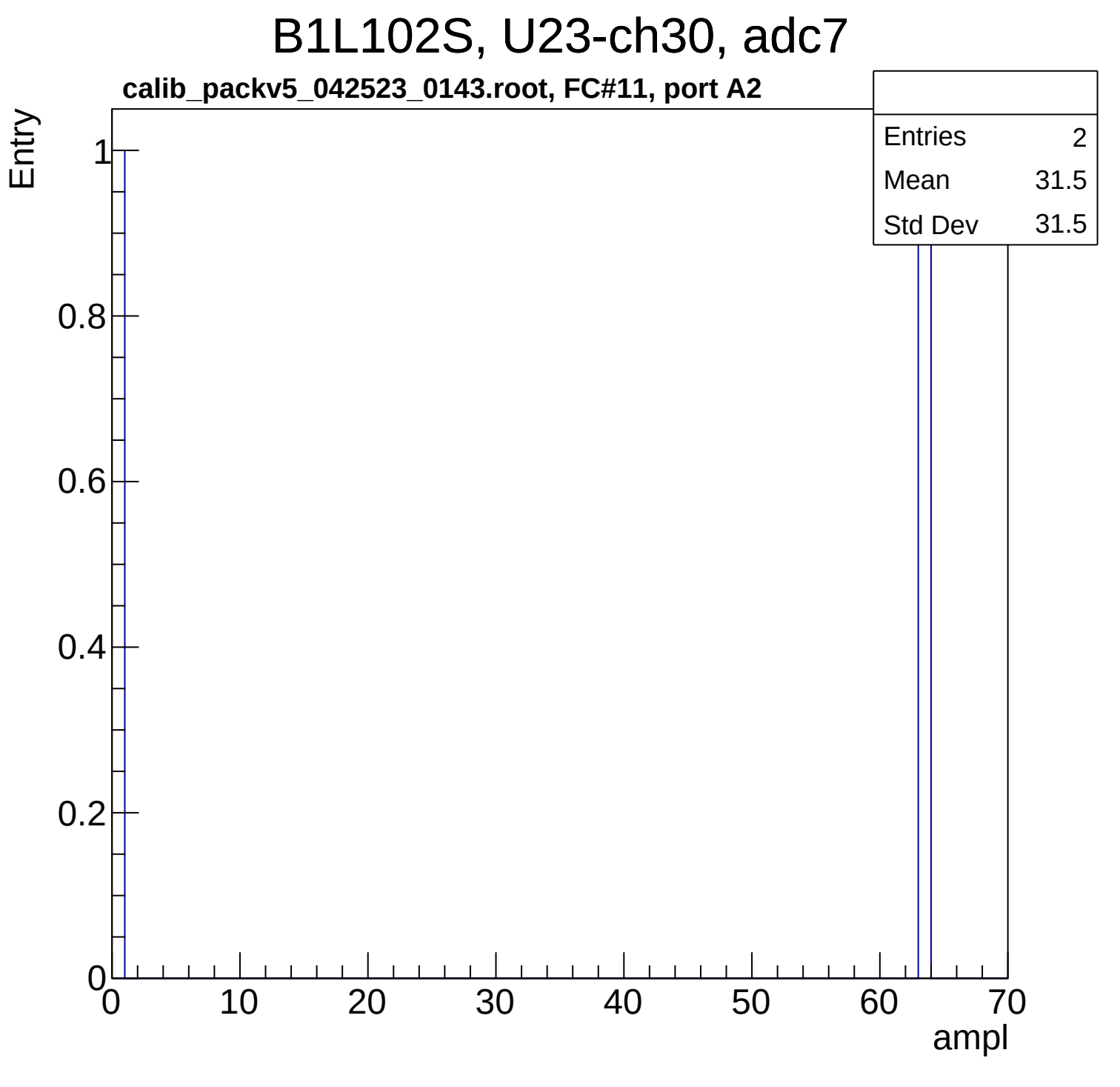

B1L102S, U23-ch30, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 42.32
Std Dev 3.238
Gaus mean : 42.9909
Gaus Width: 4.1456
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

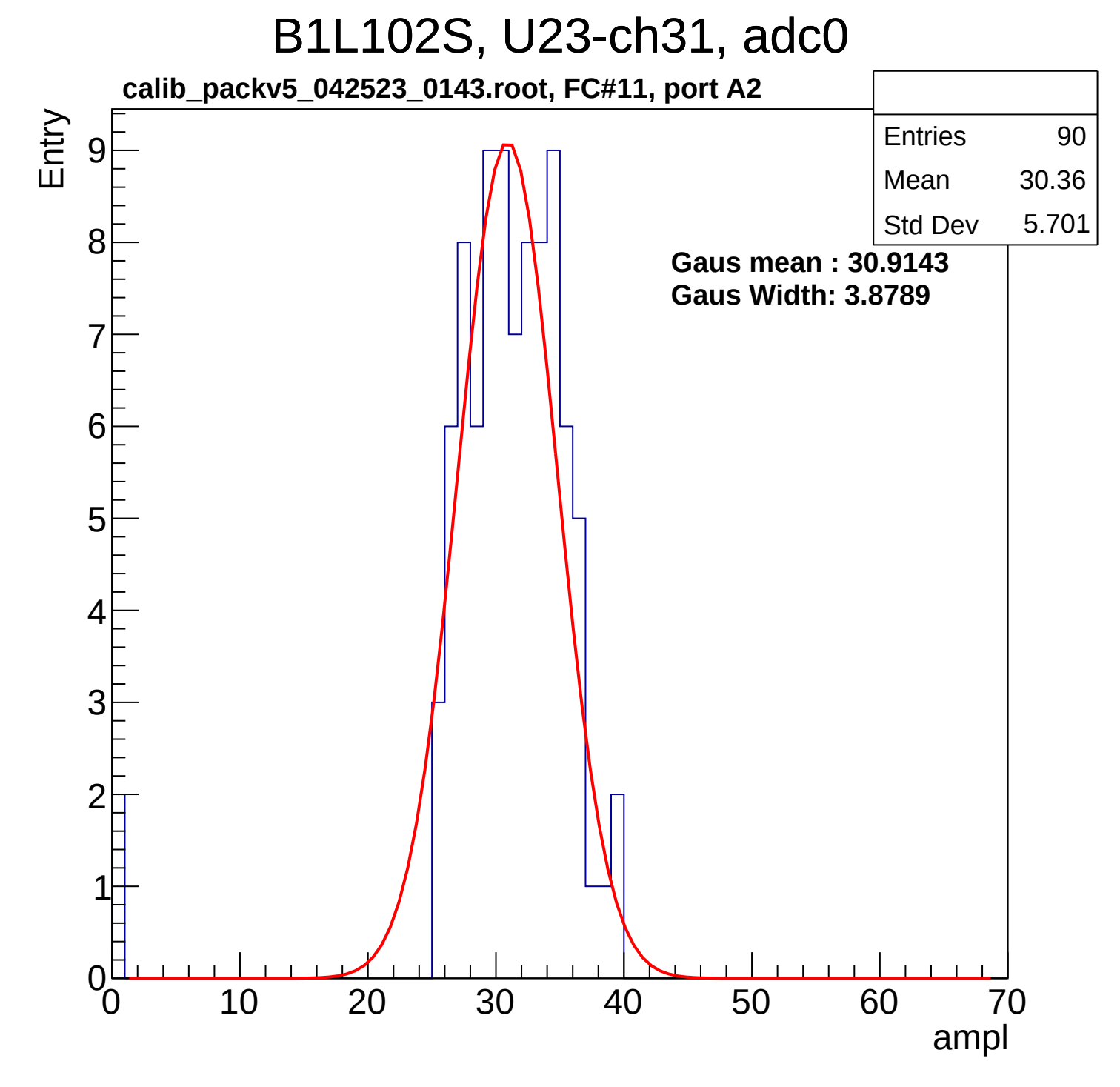

B1L102S, U23-ch30, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 47.69
Std Dev 3.211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

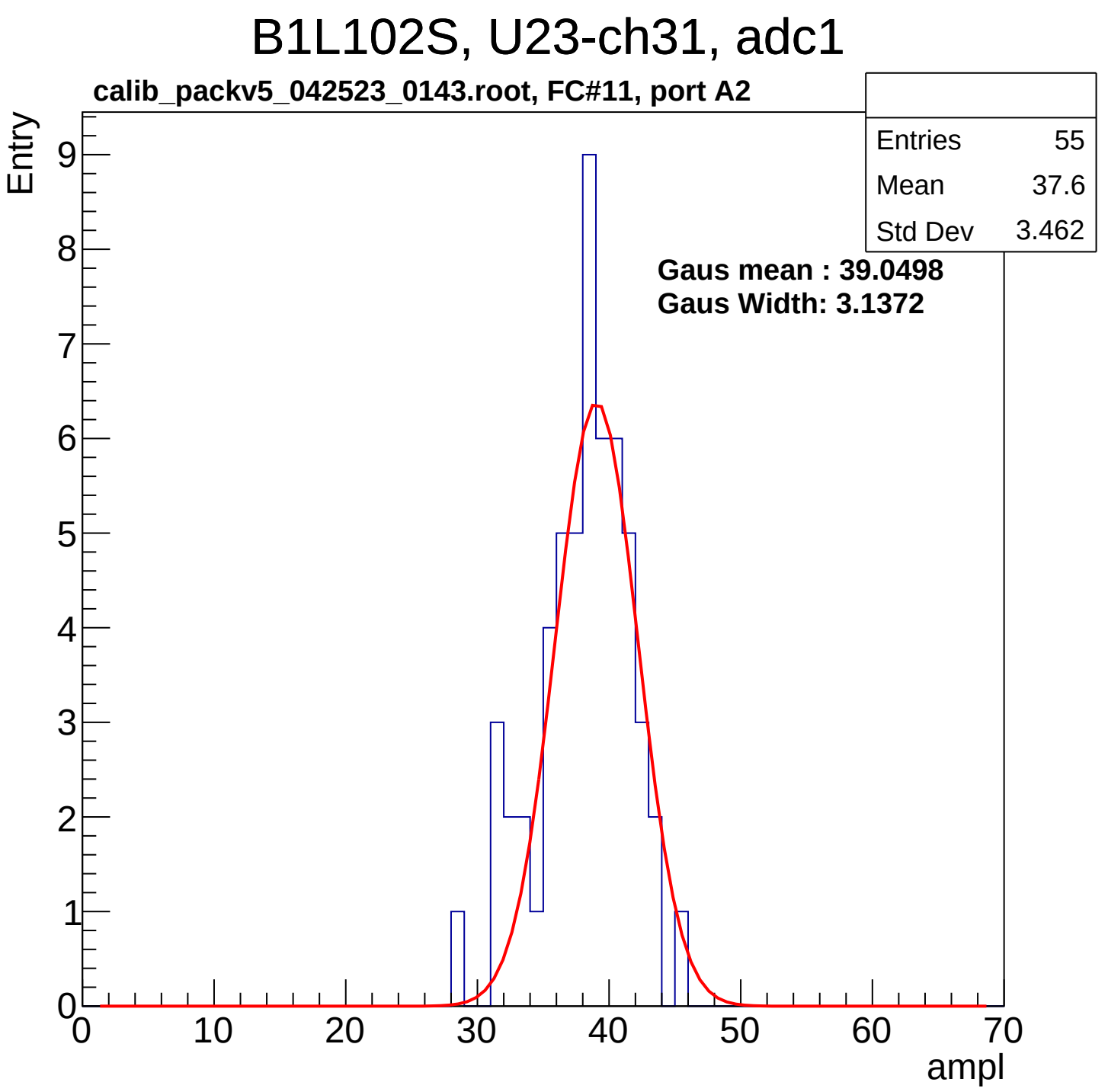

B1L102S, U23-ch30, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 53.48
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

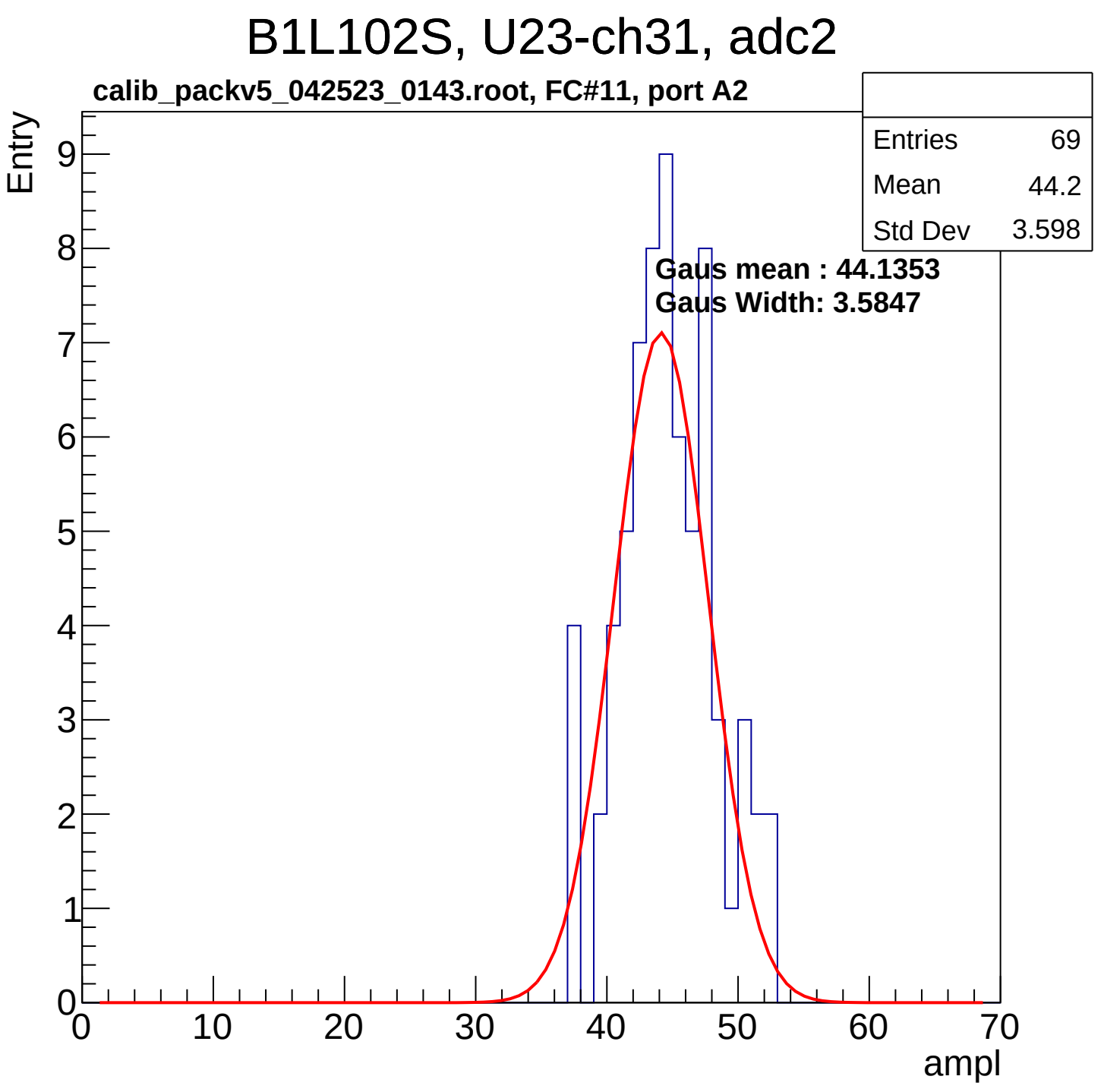

B1L102S, U23-ch30, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 59.34
Std Dev 2.418
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

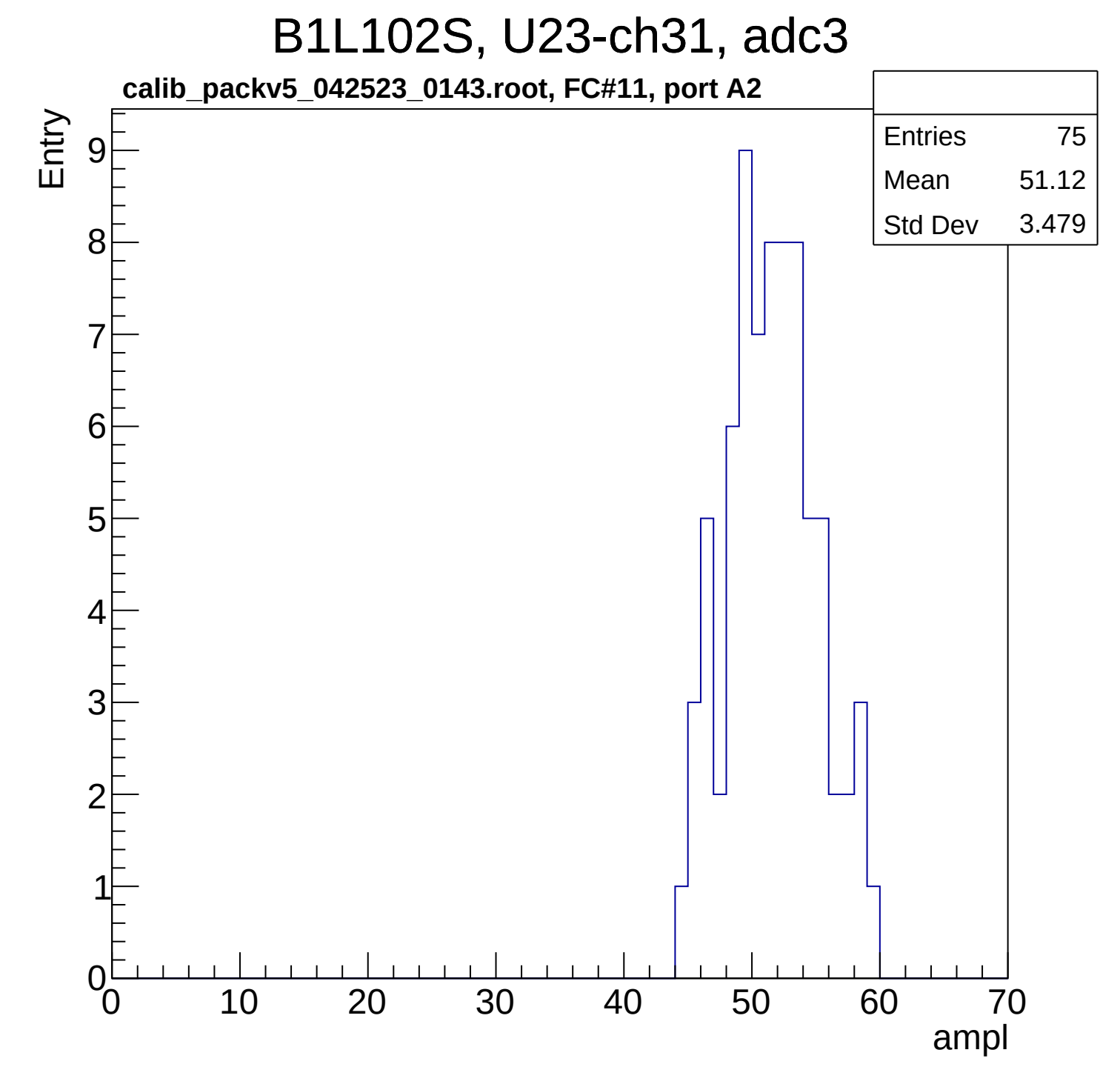

B1L102S, U23-ch30, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 55.82
Std Dev 17.7
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

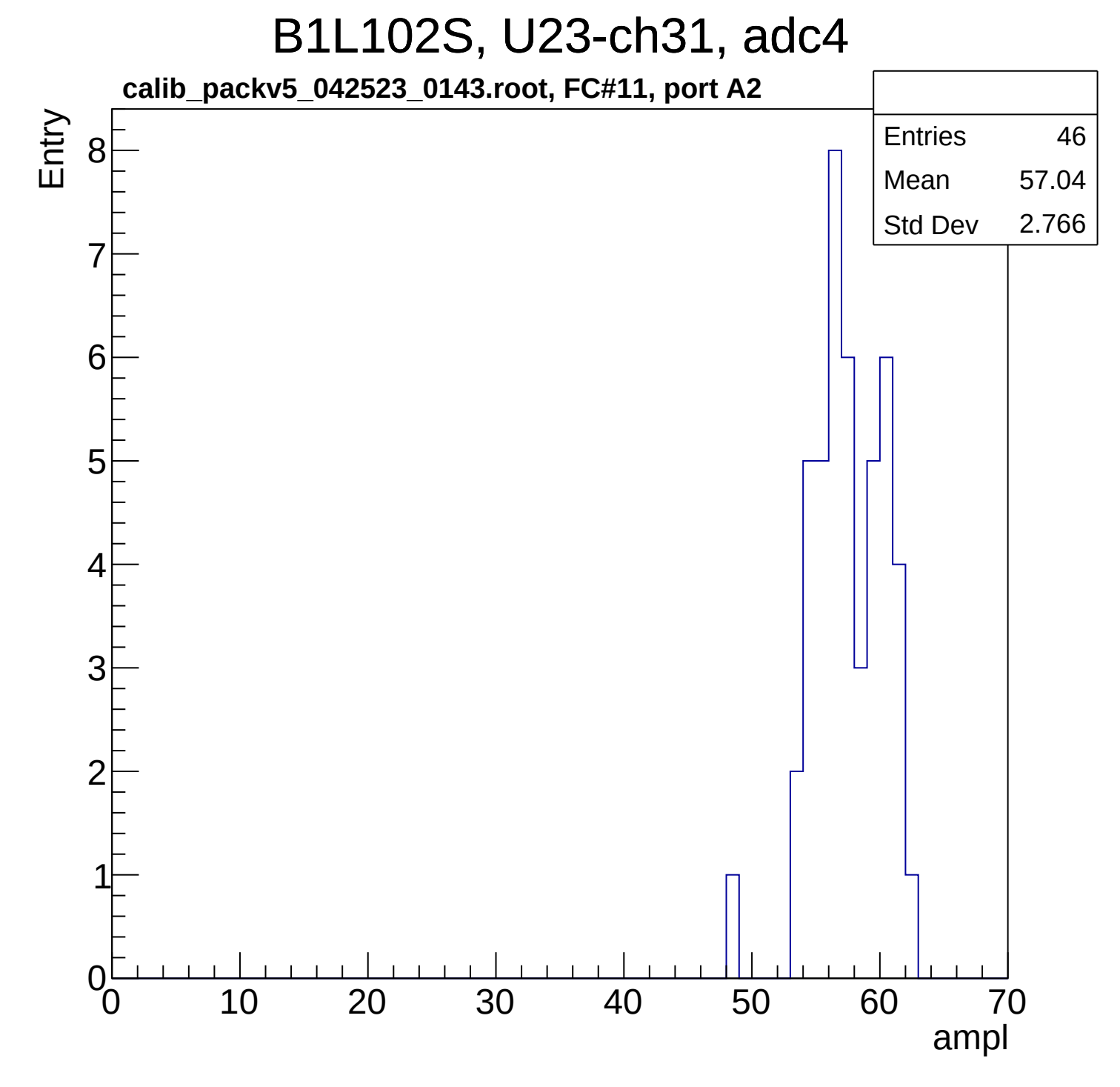

B1L102S, U23-ch30, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

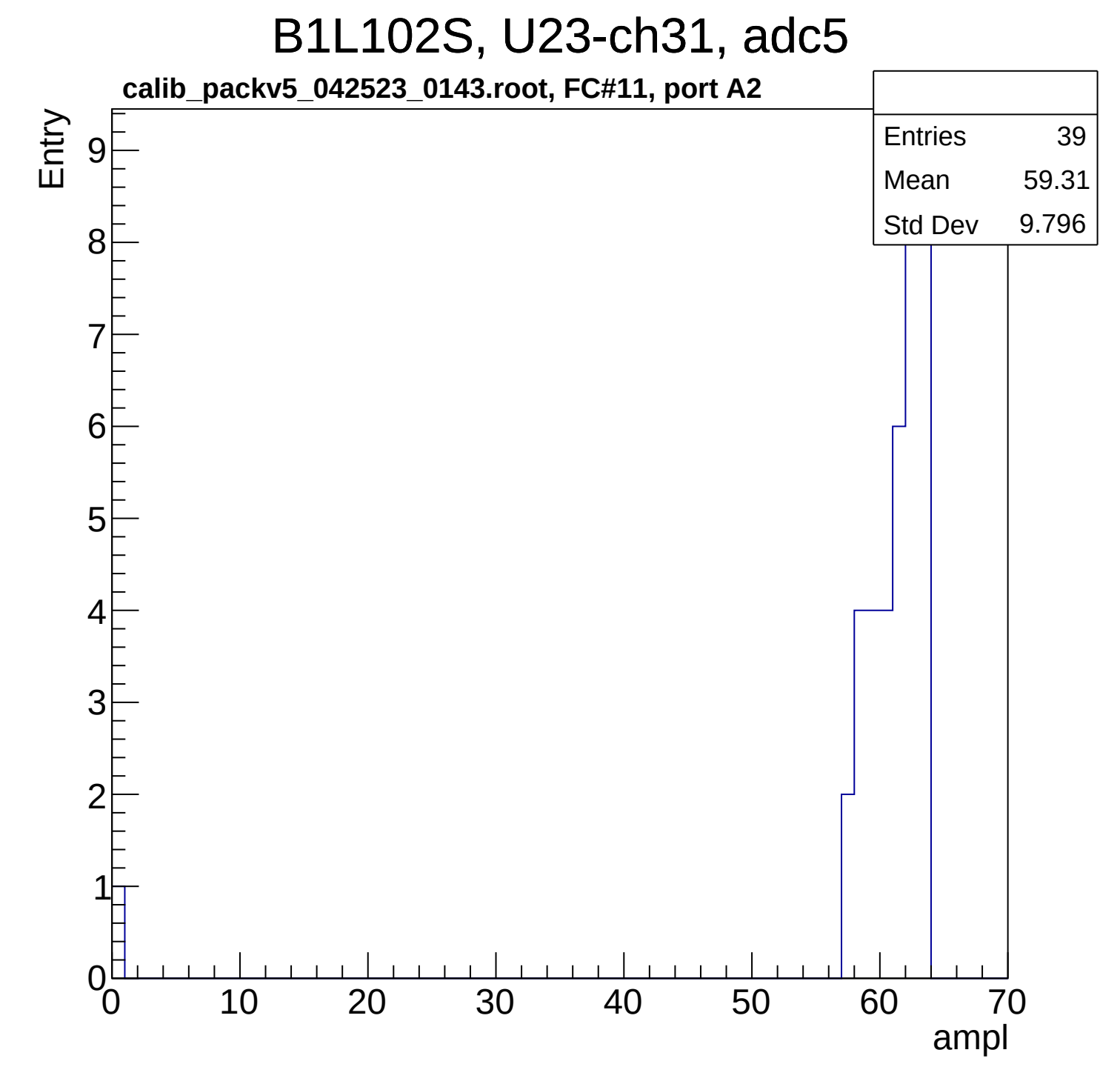

B1L102S, U23-ch31, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 90
Mean 30.36
Std Dev 5.701
Gaus mean : 30.9143
Gaus Width: 3.8789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

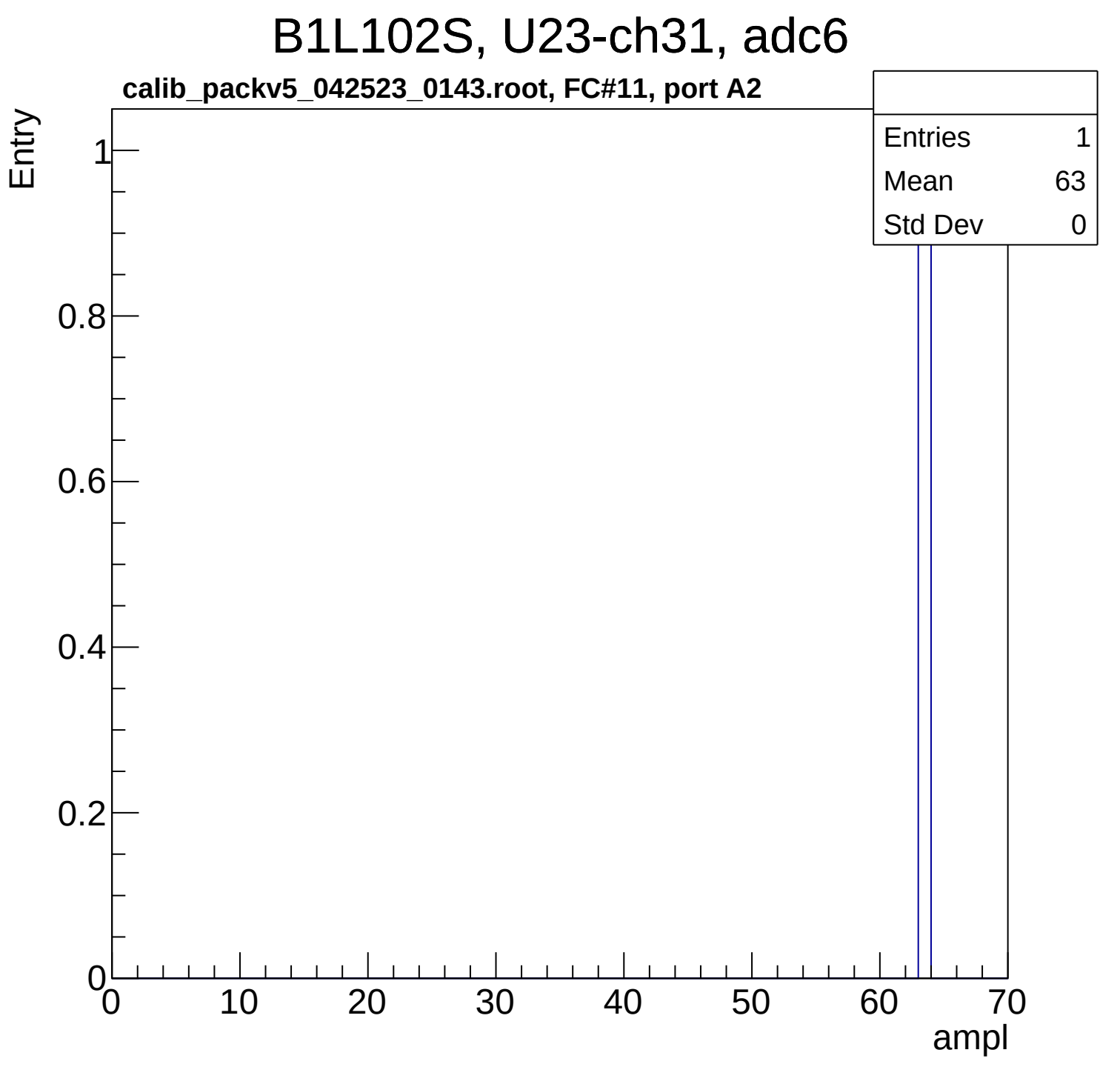

B1L102S, U23-ch31, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 37.6
Std Dev 3.462
Gaus mean : 39.0498
Gaus Width: 3.1372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

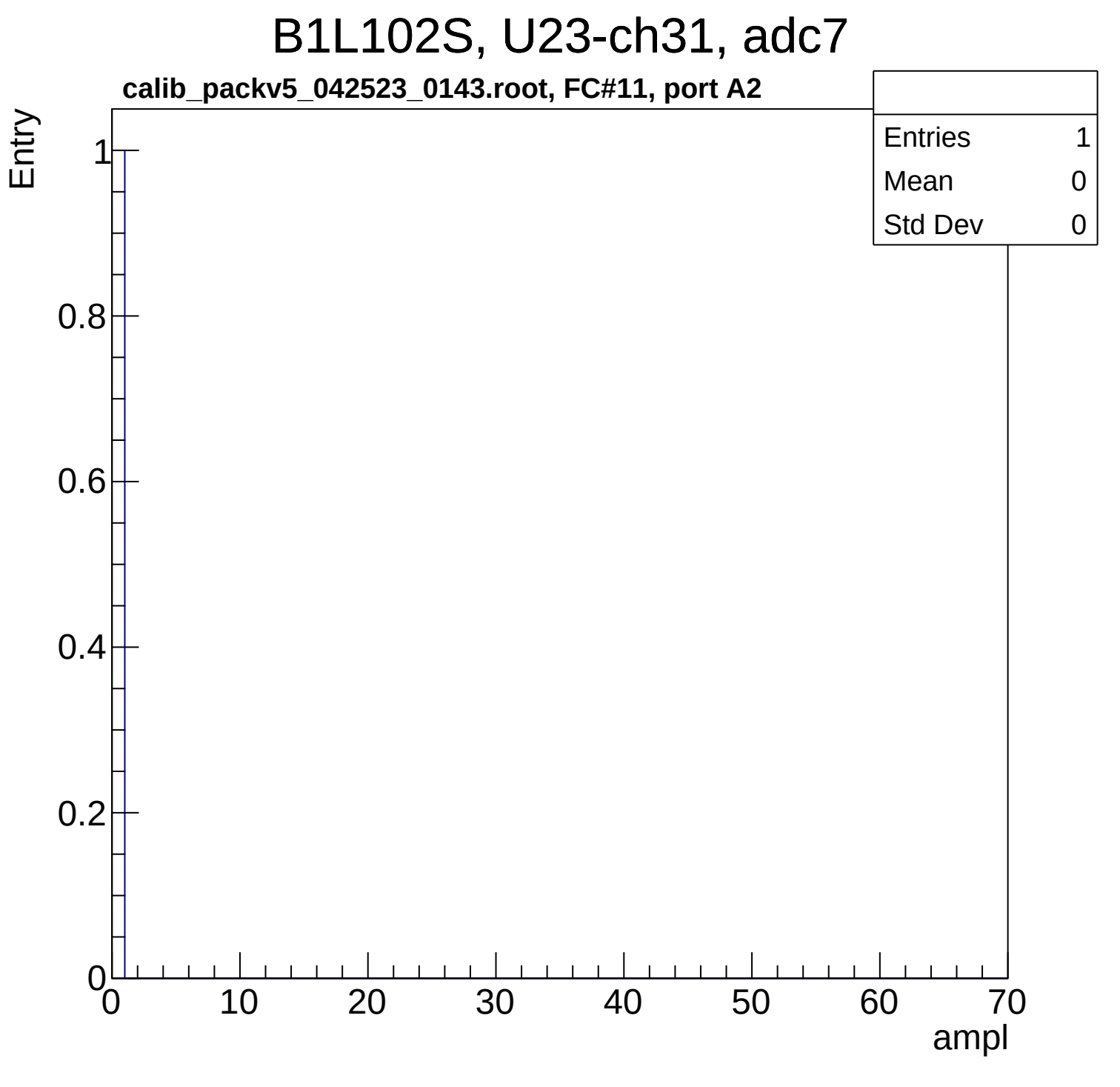

B1L102S, U23-ch31, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 44.2
Std Dev 3.598
Gaus mean : 44.1353
Gaus Width: 3.5847
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

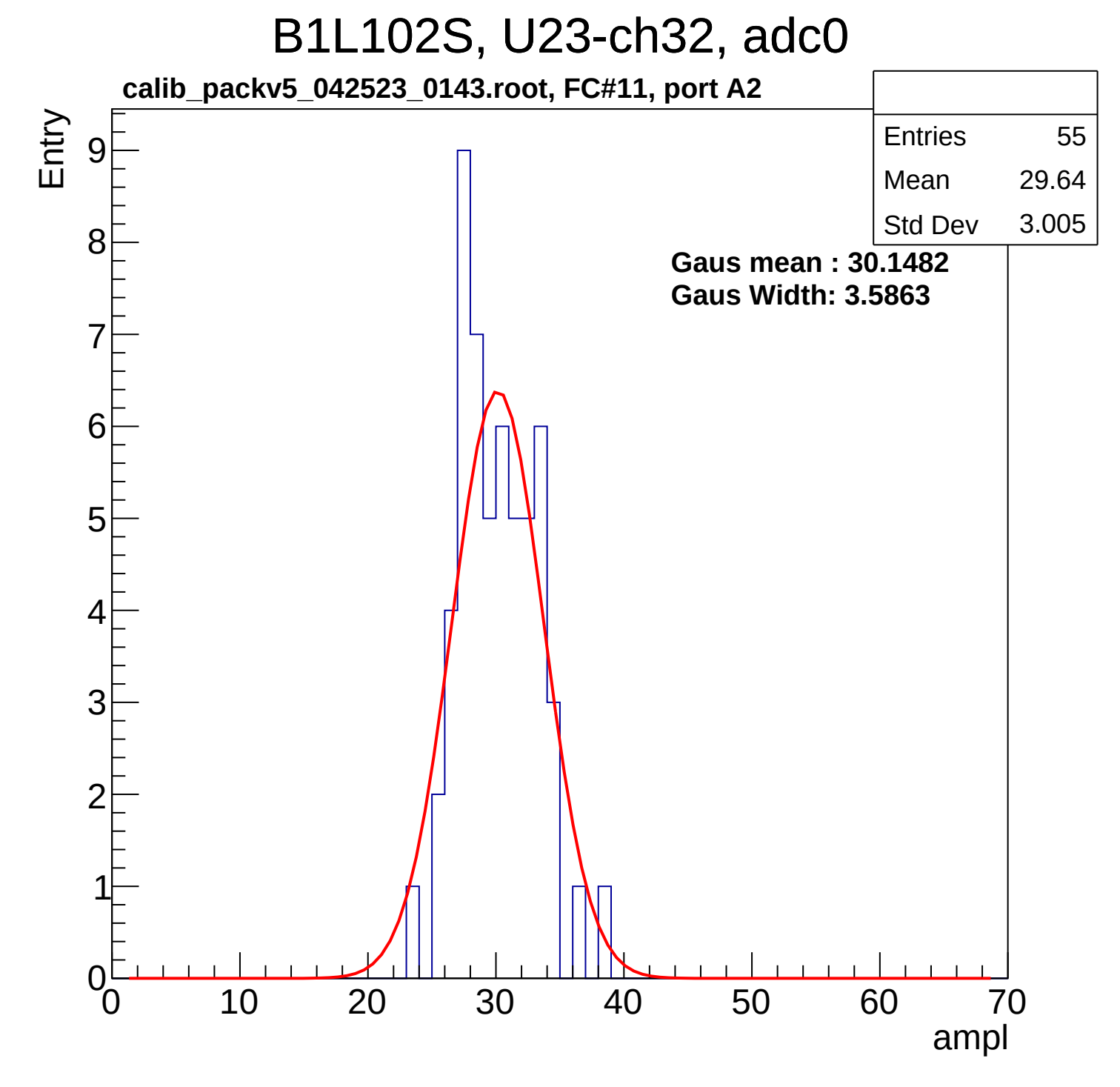

B1L102S, U23-ch31, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 51.12
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

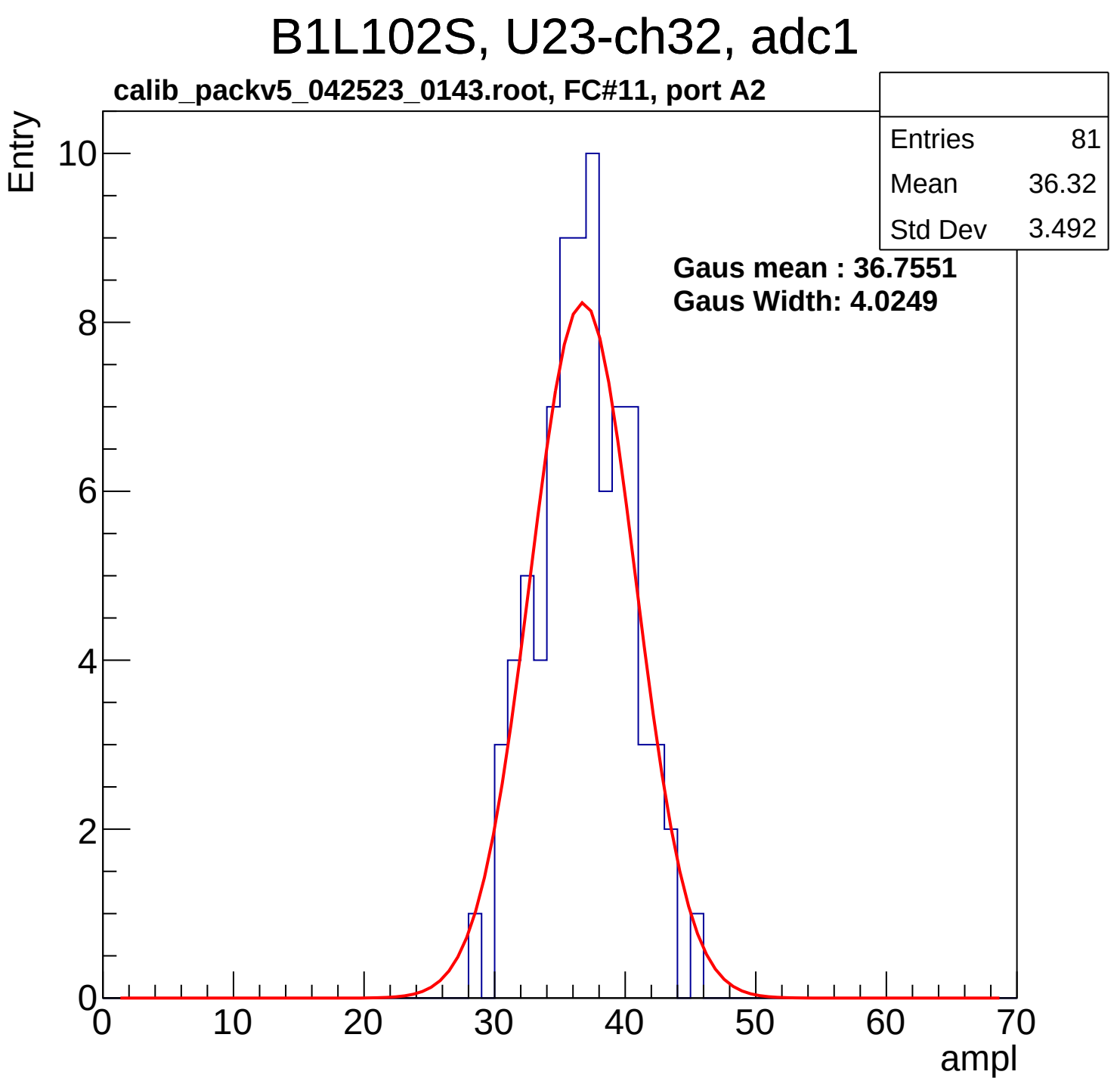

B1L102S, U23-ch31, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 57.04
Std Dev 2.766
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

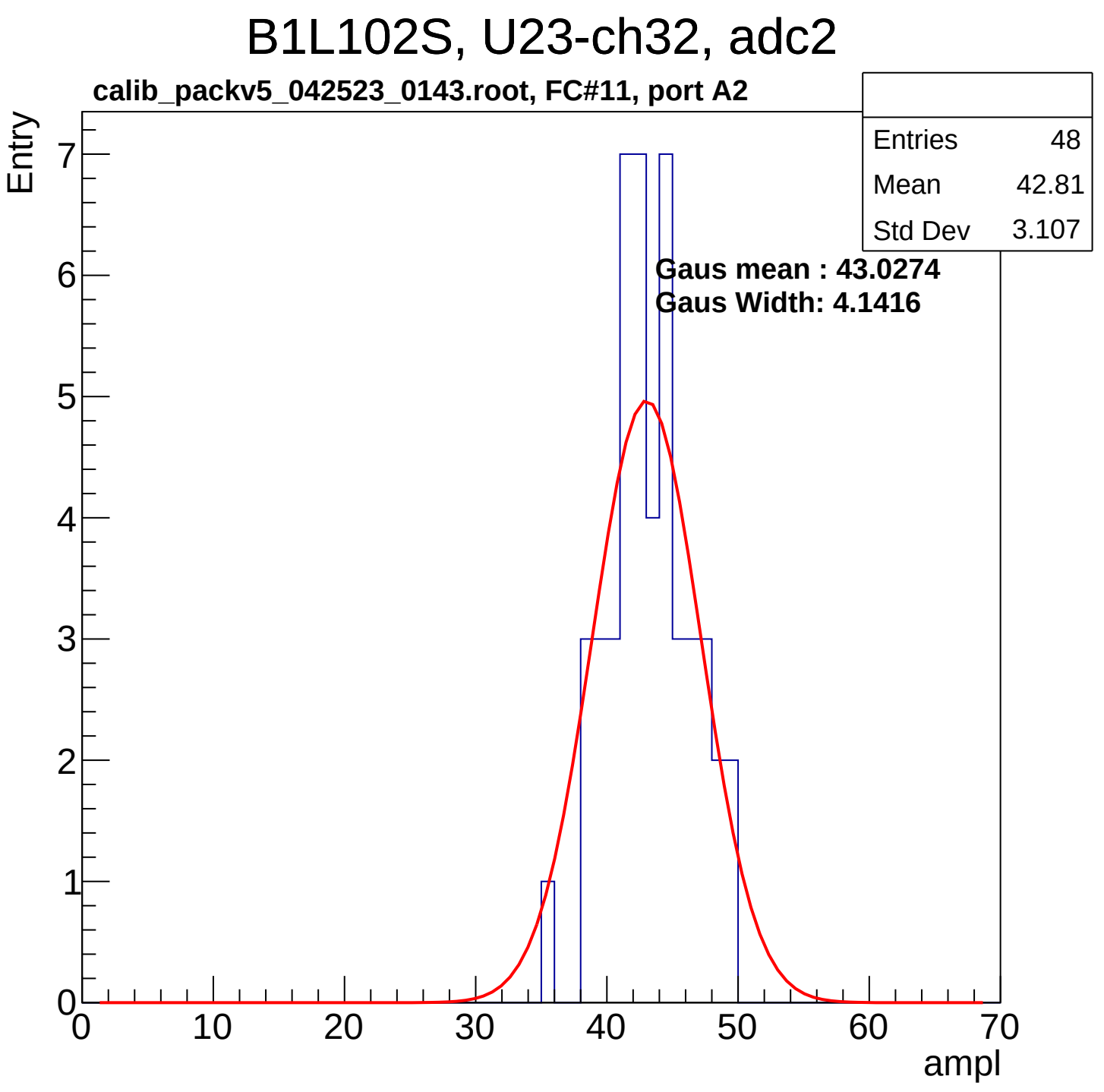

B1L102S, U23-ch31, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 59.31
Std Dev 9.796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

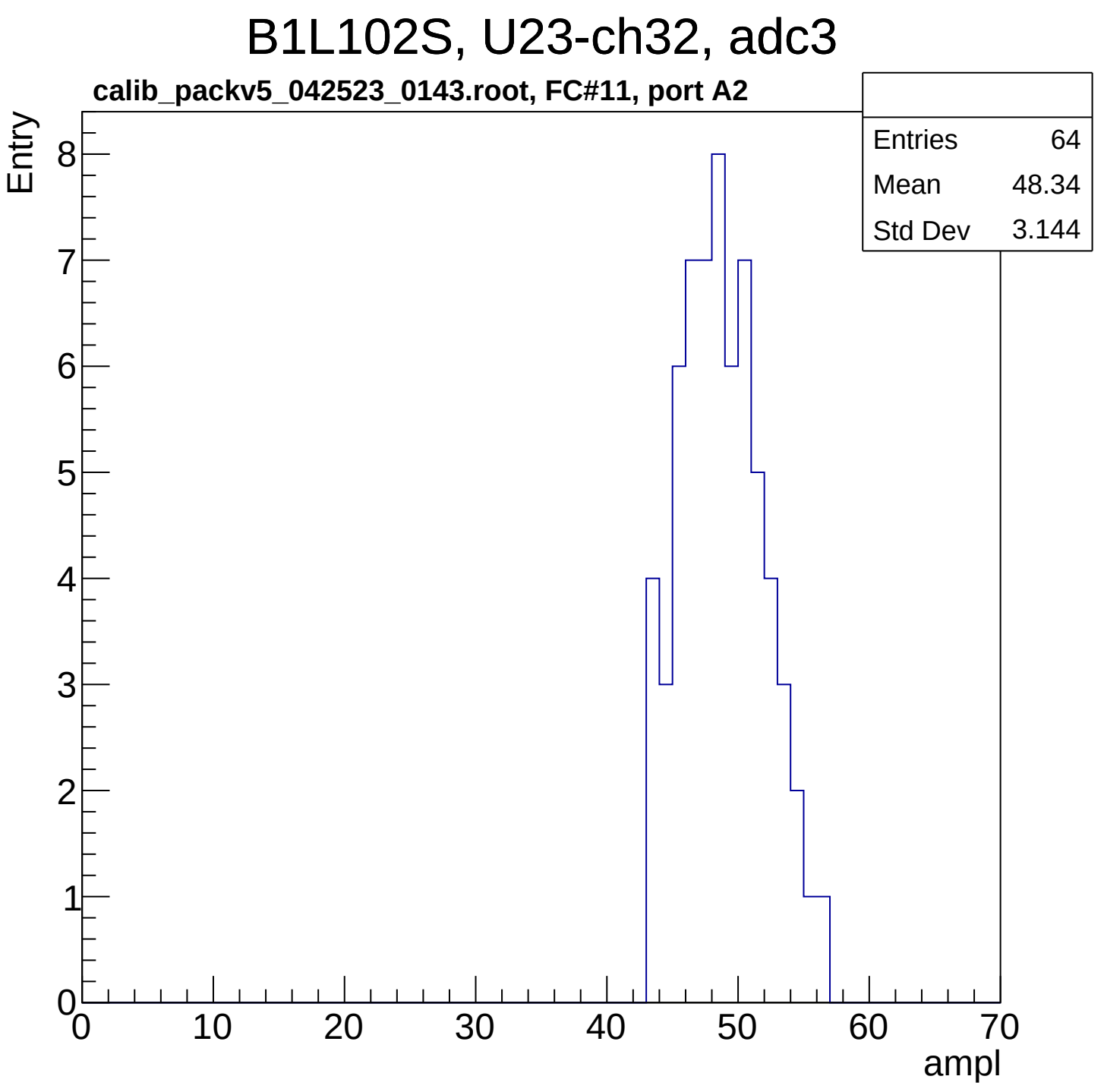

B1L102S, U23-ch31, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

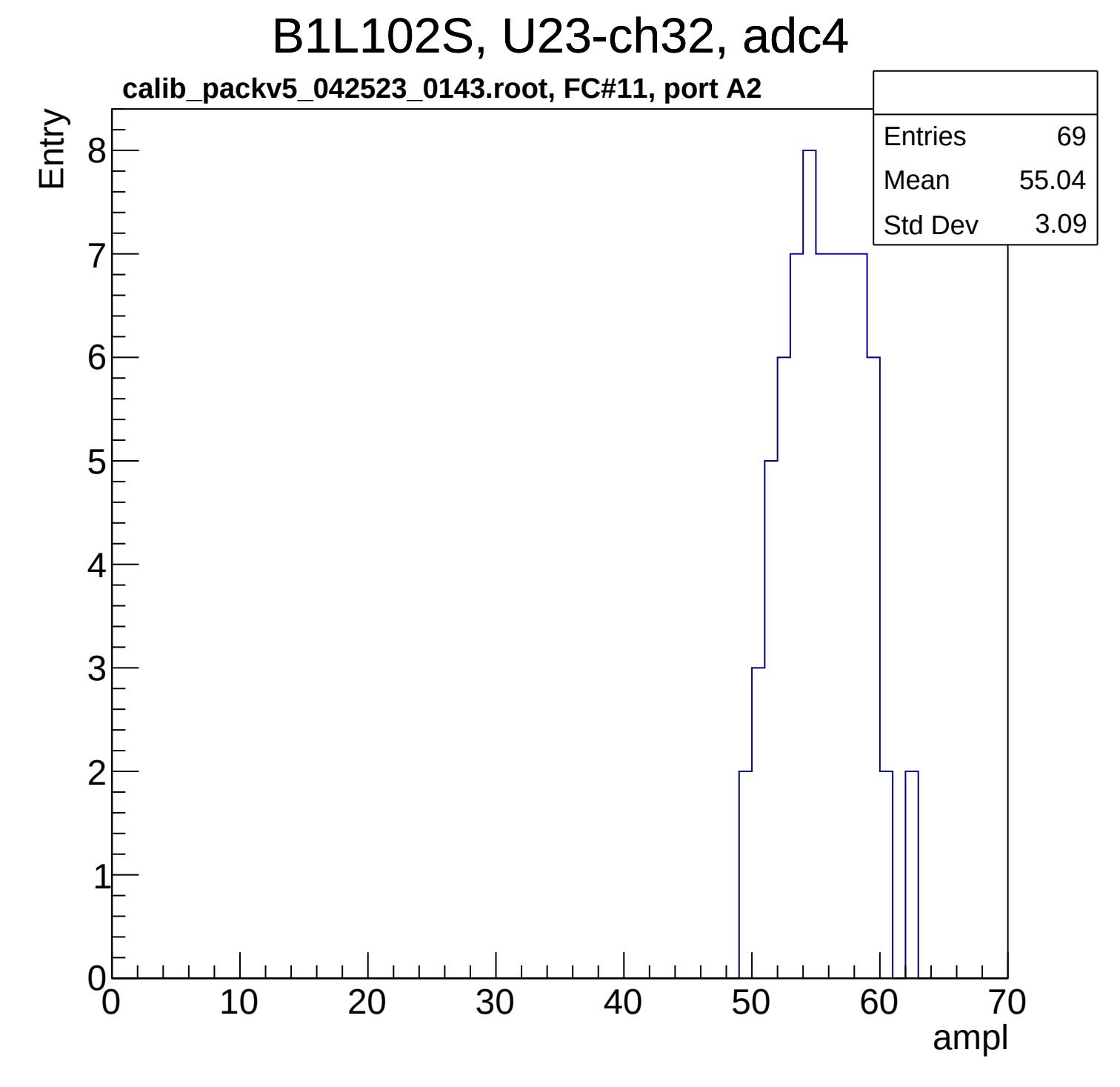

B1L102S, U23-ch31, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

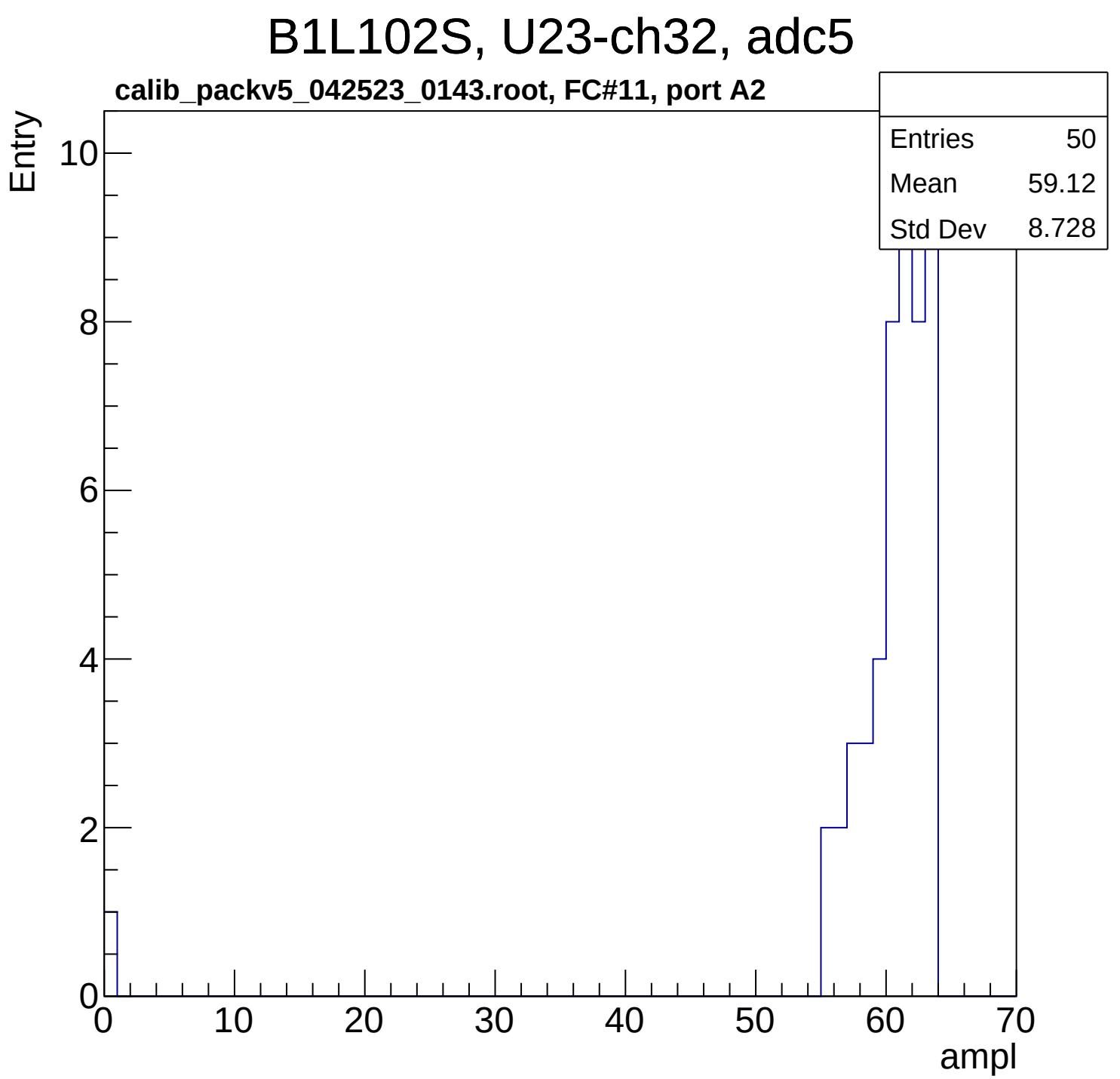

B1L102S, U23-ch32, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 29.64
Std Dev 3.005
Gaus mean : 30.1482
Gaus Width: 3.5863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

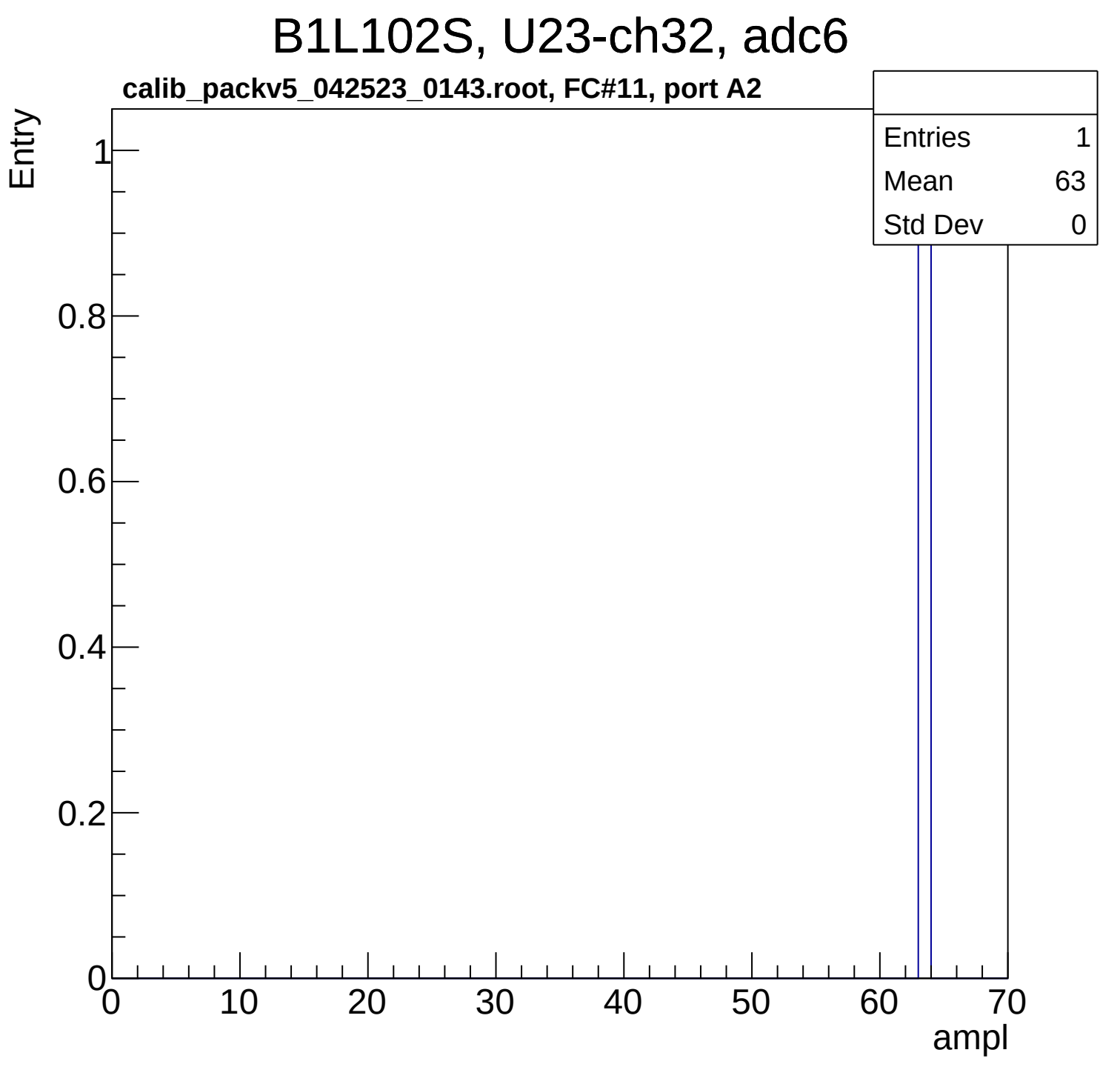

B1L102S, U23-ch32, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 36.32
Std Dev 3.492
Gaus mean : 36.7551
Gaus Width: 4.0249
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

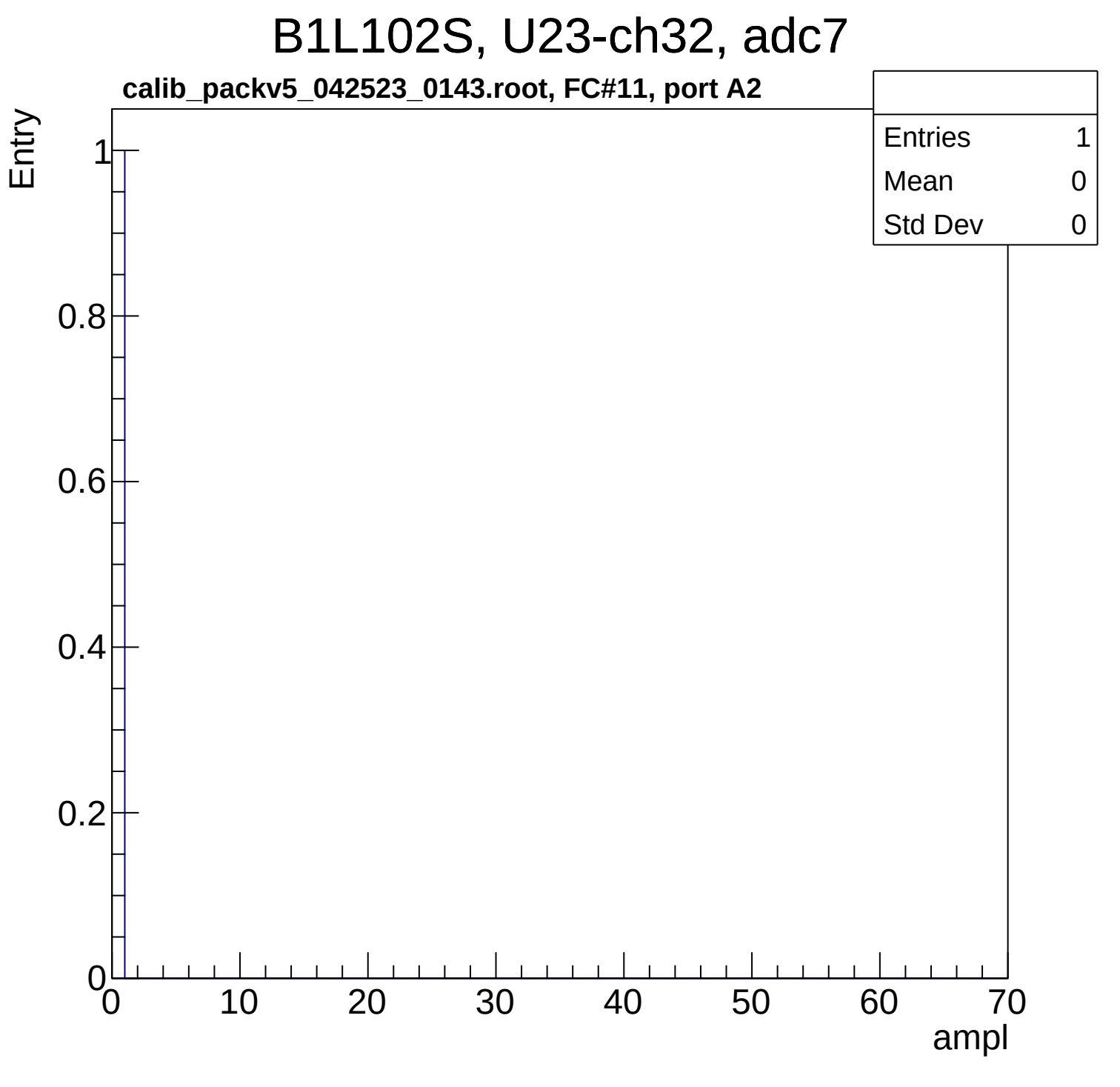

B1L102S, U23-ch32, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 42.81
Std Dev 3.107
Gaus mean : 43.0274
Gaus Width: 4.1416
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

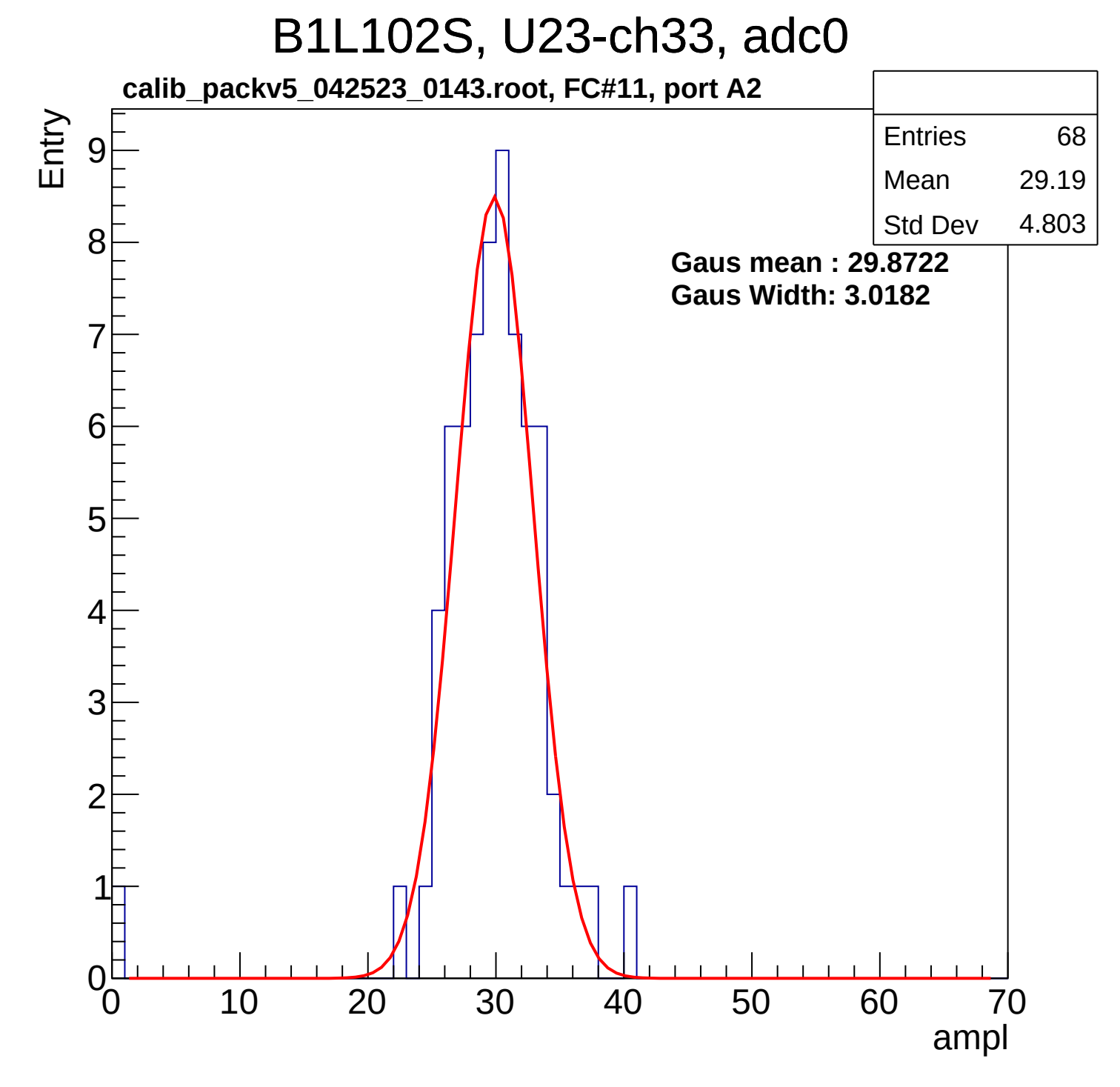

B1L102S, U23-ch32, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 48.34
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

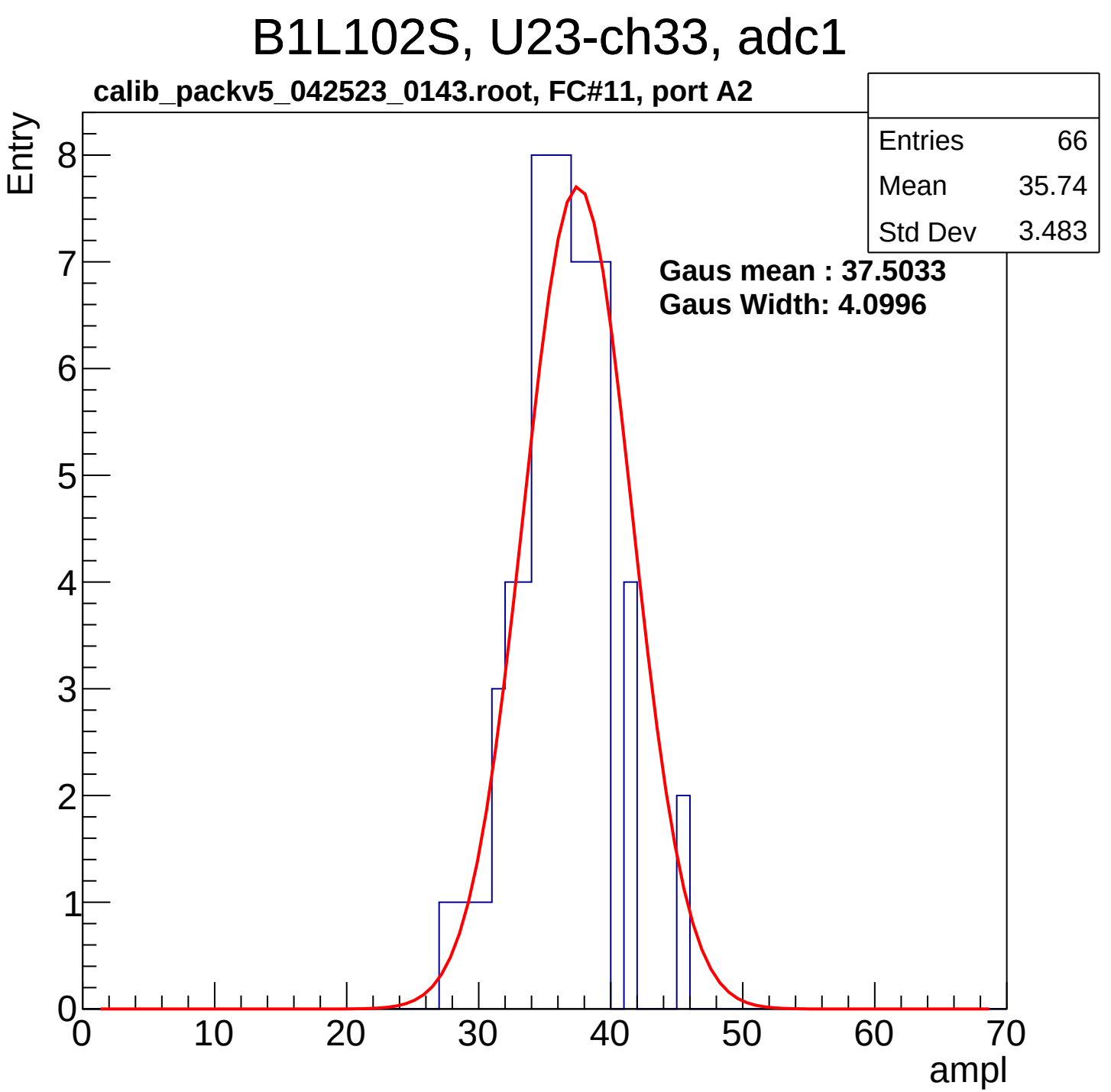

B1L102S, U23-ch32, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 55.04
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

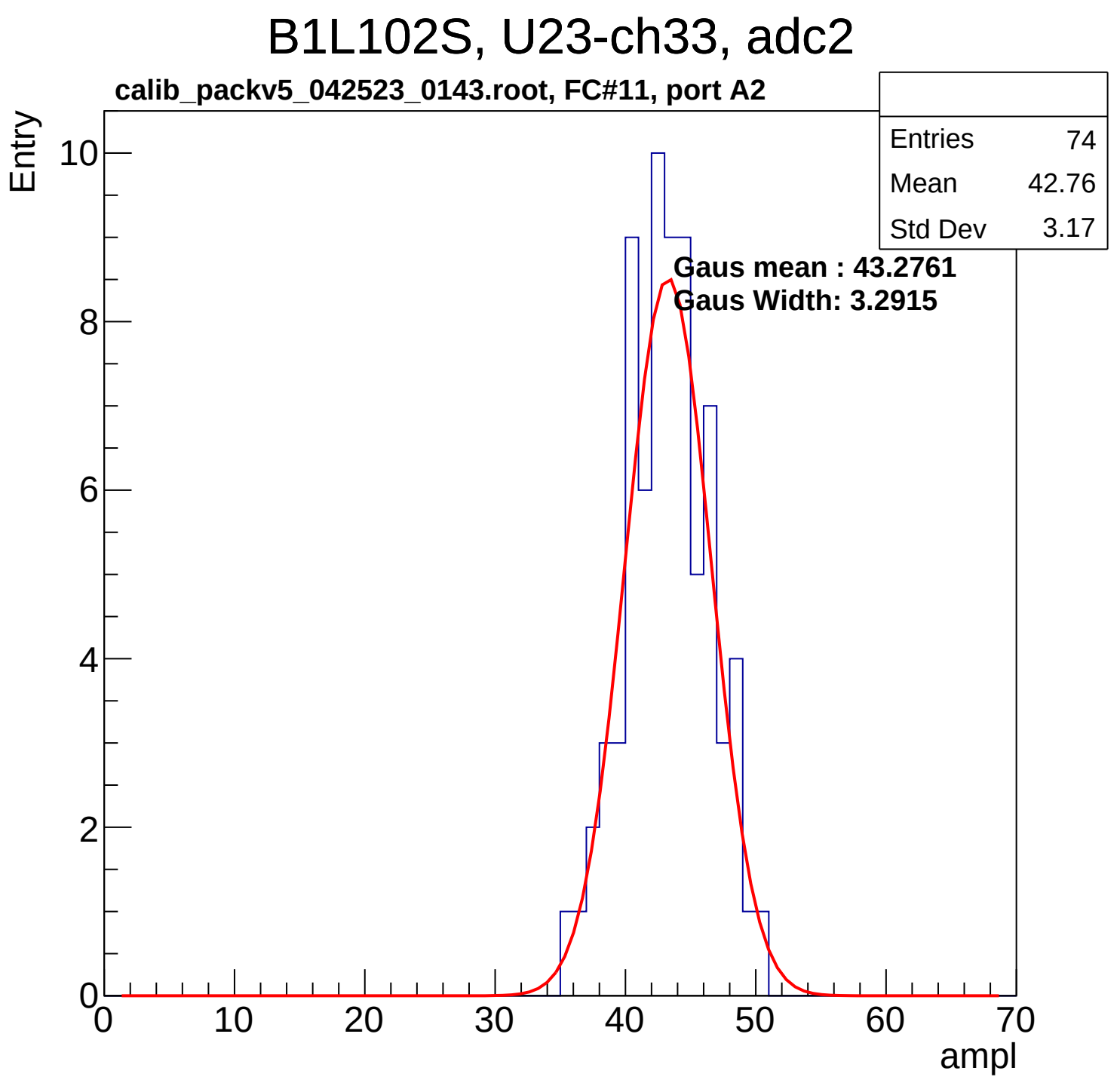

B1L102S, U23-ch32, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 59.12
Std Dev 8.728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

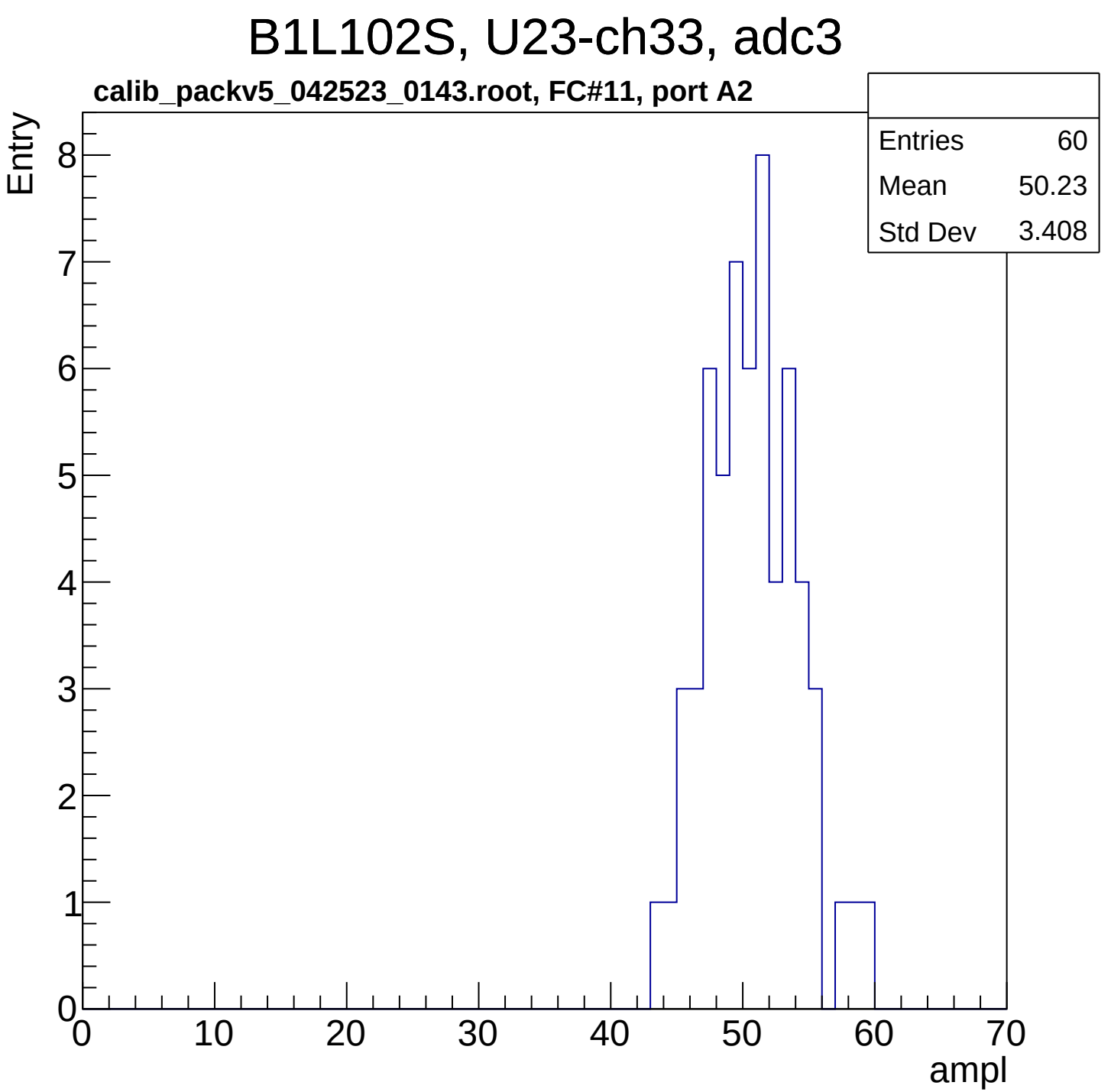

B1L102S, U23-ch32, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

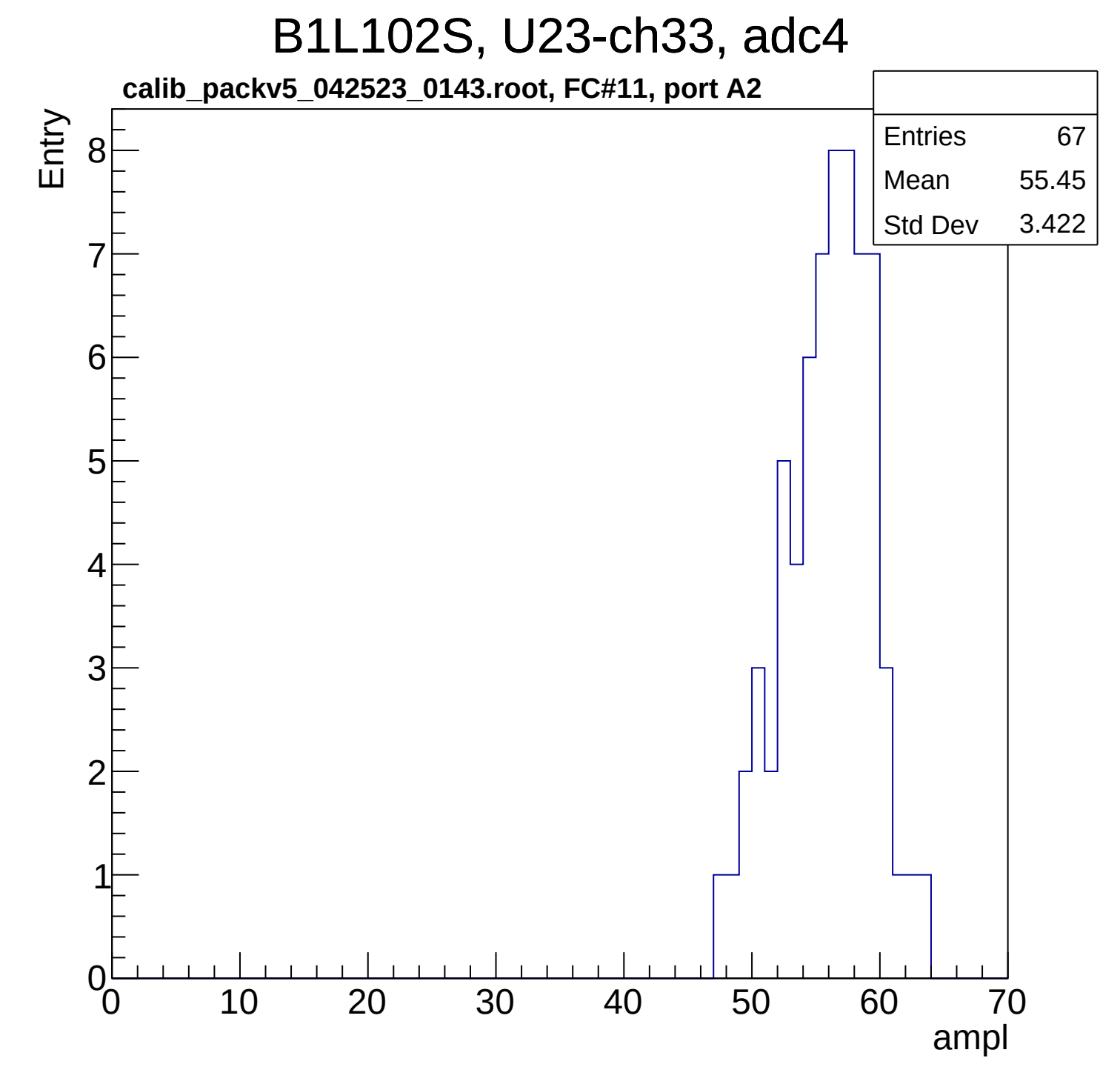

B1L102S, U23-ch32, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

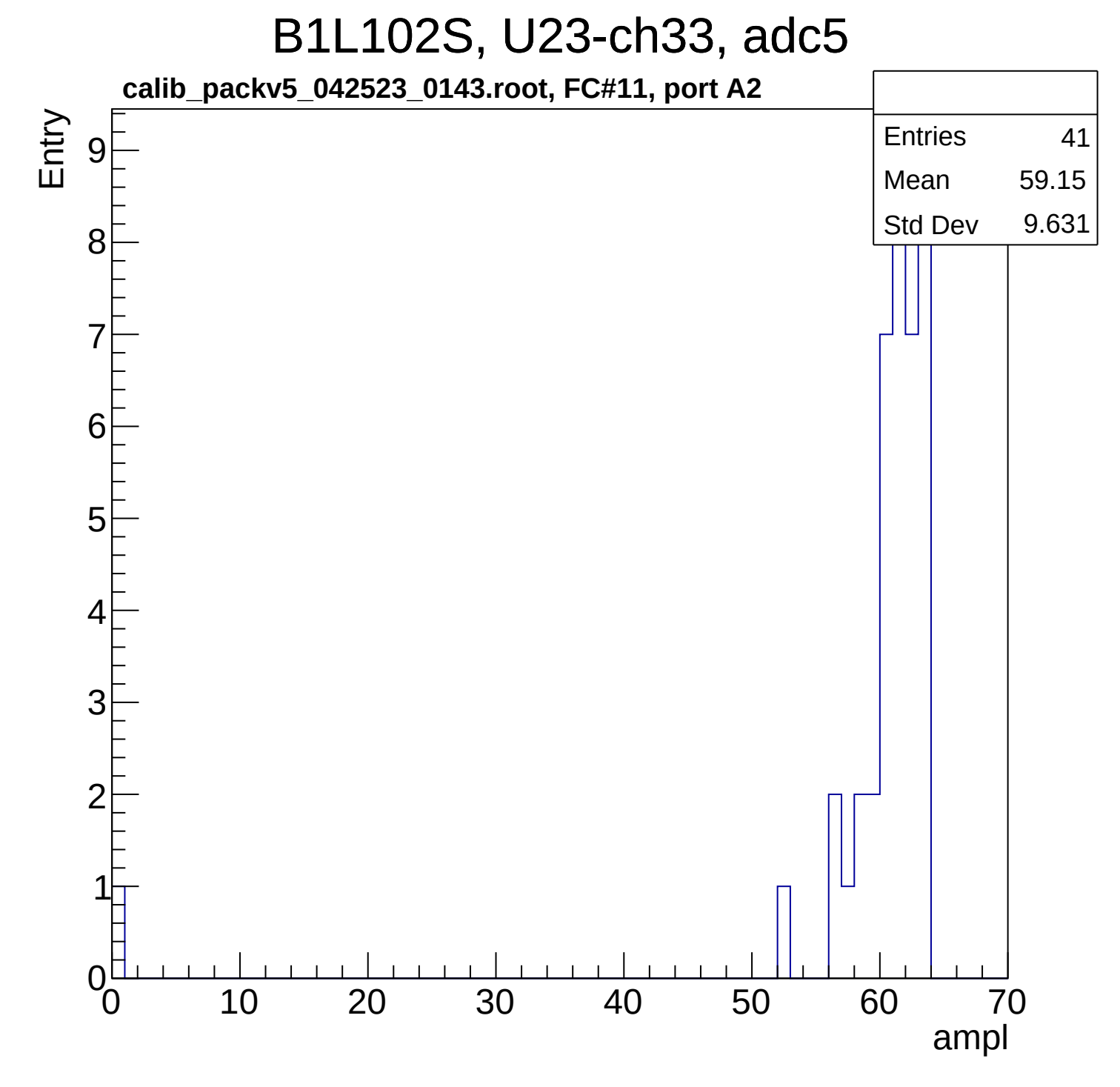

B1L102S, U23-ch33, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.19
Std Dev 4.803
Gaus mean : 29.8722
Gaus Width: 3.0182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

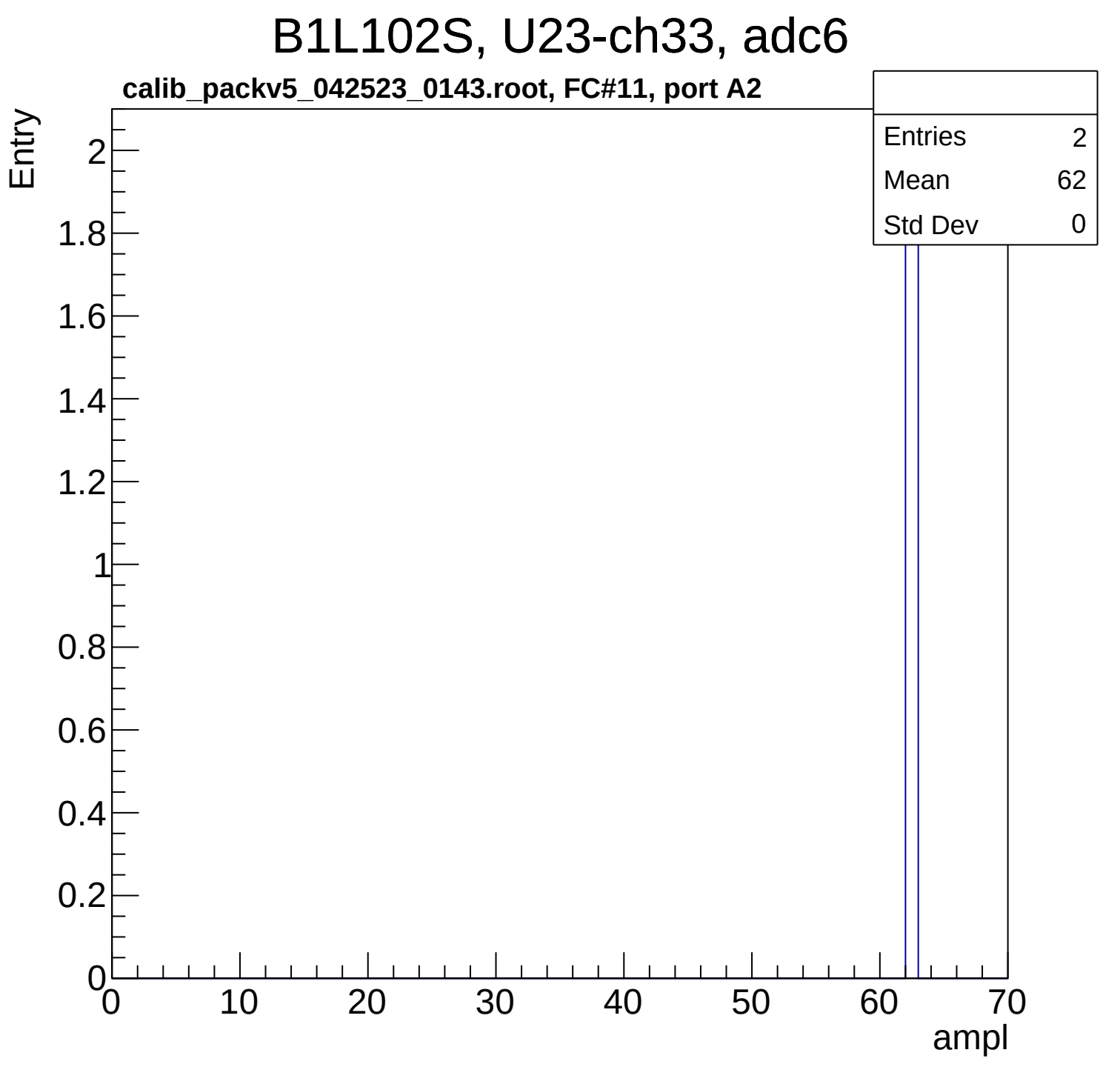

B1L102S, U23-ch33, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 35.74
Std Dev 3.483
Gaus mean : 37.5033
Gaus Width: 4.0996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

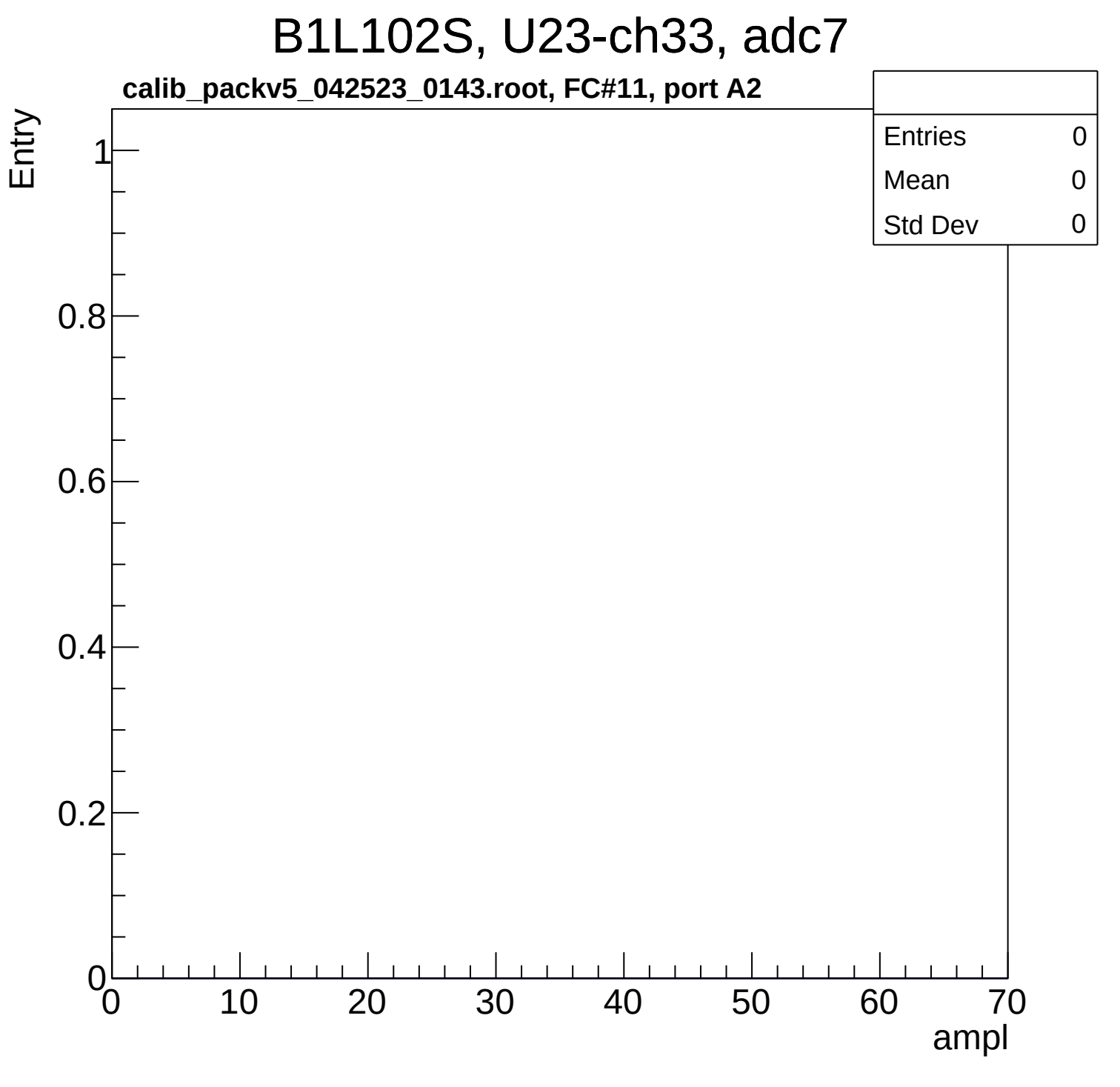

B1L102S, U23-ch33, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 42.76
Std Dev 3.17
Gaus mean : 43.2761
Gaus Width: 3.2915
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

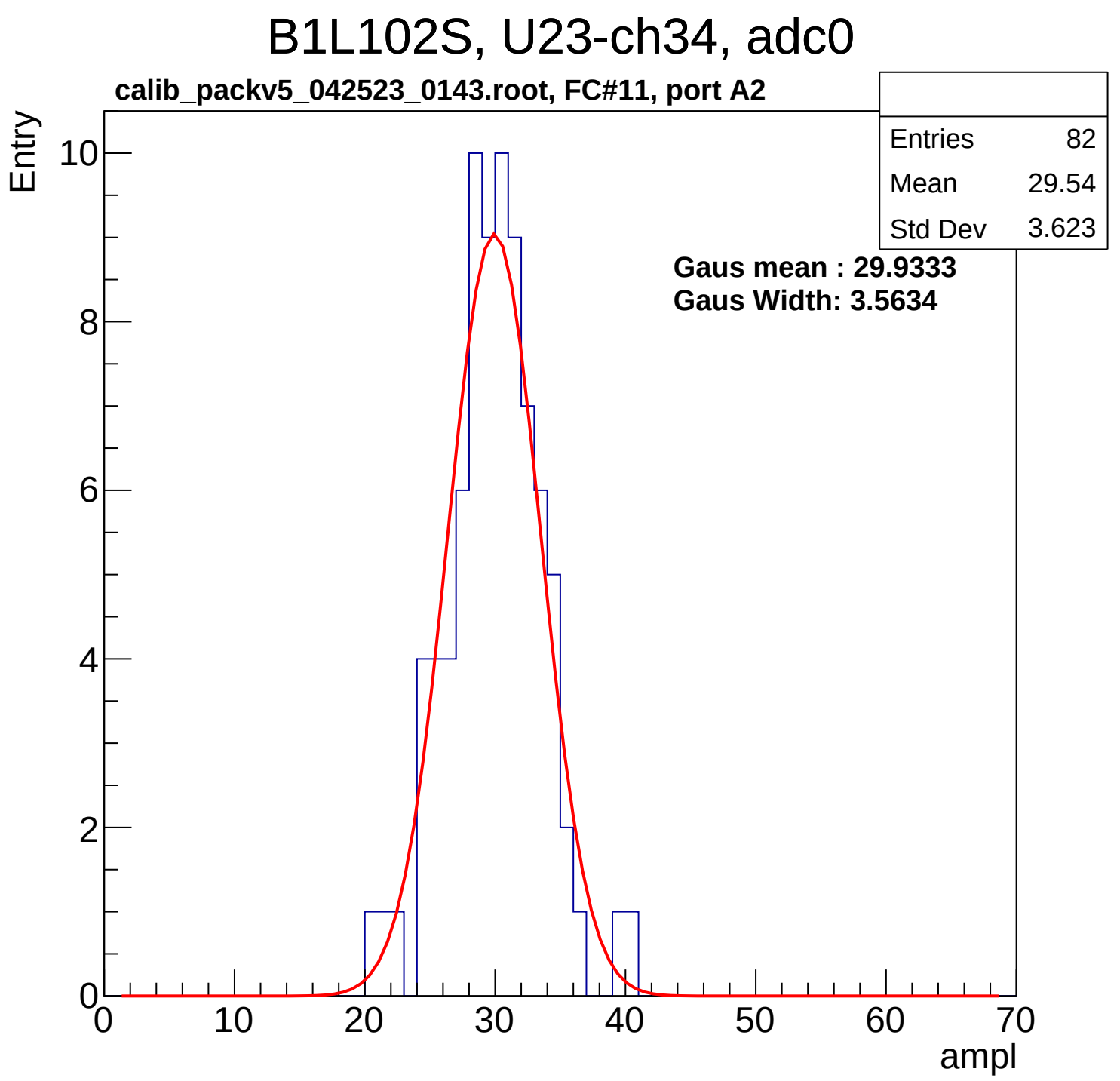

B1L102S, U23-ch33, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 50.23
Std Dev 3.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

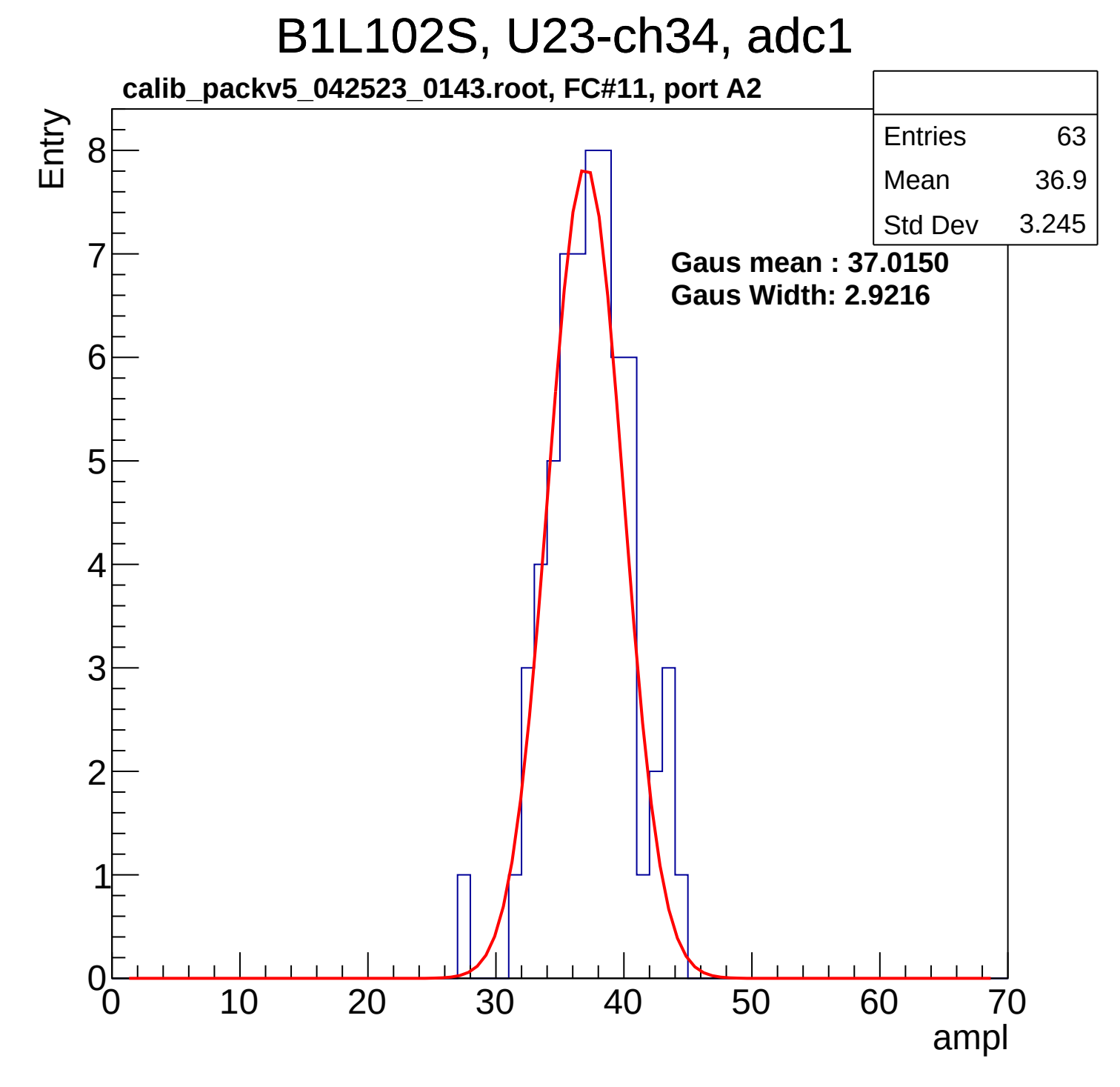

B1L102S, U23-ch33, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 55.45
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

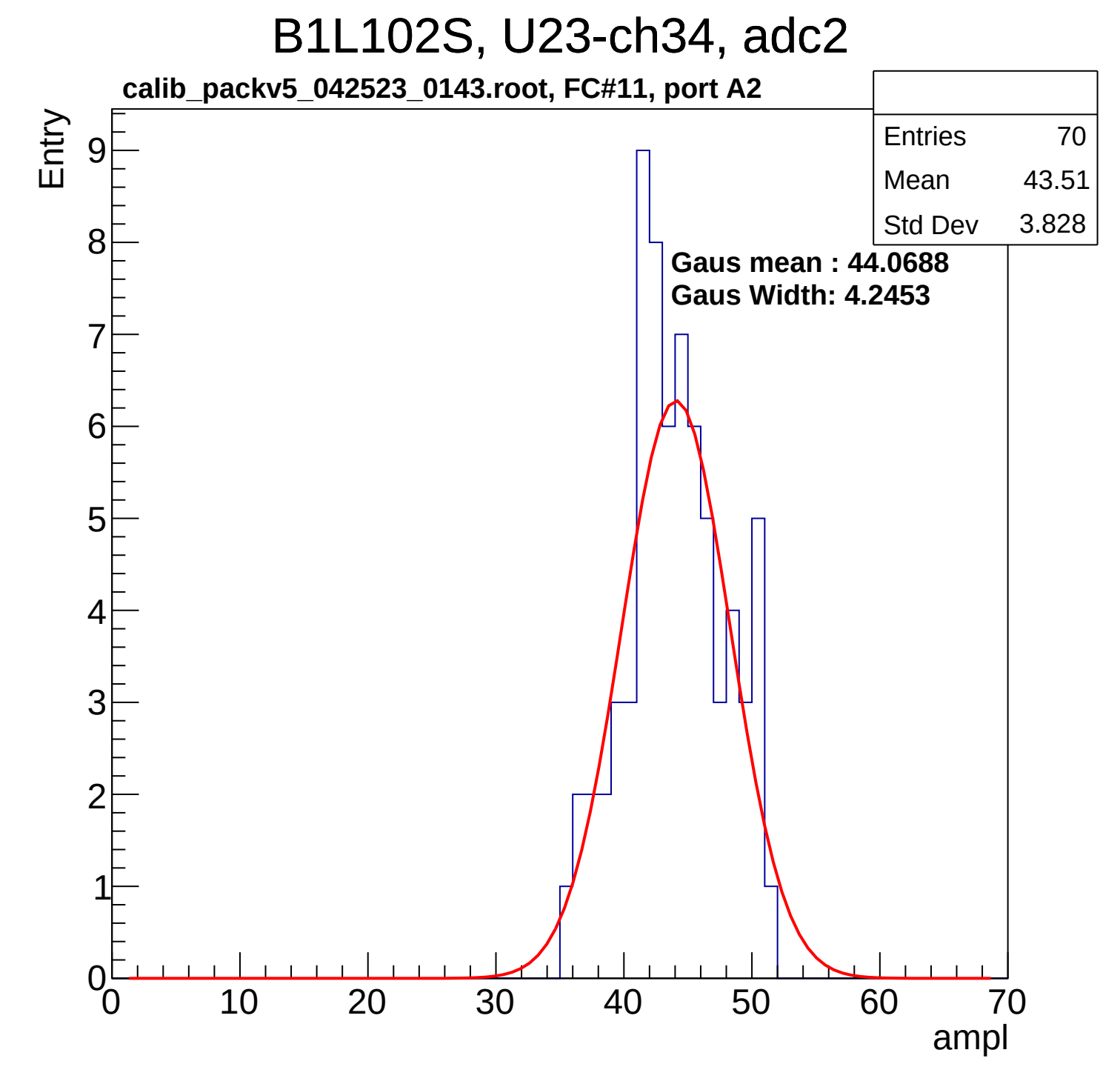

B1L102S, U23-ch33, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 59.15
Std Dev 9.631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

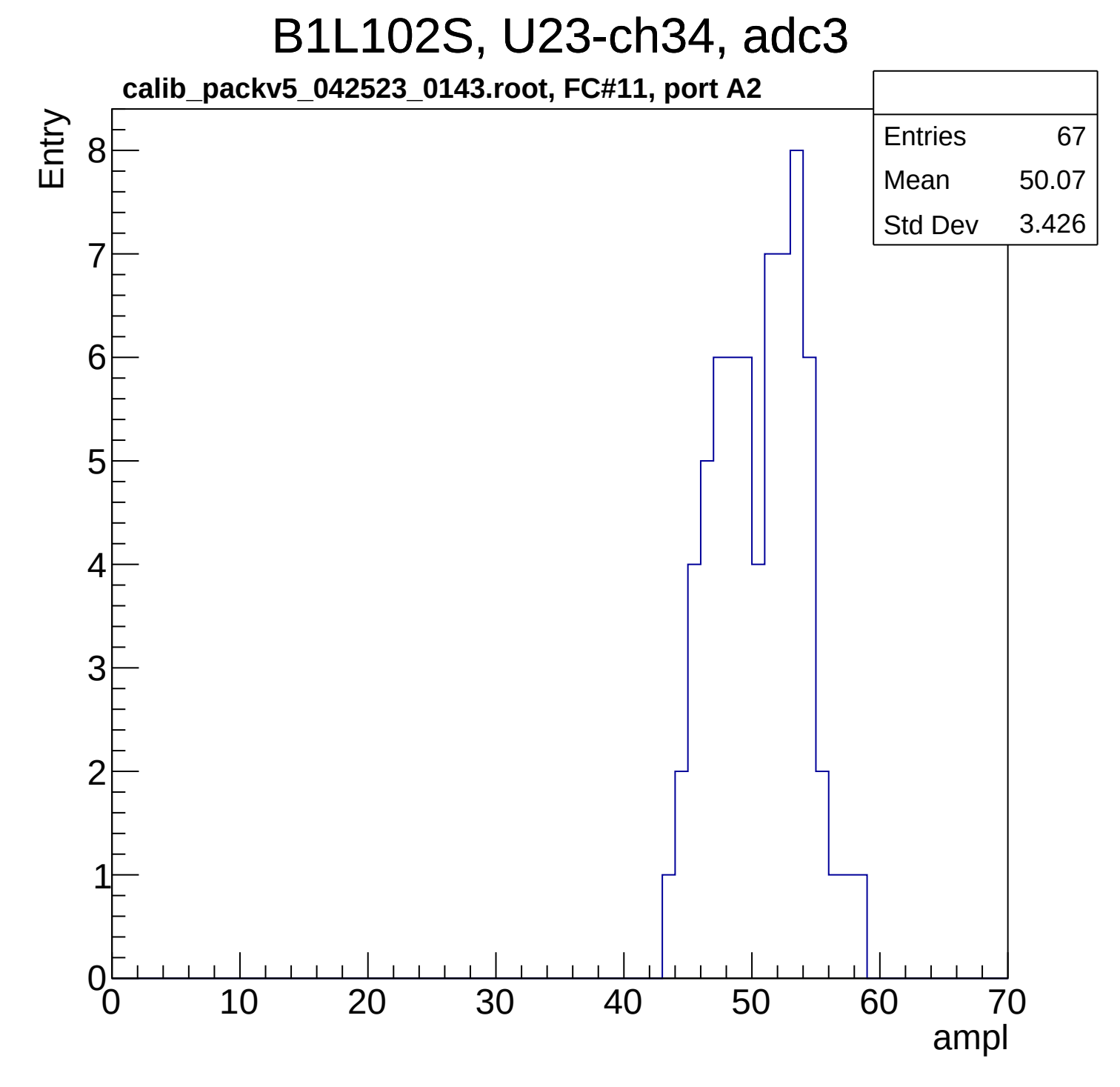

B1L102S, U23-ch33, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

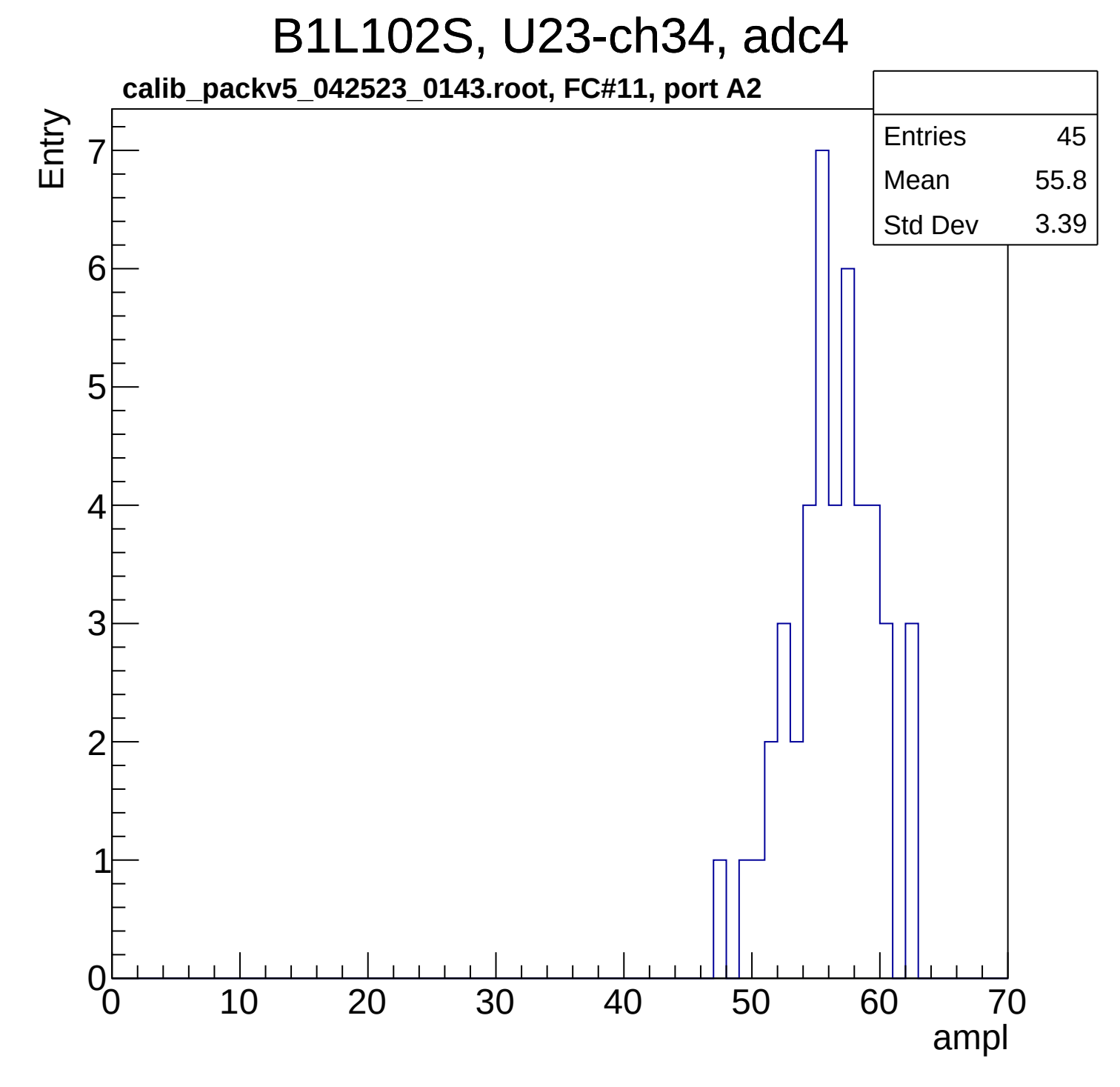

B1L102S, U23-ch33, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

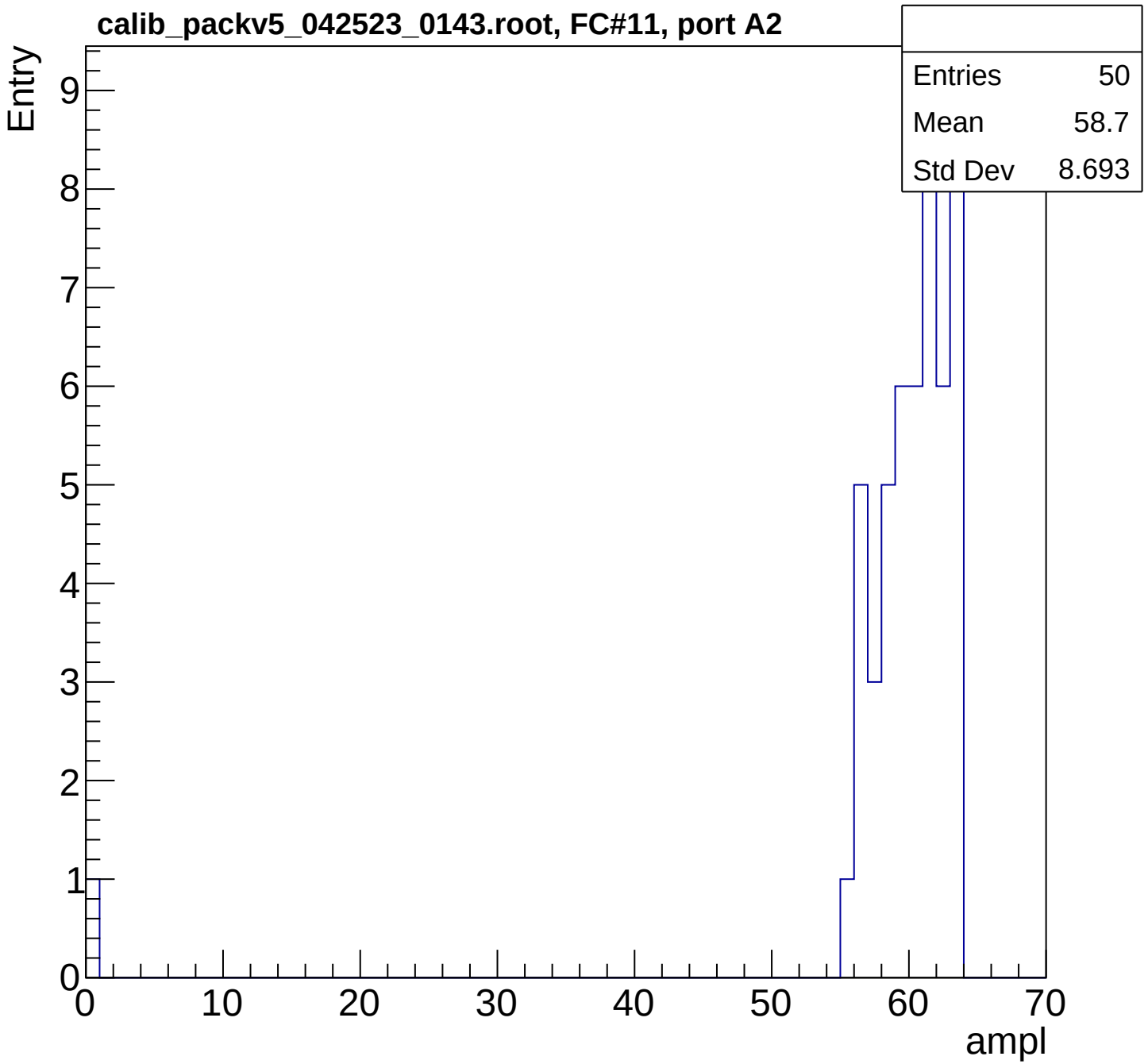

B1L102S, U23-ch34, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 29.54
Std Dev 3.623
Gaus mean : 29.9333
Gaus Width: 3.5634
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

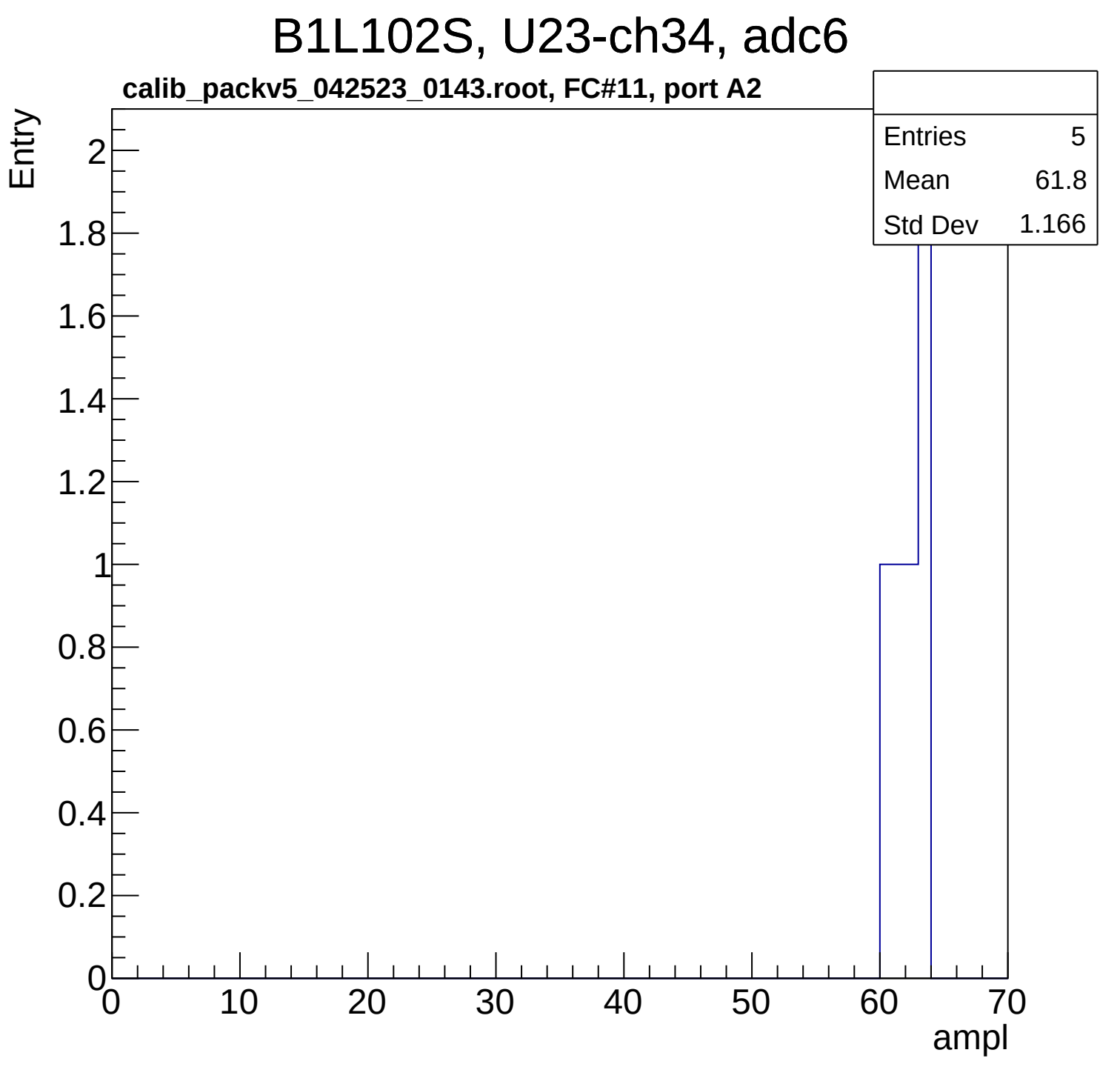

B1L102S, U23-ch34, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 36.9
Std Dev 3.245
Gaus mean : 37.0150
Gaus Width: 2.9216
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

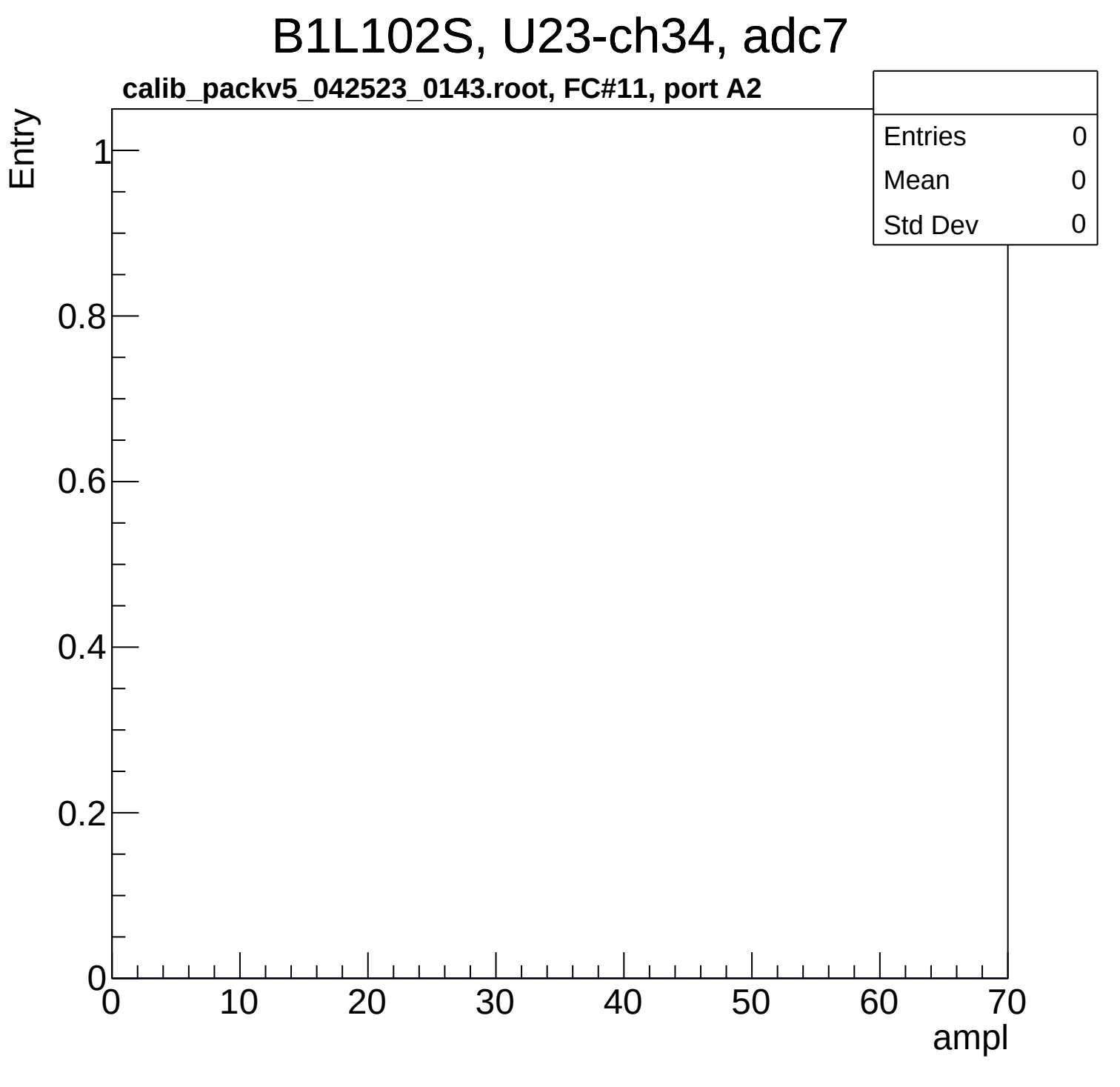

B1L102S, U23-ch34, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 43.51
Std Dev 3.828
Gaus mean : 44.0688
Gaus Width: 4.2453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

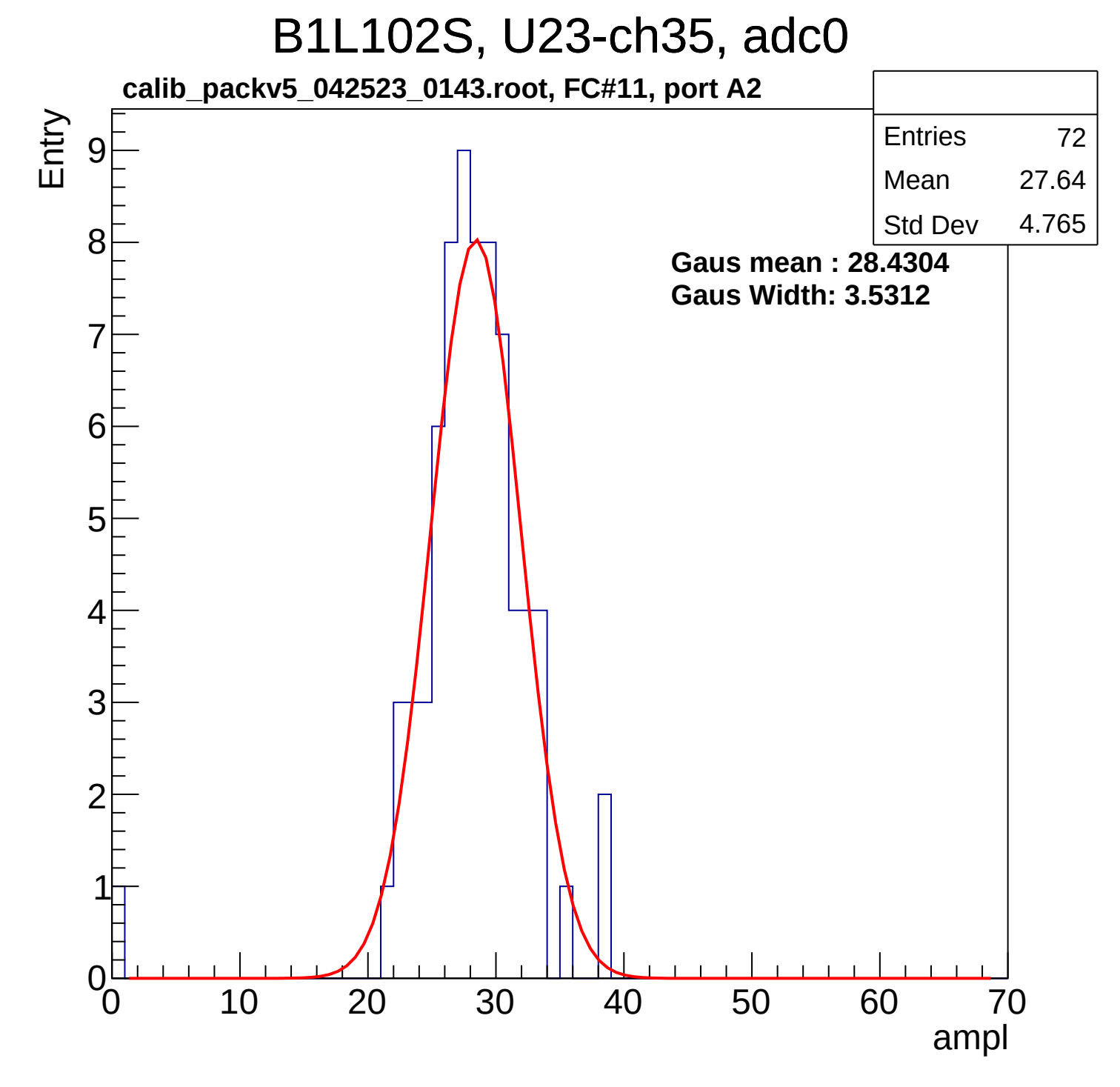

B1L102S, U23-ch34, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 50.07
Std Dev 3.426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

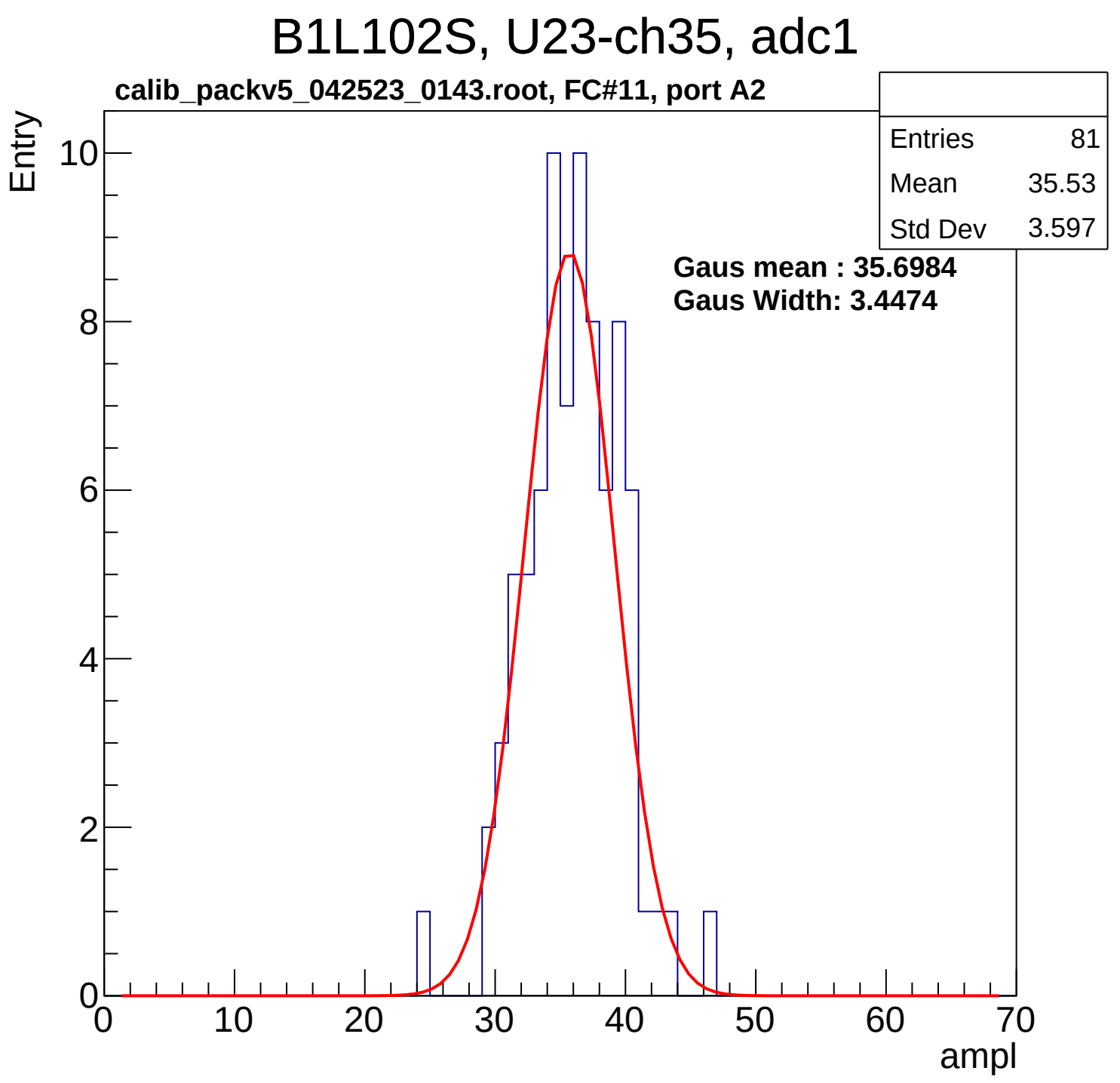

B1L102S, U23-ch34, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 55.8
Std Dev 3.39
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

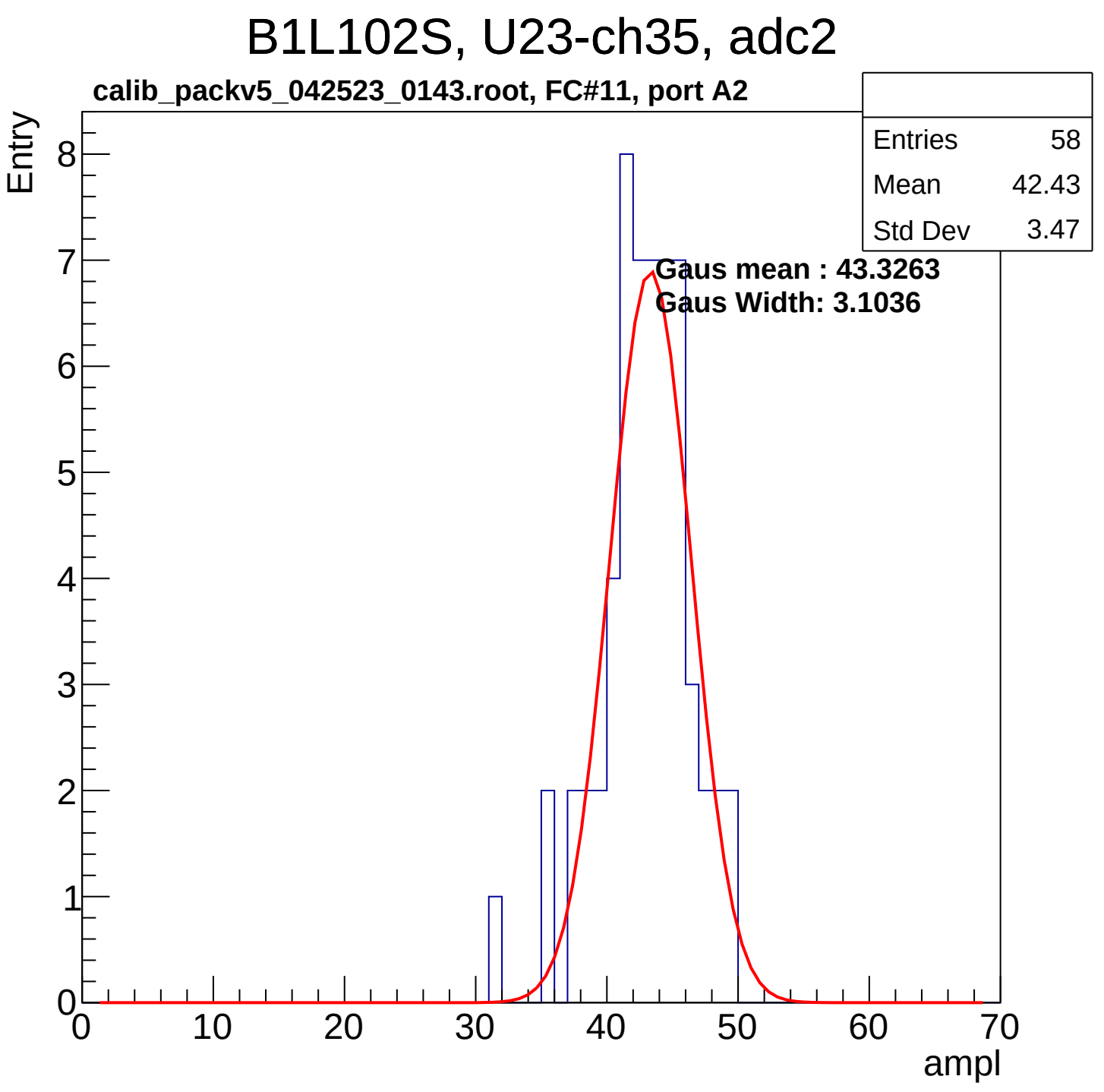

B1L102S, U23-ch34, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 58.7
Std Dev 8.693
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

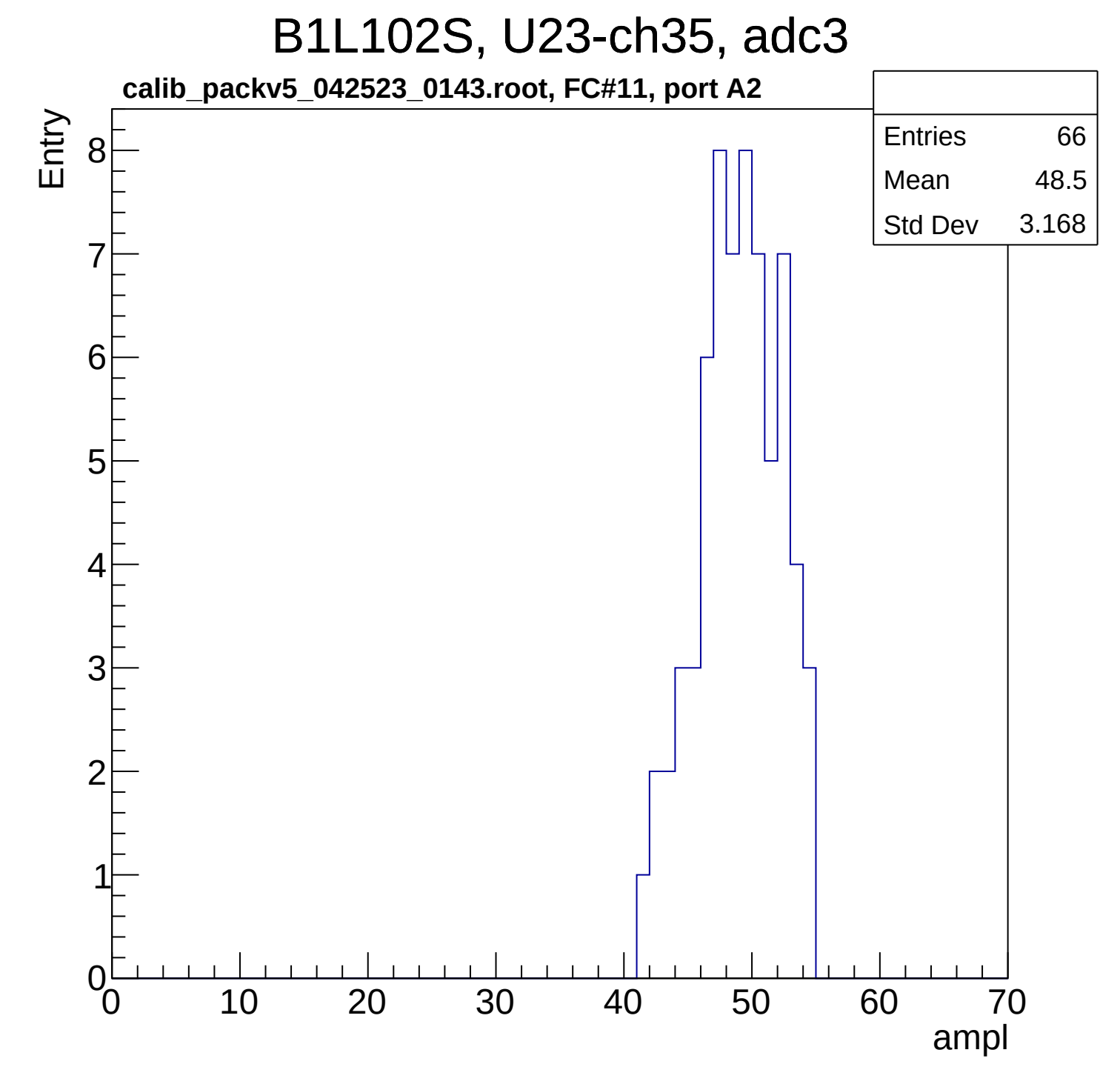

B1L102S, U23-ch34, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

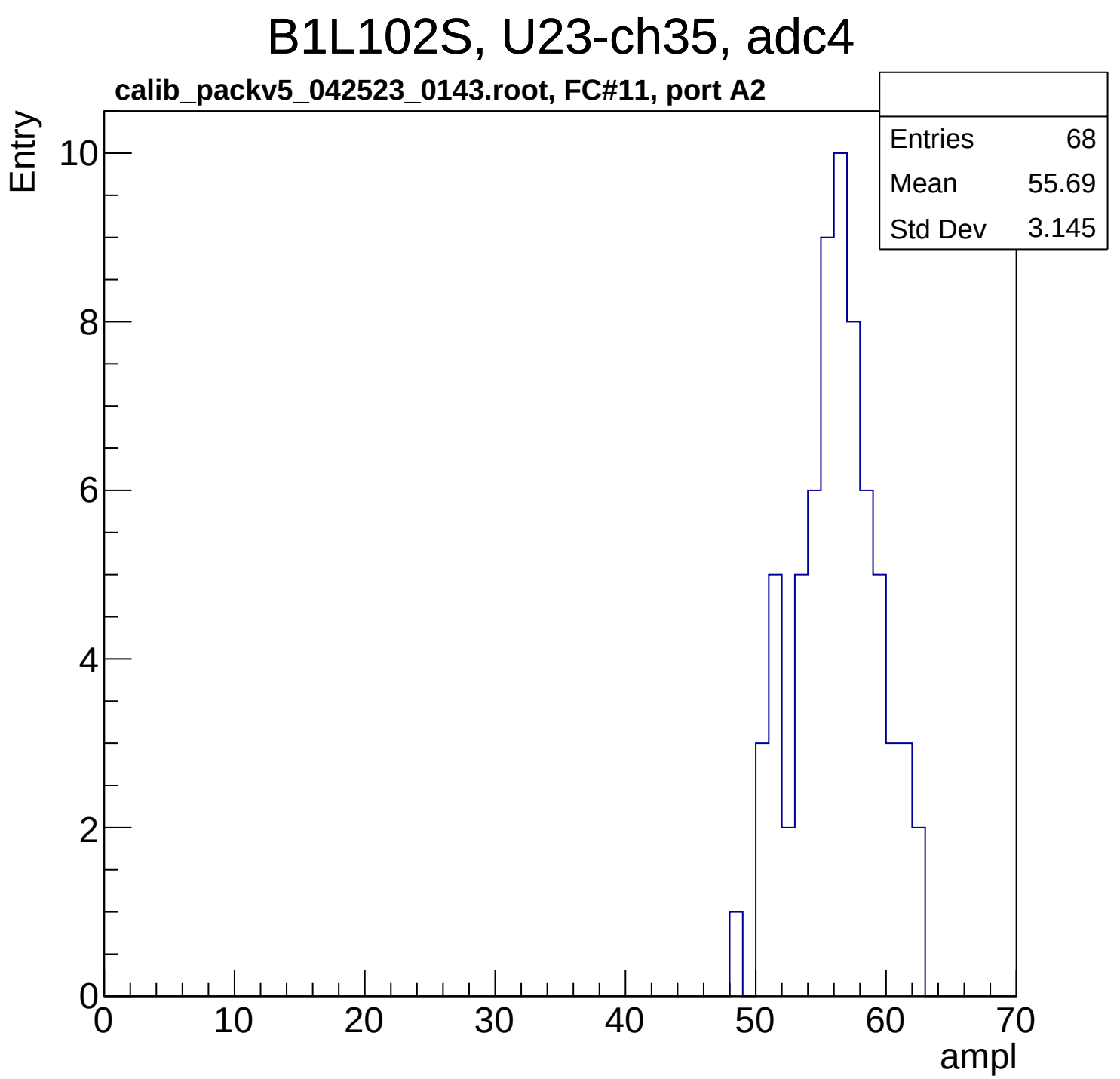

B1L102S, U23-ch34, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

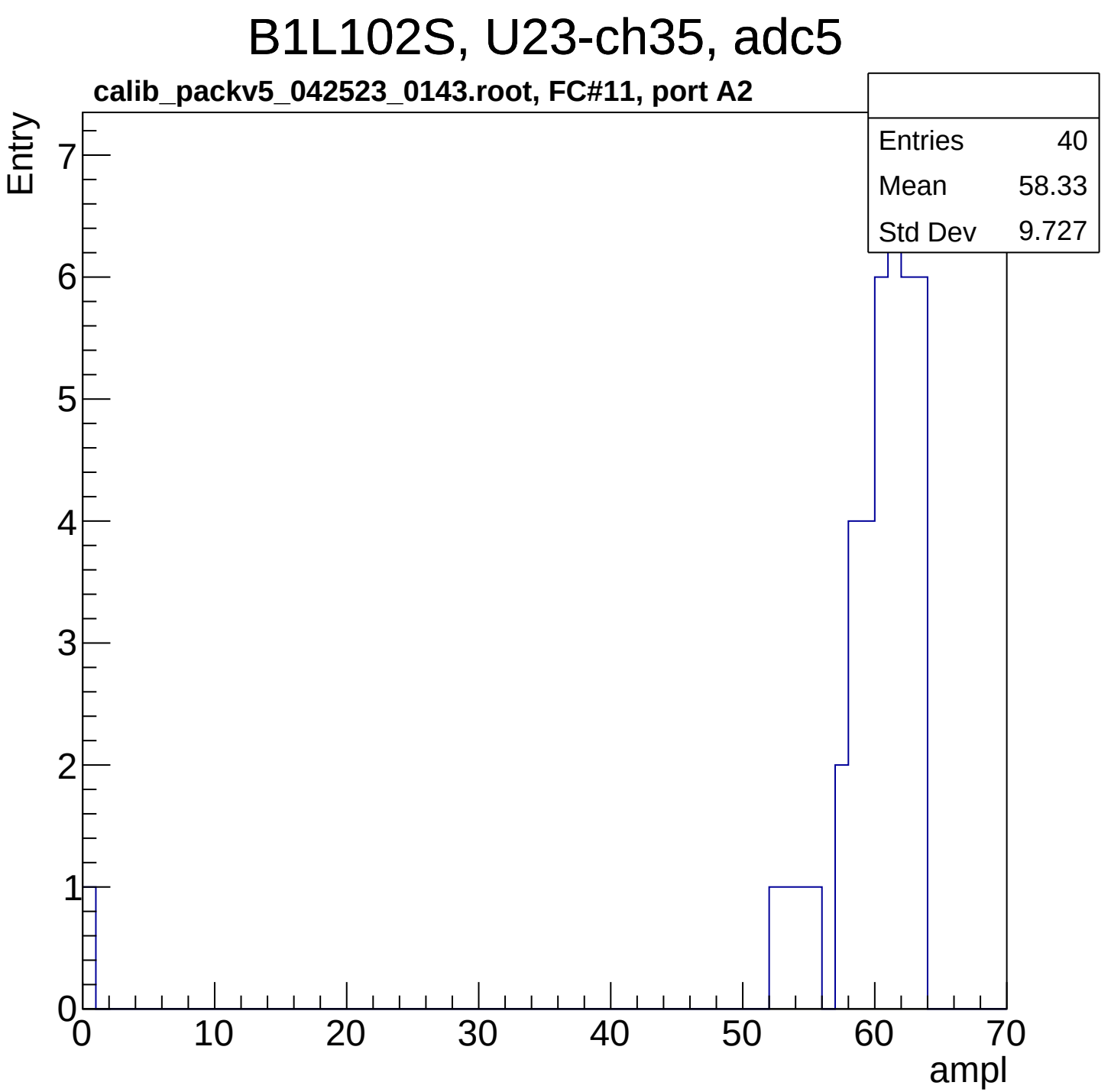

B1L102S, U23-ch35, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 27.64
Std Dev 4.765
Gaus mean : 28.4304
Gaus Width: 3.5312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

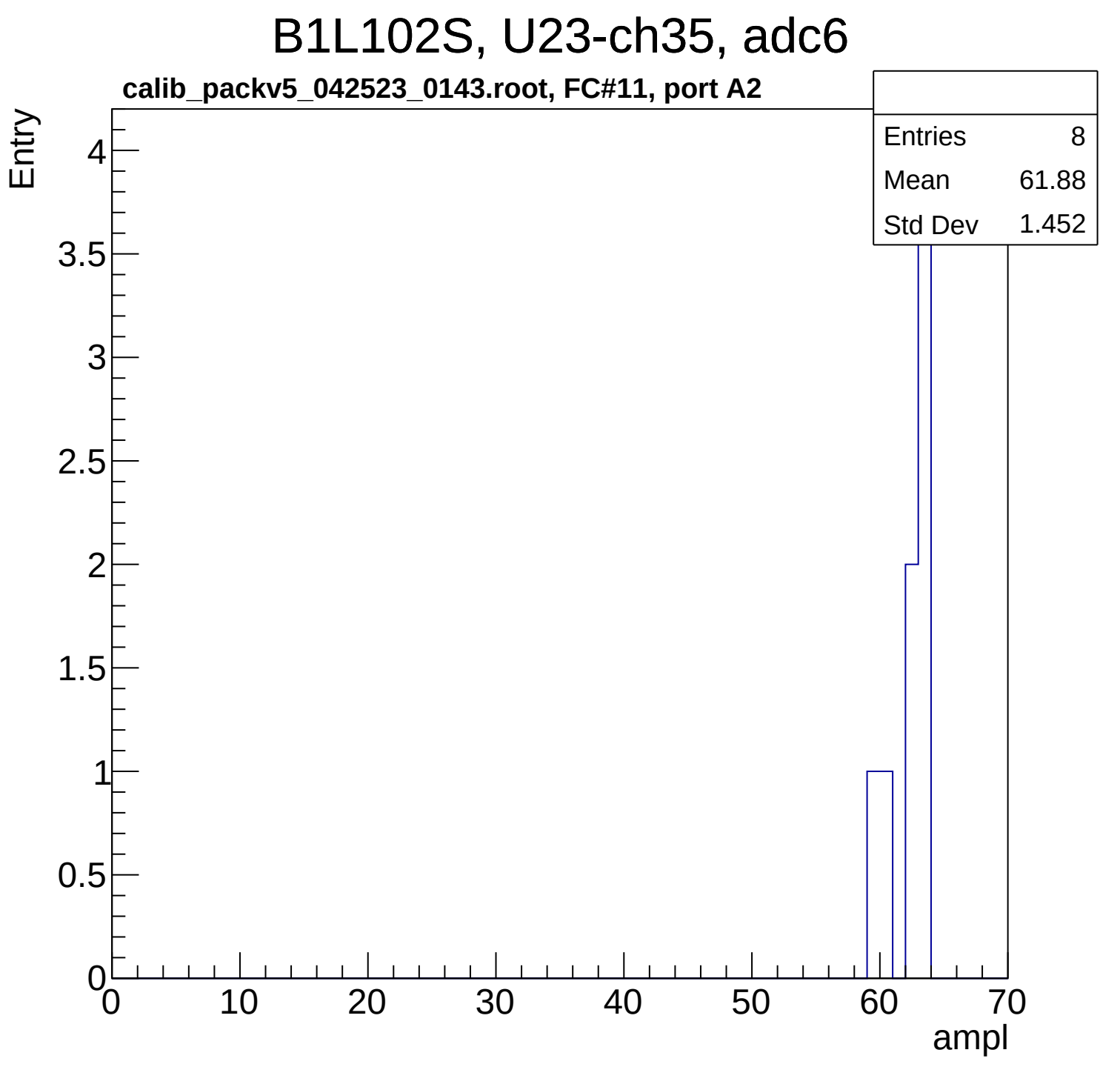

B1L102S, U23-ch35, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 35.53
Std Dev 3.597
Gaus mean : 35.6984
Gaus Width: 3.4474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

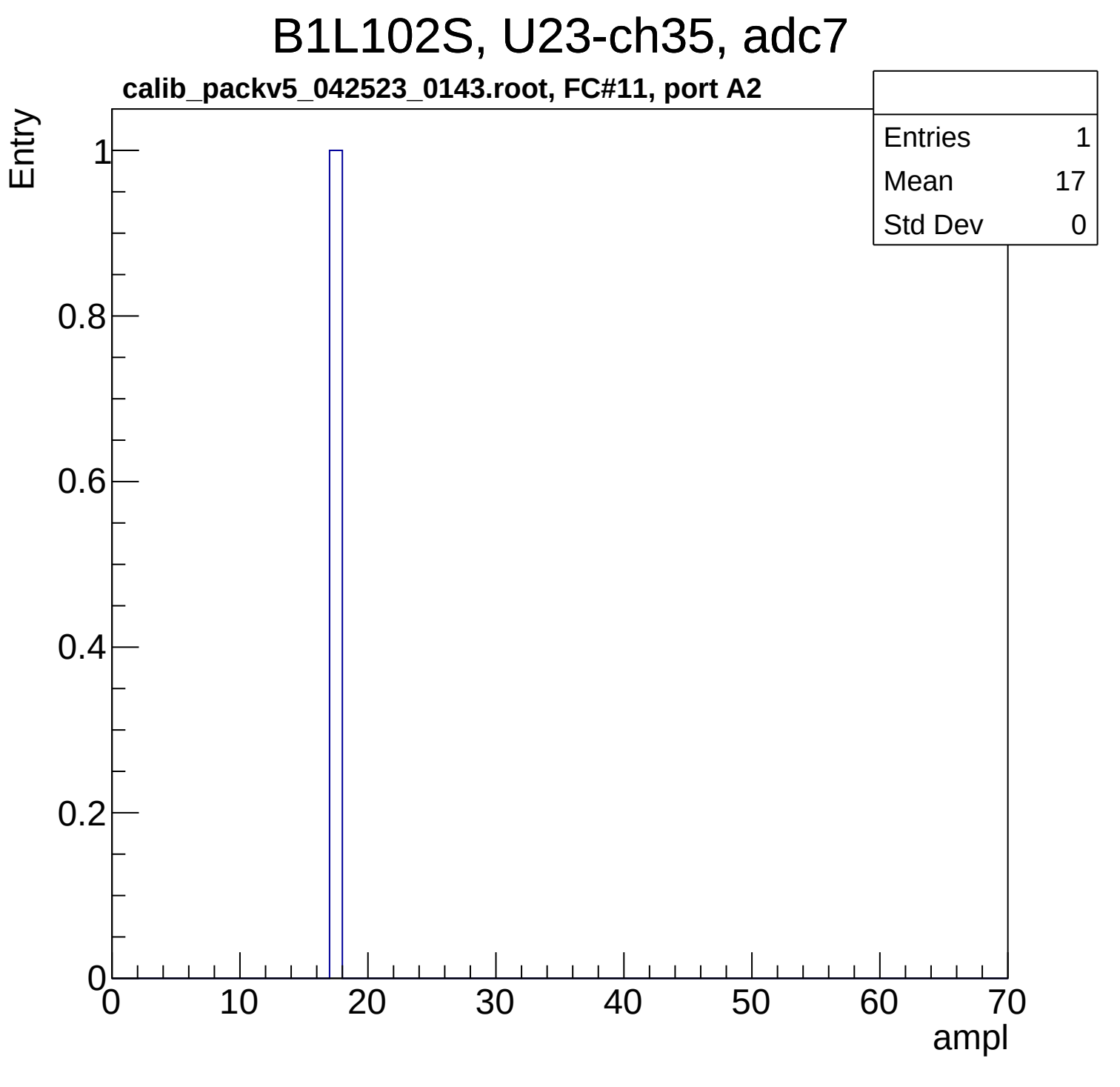

B1L102S, U23-ch35, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 42.43
Std Dev 3.47
Gaus mean : 43.3263
Gaus Width: 3.1036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

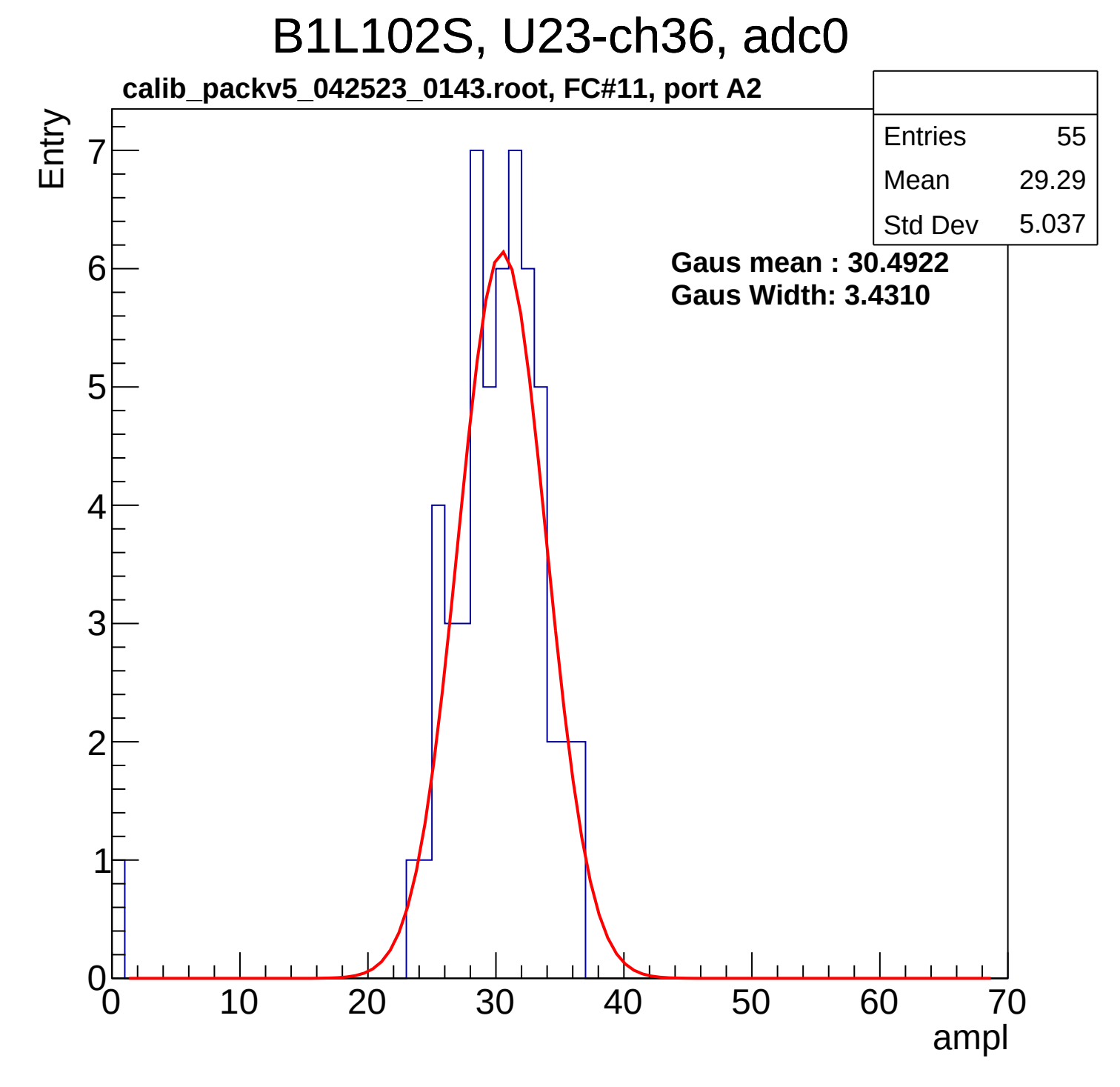

B1L102S, U23-ch35, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 48.5
Std Dev 3.168
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

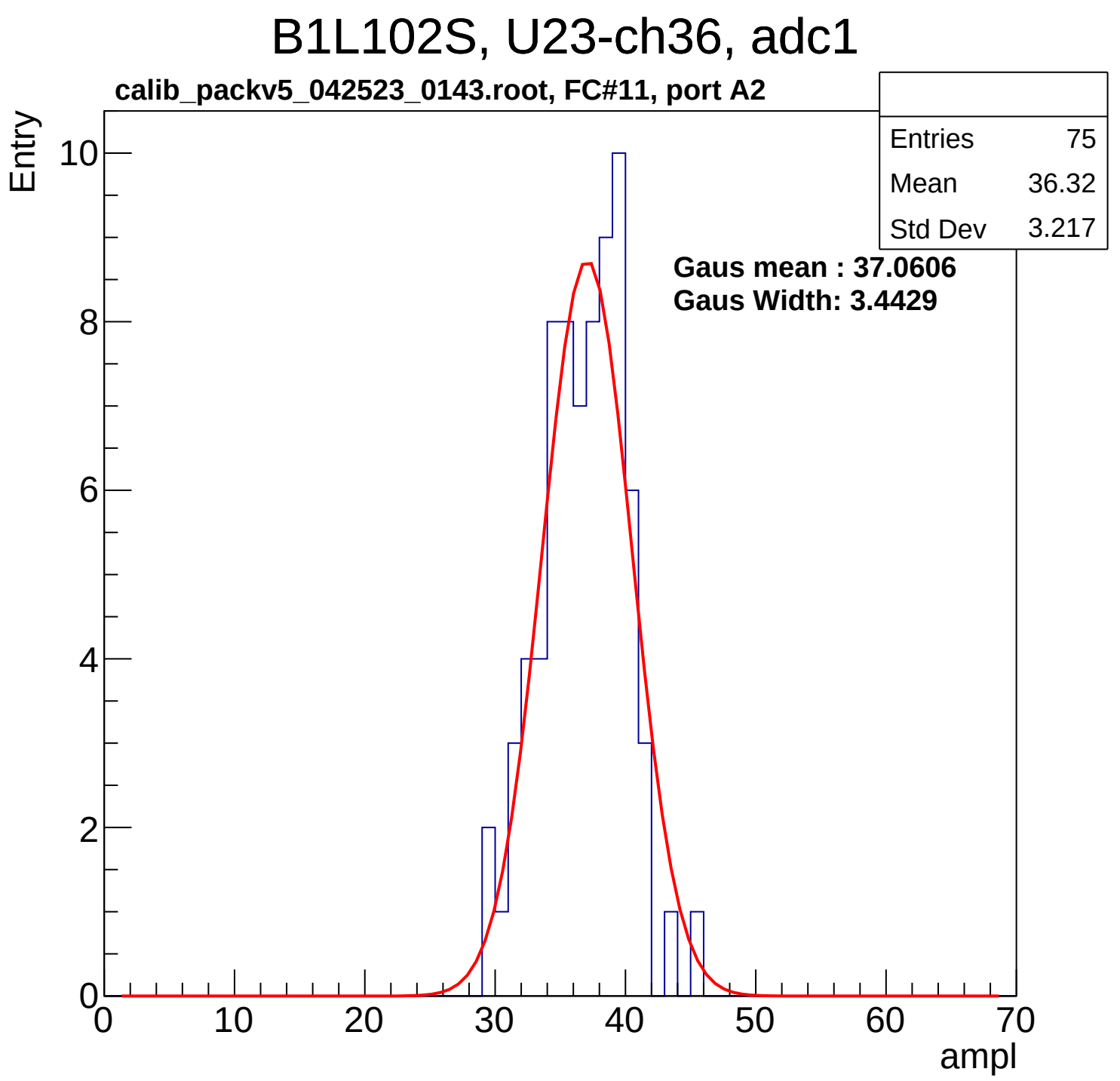

B1L102S, U23-ch35, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 55.69
Std Dev 3.145
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

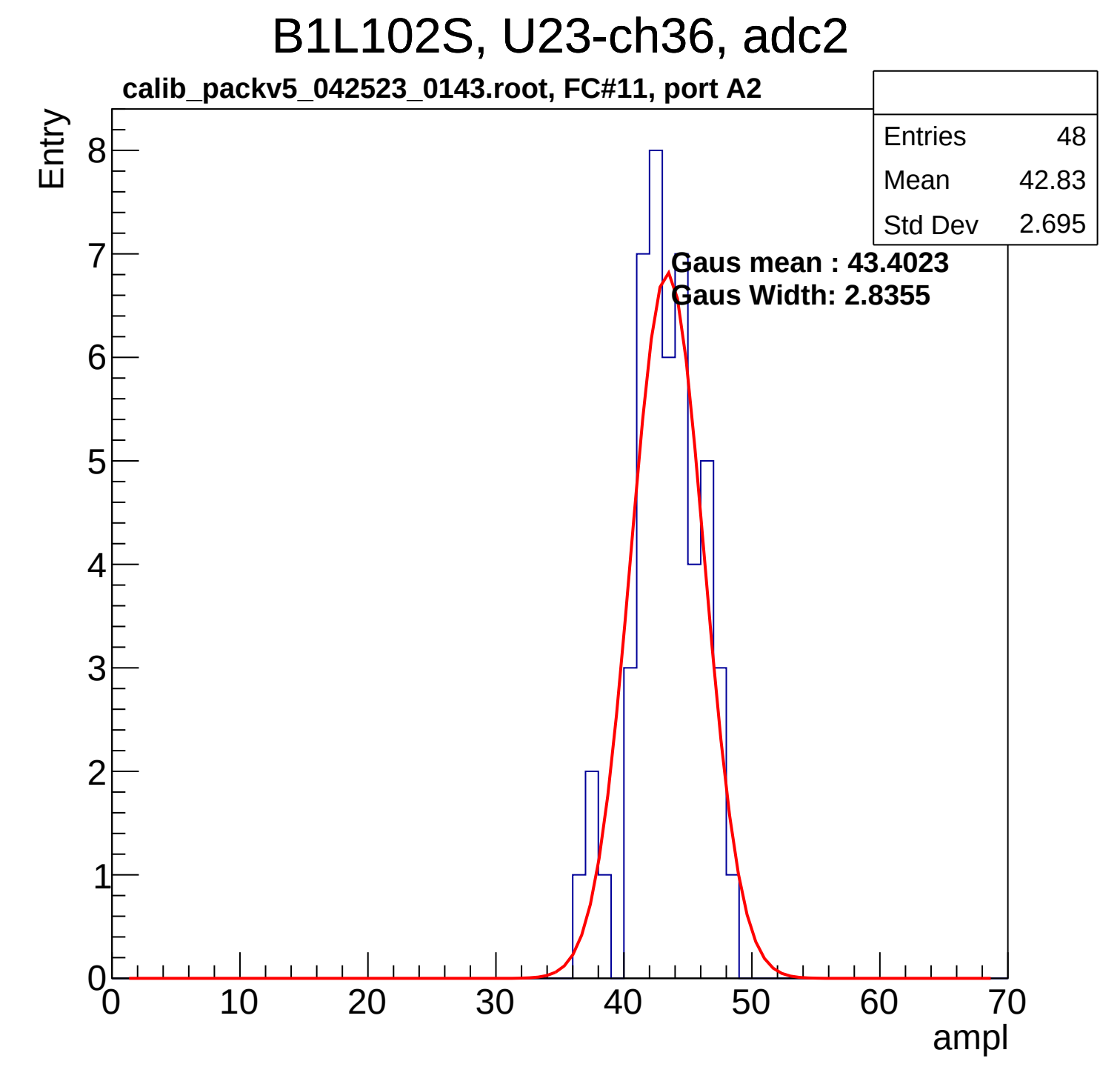

B1L102S, U23-ch35, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.33
Std Dev 9.727
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

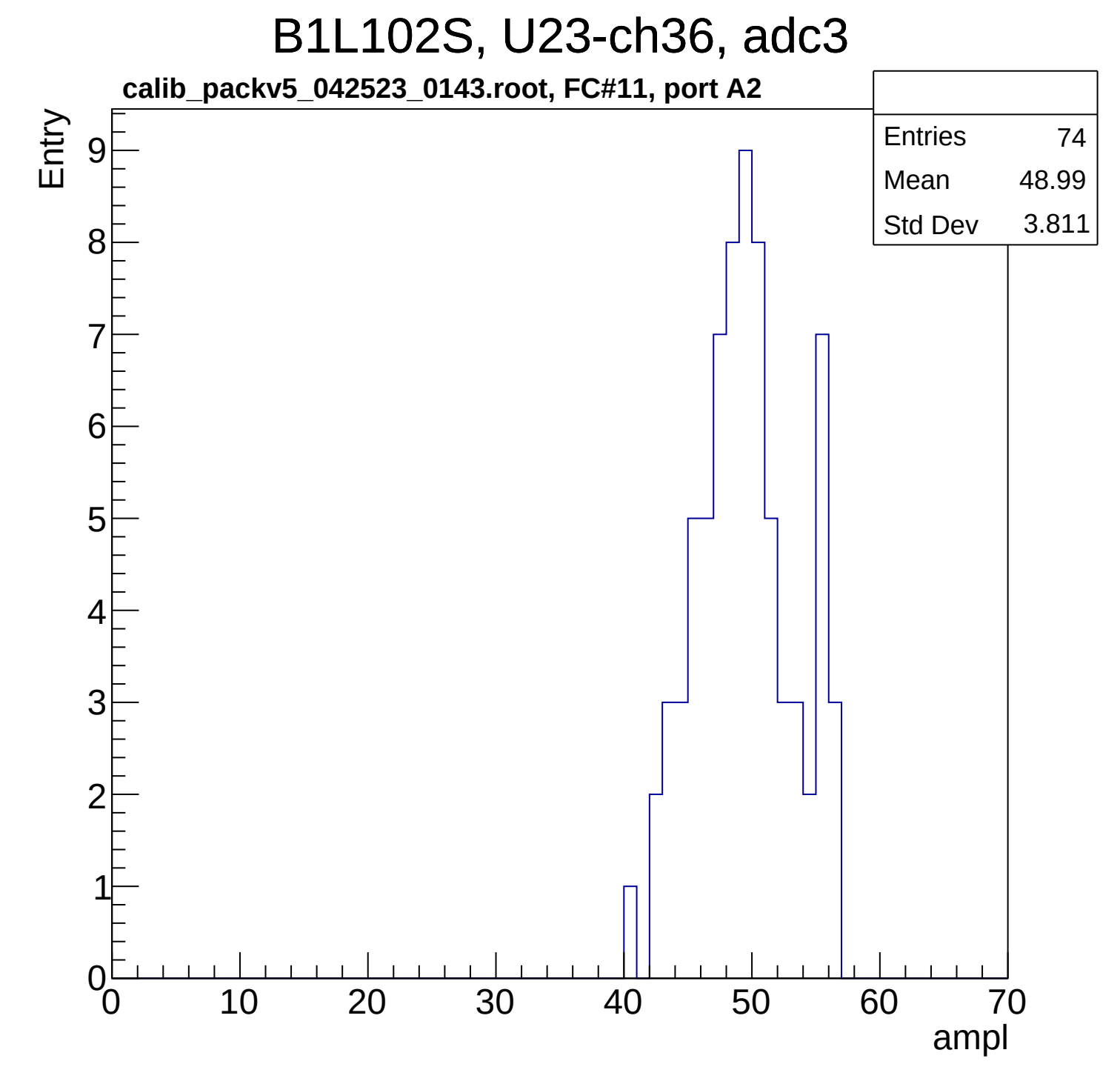

B1L102S, U23-ch35, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.88
Std Dev 1.452
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

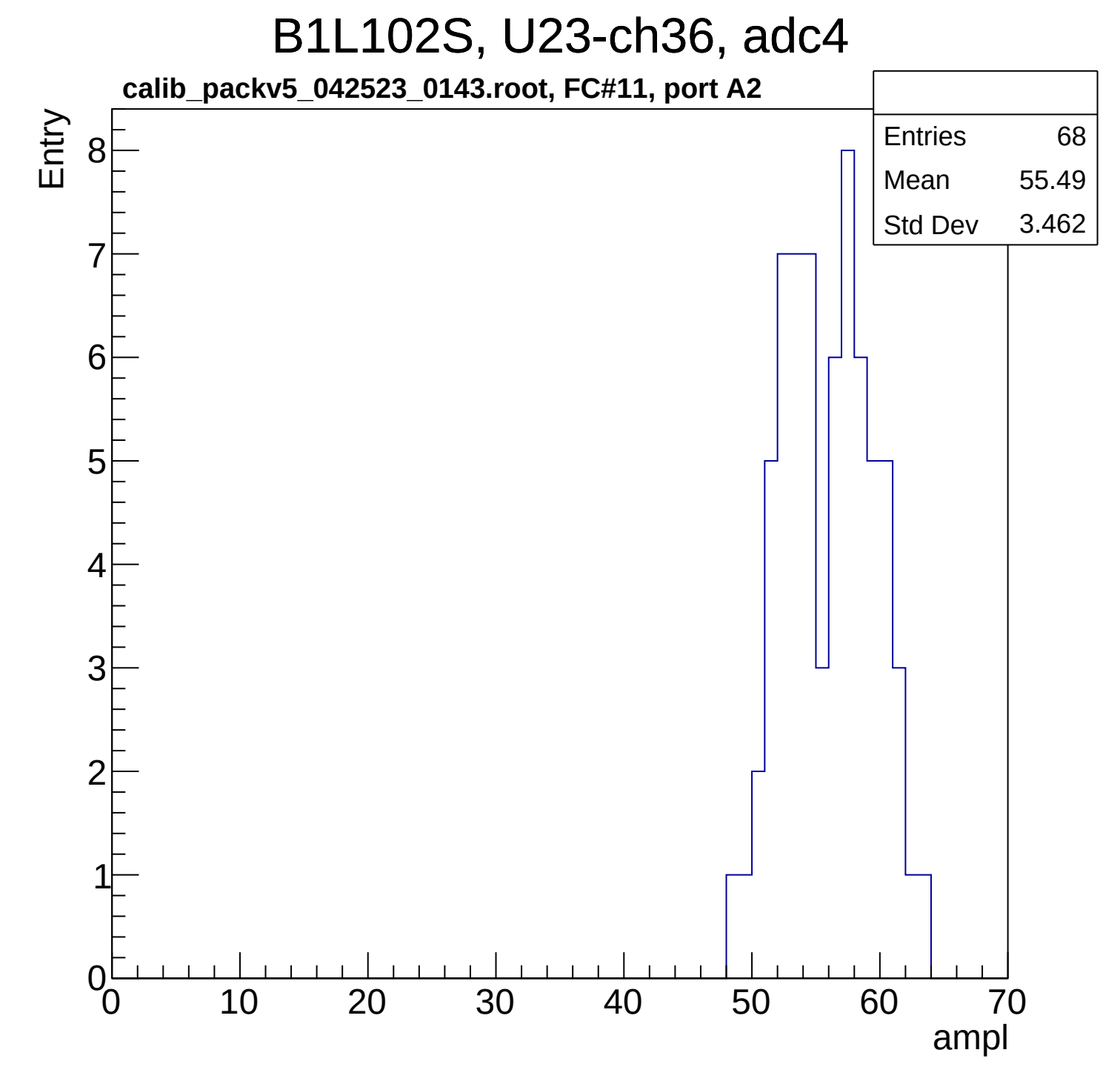

B1L102S, U23-ch35, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 17
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

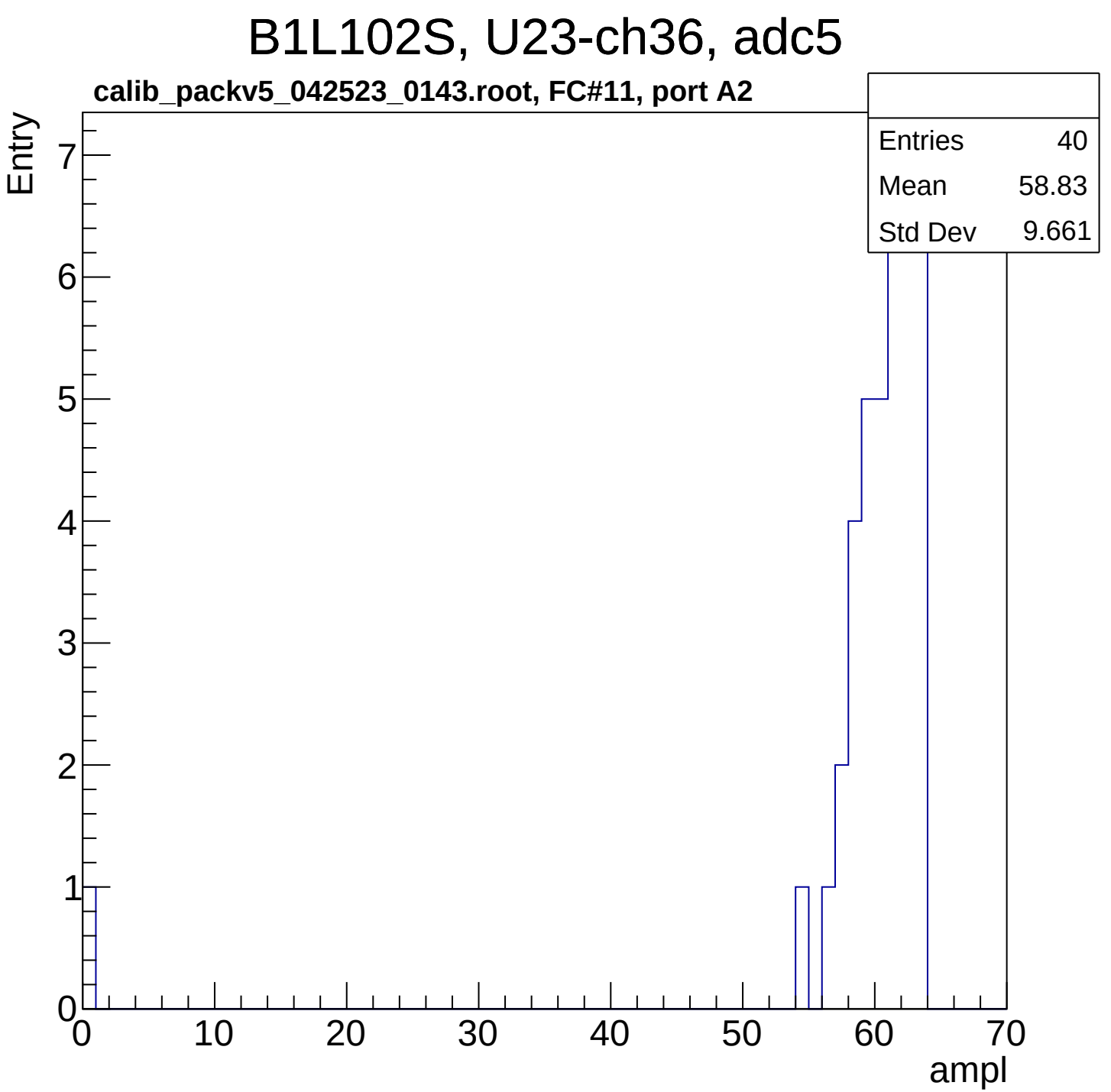

B1L102S, U23-ch36, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 29.29
Std Dev 5.037
Gaus mean : 30.4922
Gaus Width: 3.4310
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

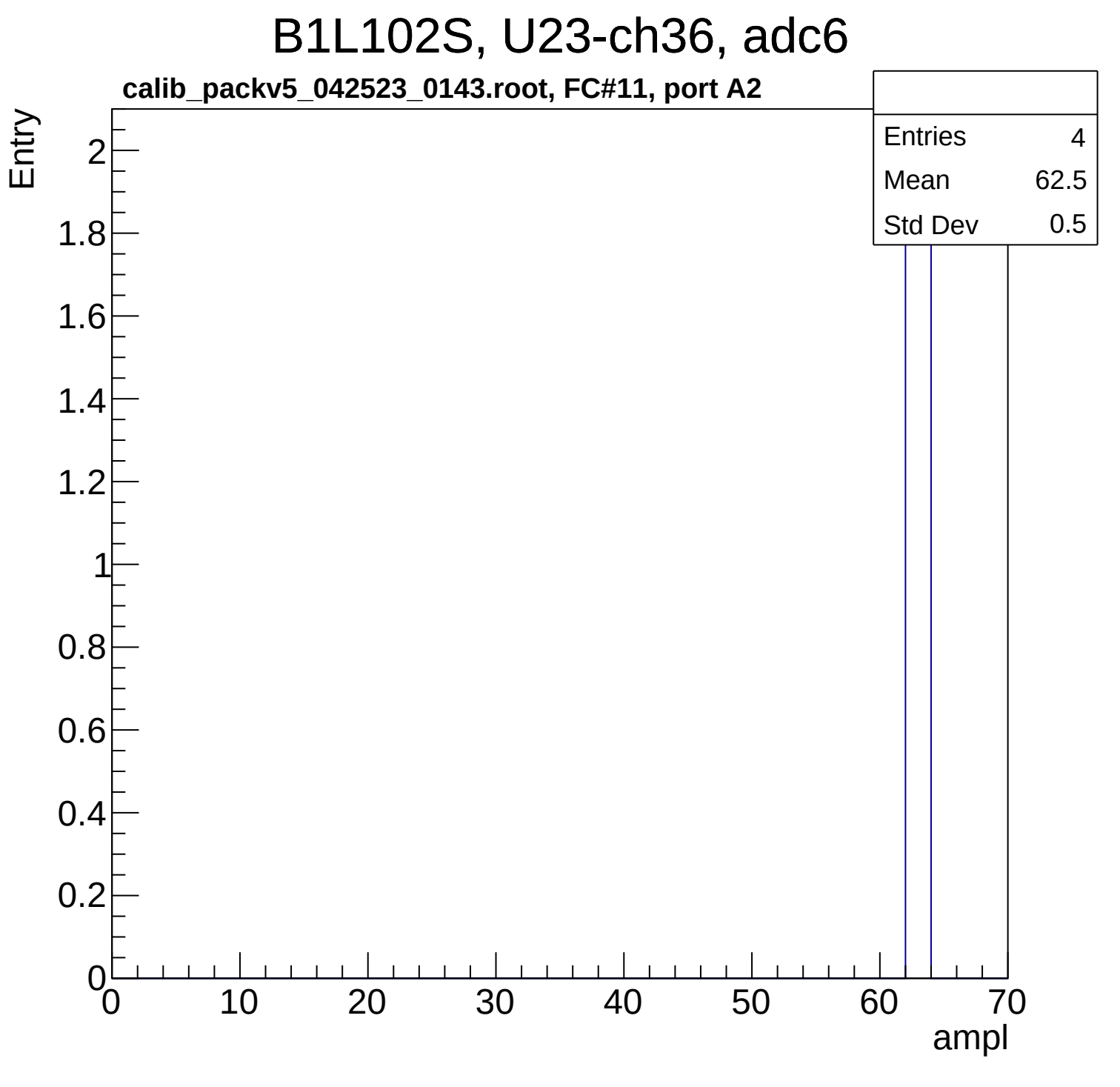

B1L102S, U23-ch36, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 36.32
Std Dev 3.217
Gaus mean : 37.0606
Gaus Width: 3.4429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

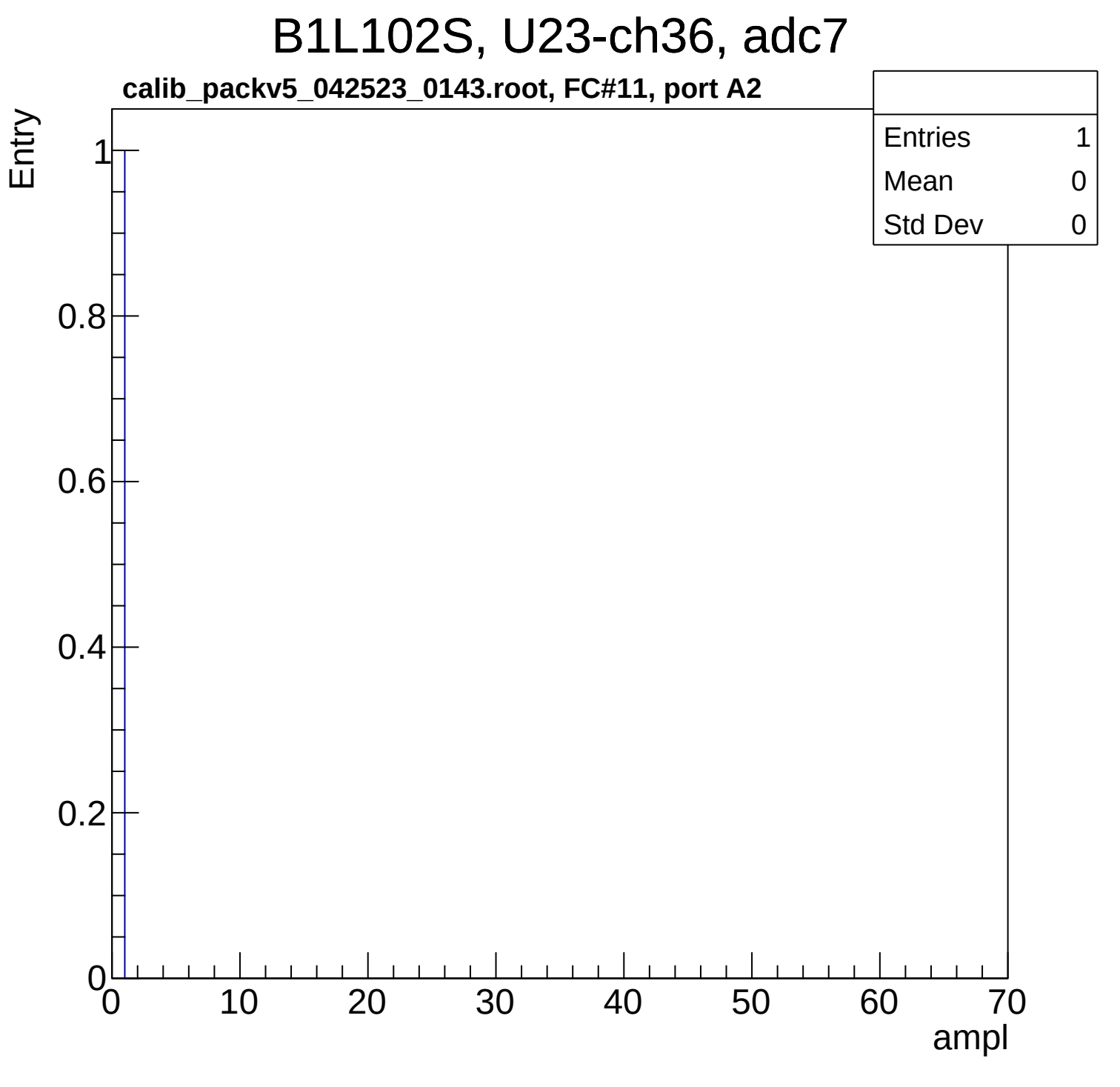

B1L102S, U23-ch36, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 42.83
Std Dev 2.695
Gaus mean : 43.4023
Gaus Width: 2.8355
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

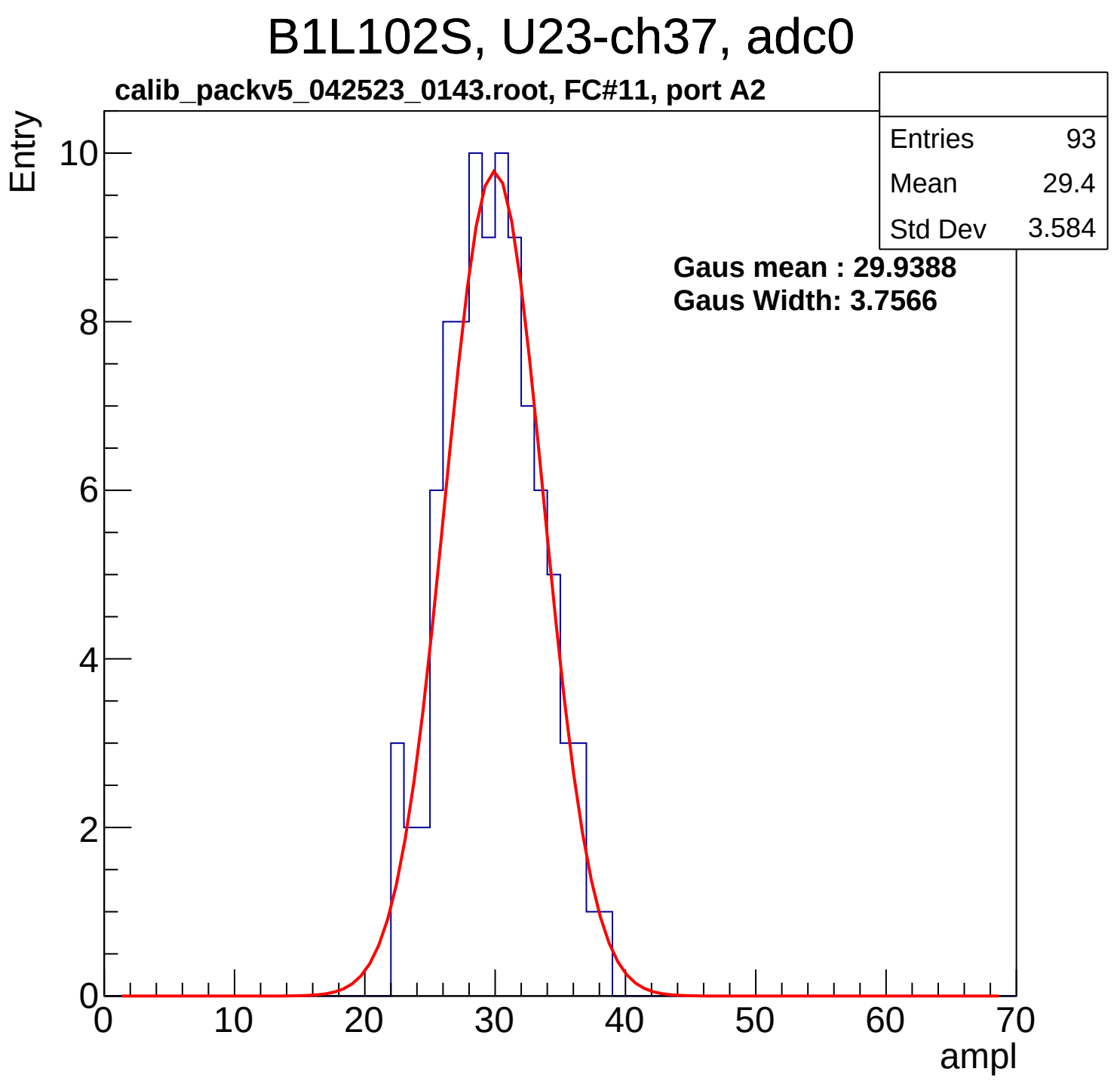

B1L102S, U23-ch36, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 48.99
Std Dev 3.811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

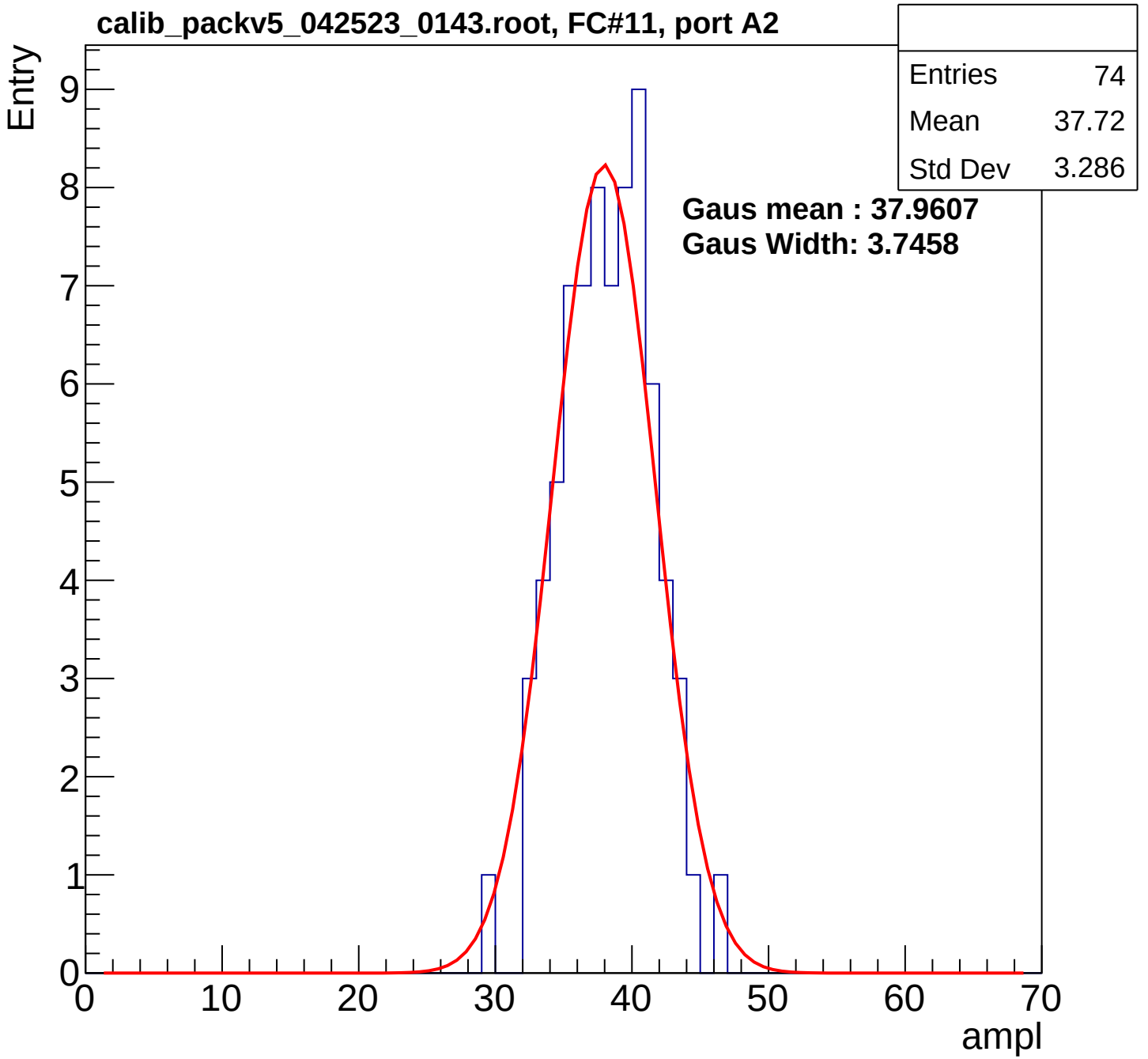

B1L102S, U23-ch36, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 55.49
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

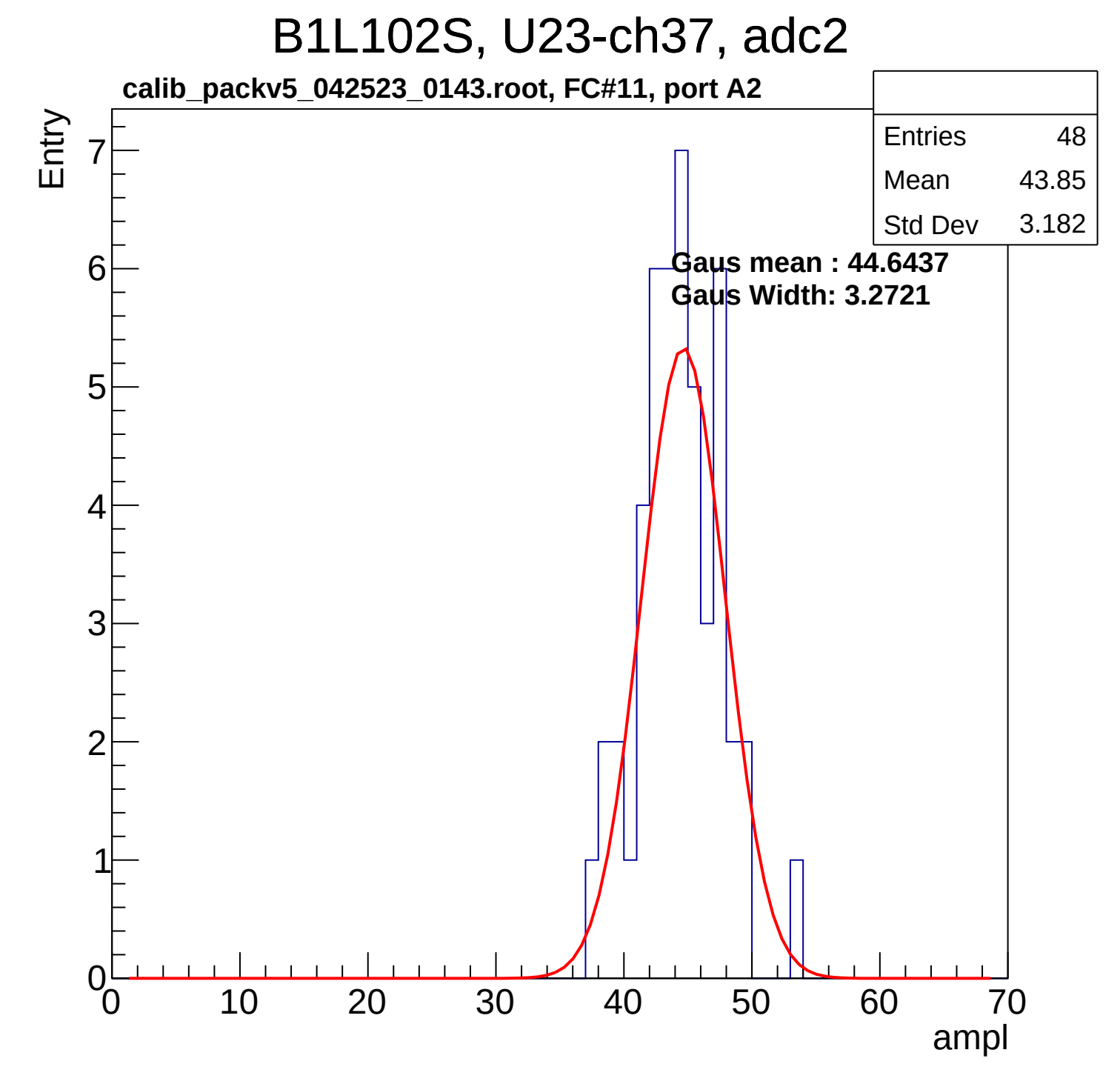

B1L102S, U23-ch36, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.83
Std Dev 9.661
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

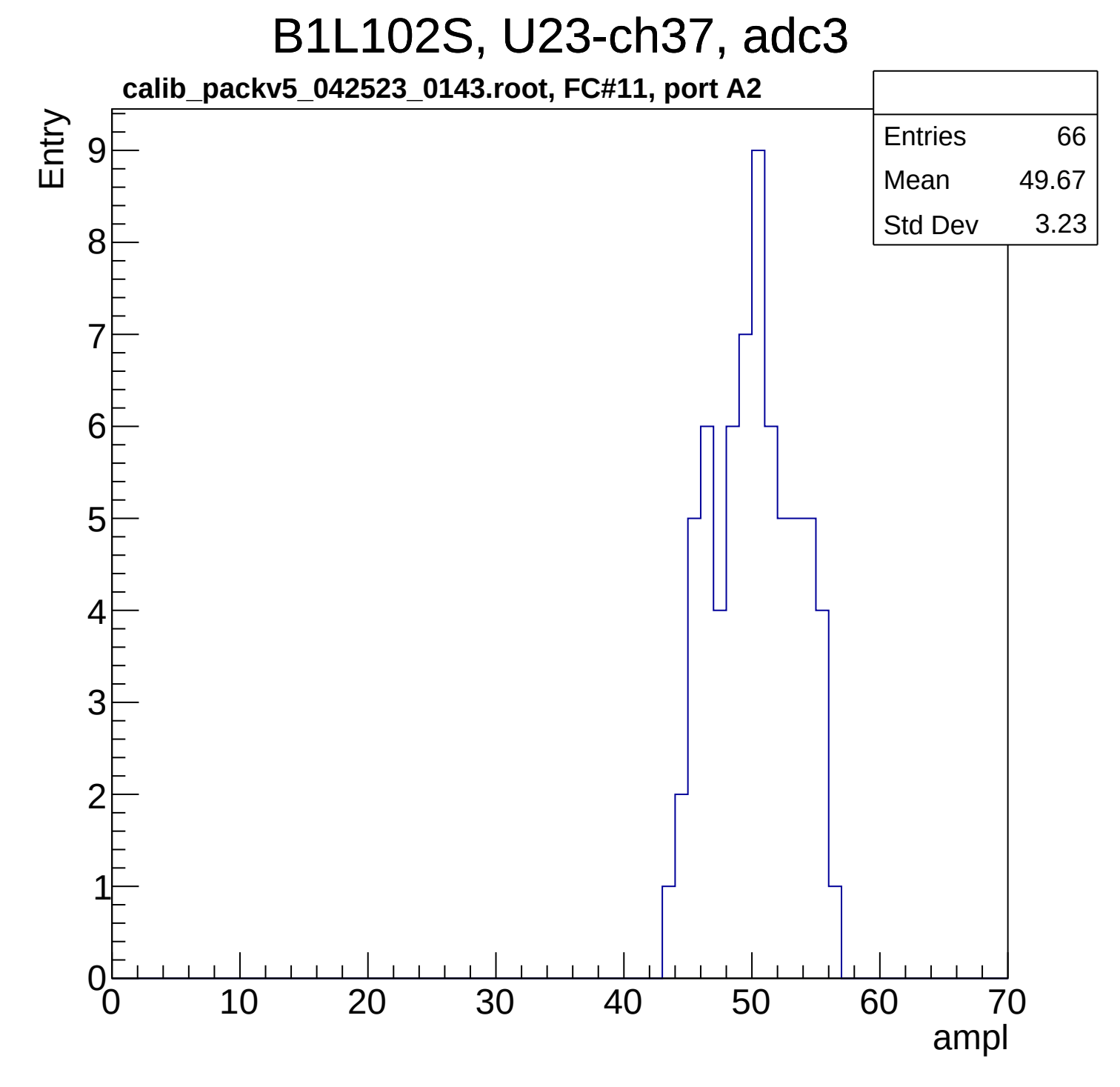

B1L102S, U23-ch36, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

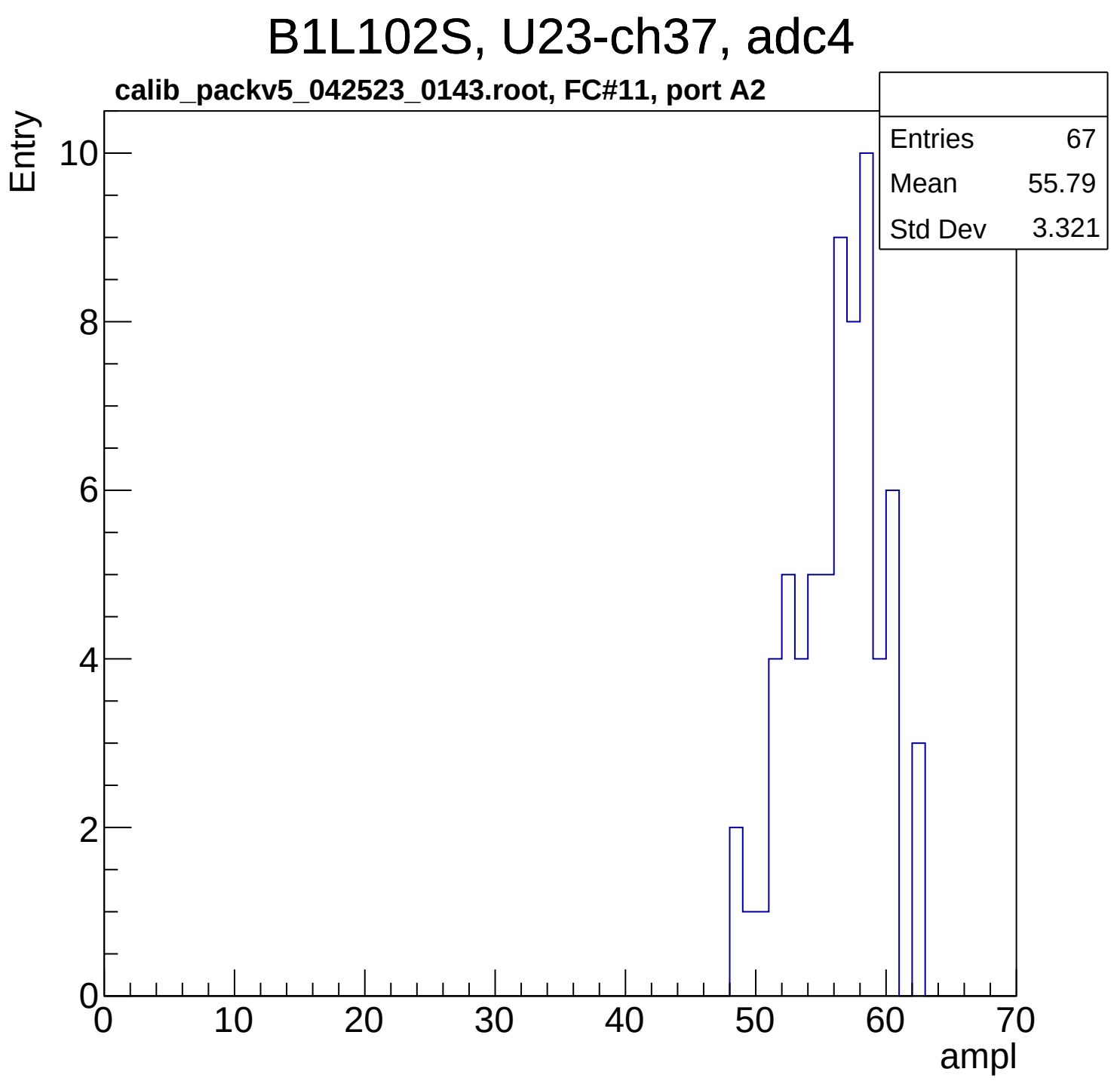

B1L102S, U23-ch36, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

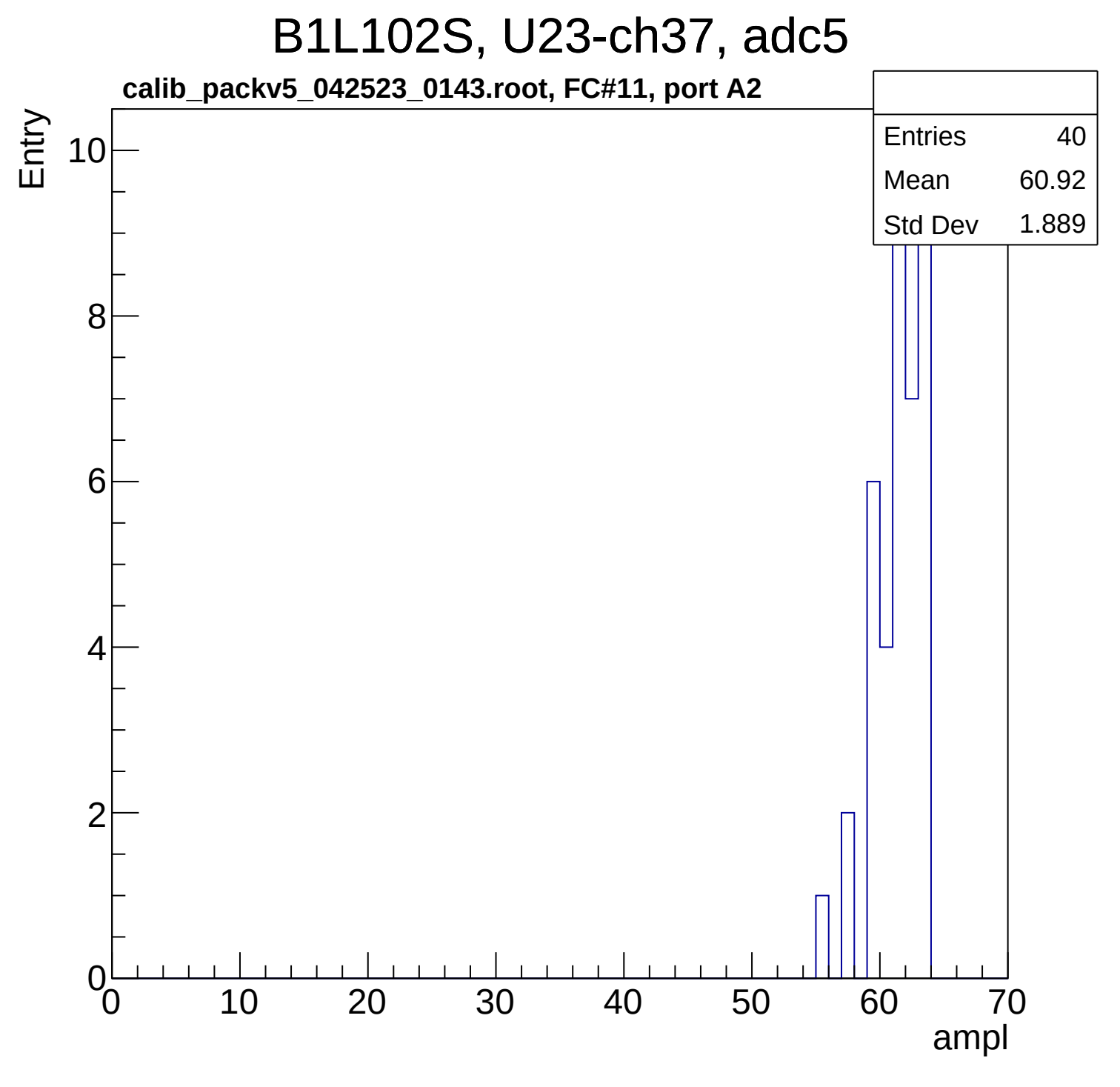

B1L102S, U23-ch37, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 93
Mean 29.4
Std Dev 3.584
Gaus mean : 29.9388
Gaus Width: 3.7566
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

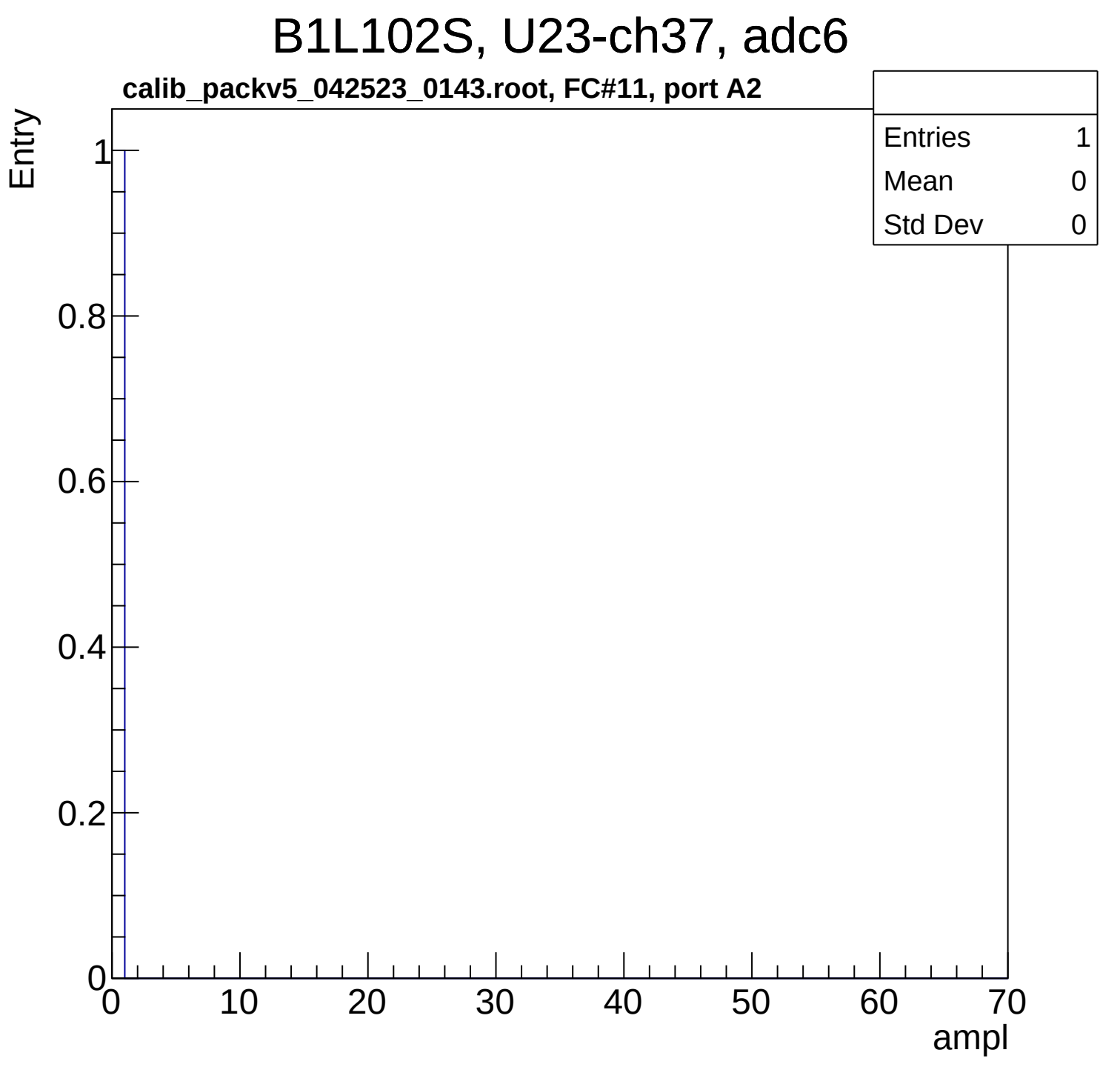

B1L102S, U23-ch37, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 37.72
Std Dev 3.286
Gaus mean : 37.9607
Gaus Width: 3.7458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

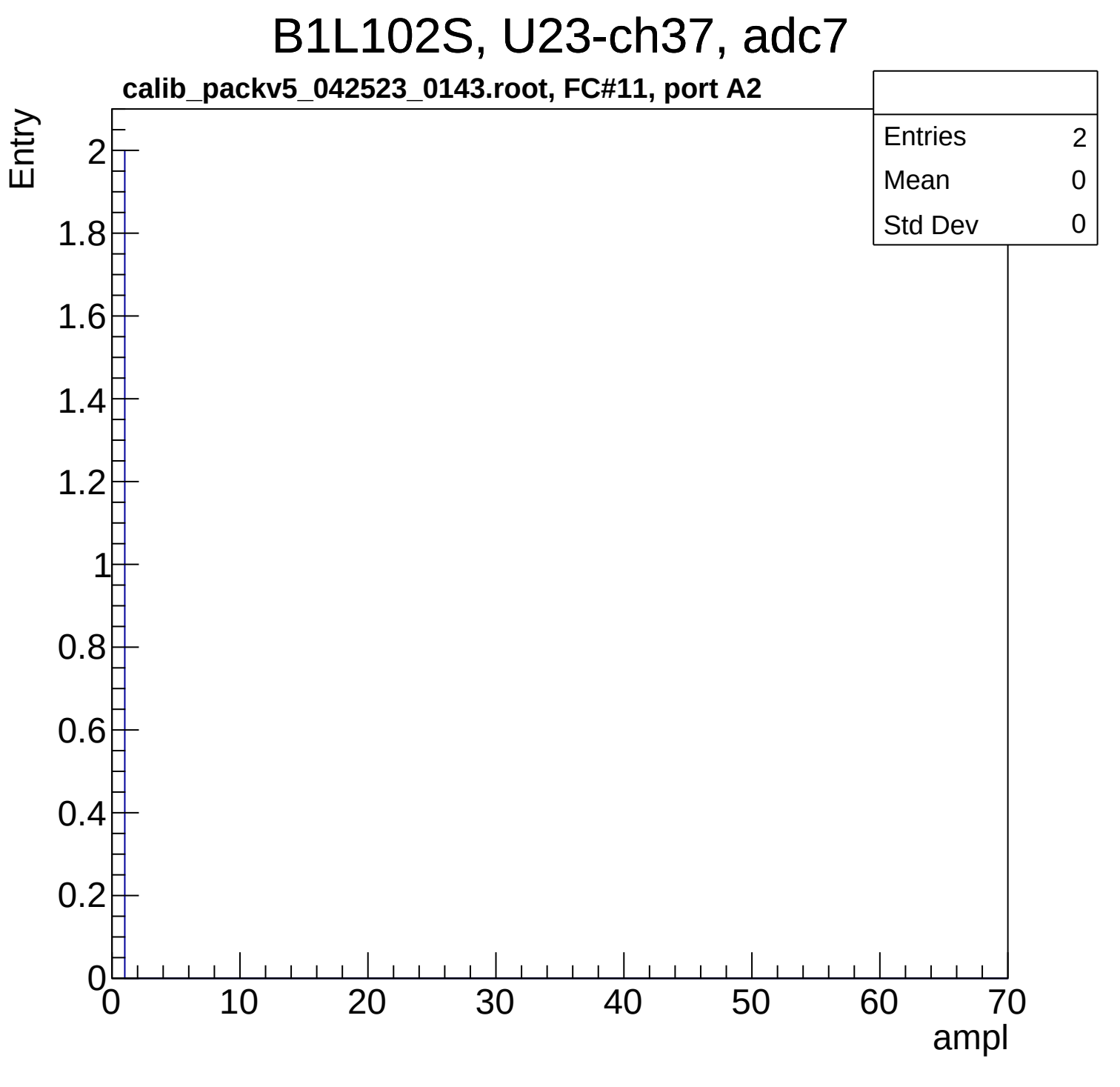

B1L102S, U23-ch37, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 43.85
Std Dev 3.182
Gaus mean : 44.6437
Gaus Width: 3.2721
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

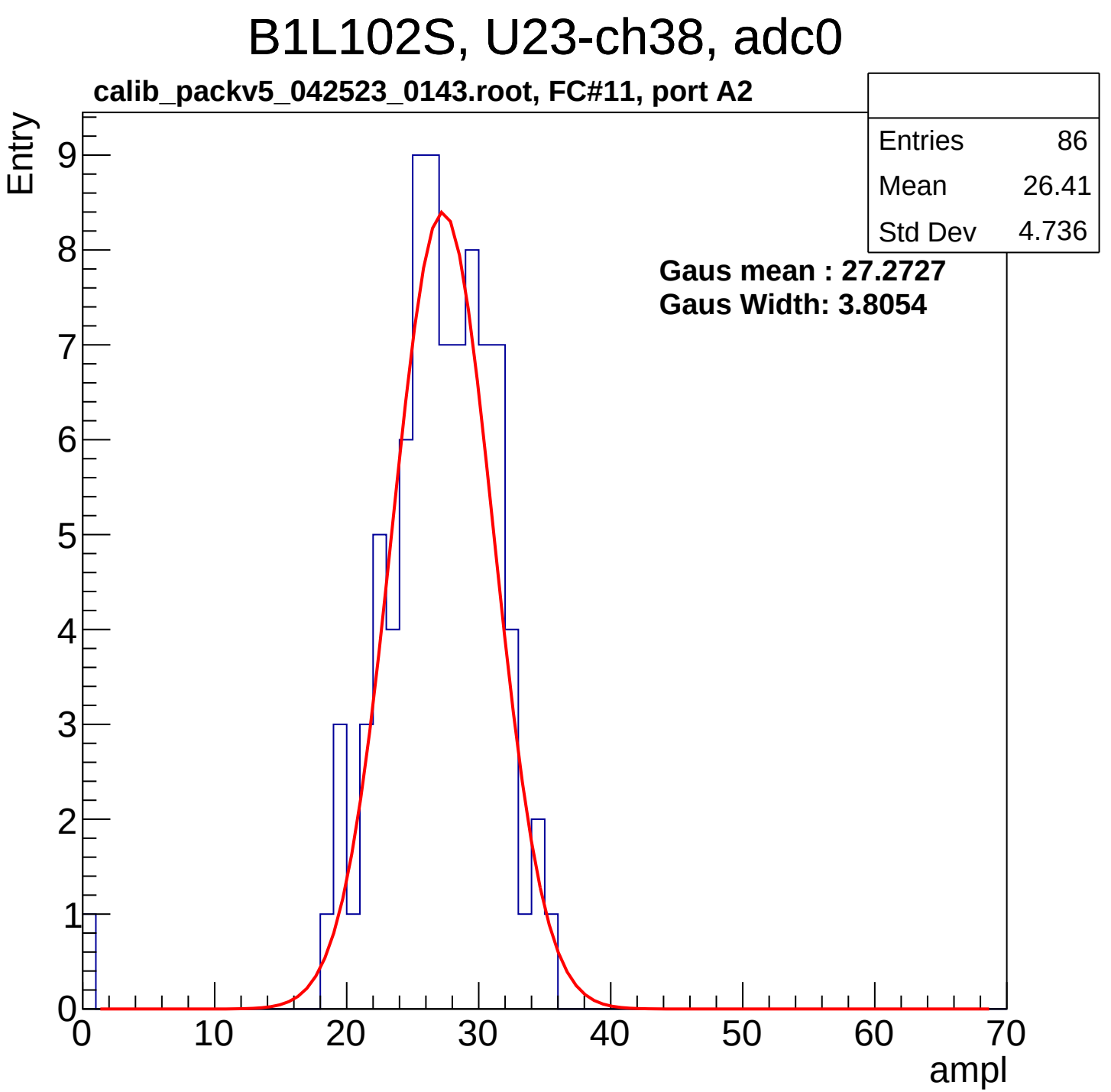

B1L102S, U23-ch37, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 49.67
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

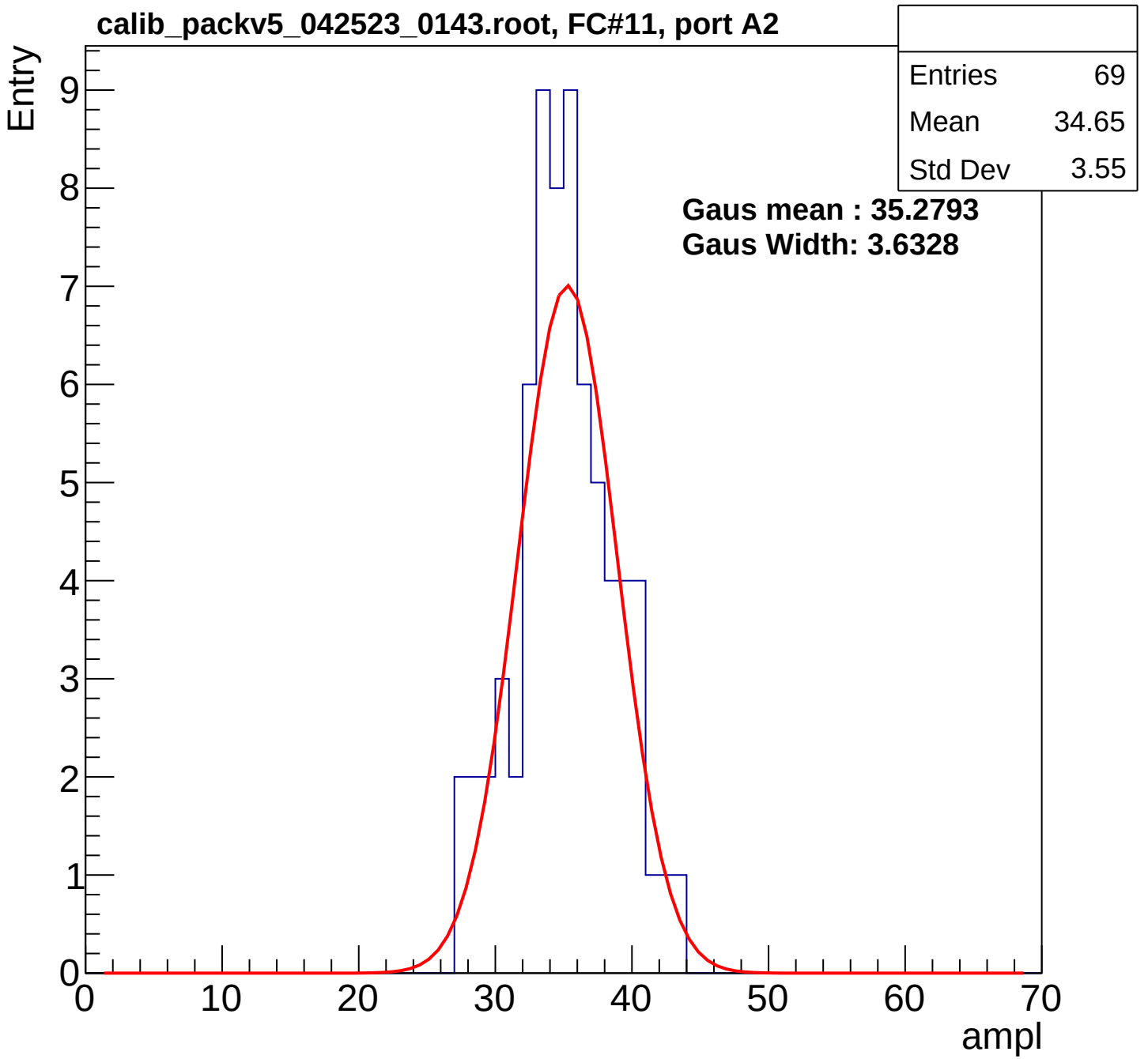

B1L102S, U23-ch37, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 55.79
Std Dev 3.321
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

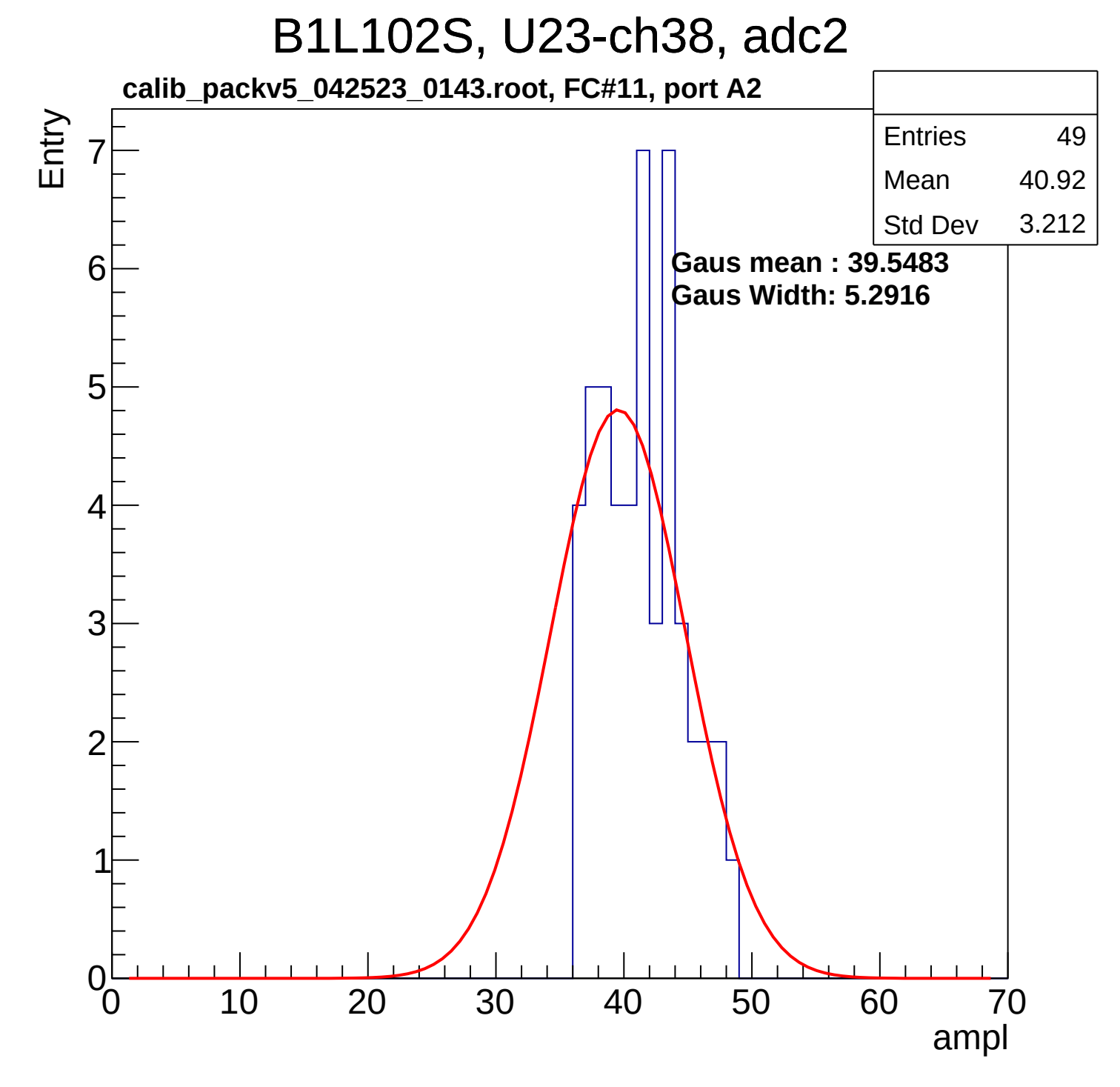

B1L102S, U23-ch37, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 60.92
Std Dev 1.889
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

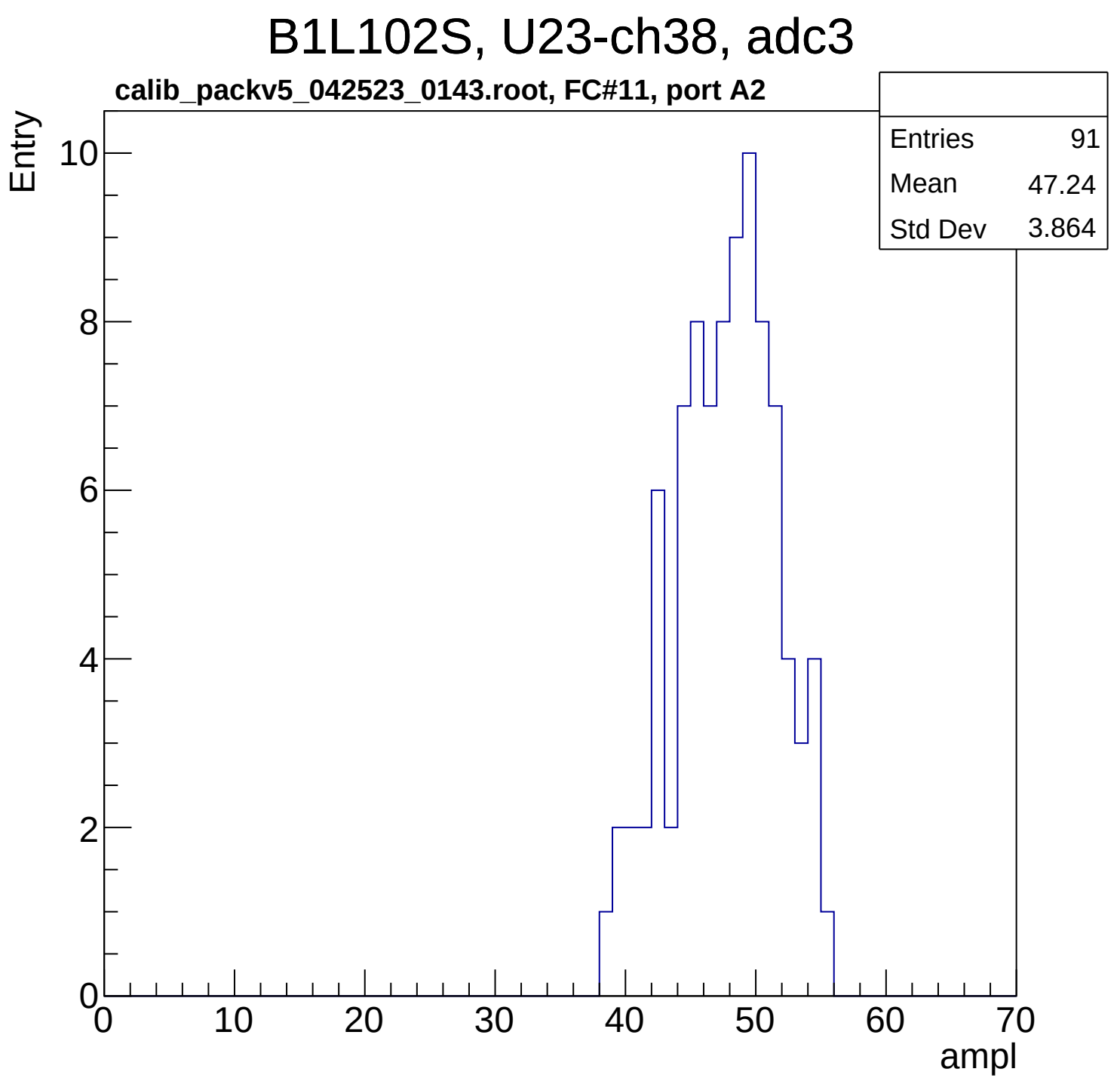

B1L102S, U23-ch37, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

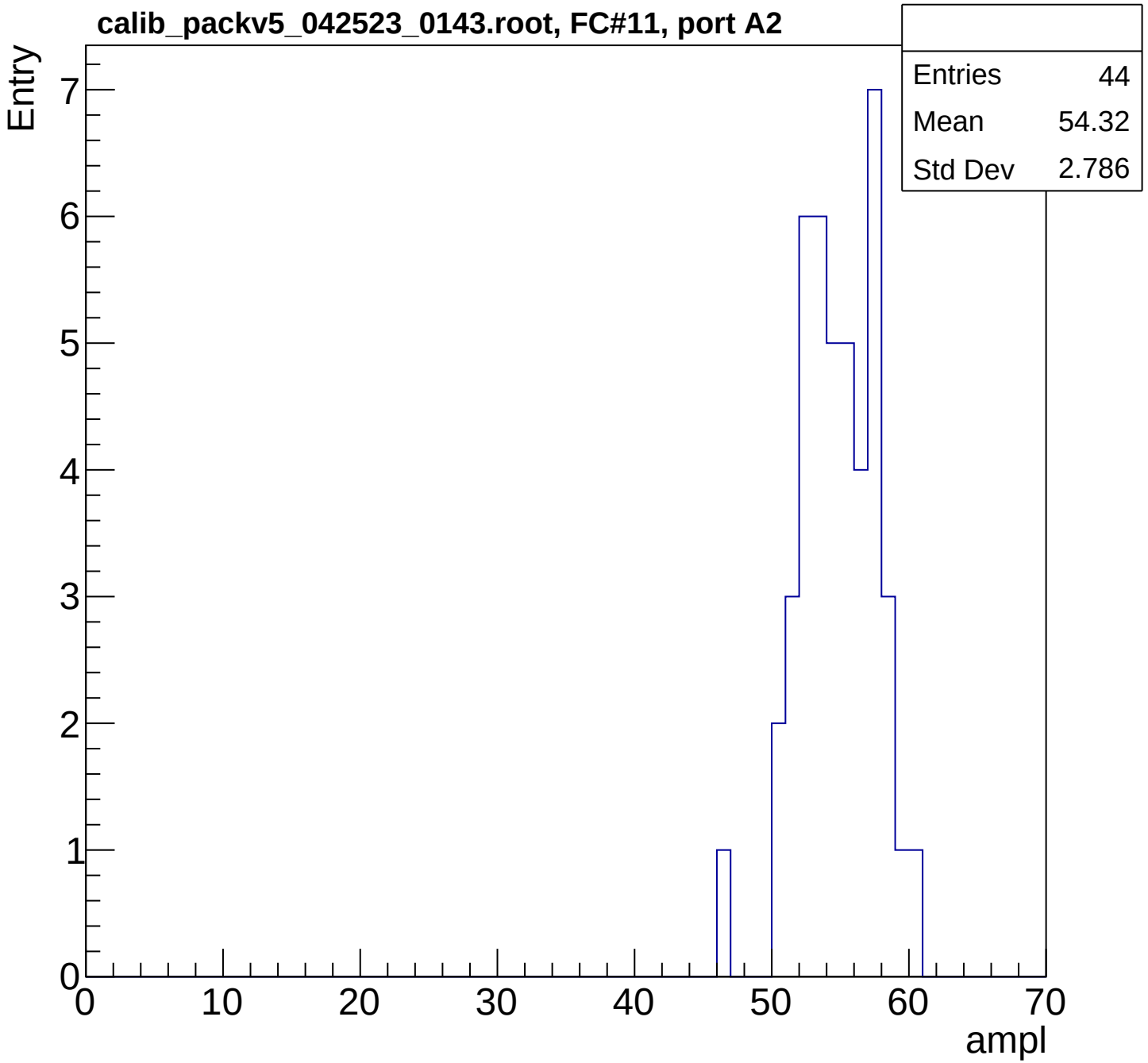

B1L102S, U23-ch37, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

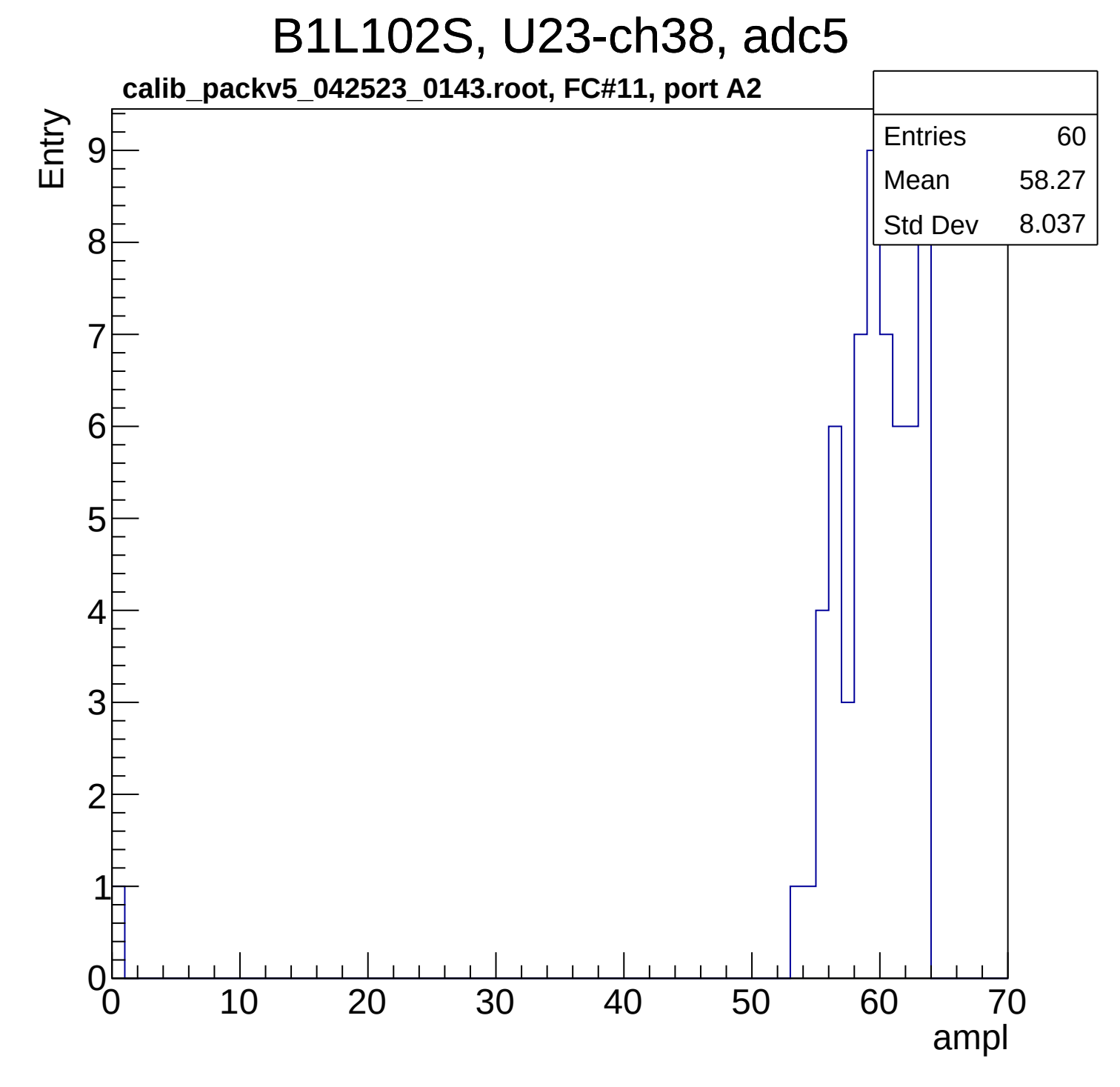

B1L102S, U23-ch38, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 26.41
Std Dev 4.736
Gaus mean : 27.2727
Gaus Width: 3.8054
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

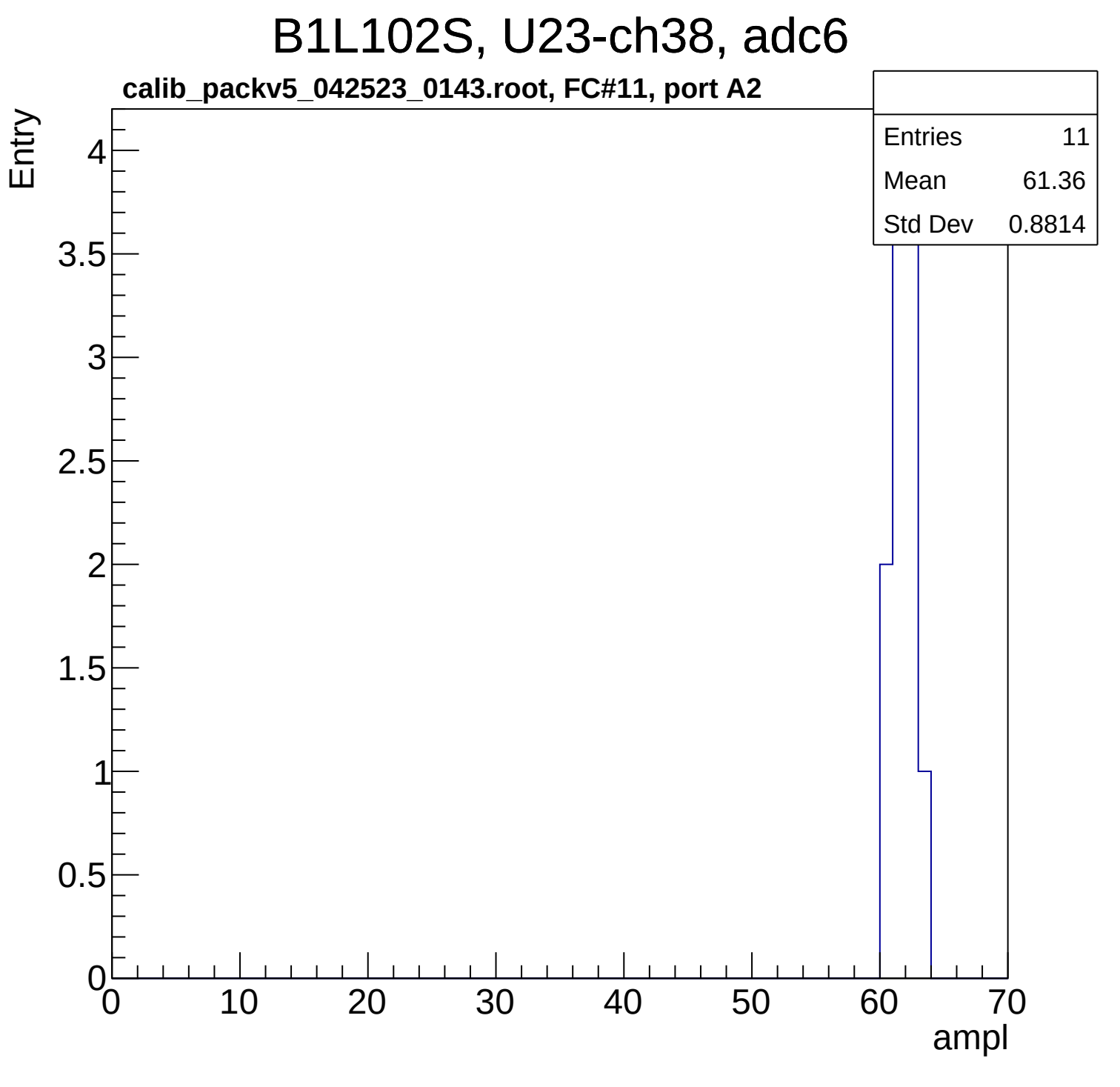

B1L102S, U23-ch38, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 34.65
Std Dev 3.55
Gaus mean : 35.2793
Gaus Width: 3.6328
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

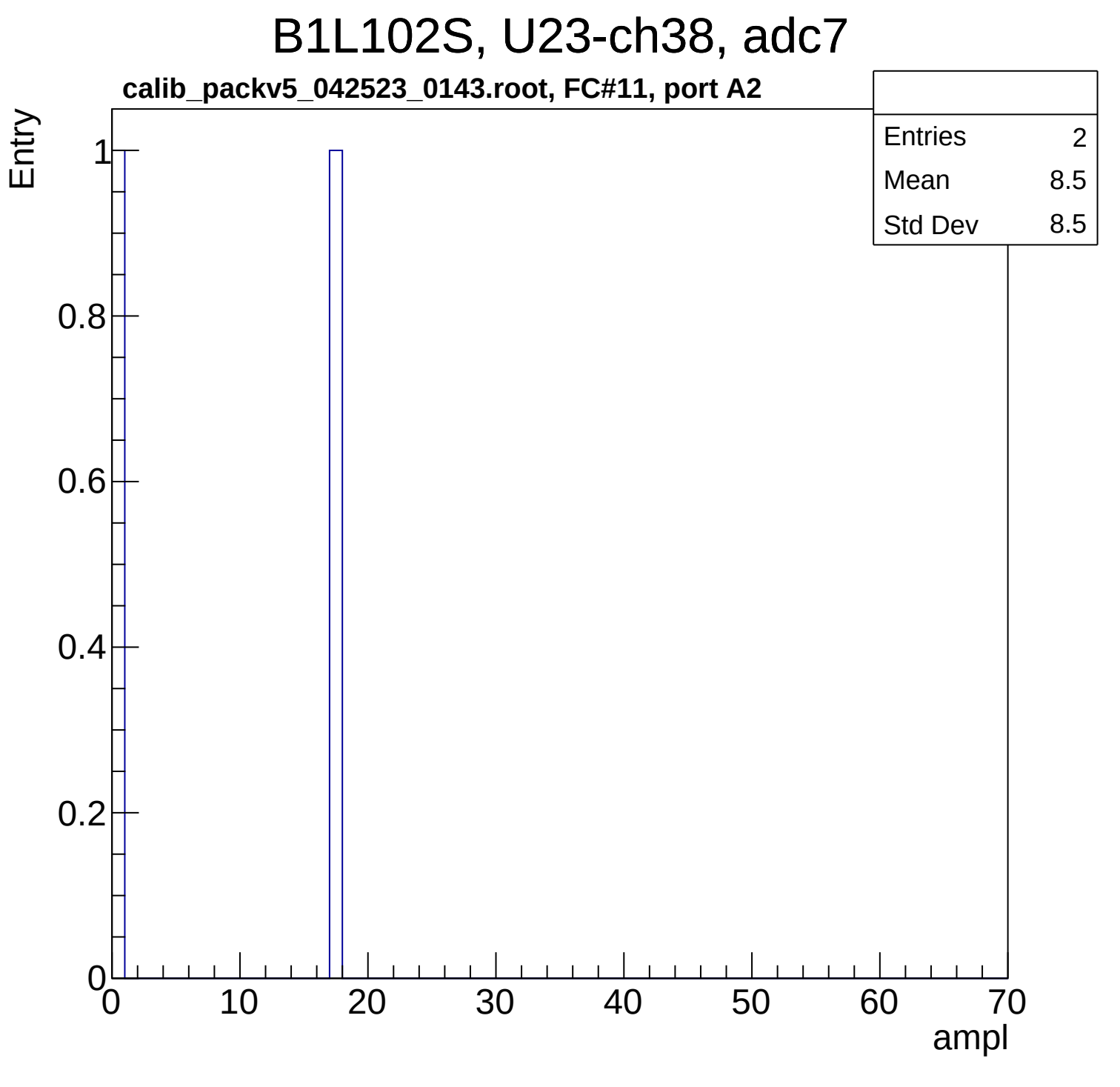

B1L102S, U23-ch38, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 40.92
Std Dev 3.212
Gaus mean : 39.5483
Gaus Width: 5.2916
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

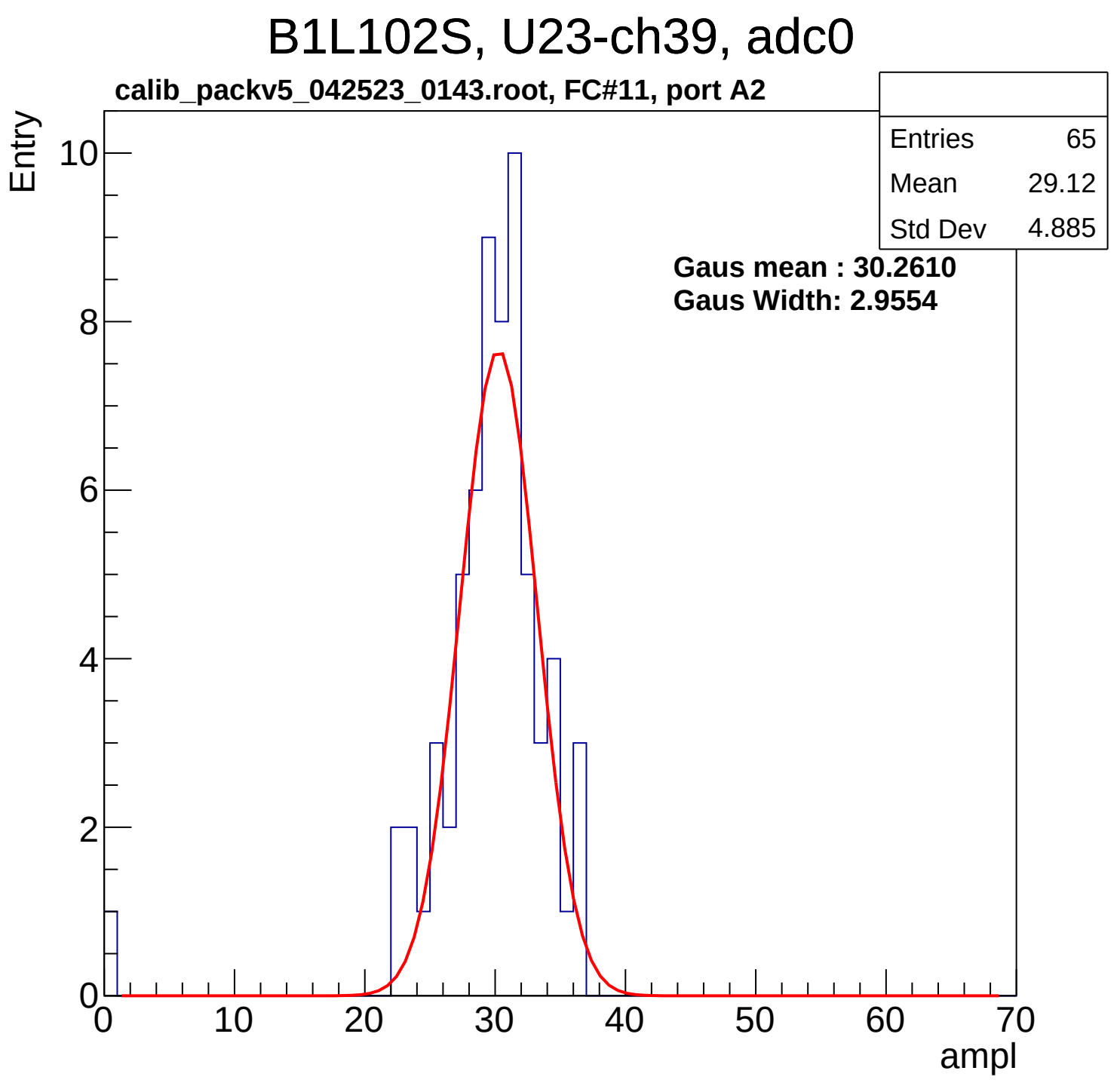

B1L102S, U23-ch38, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 91
Mean 47.24
Std Dev 3.864
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

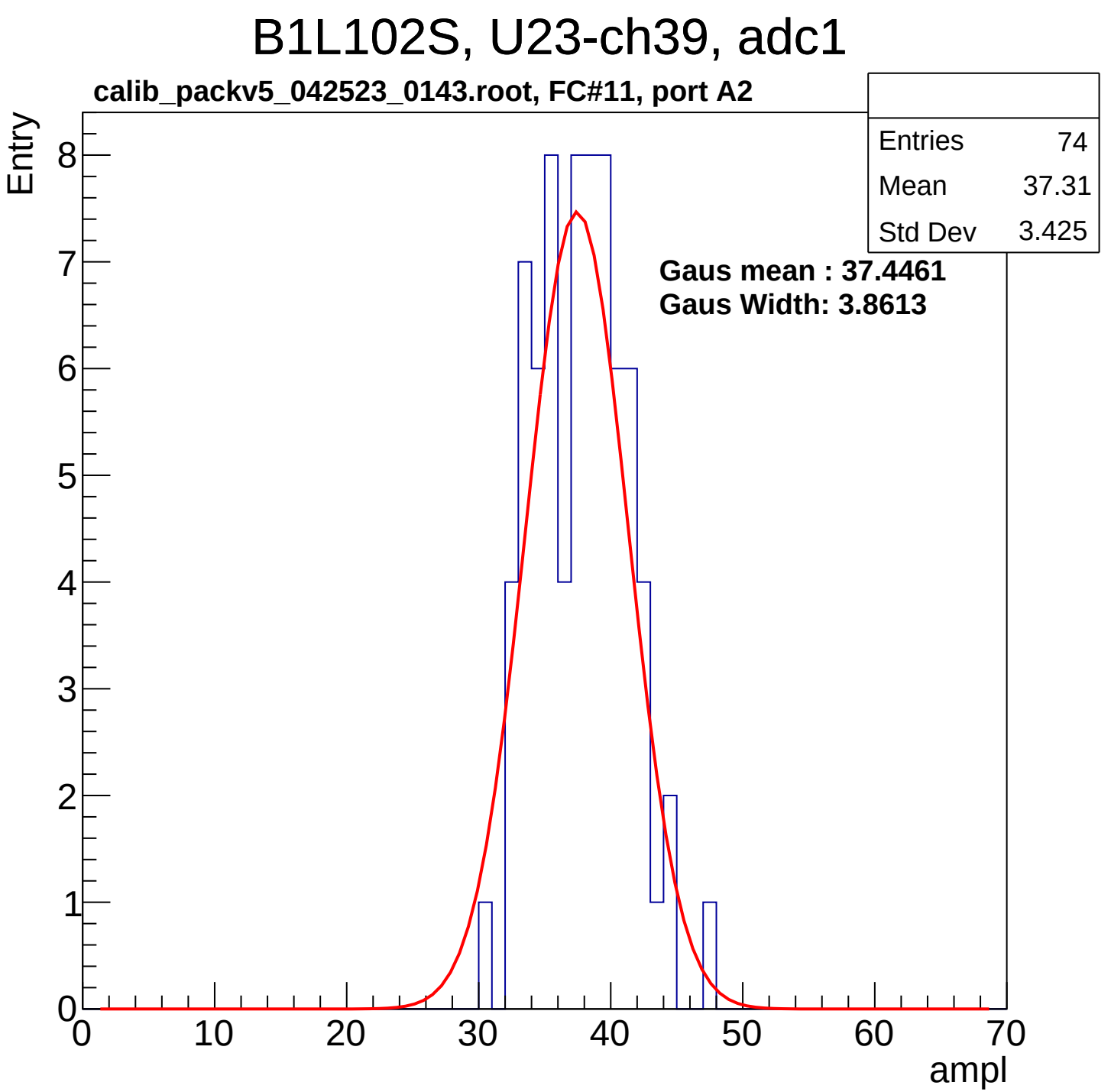

B1L102S, U23-ch38, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 54.32
Std Dev 2.786
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

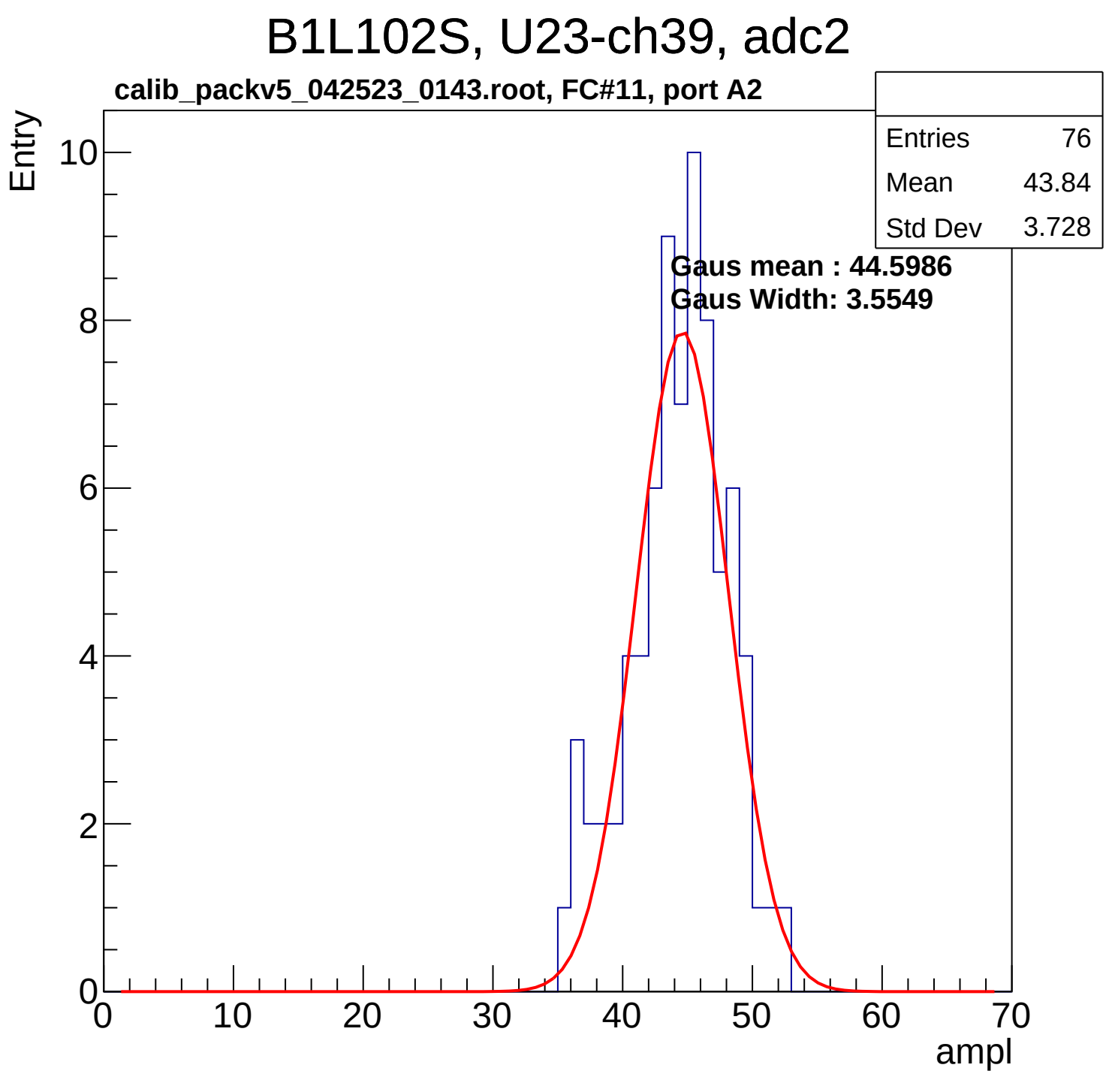

B1L102S, U23-ch38, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 58.27
Std Dev 8.037
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

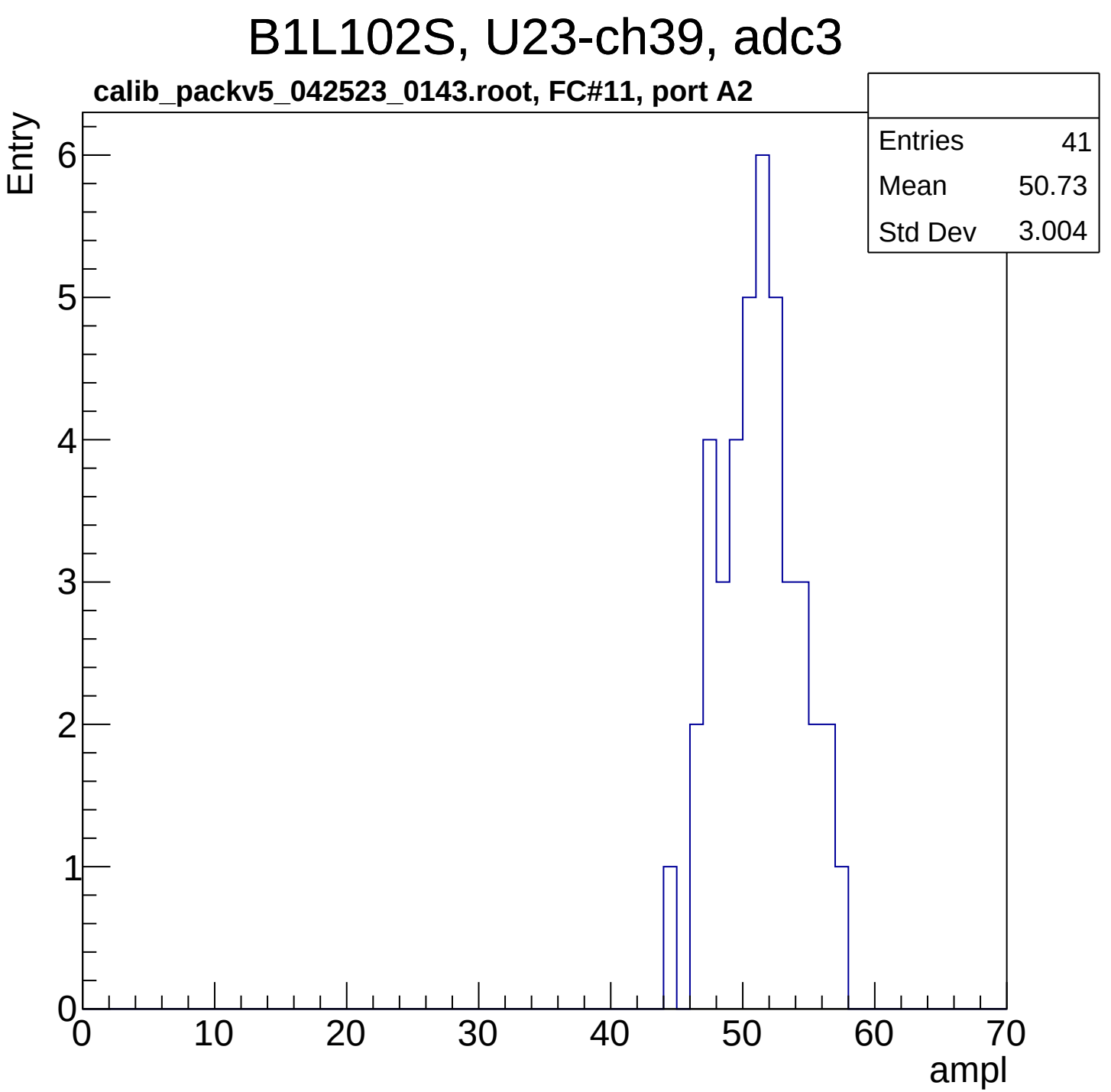

B1L102S, U23-ch38, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.36
Std Dev 0.8814
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

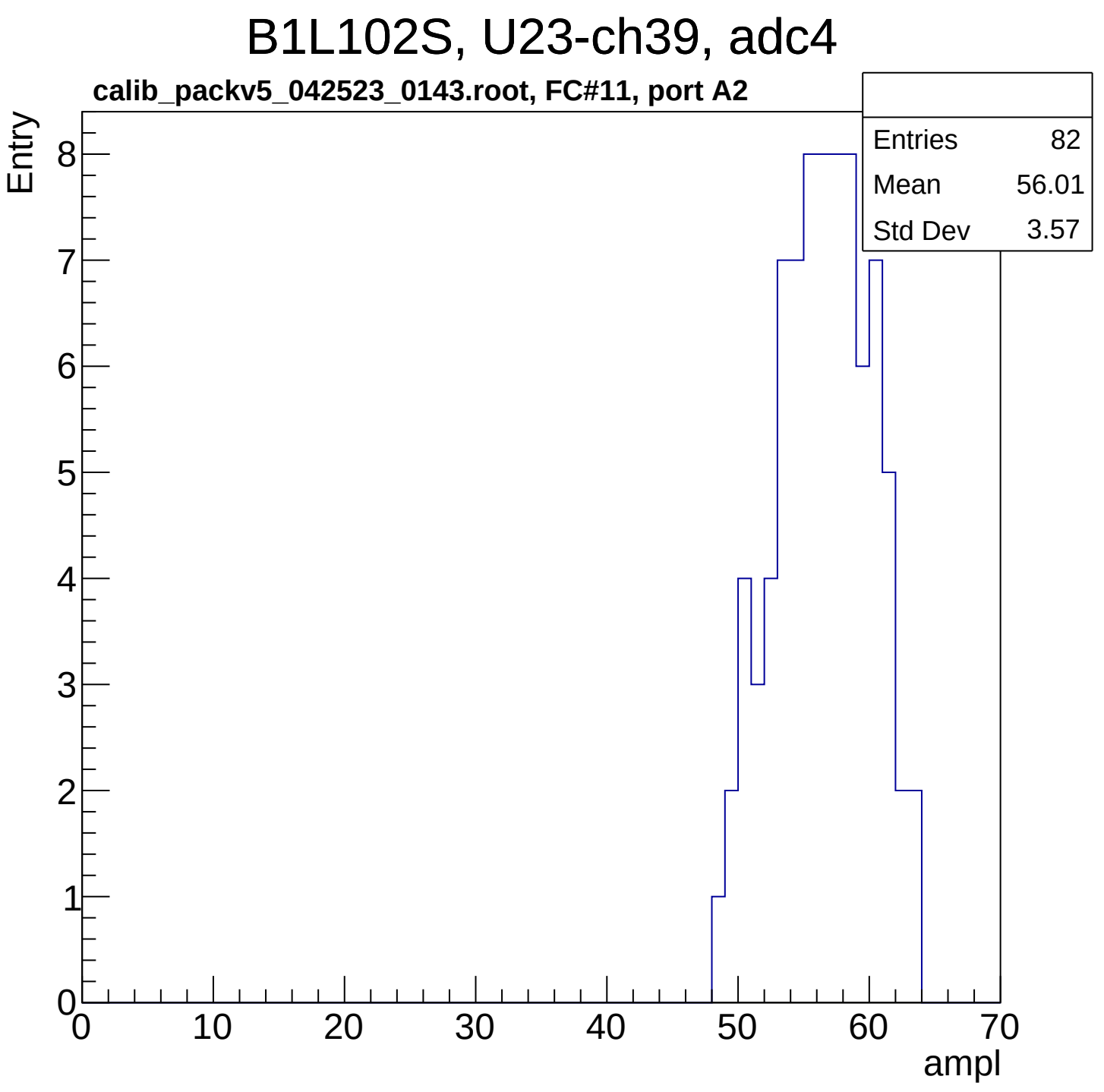

B1L102S, U23-ch38, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

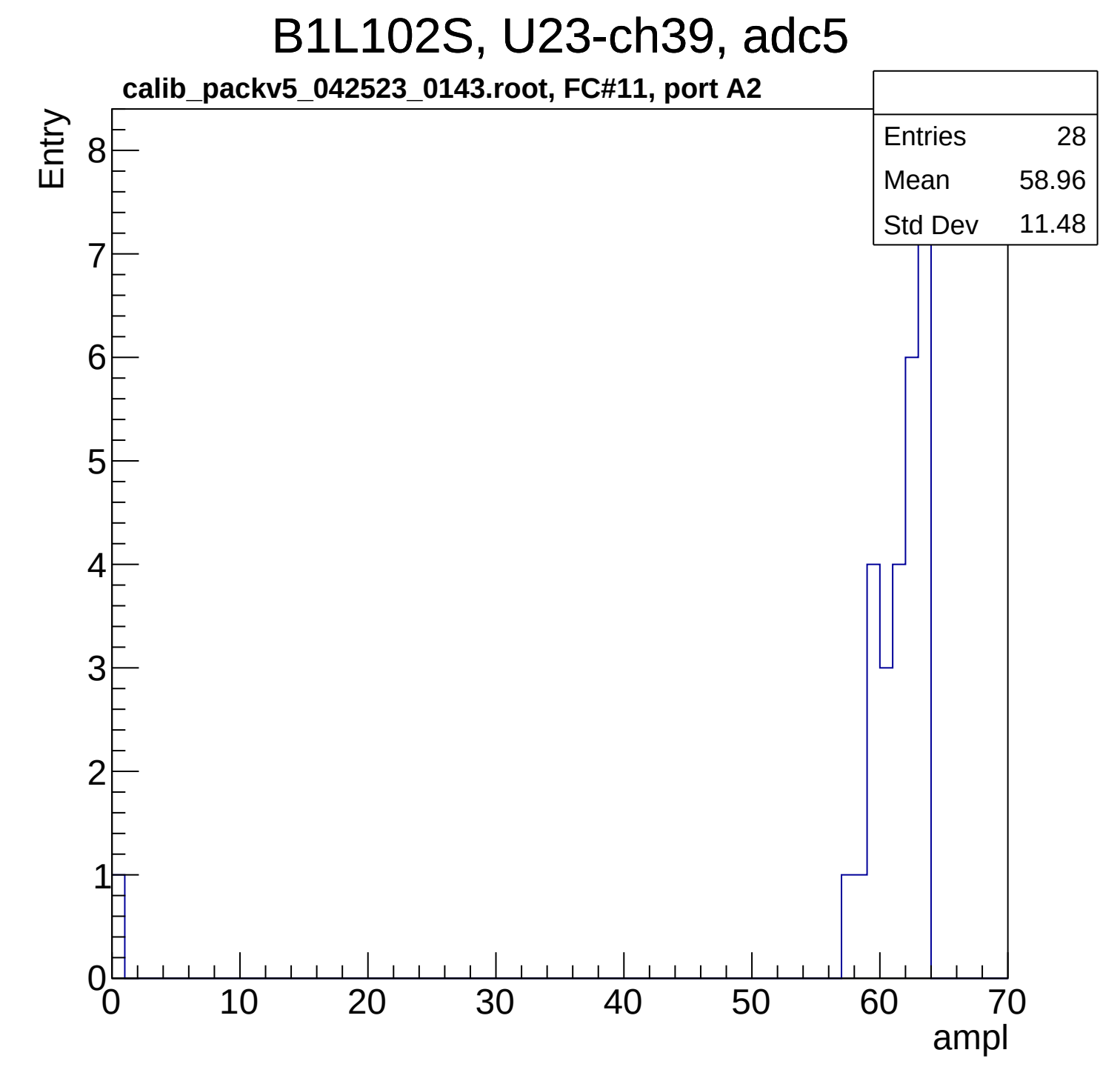

B1L102S, U23-ch39, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 29.12
Std Dev 4.885
Gaus mean : 30.2610
Gaus Width: 2.9554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

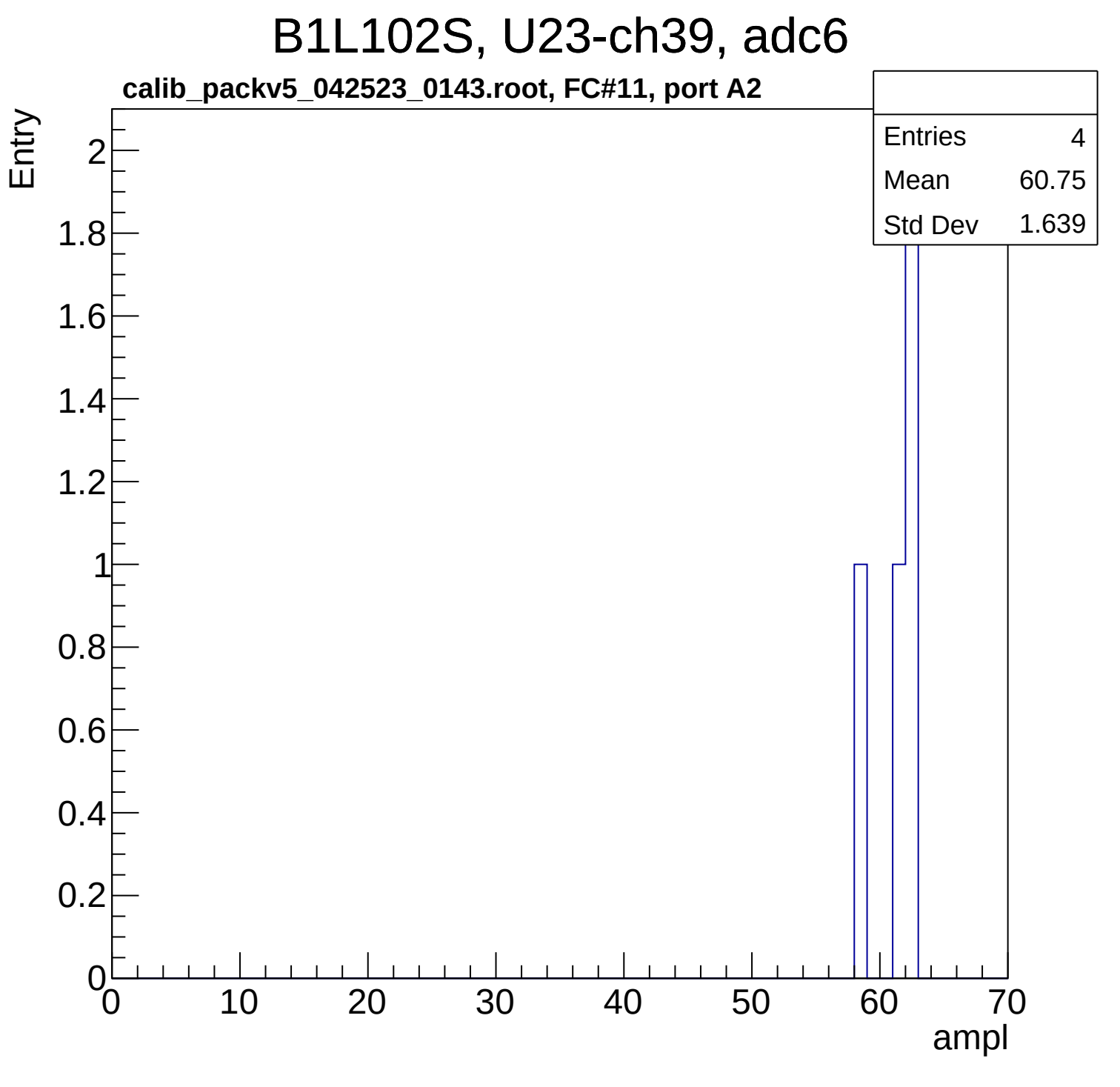

B1L102S, U23-ch39, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 37.31
Std Dev 3.425
Gaus mean : 37.4461
Gaus Width: 3.8613
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

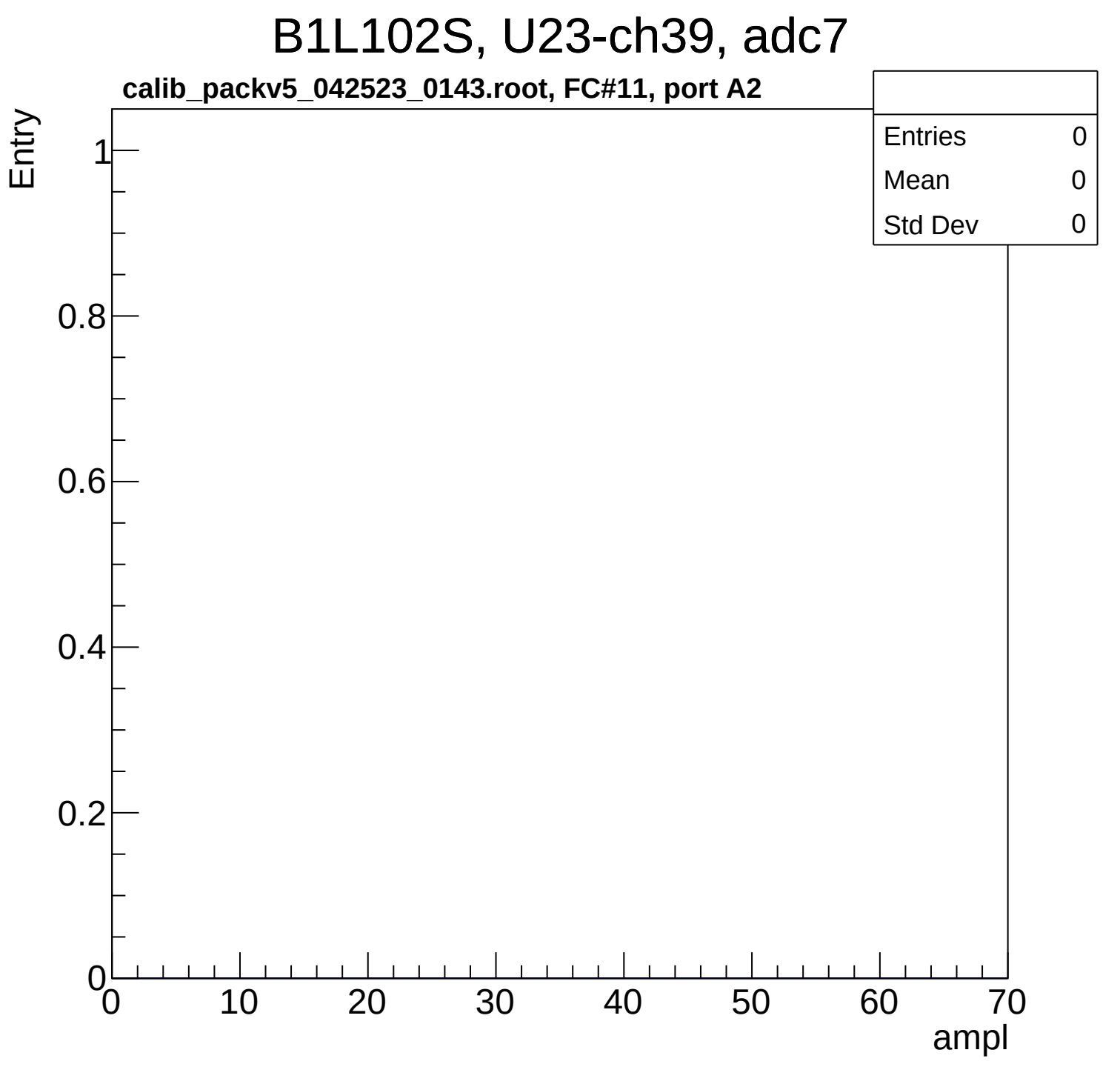

B1L102S, U23-ch39, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 43.84
Std Dev 3.728
Gaus mean : 44.5986
Gaus Width: 3.5549
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

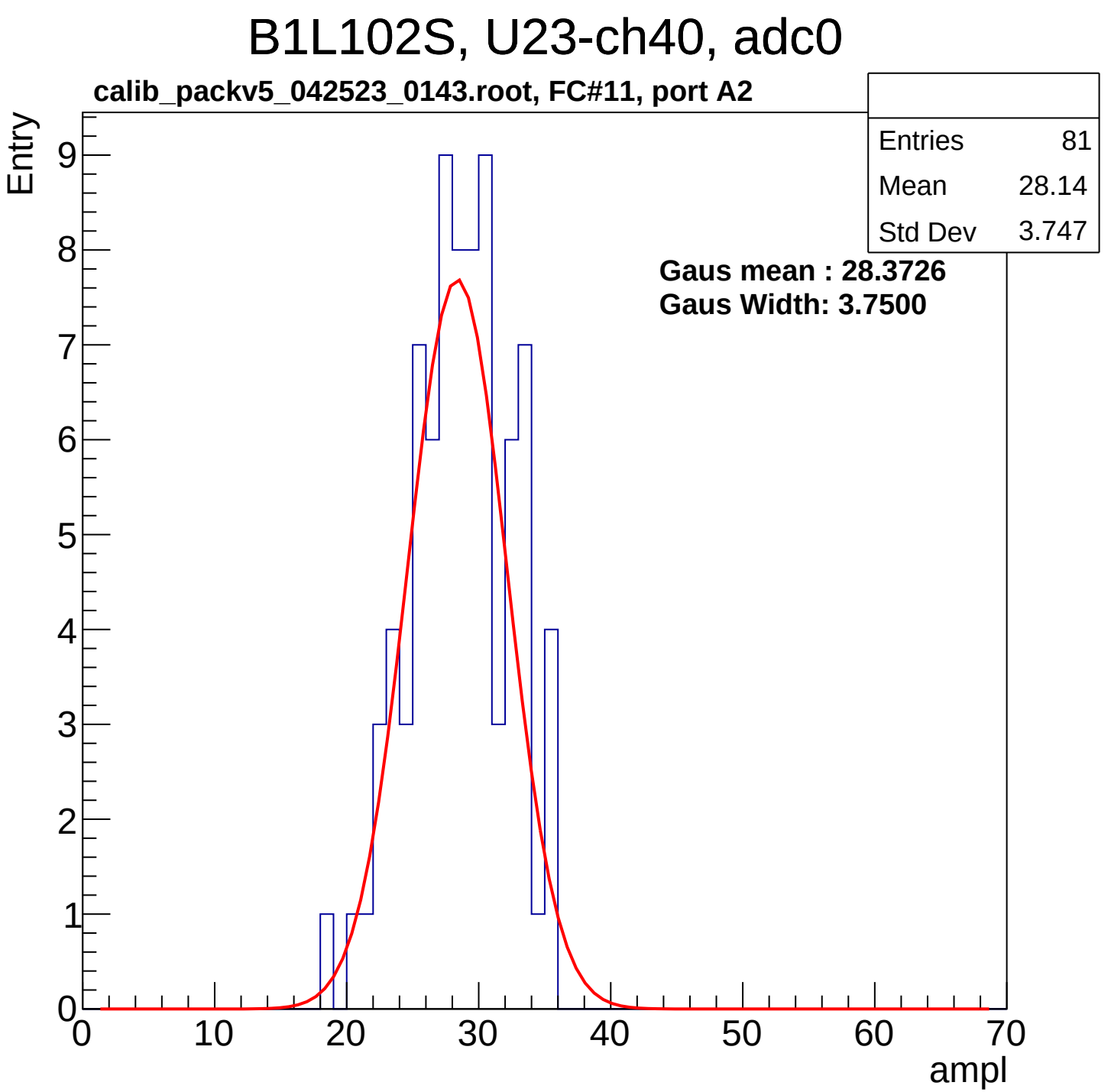

B1L102S, U23-ch39, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 50.73
Std Dev 3.004
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

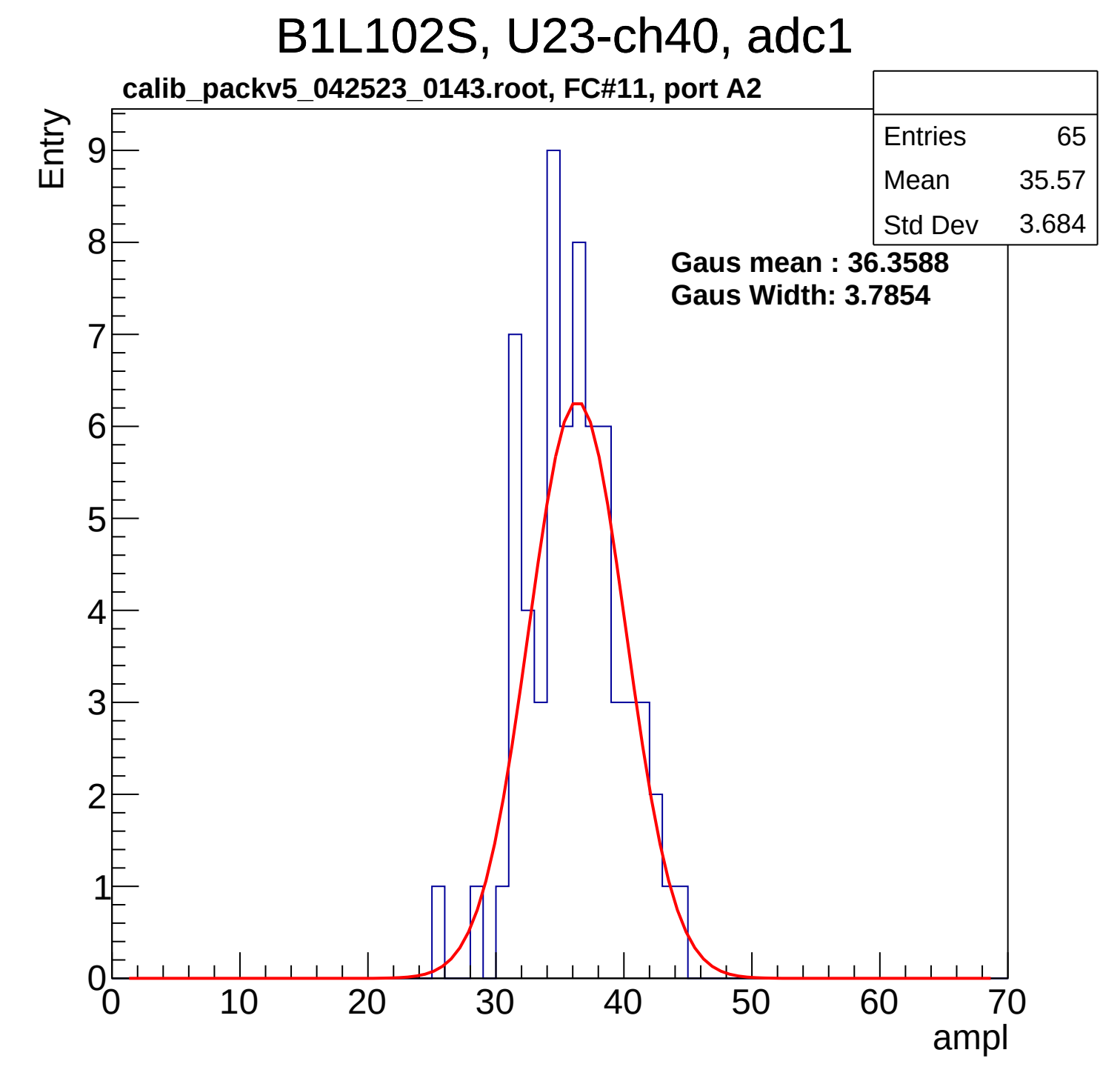

B1L102S, U23-ch39, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 56.01
Std Dev 3.57
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

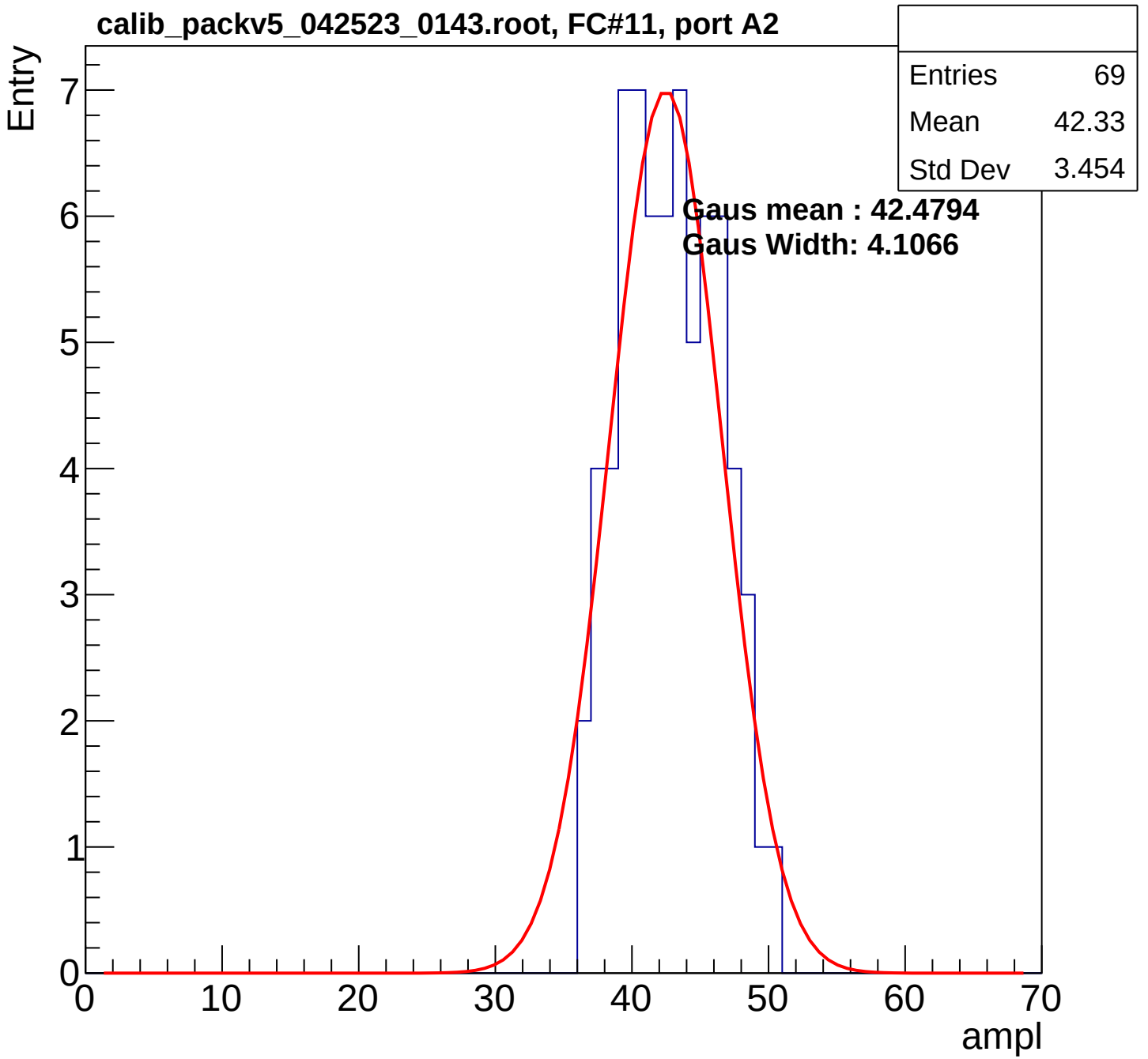

B1L102S, U23-ch39, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 28
Mean 58.96
Std Dev 11.48
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

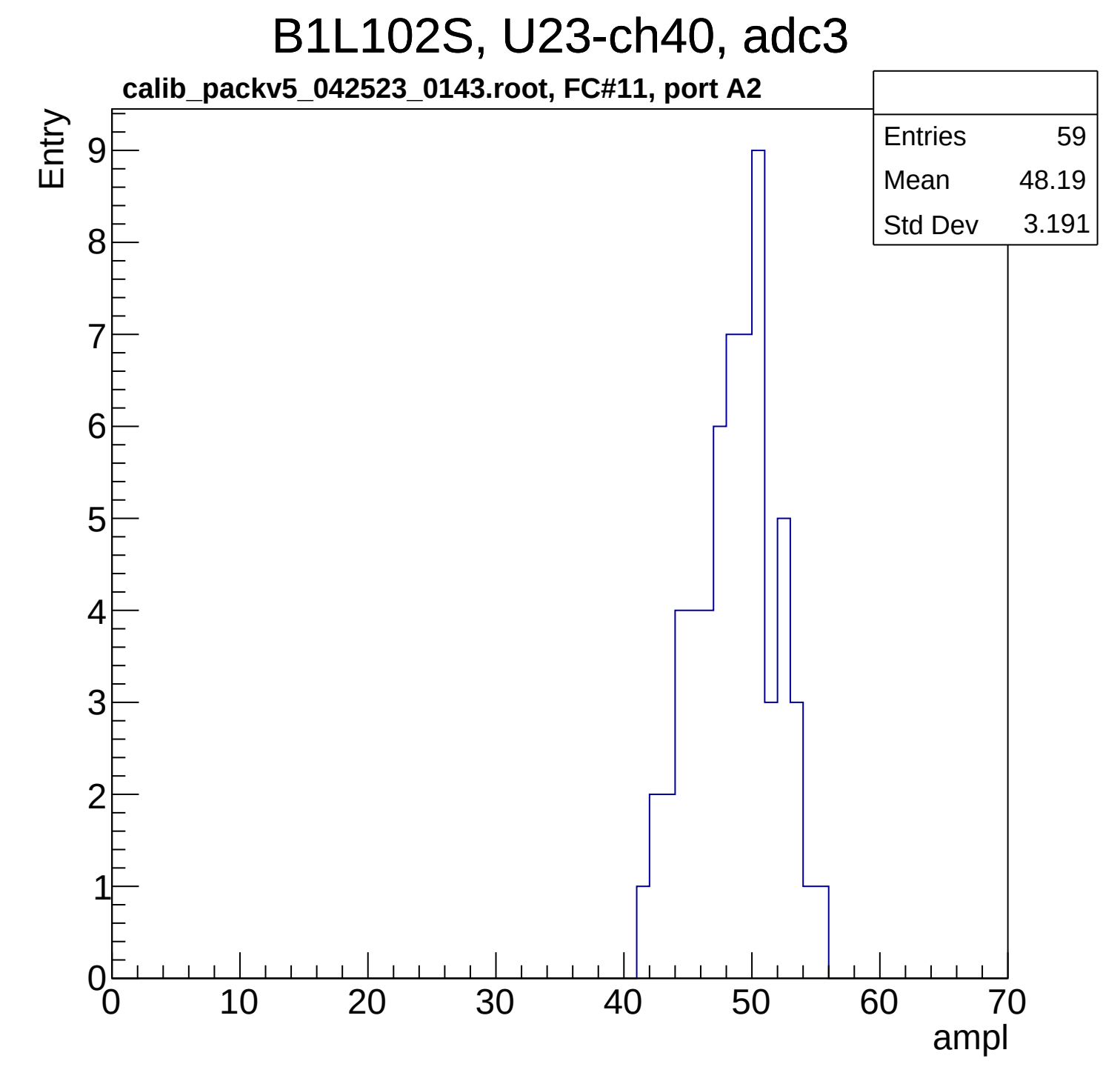

B1L102S, U23-ch39, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 60.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

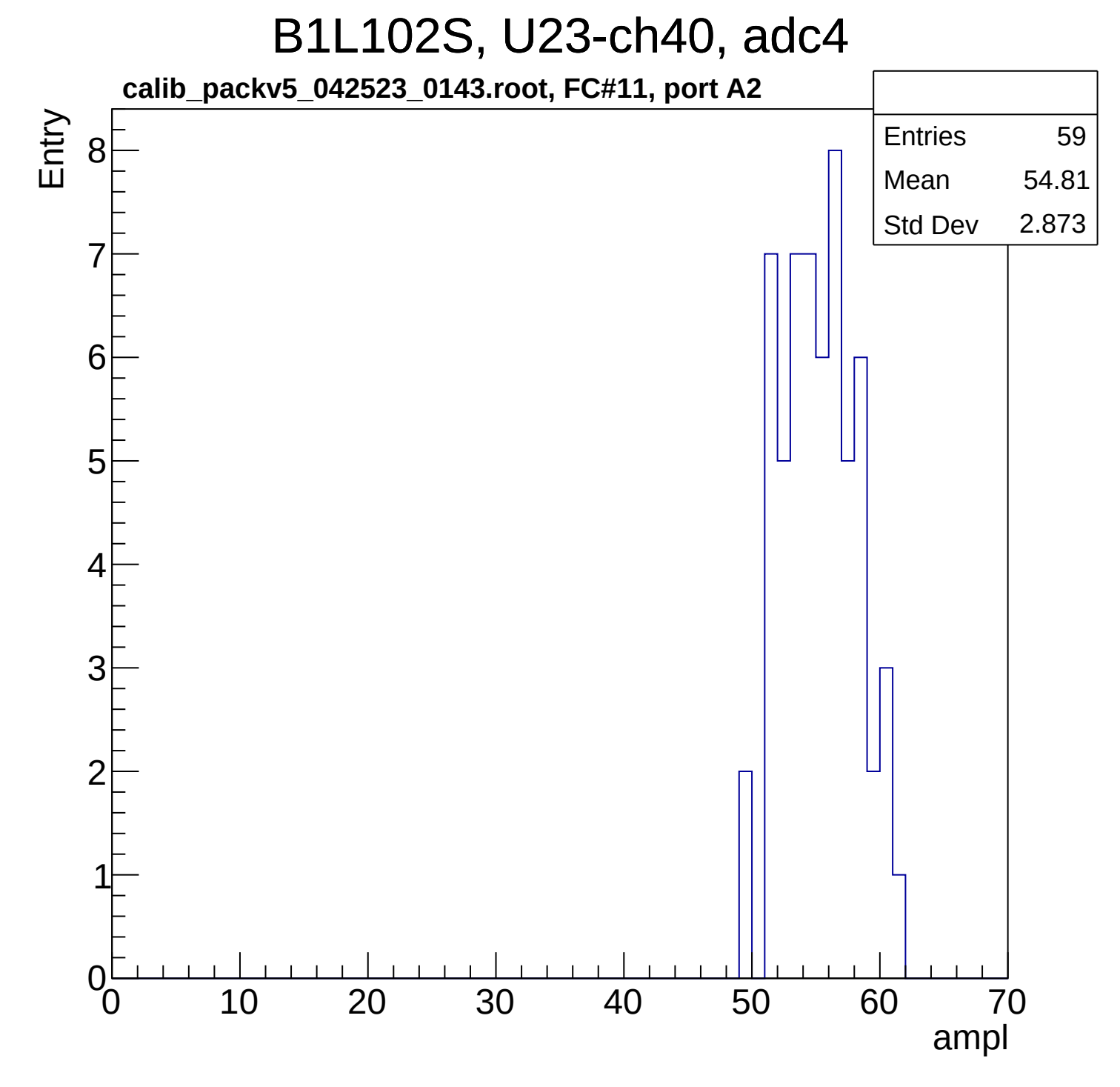

B1L102S, U23-ch39, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

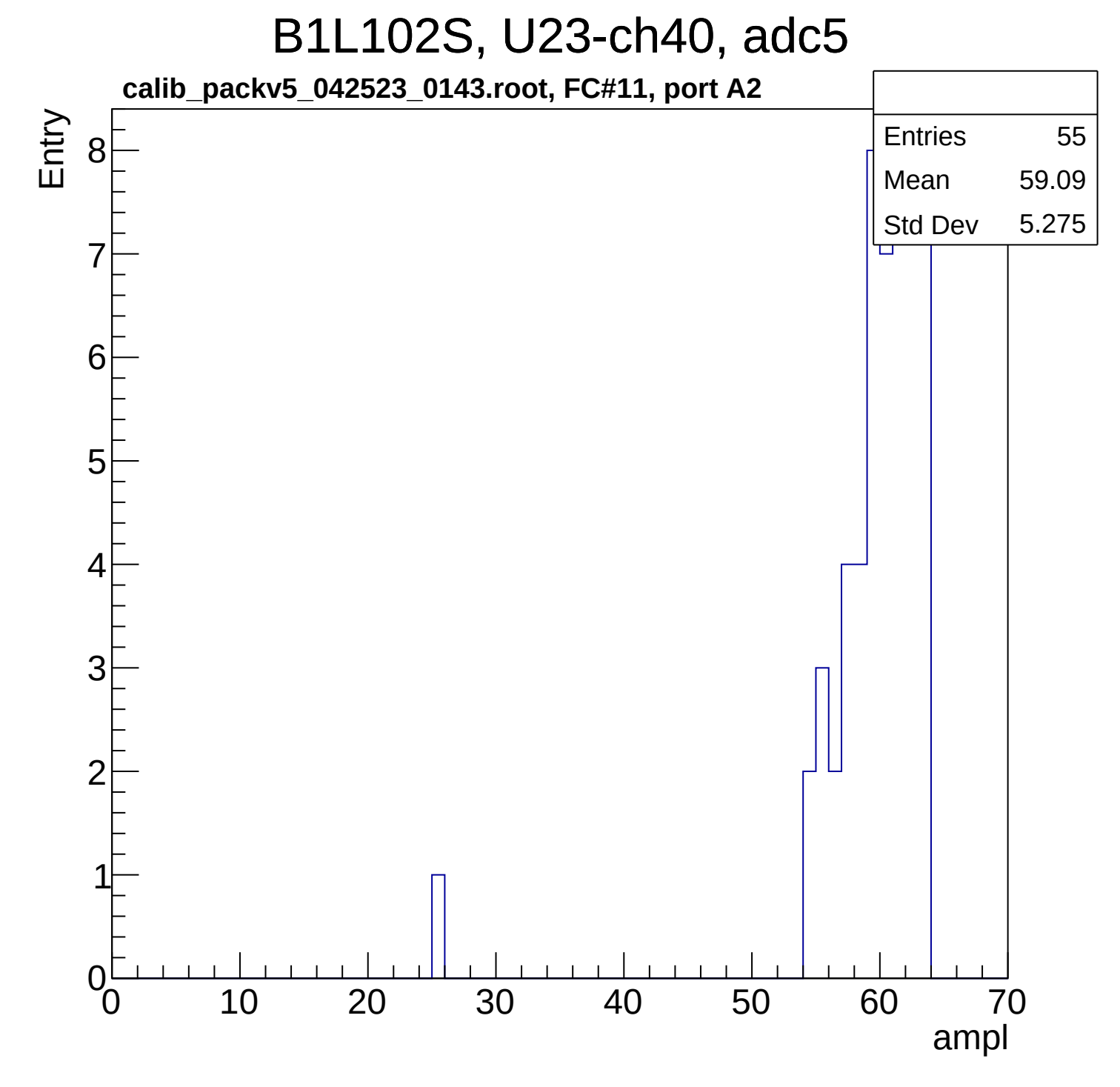

B1L102S, U23-ch40, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 28.14
Std Dev 3.747
Gaus mean : 28.3726
Gaus Width: 3.7500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

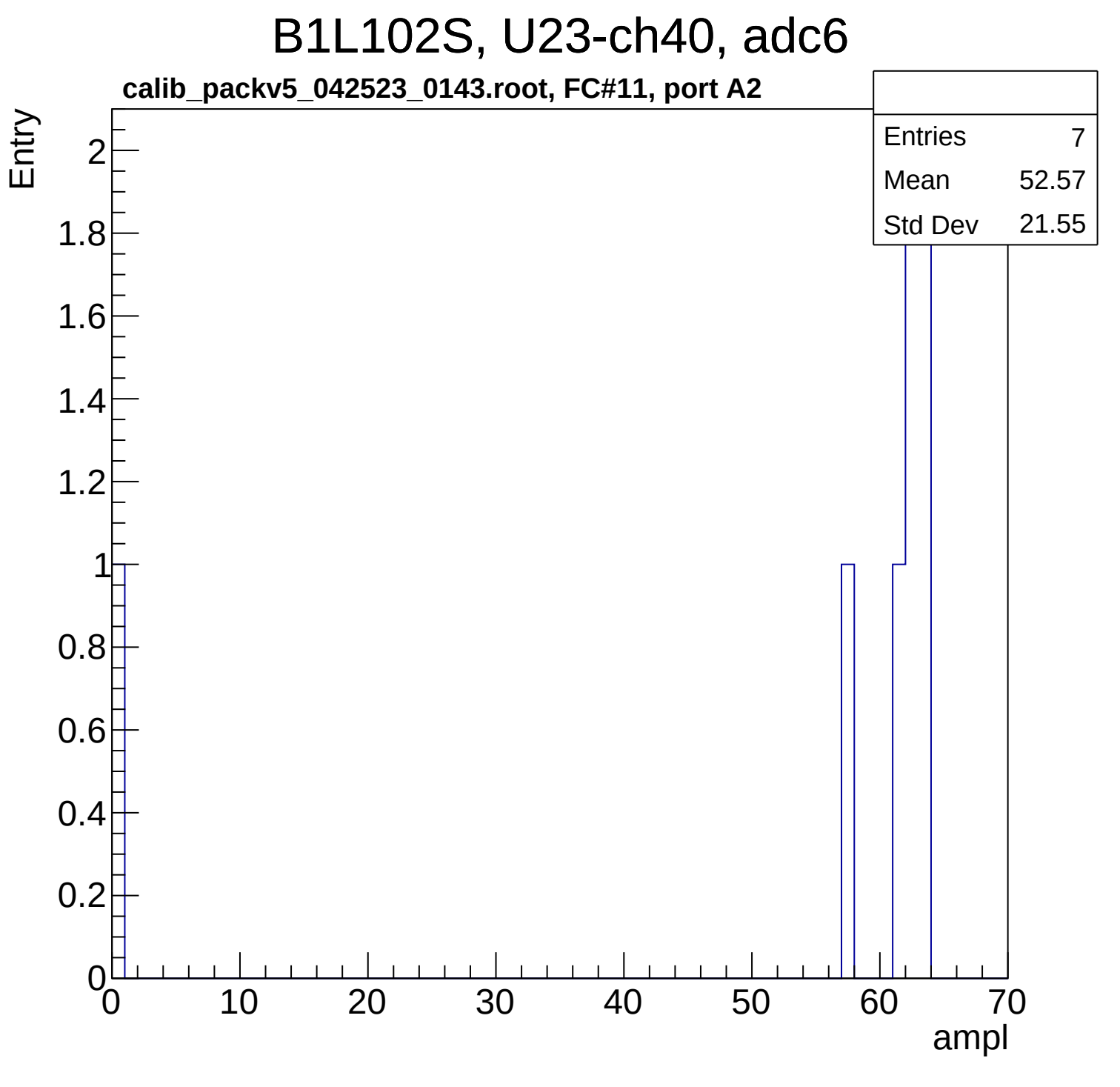

B1L102S, U23-ch40, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.57
Std Dev 3.684
Gaus mean : 36.3588
Gaus Width: 3.7854
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

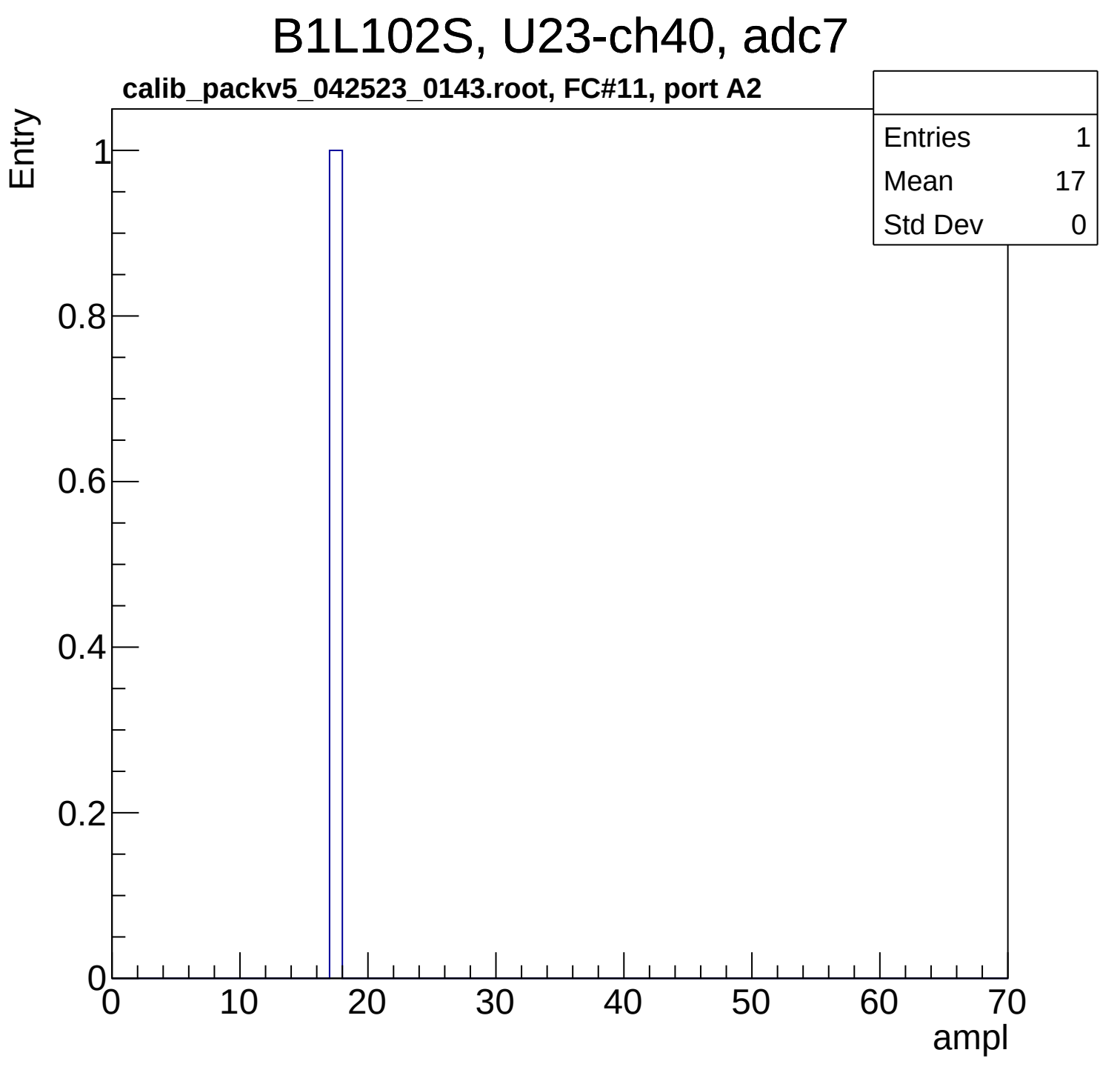

B1L102S, U23-ch40, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 42.33
Std Dev 3.454
Gaus mean : 42.4794
Gaus Width: 4.1066
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

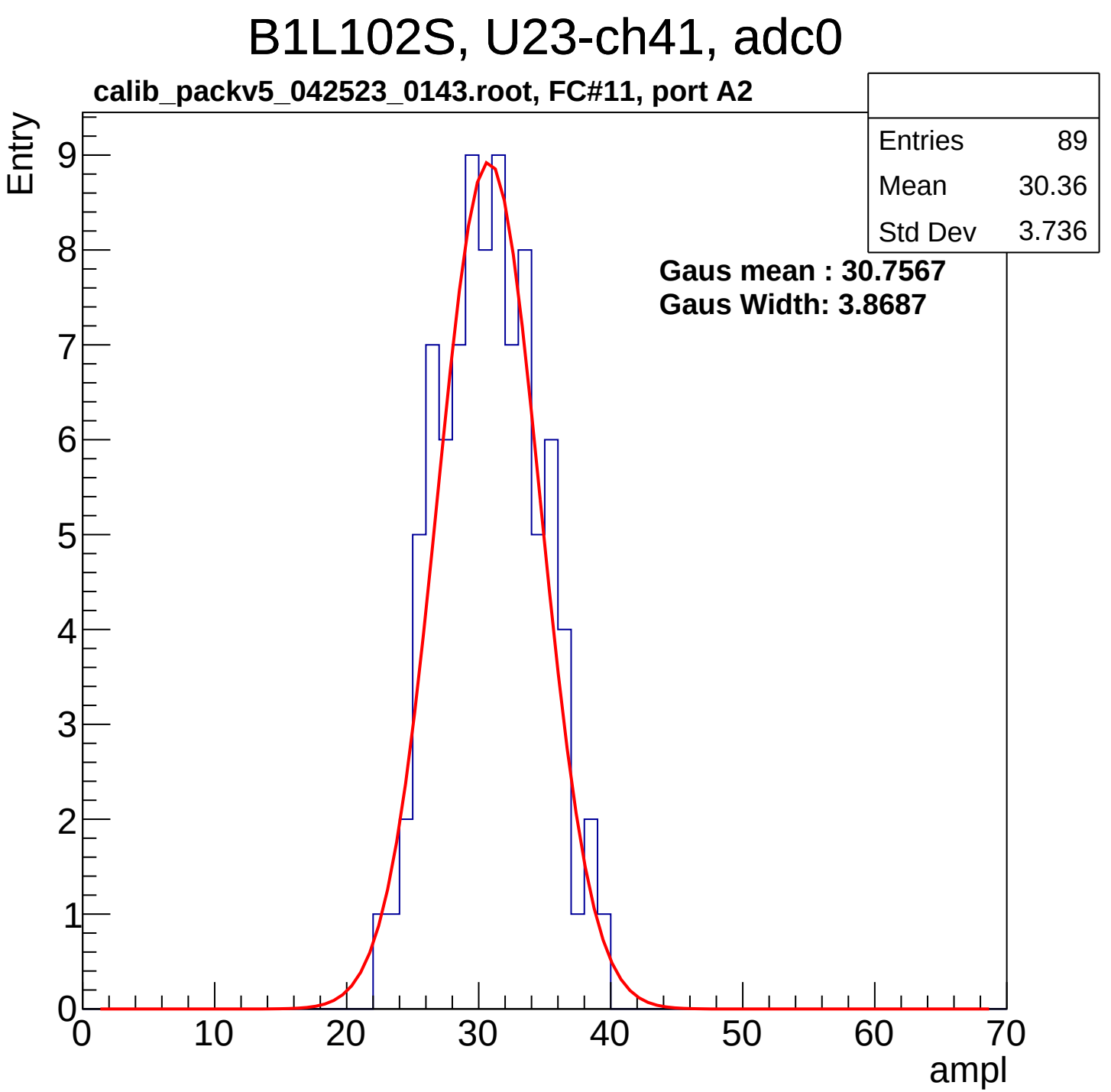

B1L102S, U23-ch40, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.19
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

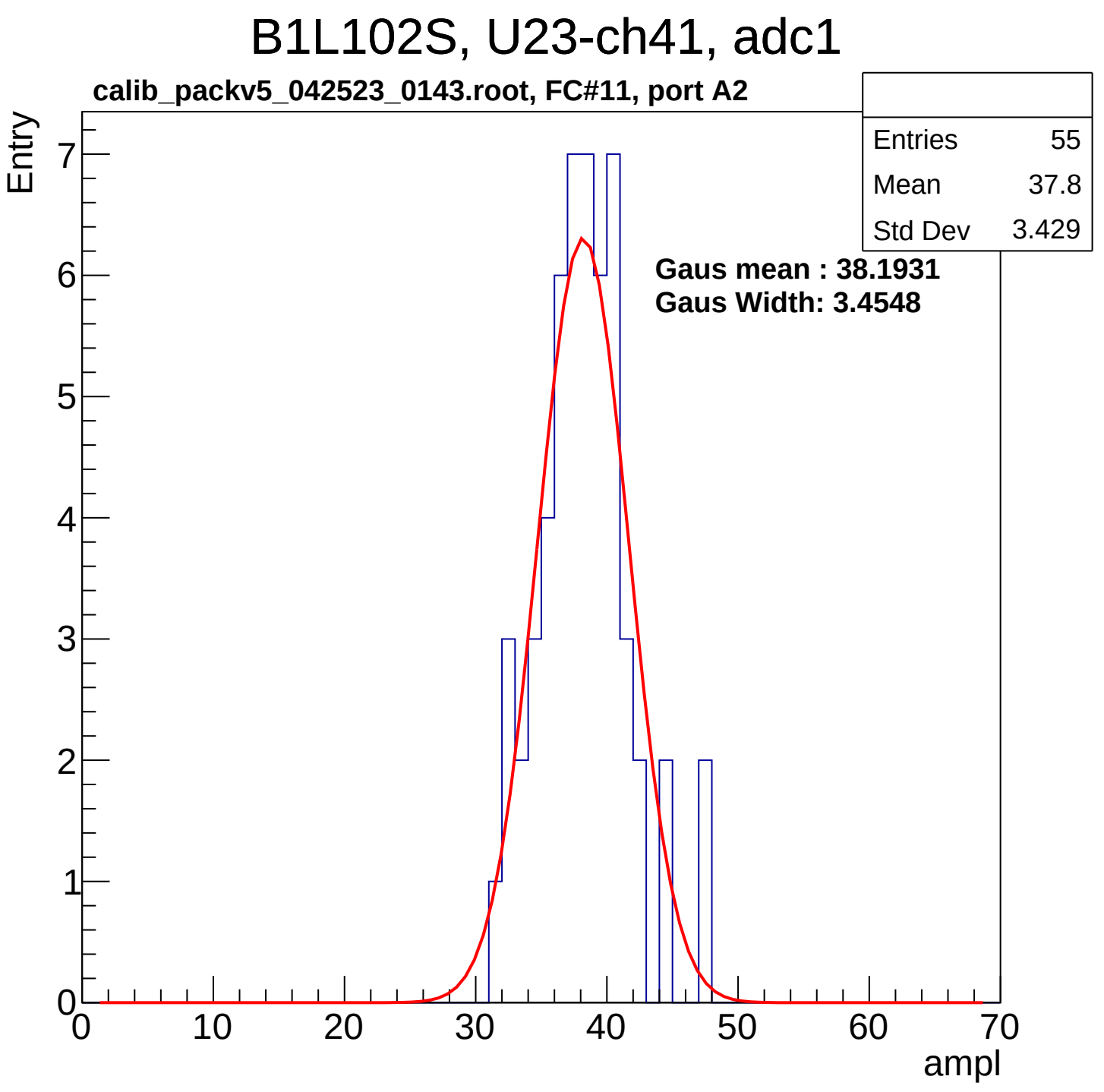

B1L102S, U23-ch40, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 54.81
Std Dev 2.873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

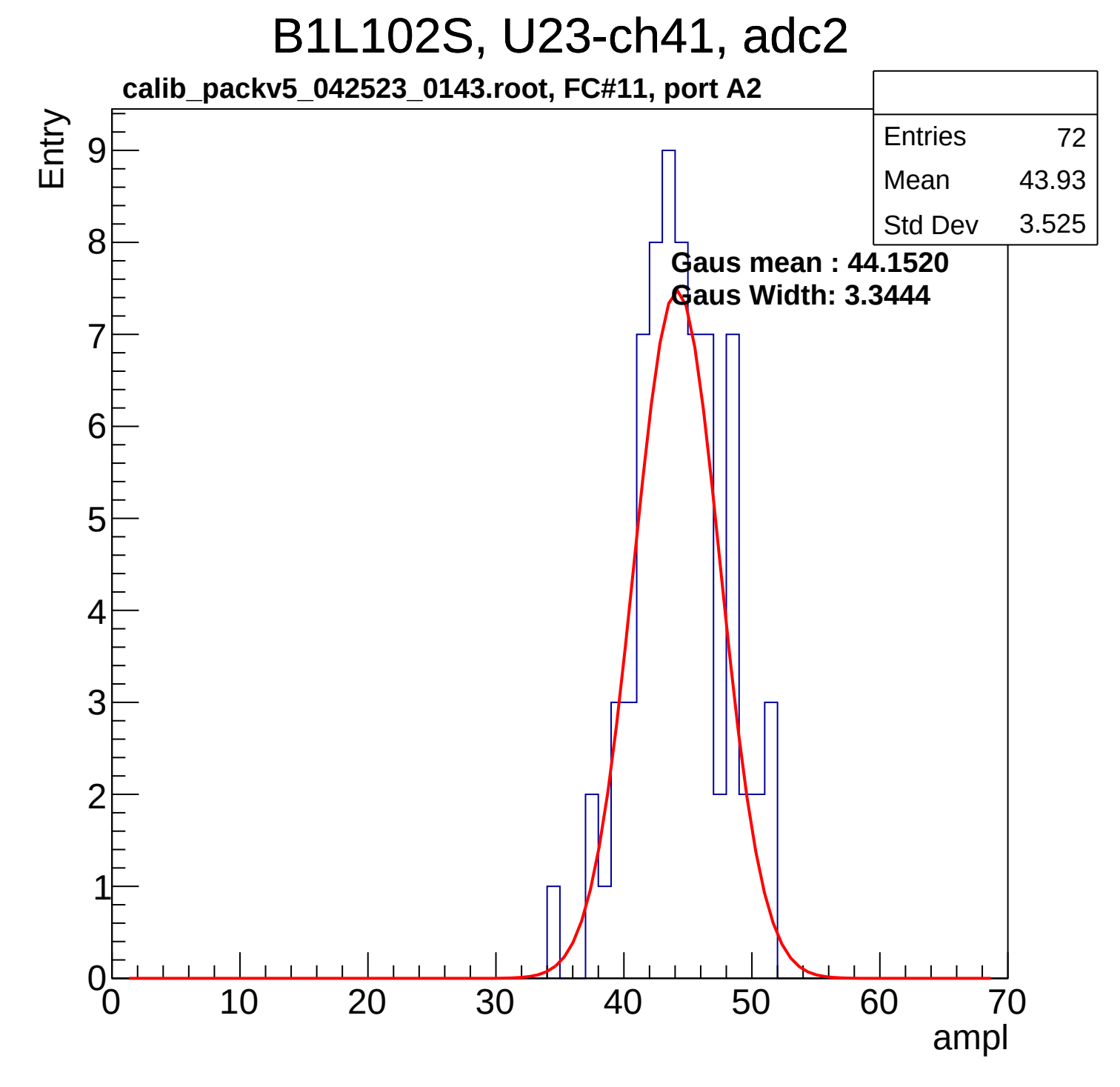

B1L102S, U23-ch40, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 59.09
Std Dev 5.275
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

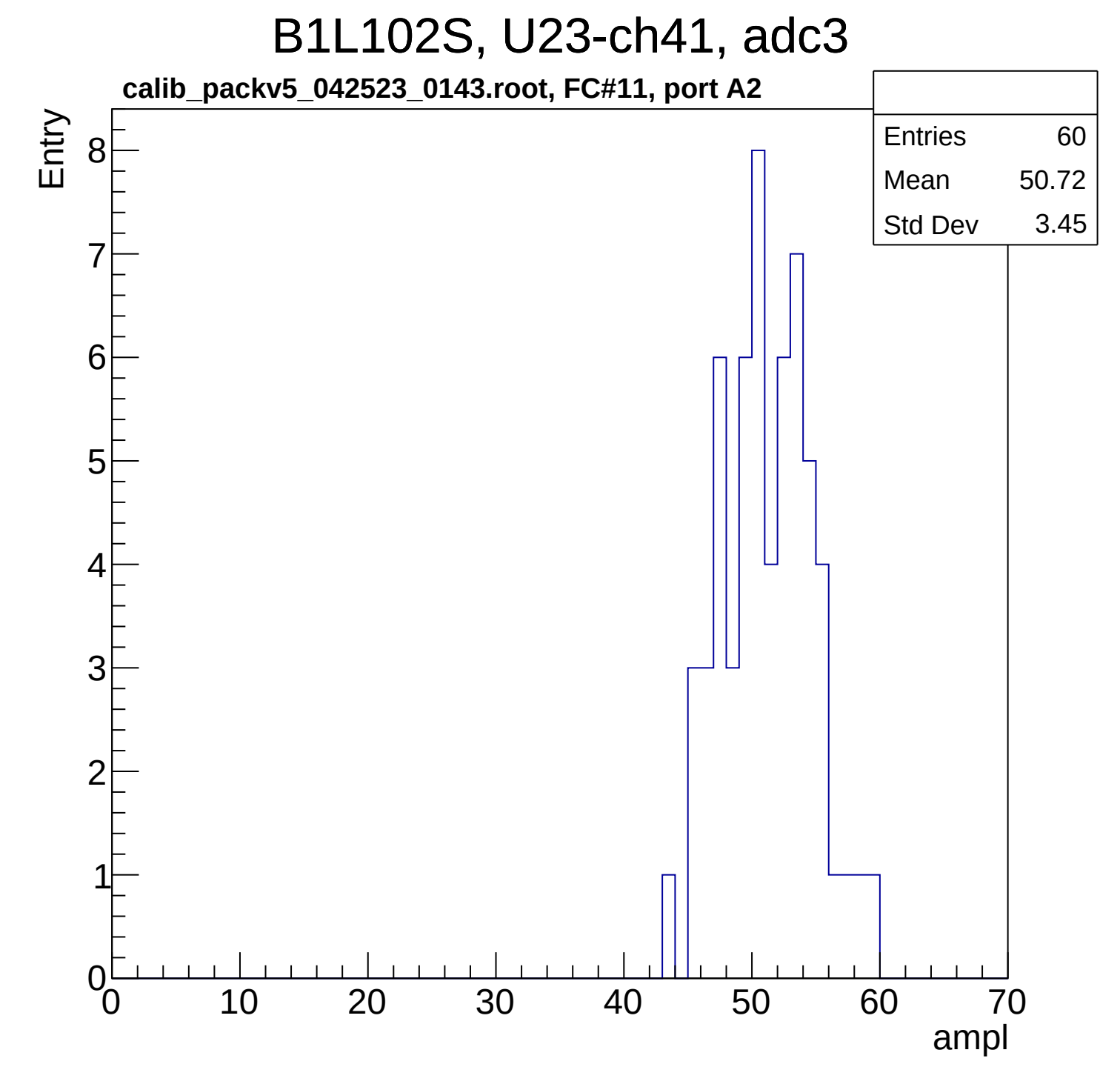

B1L102S, U23-ch40, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 52.57
Std Dev 21.55
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

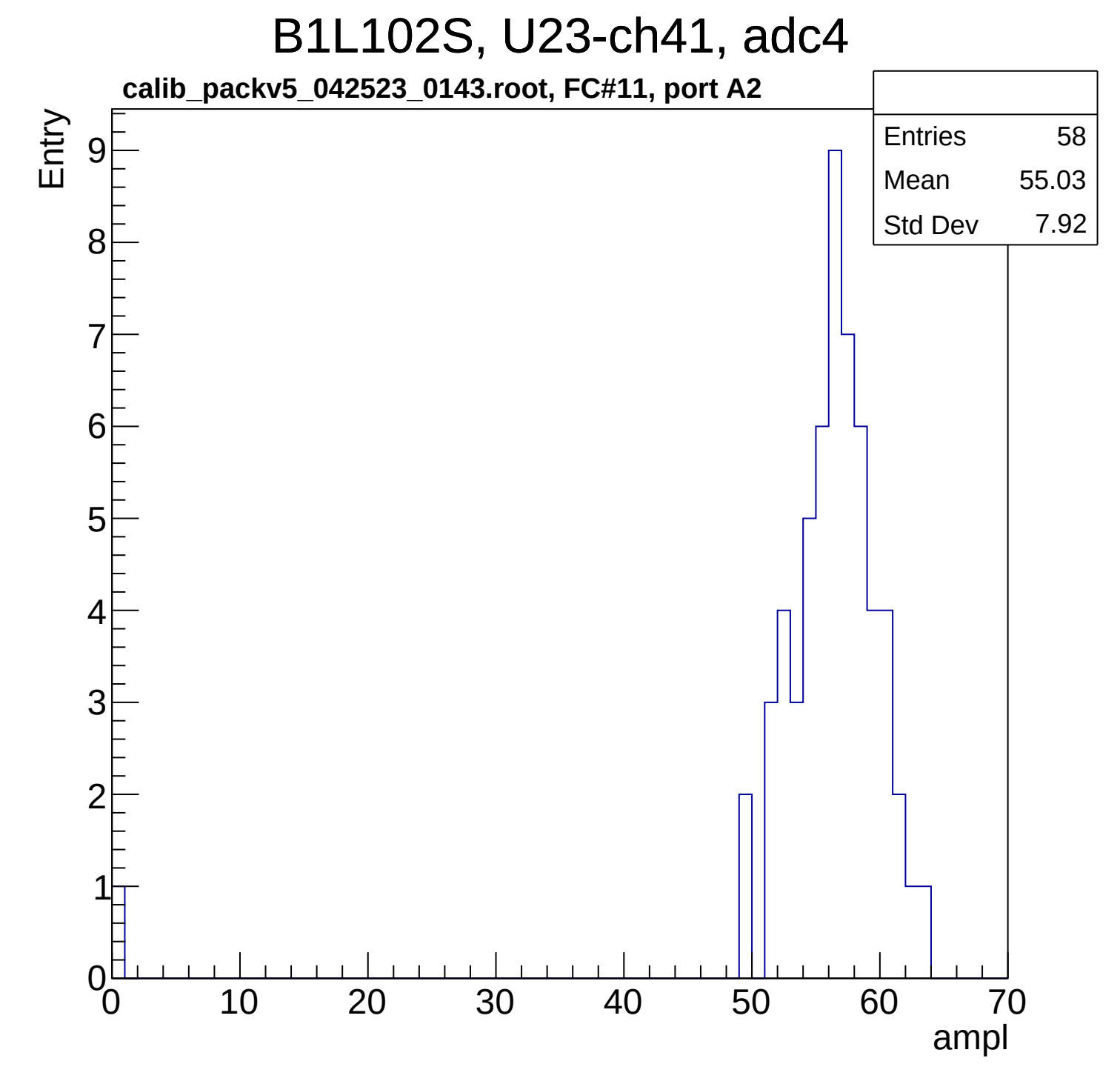

B1L102S, U23-ch40, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 17
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

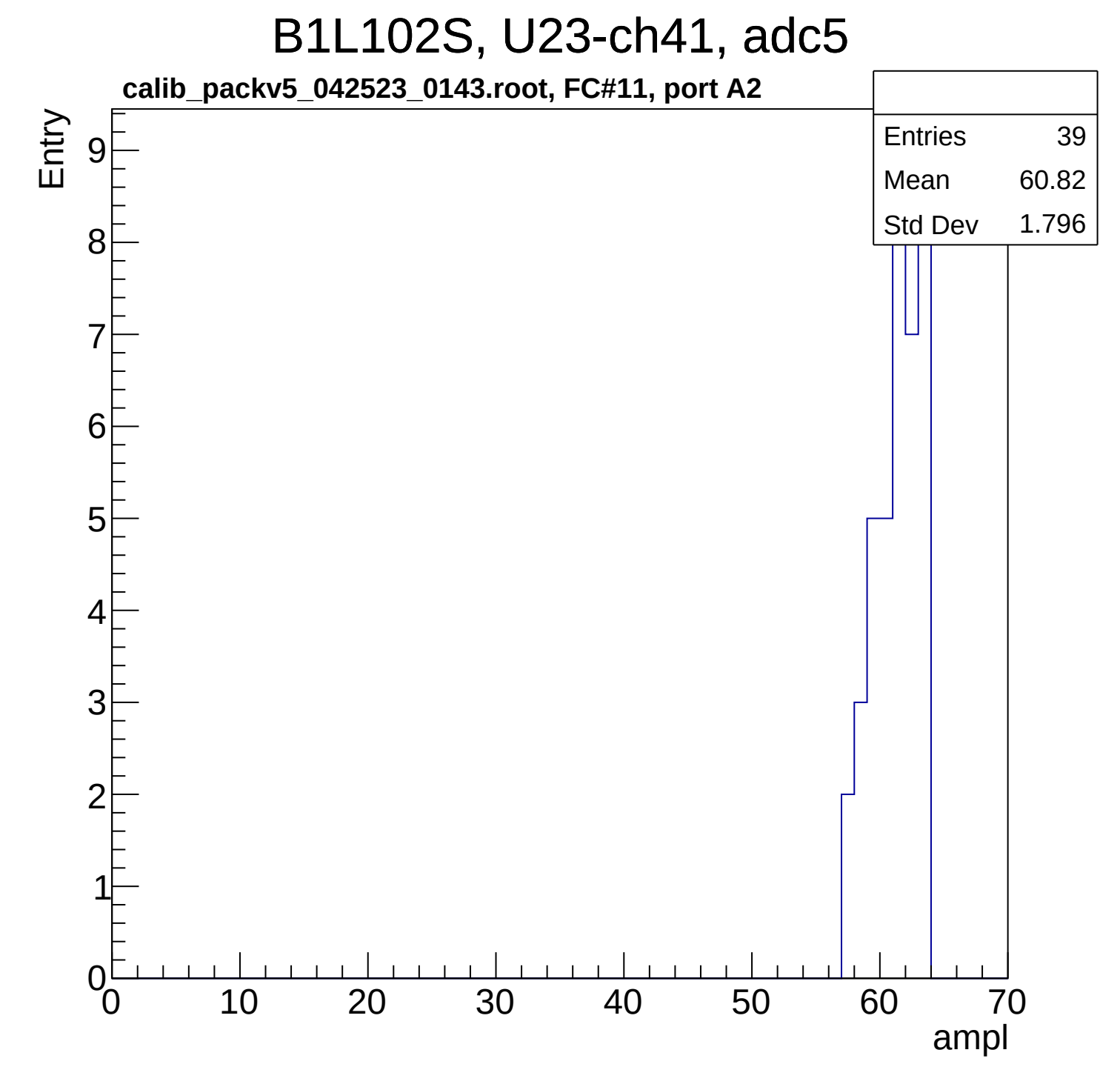

B1L102S, U23-ch41, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 30.36
Std Dev 3.736
Gaus mean : 30.7567
Gaus Width: 3.8687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

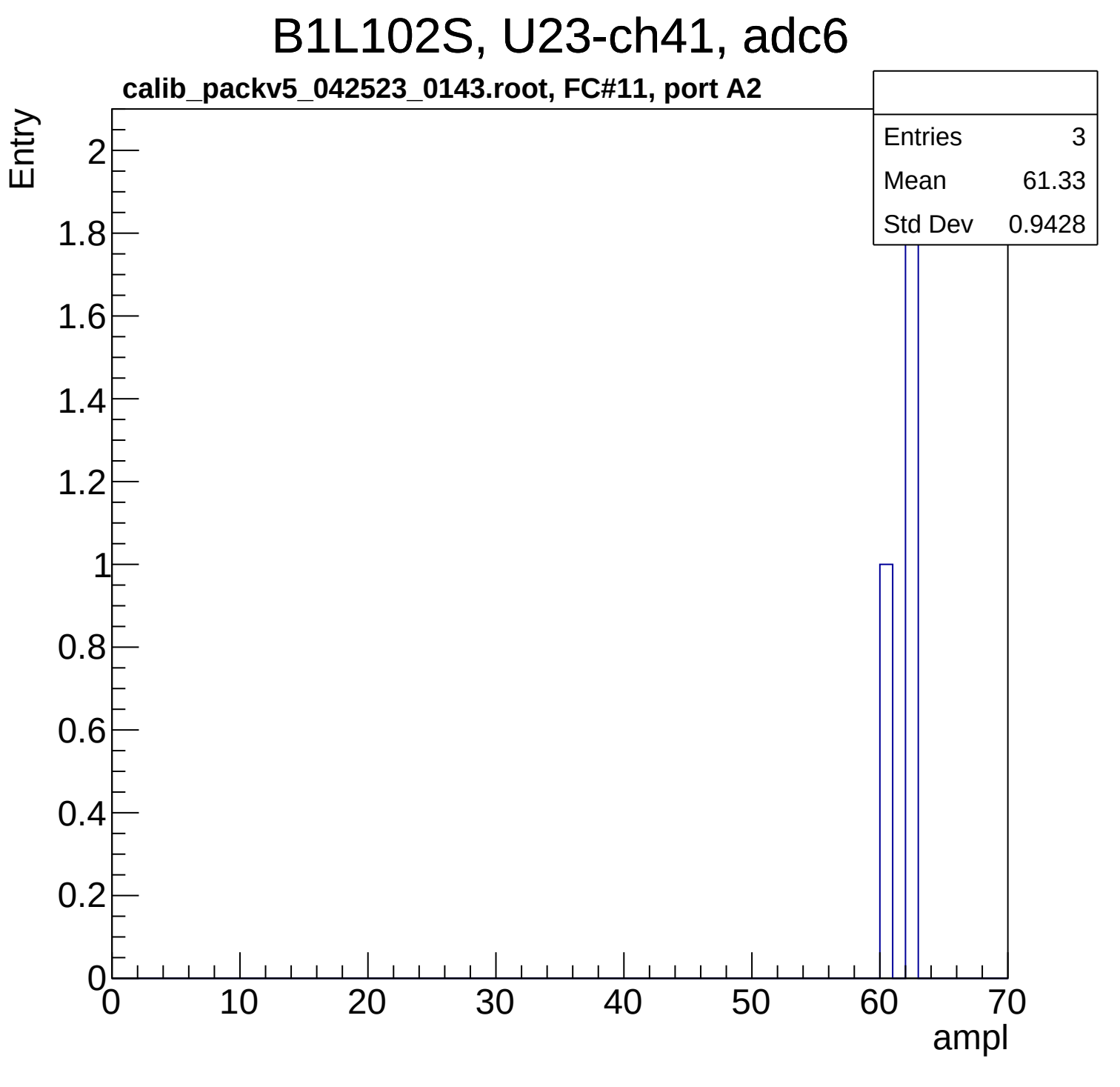

B1L102S, U23-ch41, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 37.8
Std Dev 3.429
Gaus mean : 38.1931
Gaus Width: 3.4548
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

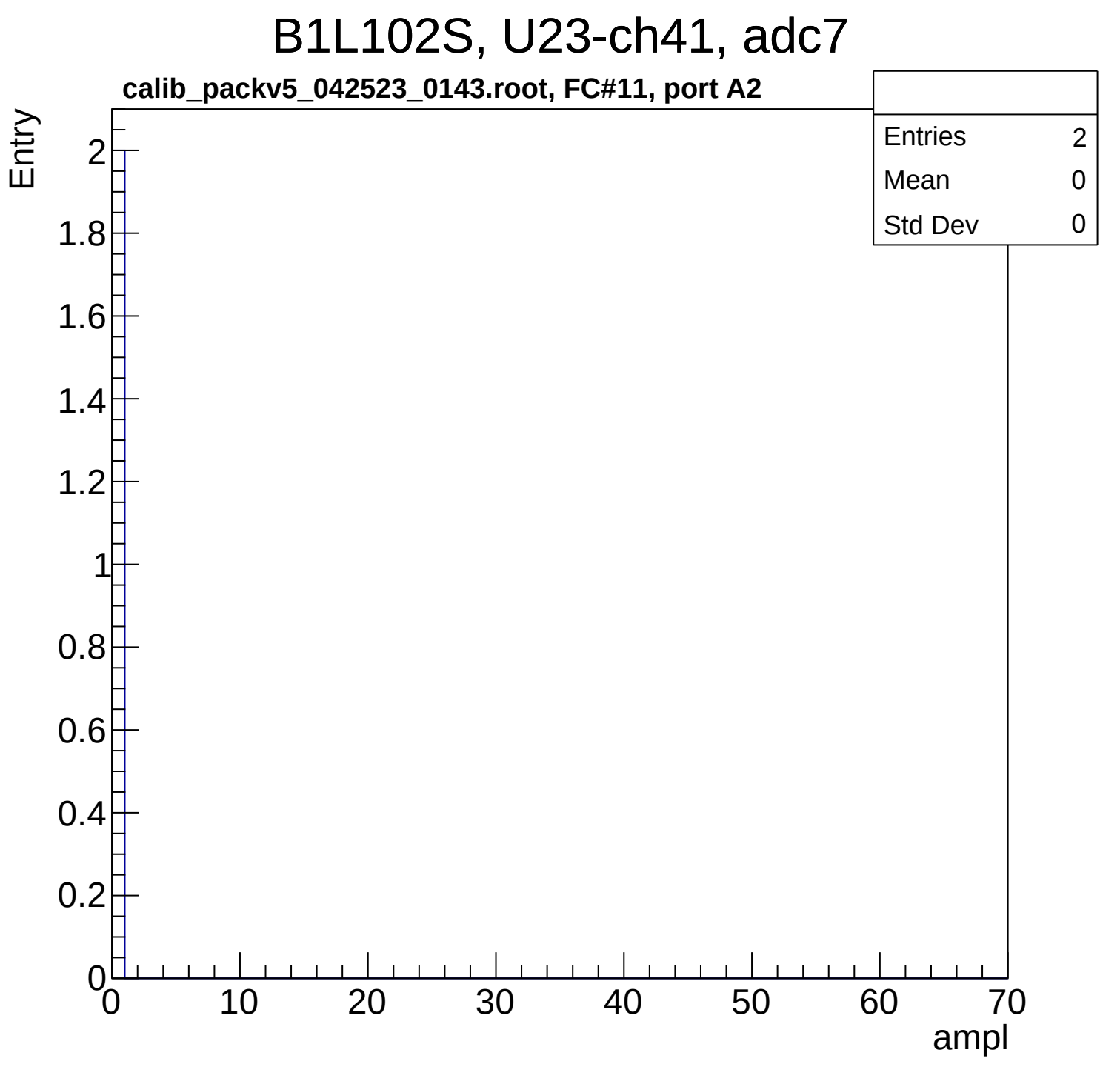

B1L102S, U23-ch41, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 43.93
Std Dev 3.525
Gaus mean : 44.1520
Gaus Width: 3.3444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

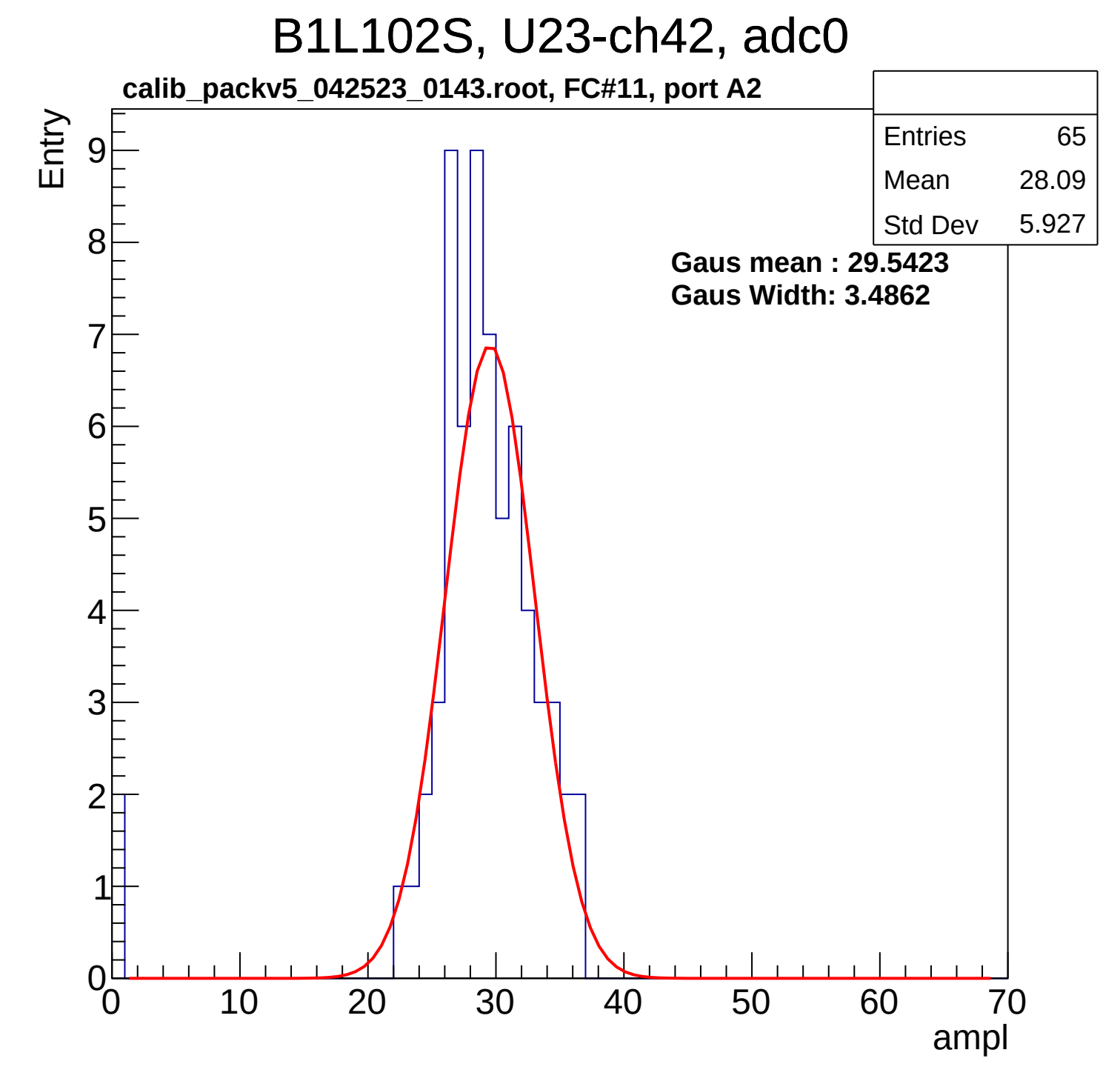

B1L102S, U23-ch41, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 50.72
Std Dev 3.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

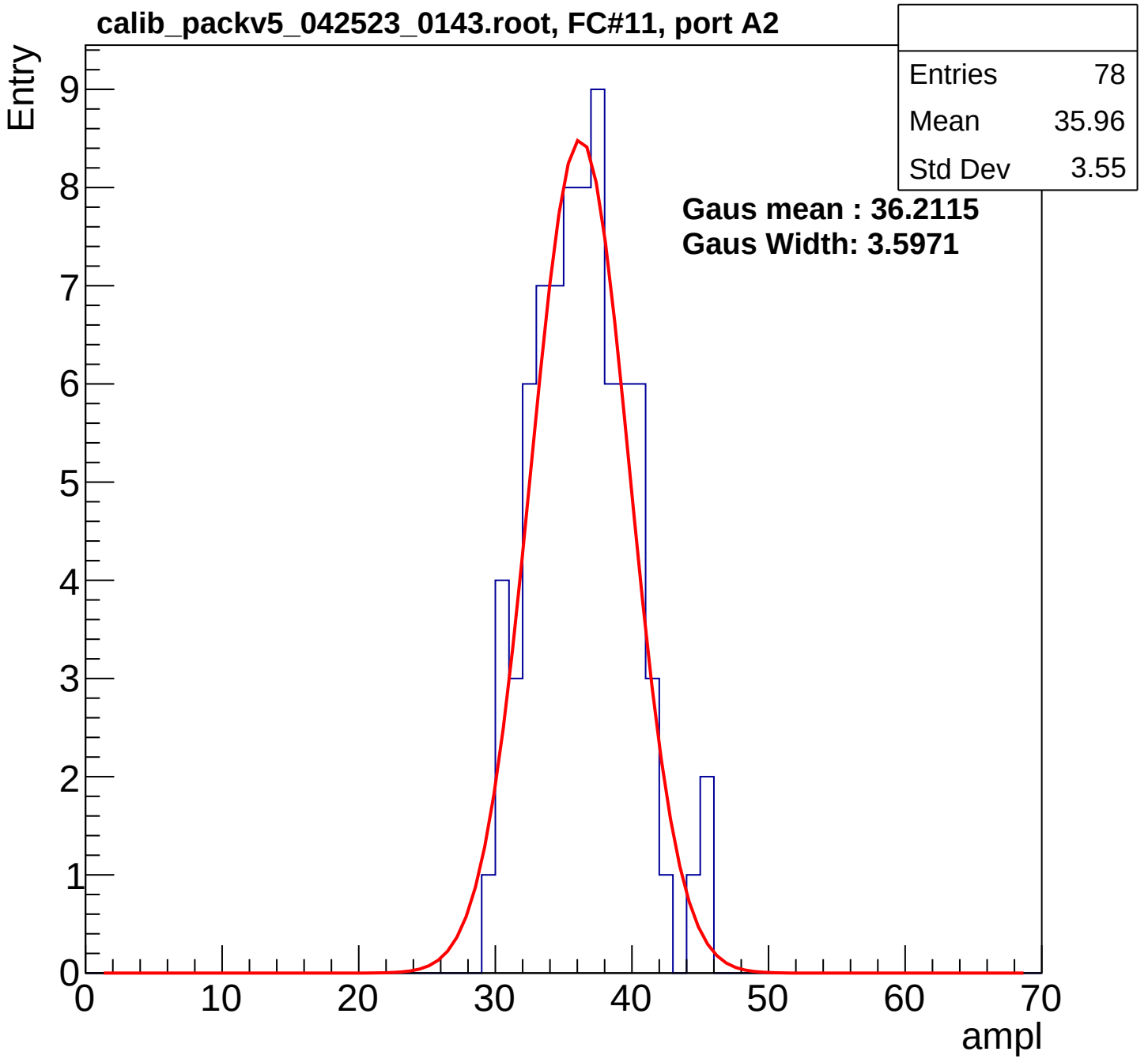

B1L102S, U23-ch41, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.03
Std Dev 7.92
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

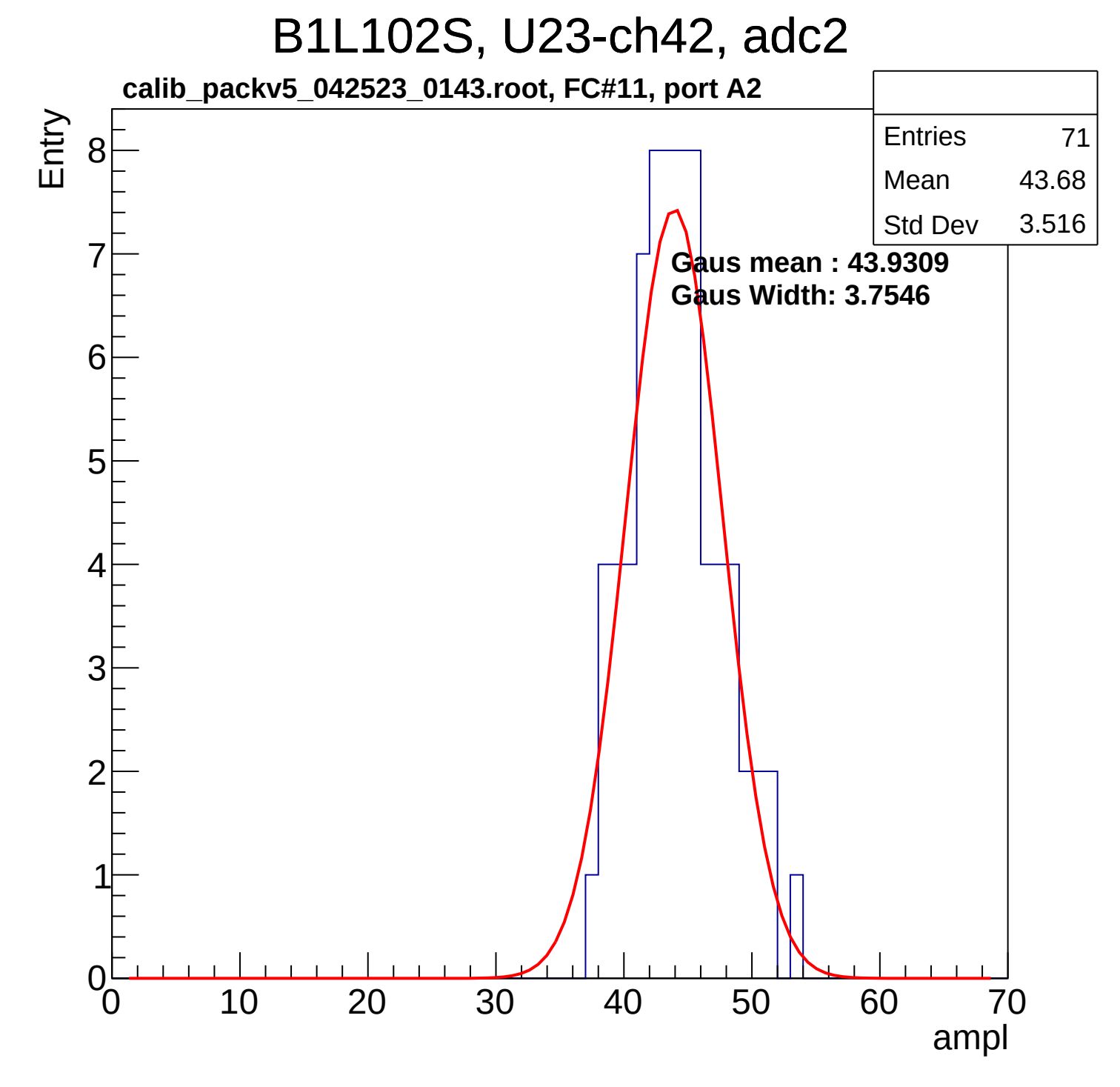

B1L102S, U23-ch41, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 60.82
Std Dev 1.796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

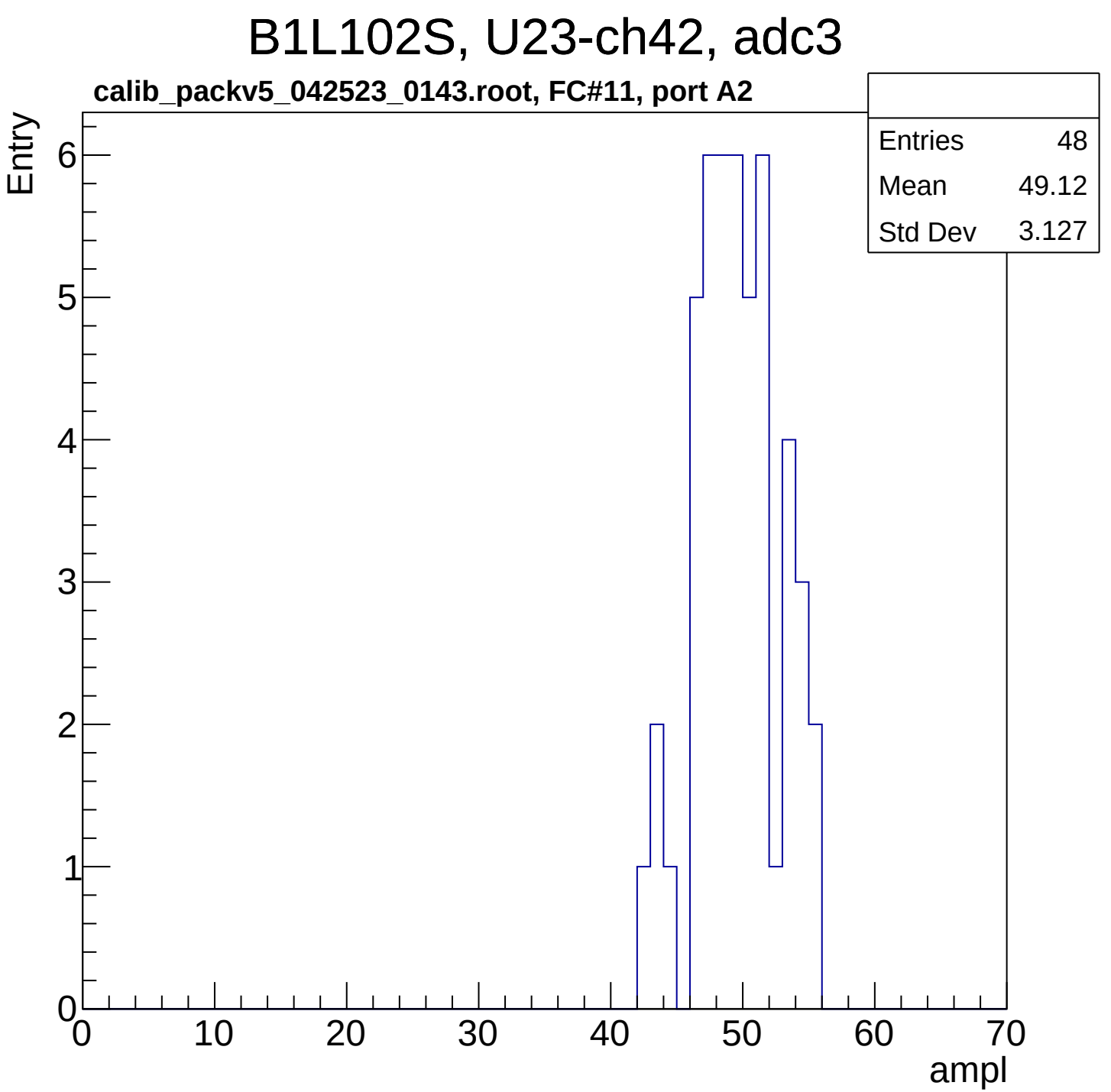

B1L102S, U23-ch41, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

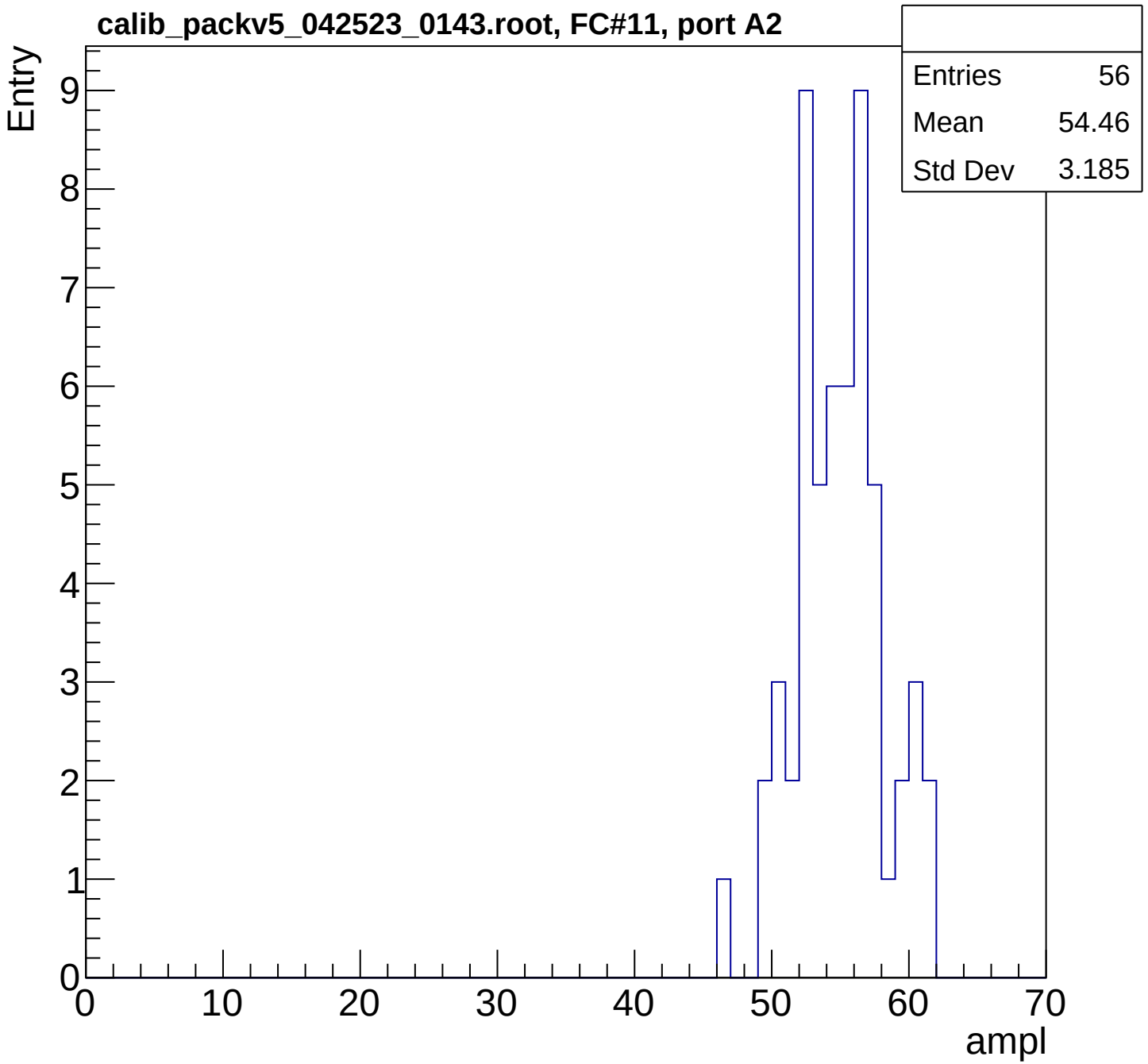

B1L102S, U23-ch41, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

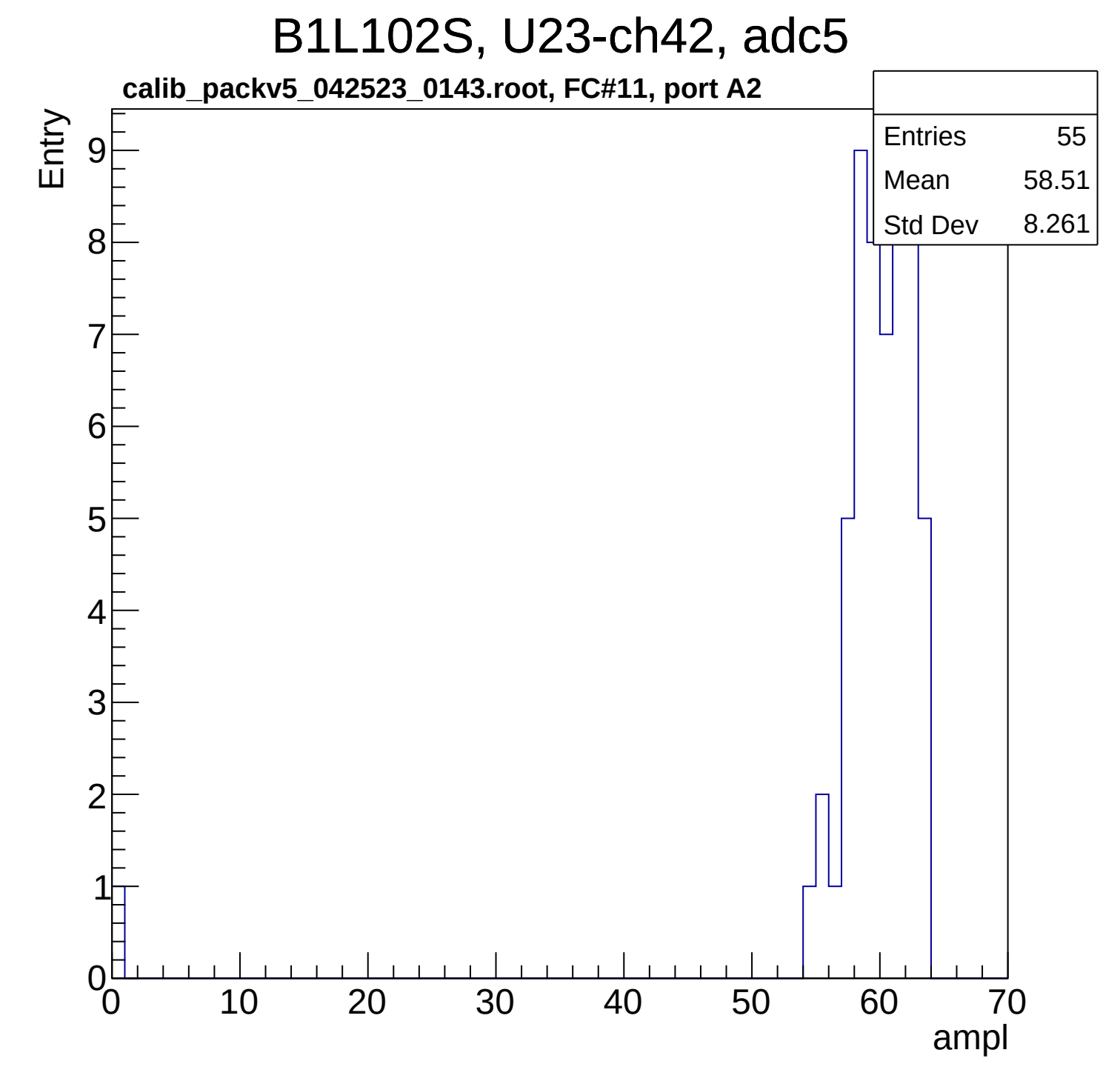

B1L102S, U23-ch42, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 28.09
Std Dev 5.927
Gaus mean : 29.5423
Gaus Width: 3.4862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

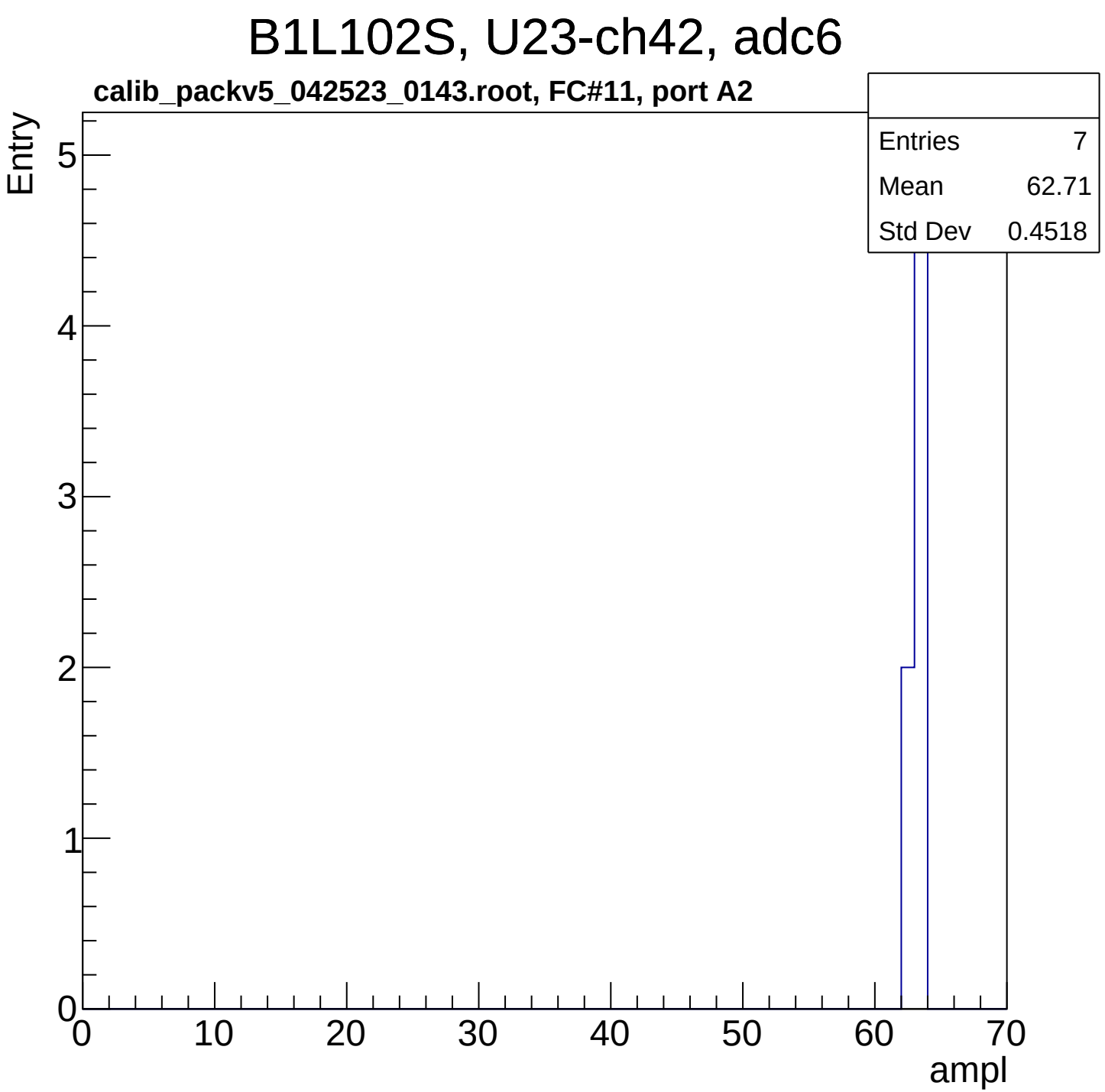

B1L102S, U23-ch42, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 35.96
Std Dev 3.55
Gaus mean : 36.2115
Gaus Width: 3.5971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

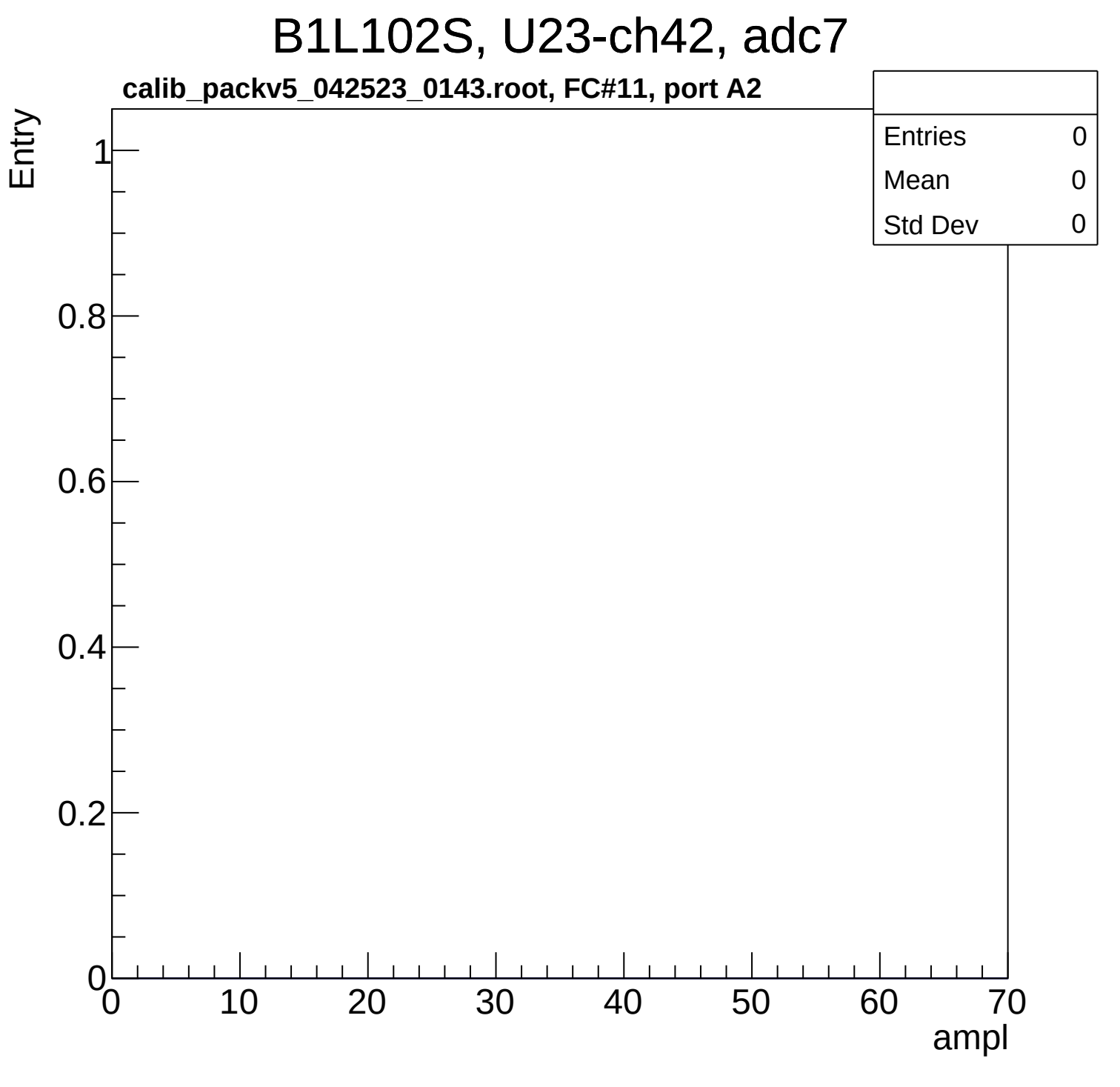

B1L102S, U23-ch42, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 43.68
Std Dev 3.516
Gaus mean : 43.9309
Gaus Width: 3.7546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

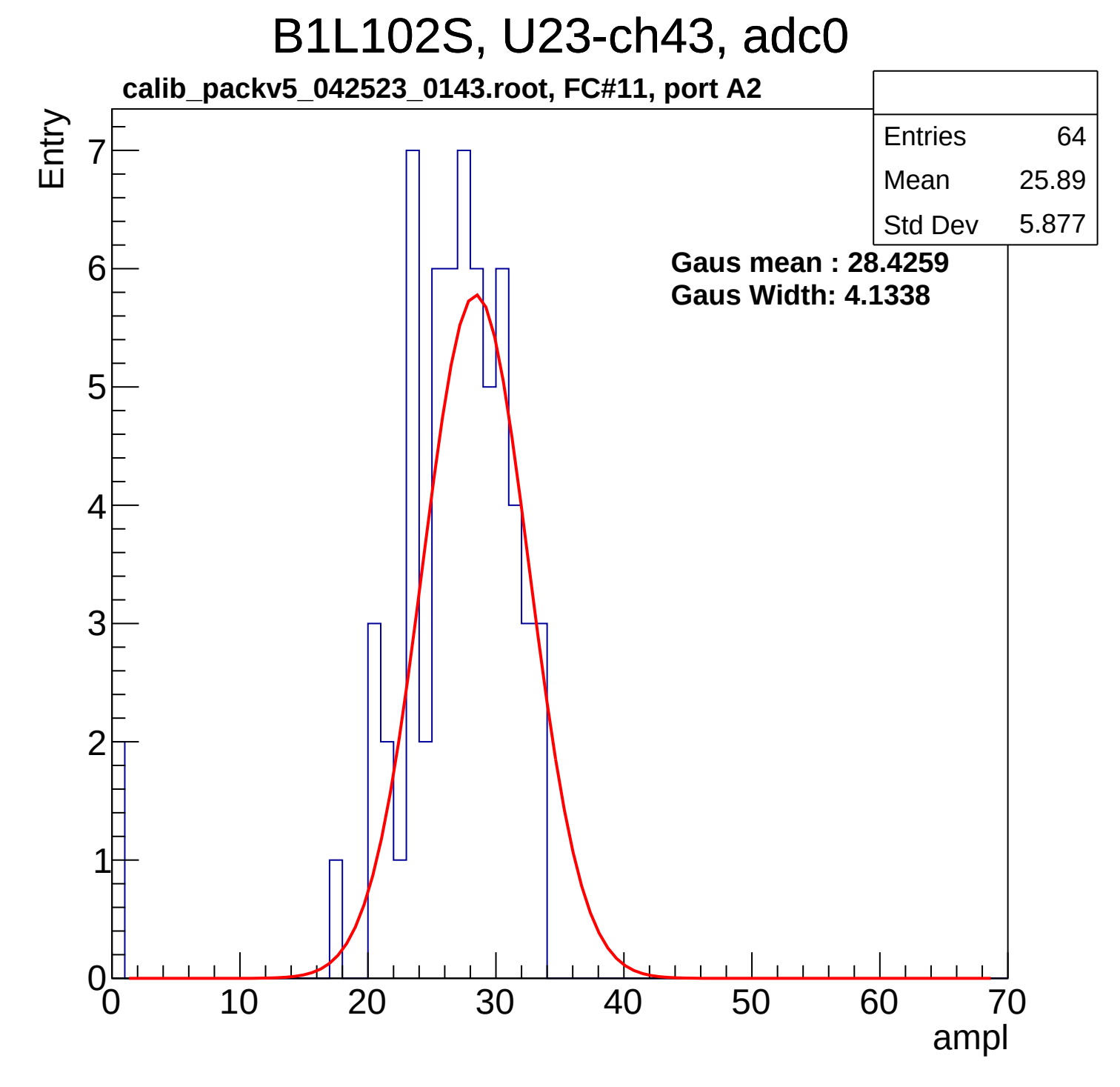

B1L102S, U23-ch42, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 49.12
Std Dev 3.127
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

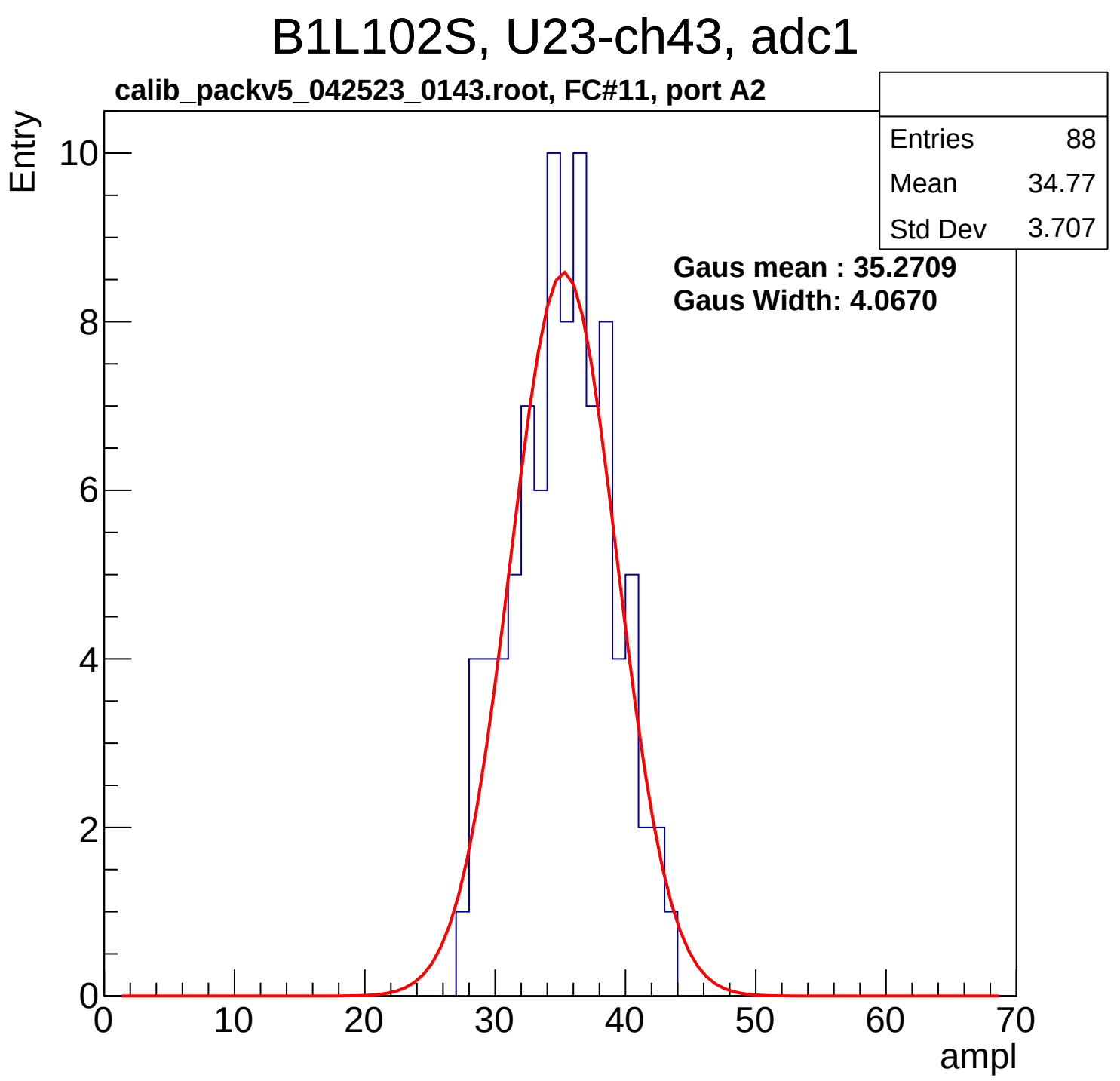

B1L102S, U23-ch42, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 54.46
Std Dev 3.185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

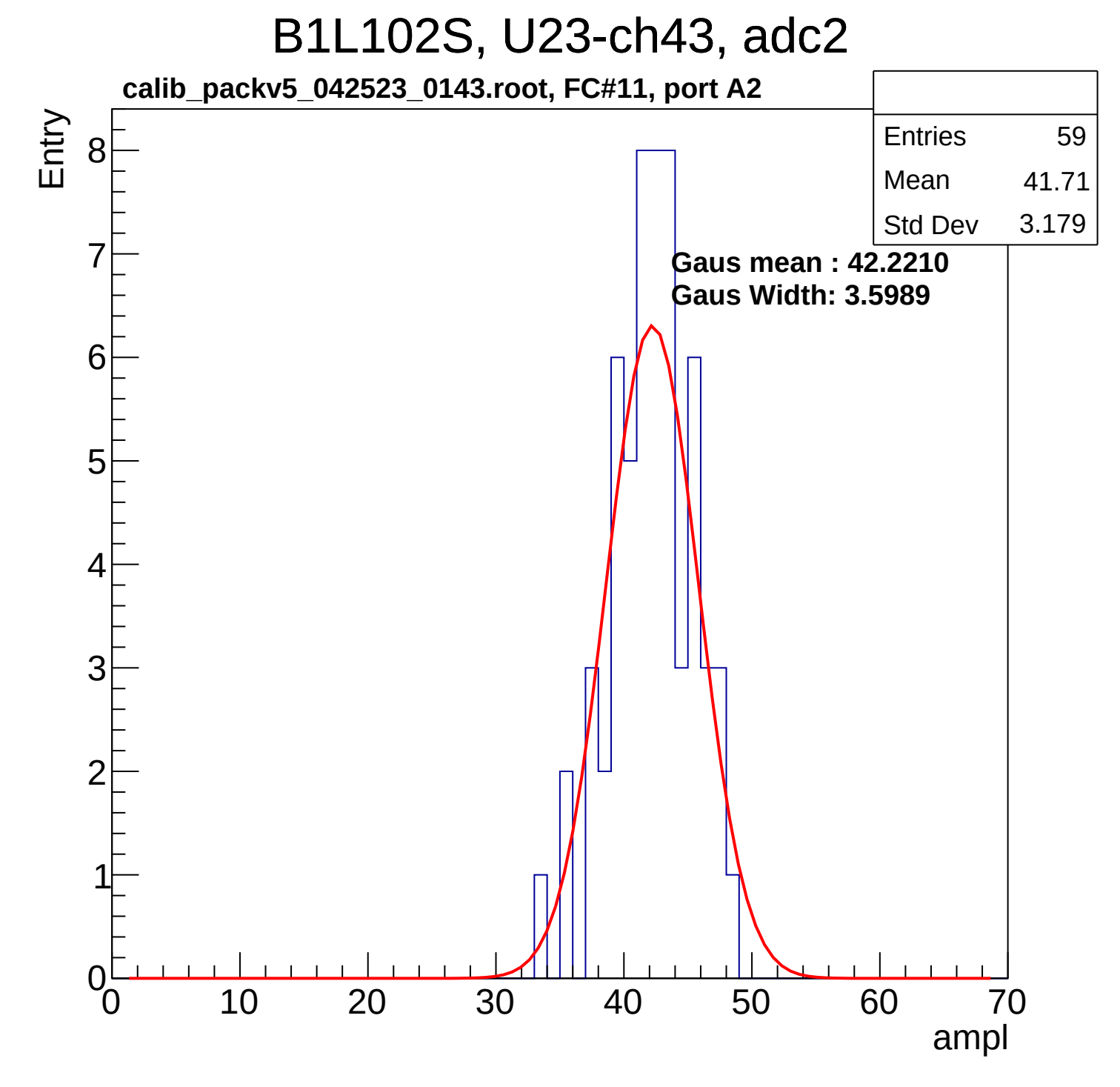

B1L102S, U23-ch42, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 58.51
Std Dev 8.261
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

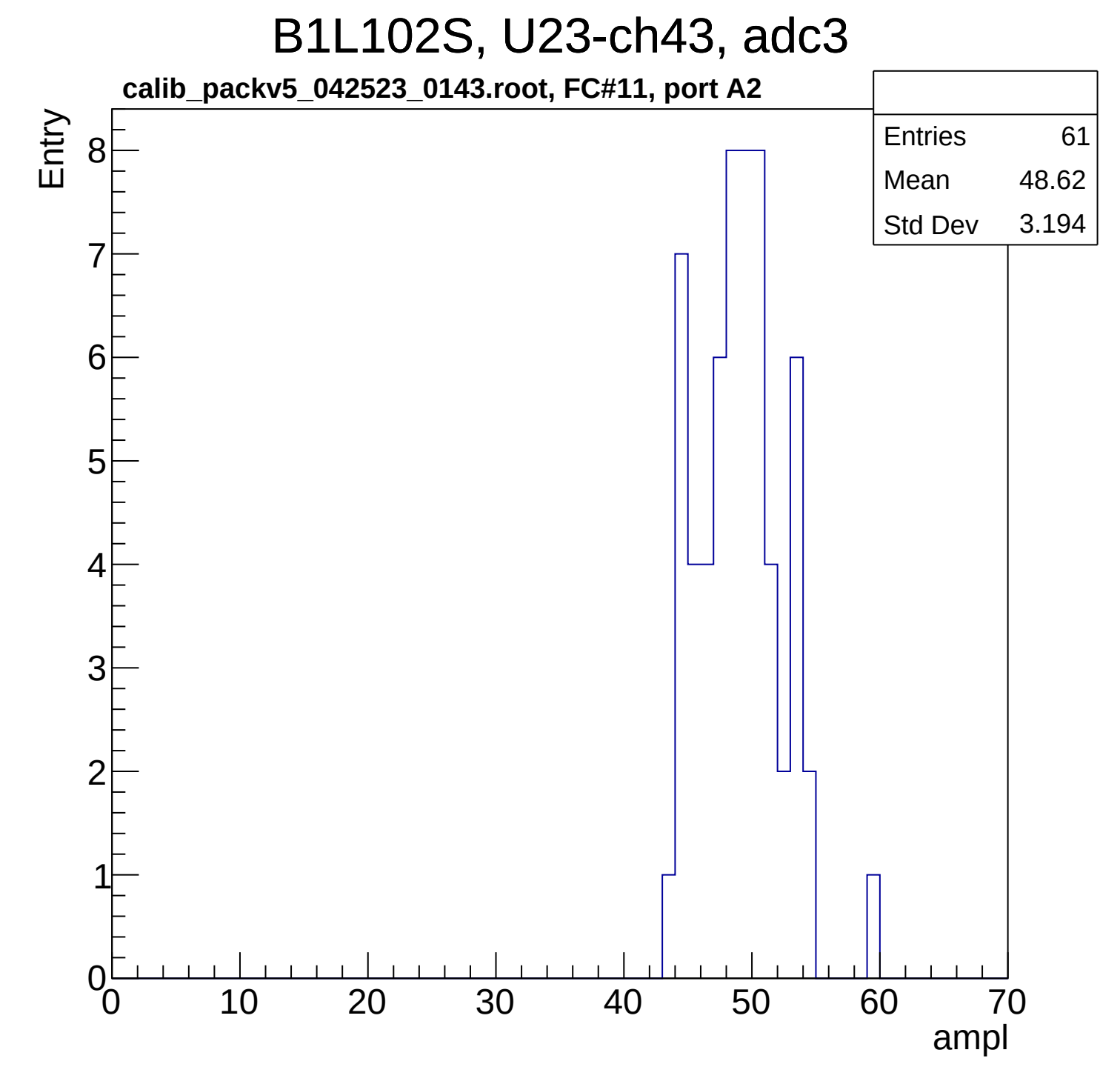

B1L102S, U23-ch42, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 62.71
Std Dev 0.4518
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

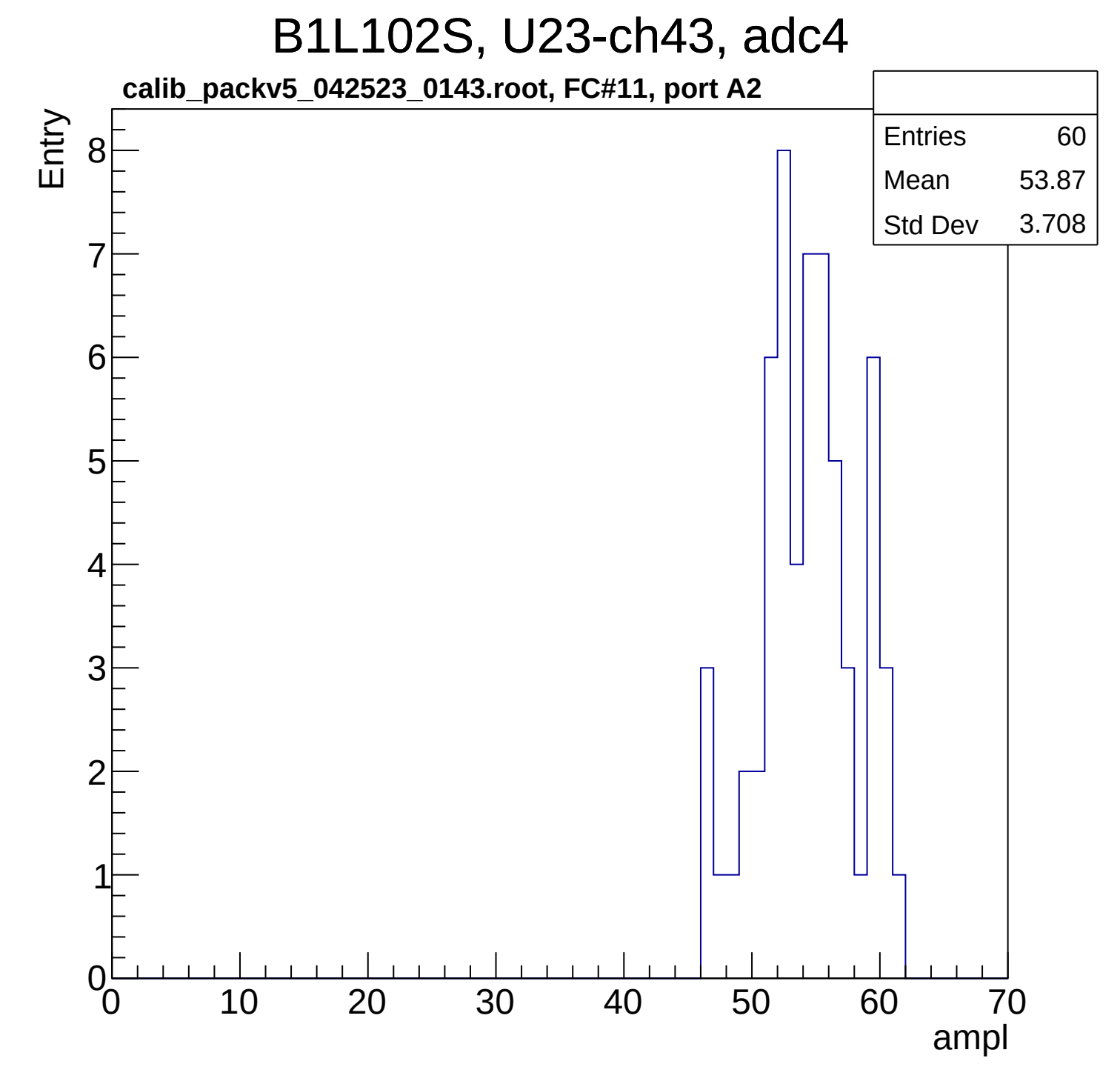

B1L102S, U23-ch42, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

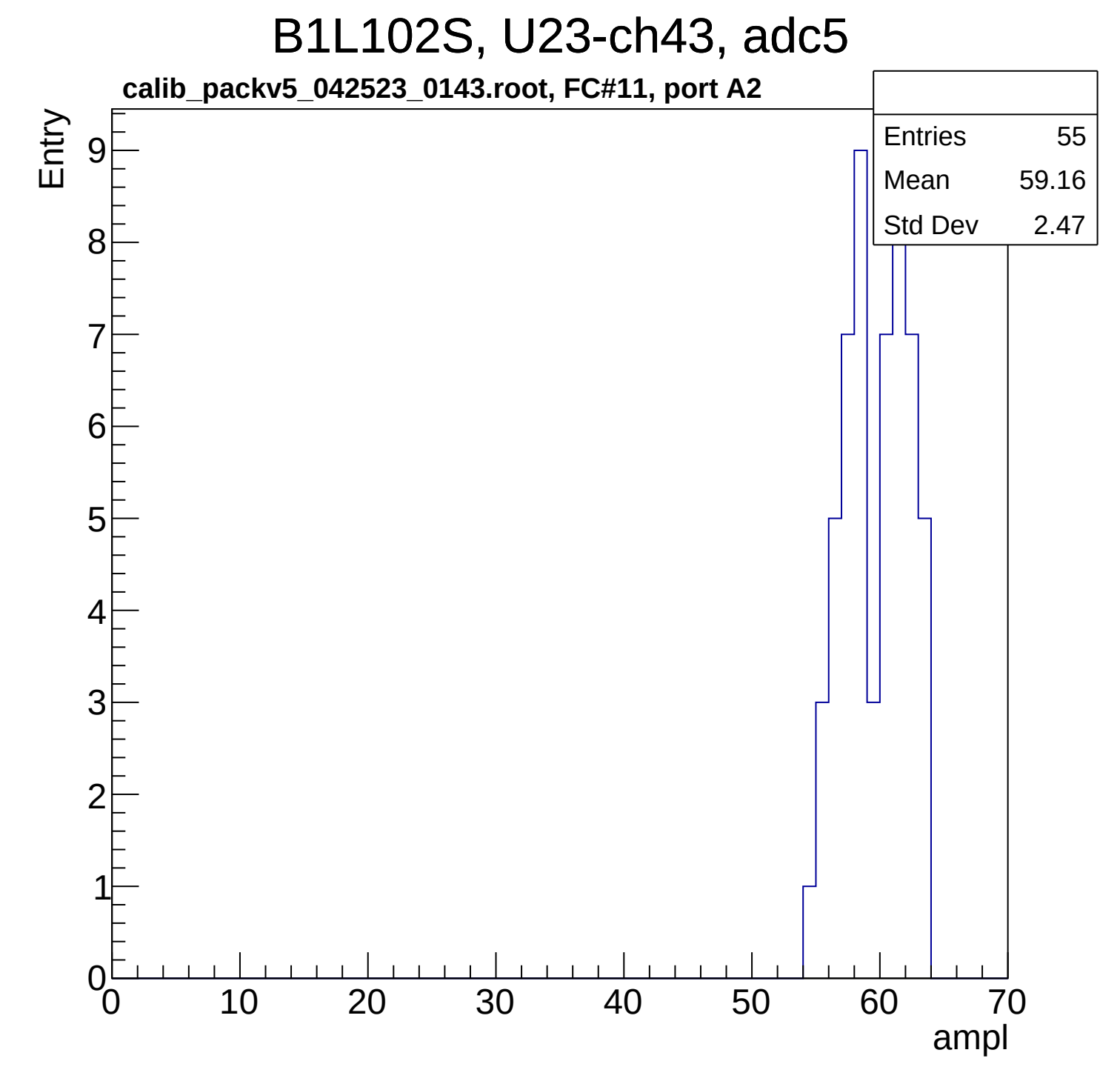

B1L102S, U23-ch43, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 25.89
Std Dev 5.877
Gaus mean : 28.4259
Gaus Width: 4.1338
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

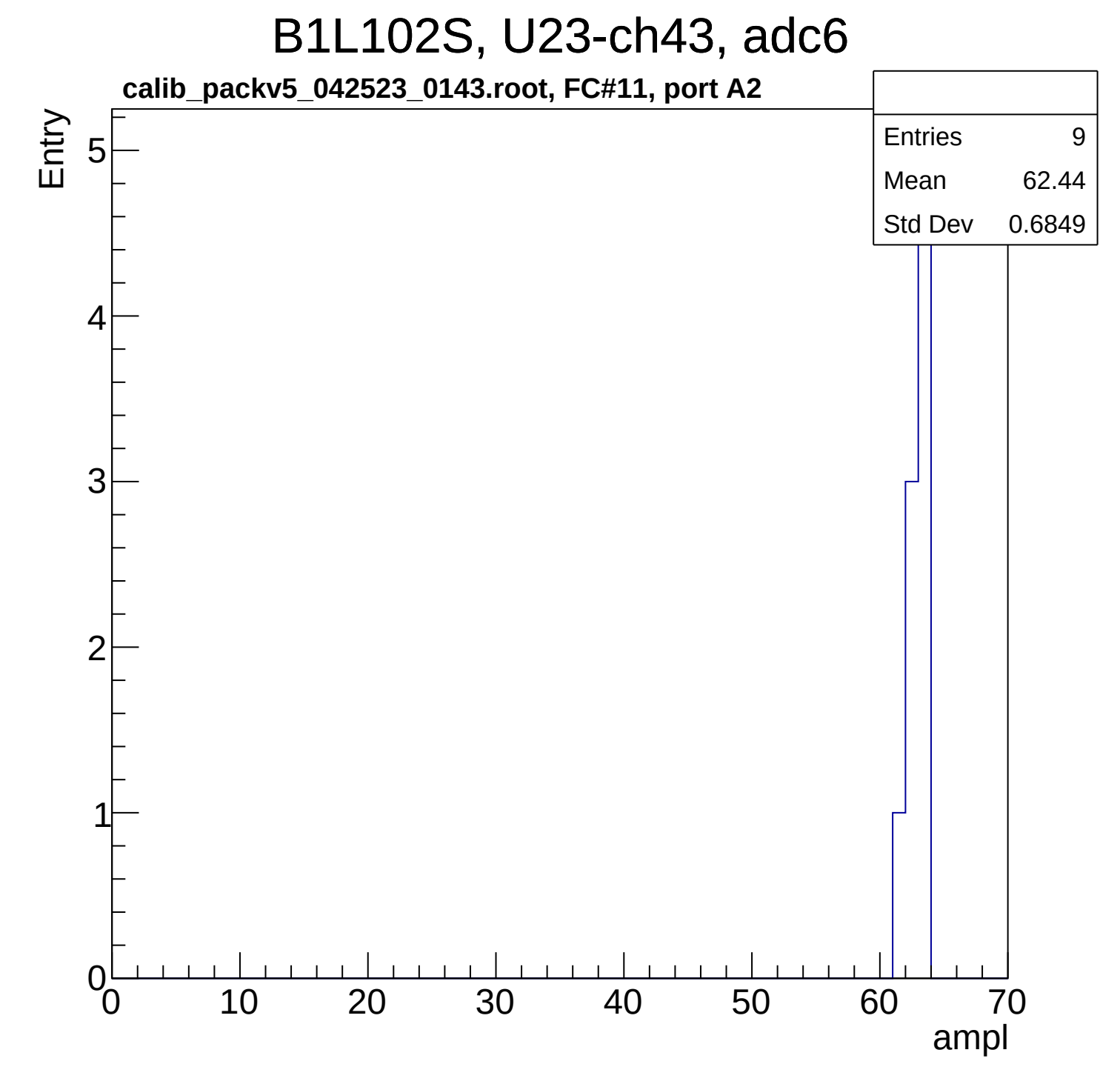

B1L102S, U23-ch43, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 34.77
Std Dev 3.707
Gaus mean : 35.2709
Gaus Width: 4.0670
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

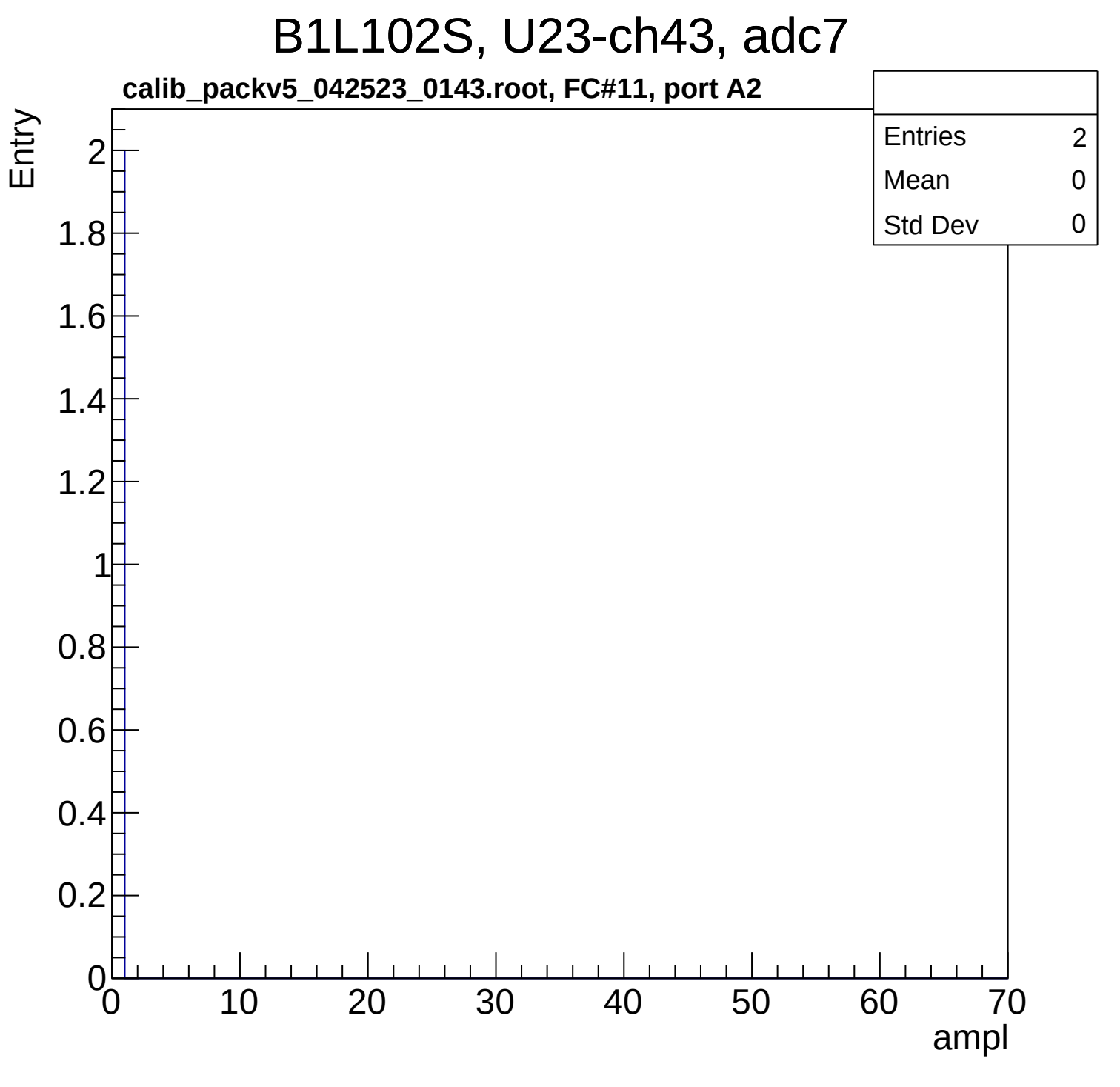

B1L102S, U23-ch43, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 41.71
Std Dev 3.179
Gaus mean : 42.2210
Gaus Width: 3.5989
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

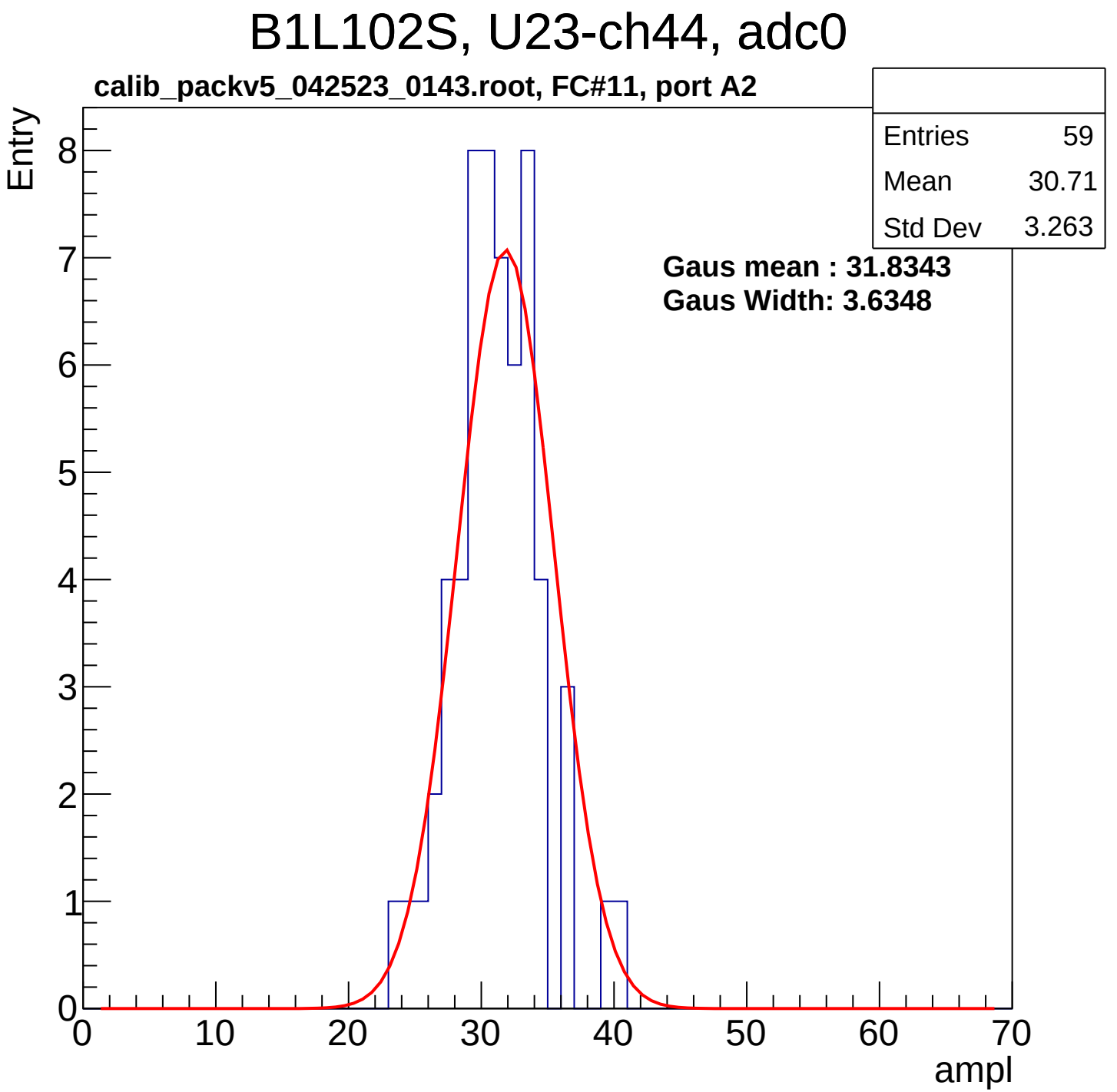

B1L102S, U23-ch43, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 48.62
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

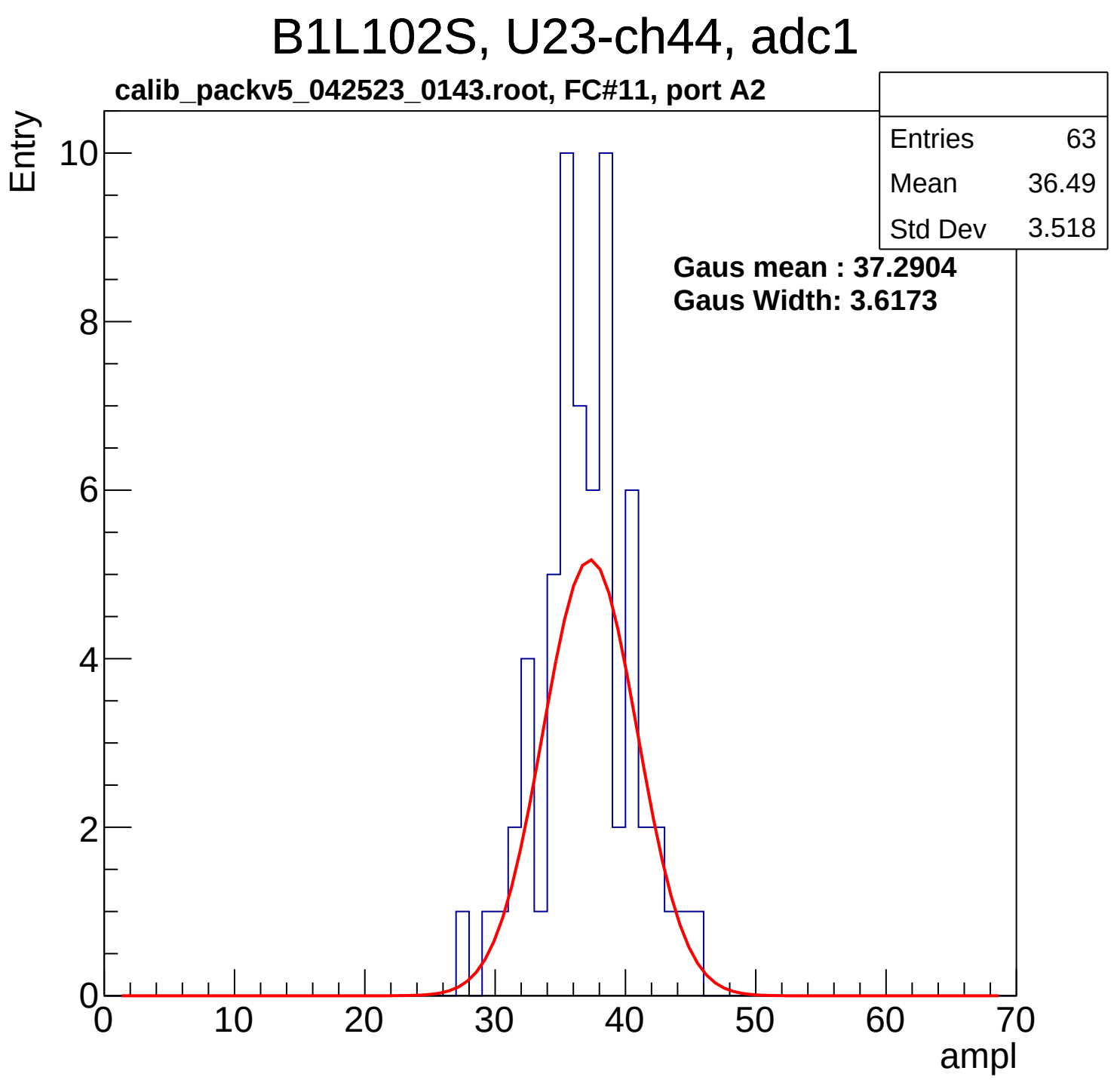

B1L102S, U23-ch43, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 53.87
Std Dev 3.708
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

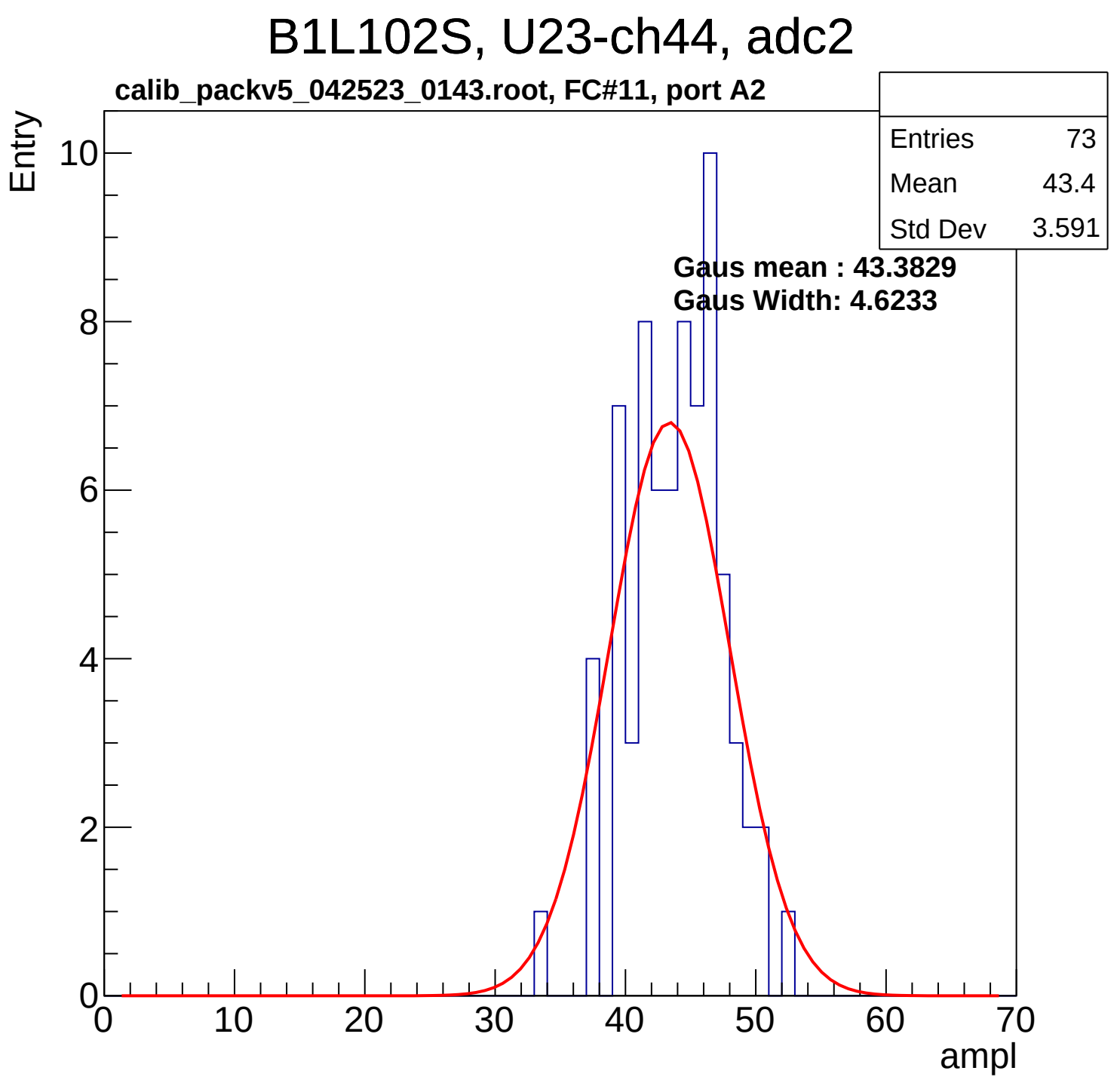

B1L102S, U23-ch43, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 59.16
Std Dev 2.47
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

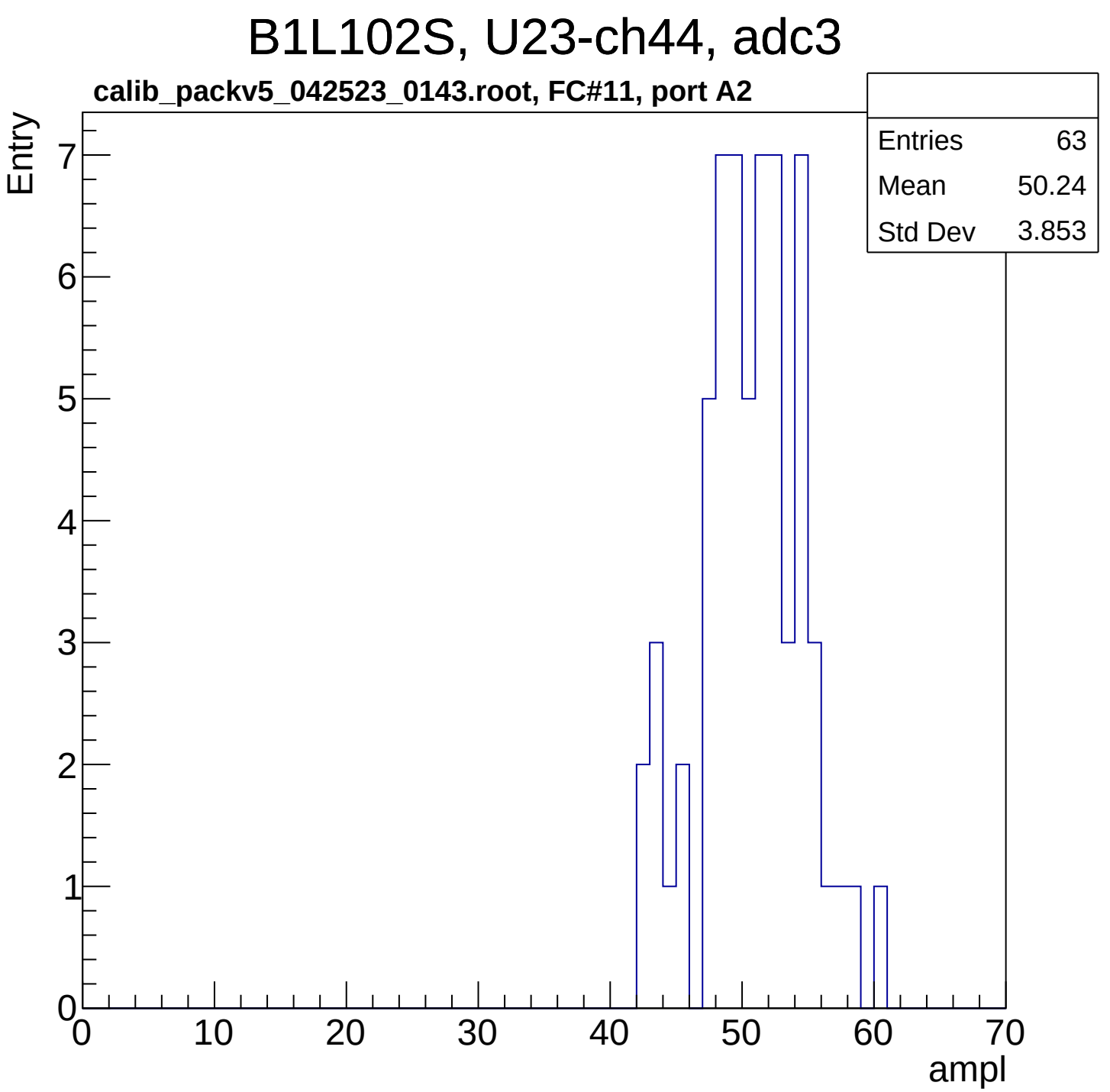

B1L102S, U23-ch43, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62.44
Std Dev 0.6849
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

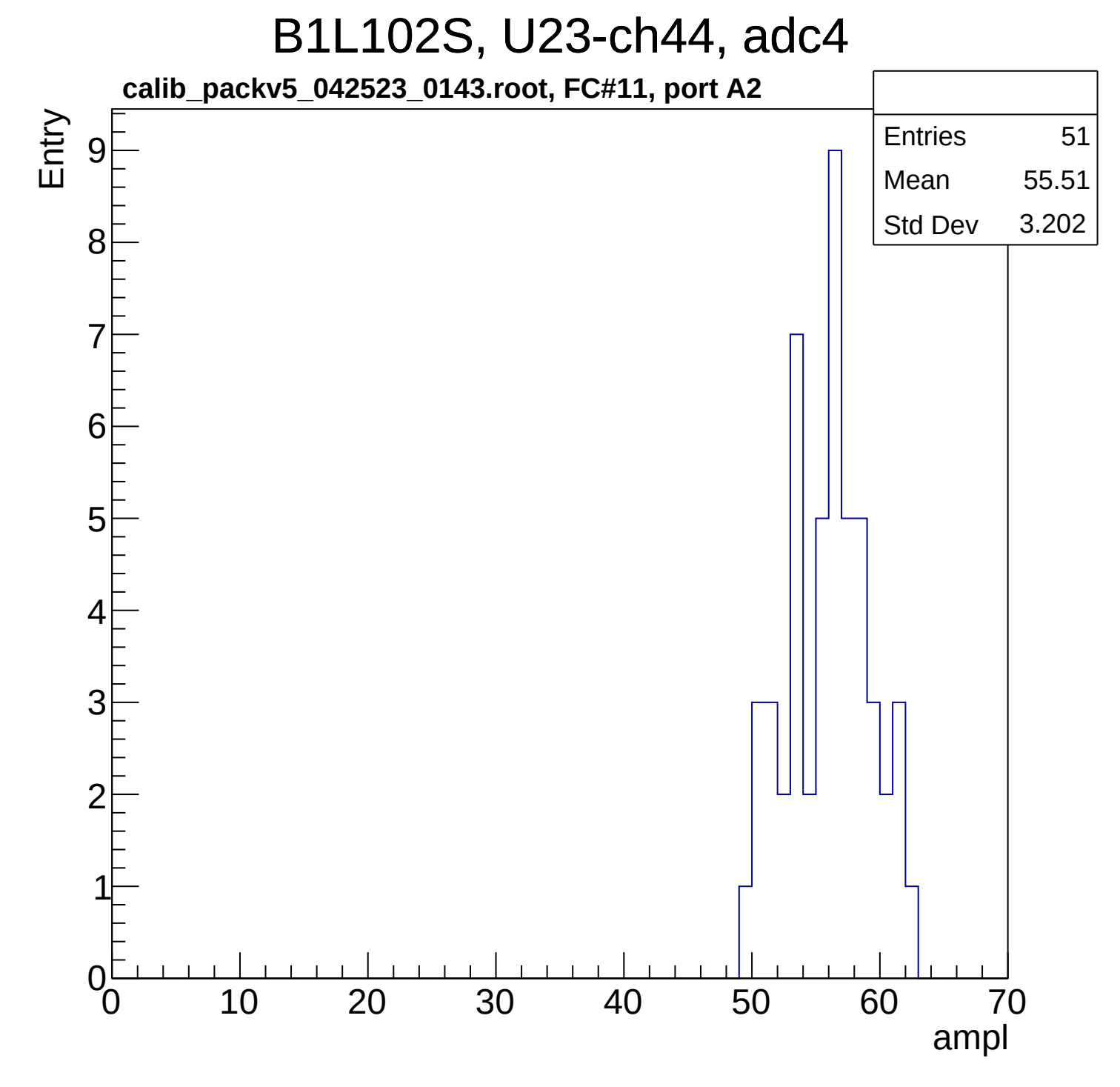

B1L102S, U23-ch43, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

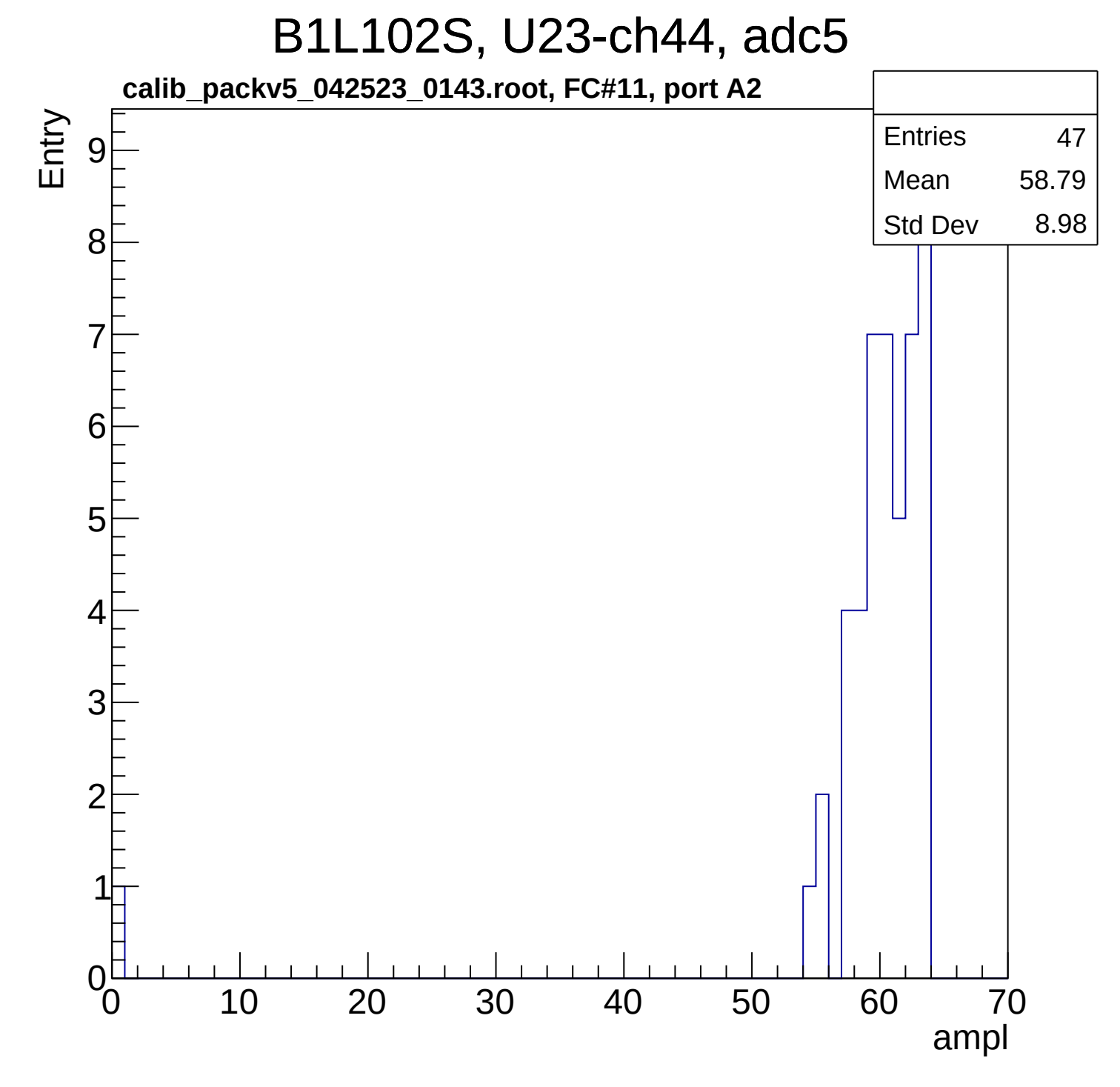

B1L102S, U23-ch44, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 30.71
Std Dev 3.263
Gaus mean : 31.8343
Gaus Width: 3.6348
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

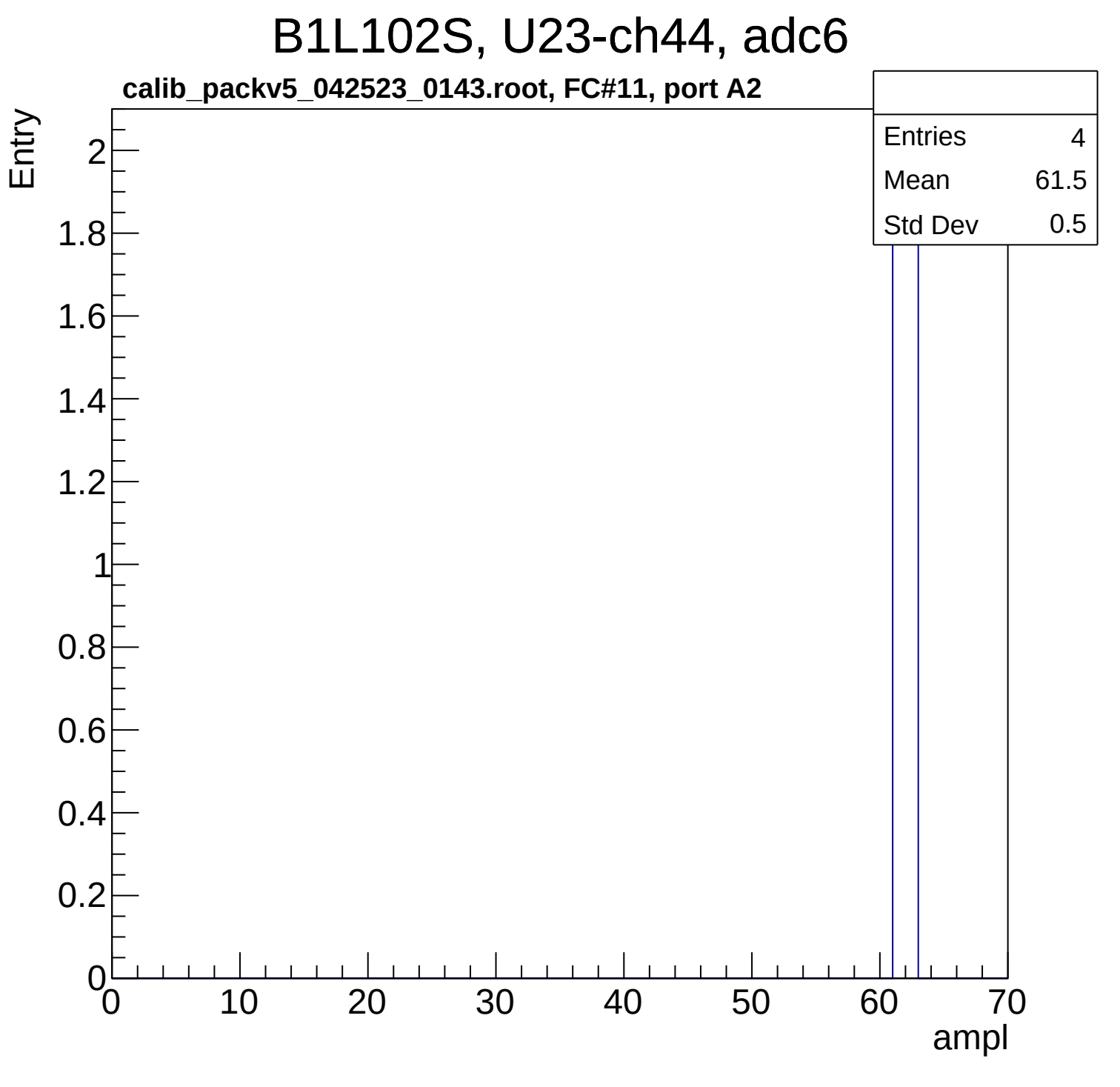

B1L102S, U23-ch44, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 36.49
Std Dev 3.518
Gaus mean : 37.2904
Gaus Width: 3.6173
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

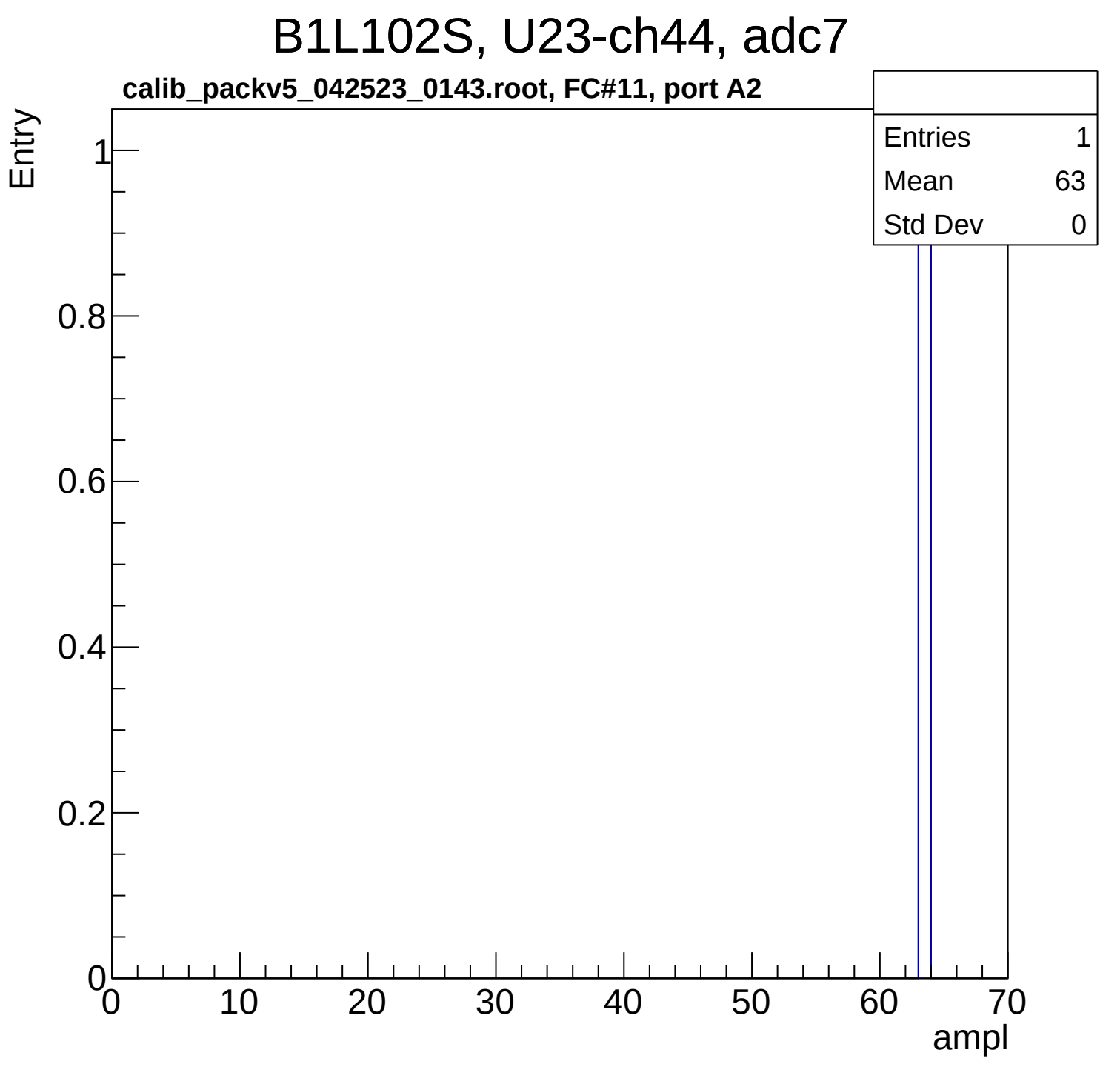

B1L102S, U23-ch44, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 43.4
Std Dev 3.591
Gaus mean : 43.3829
Gaus Width: 4.6233
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

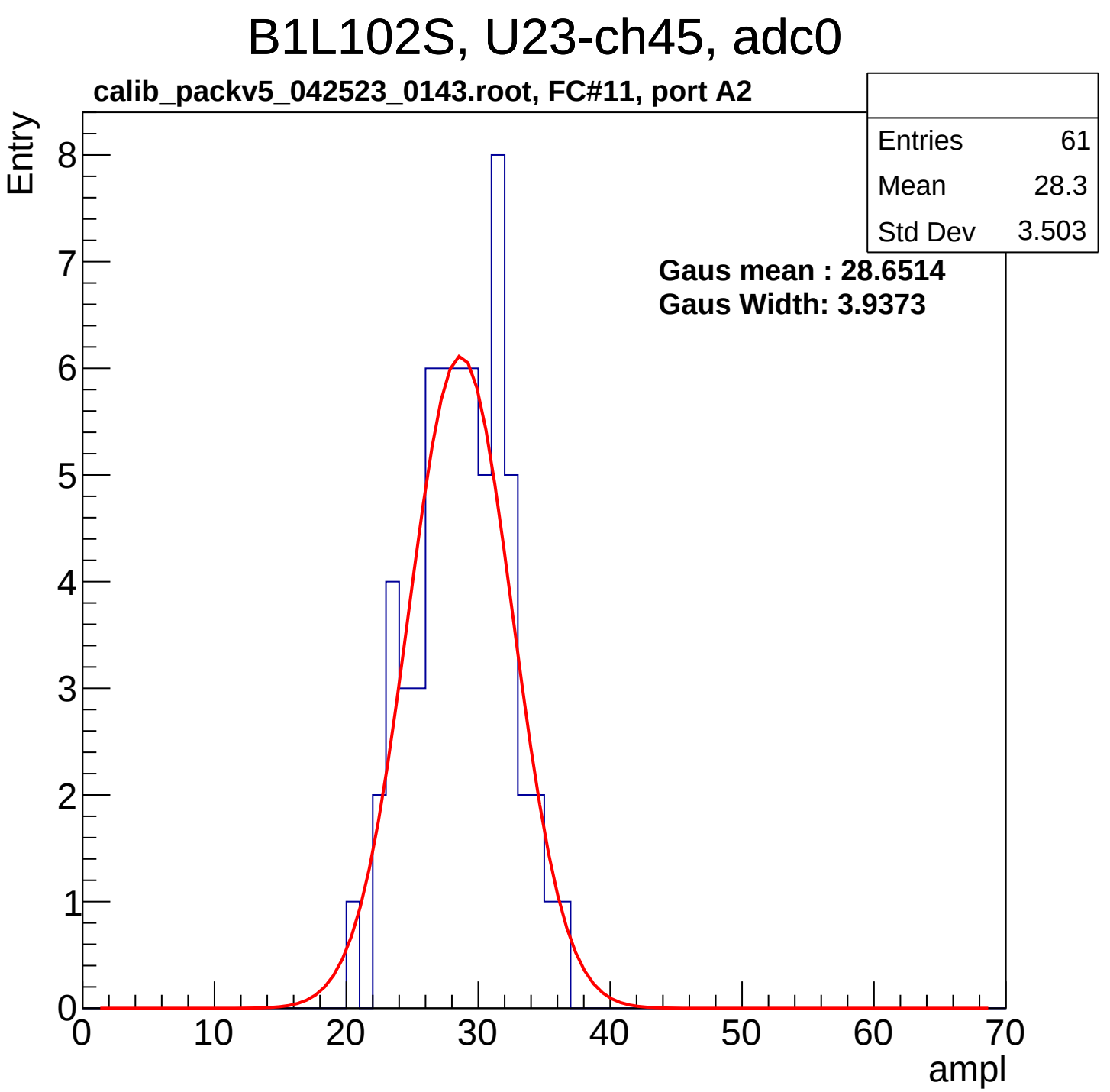

B1L102S, U23-ch44, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 50.24
Std Dev 3.853
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

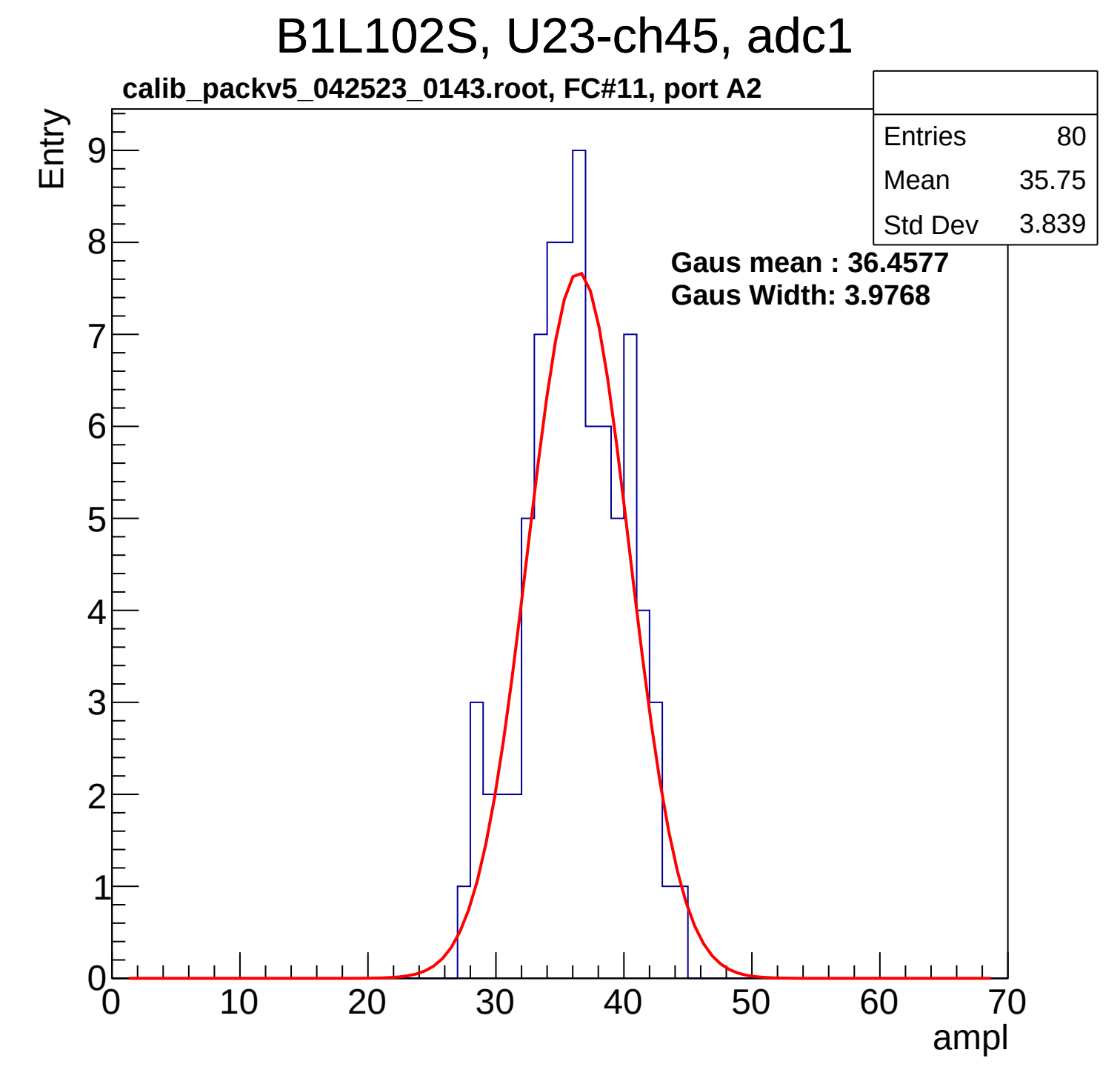

B1L102S, U23-ch44, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 55.51
Std Dev 3.202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

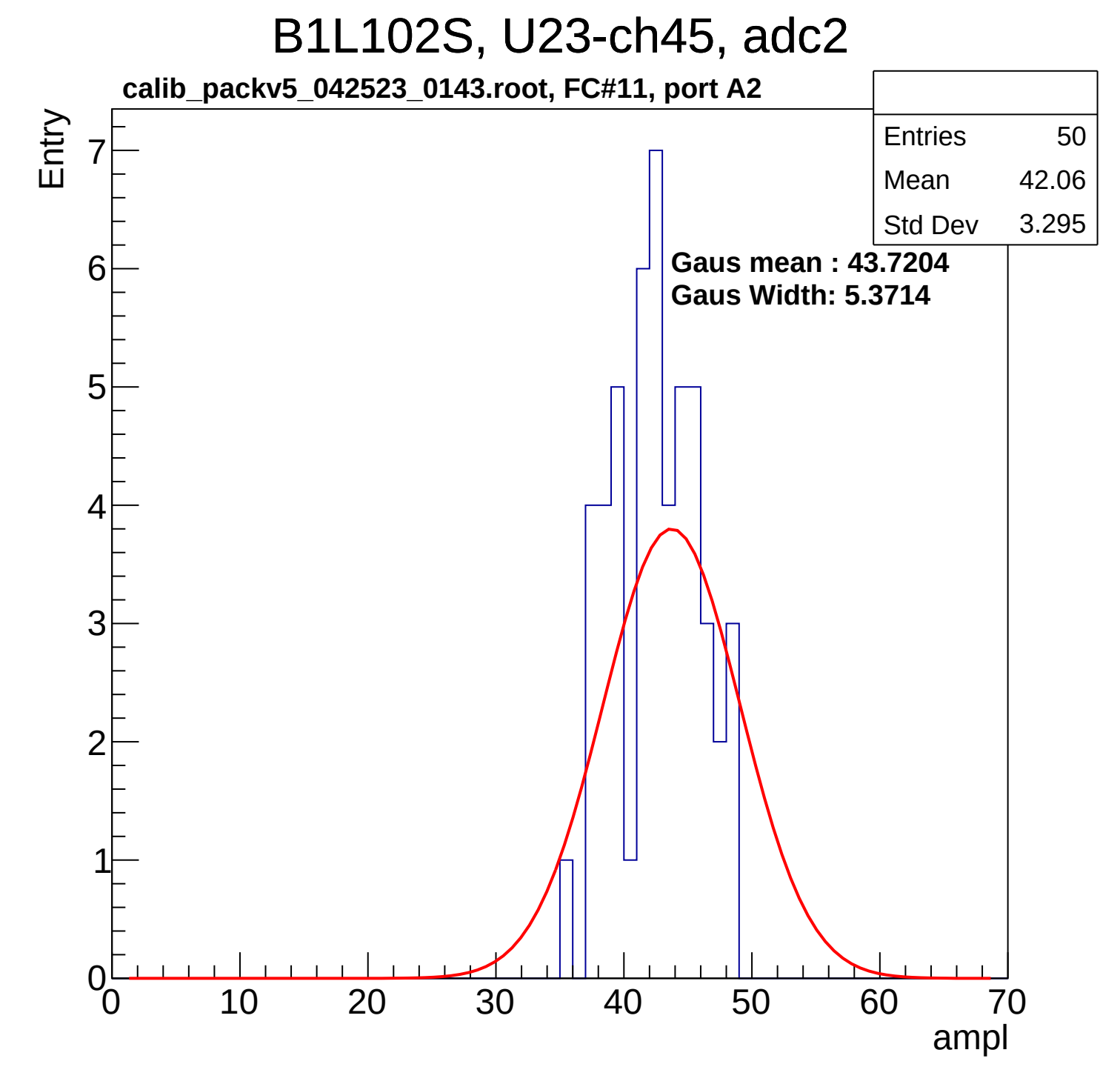

B1L102S, U23-ch44, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.79
Std Dev 8.98
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

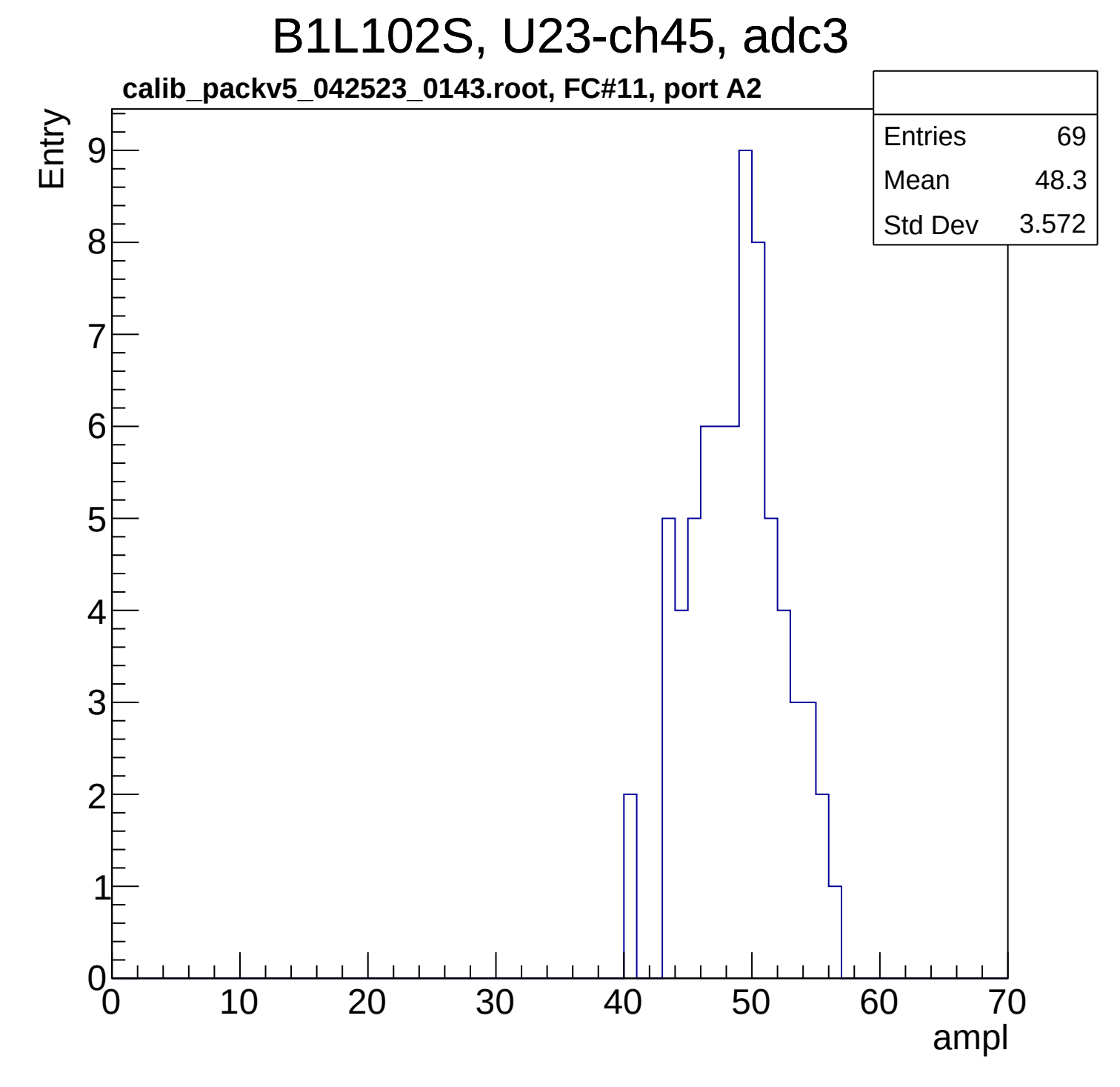

B1L102S, U23-ch44, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 61.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

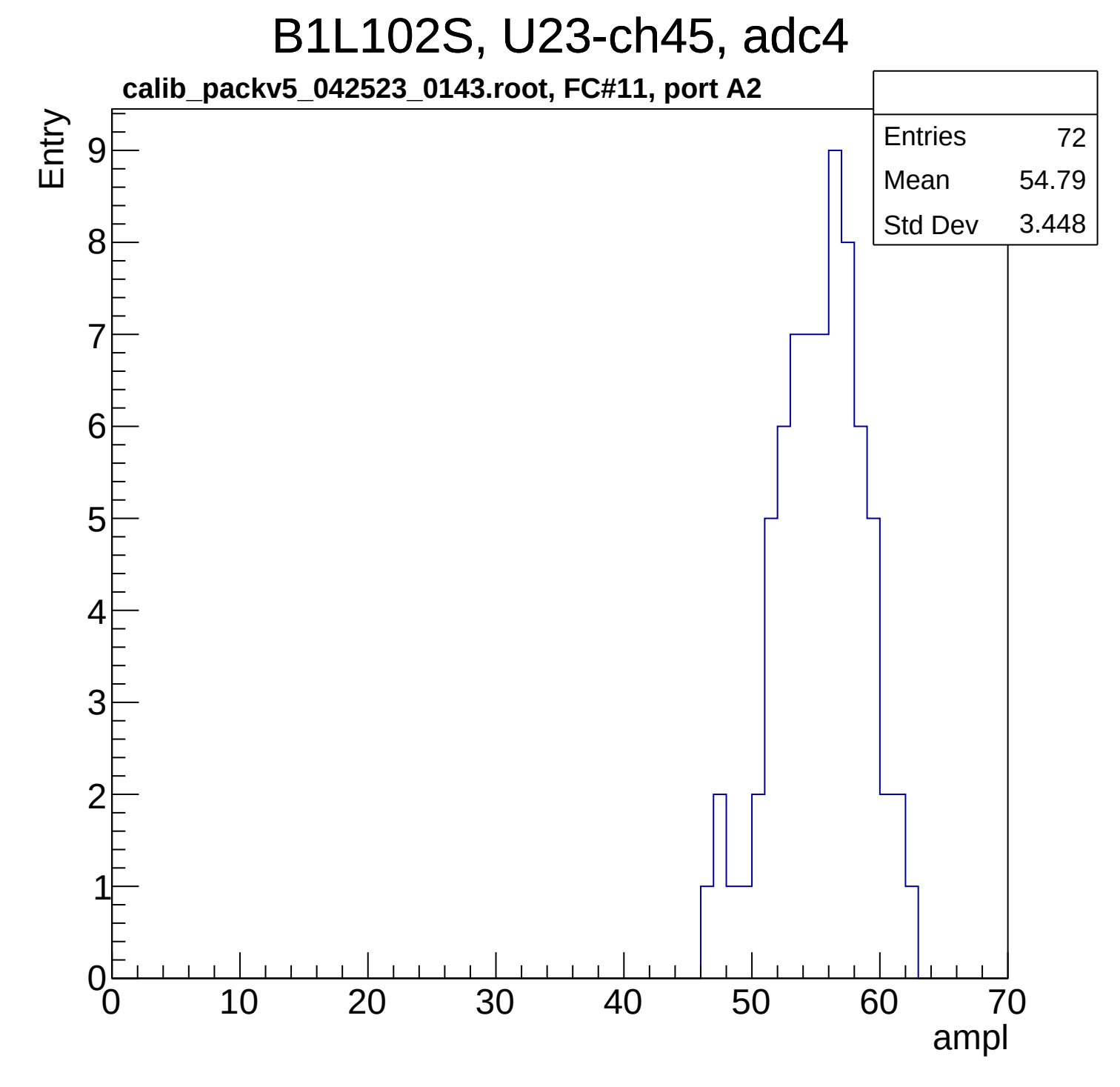

B1L102S, U23-ch44, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

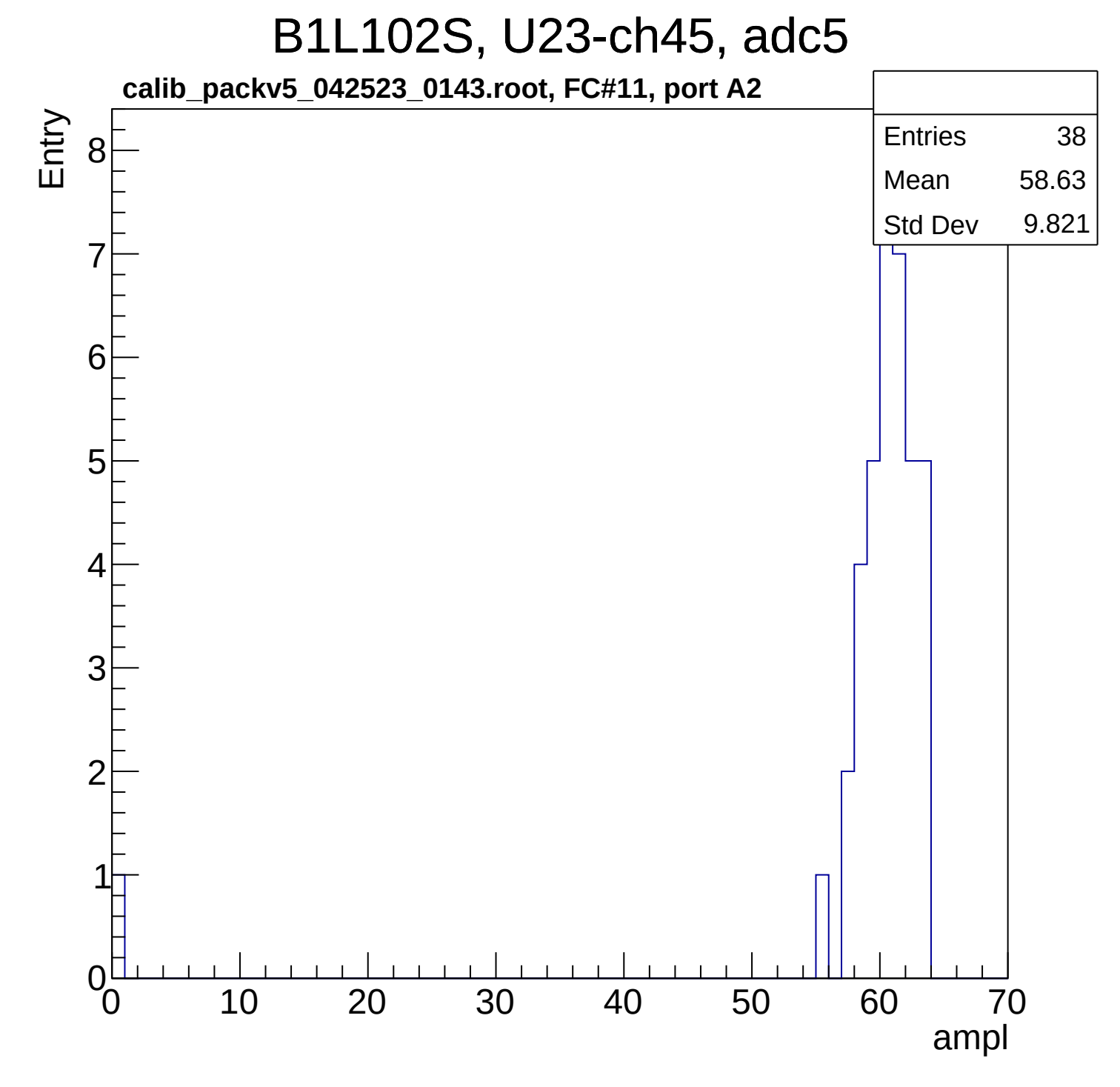

B1L102S, U23-ch45, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 28.3
Std Dev 3.503
Gaus mean : 28.6514
Gaus Width: 3.9373
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

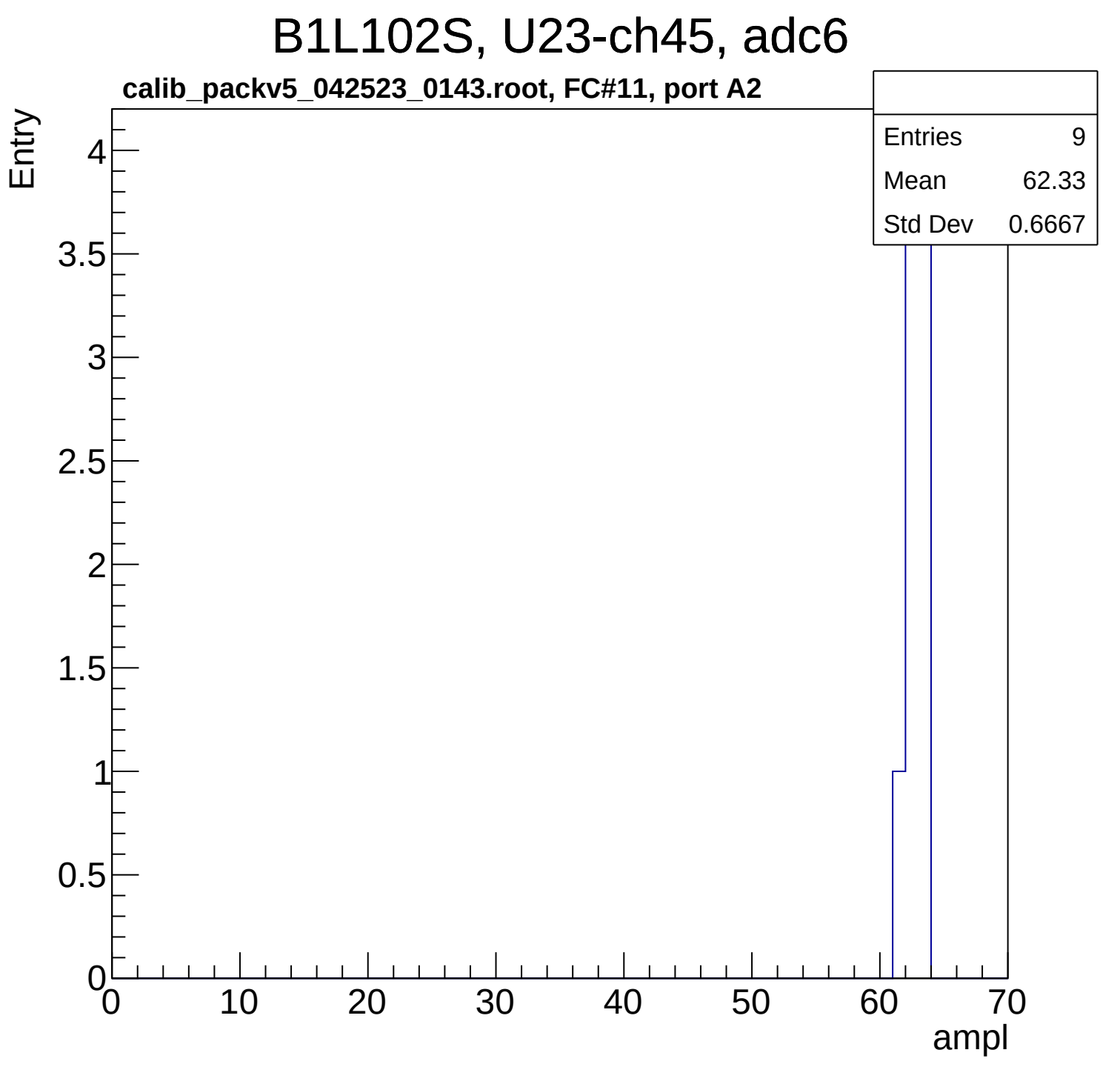

B1L102S, U23-ch45, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 35.75
Std Dev 3.839
Gaus mean : 36.4577
Gaus Width: 3.9768
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

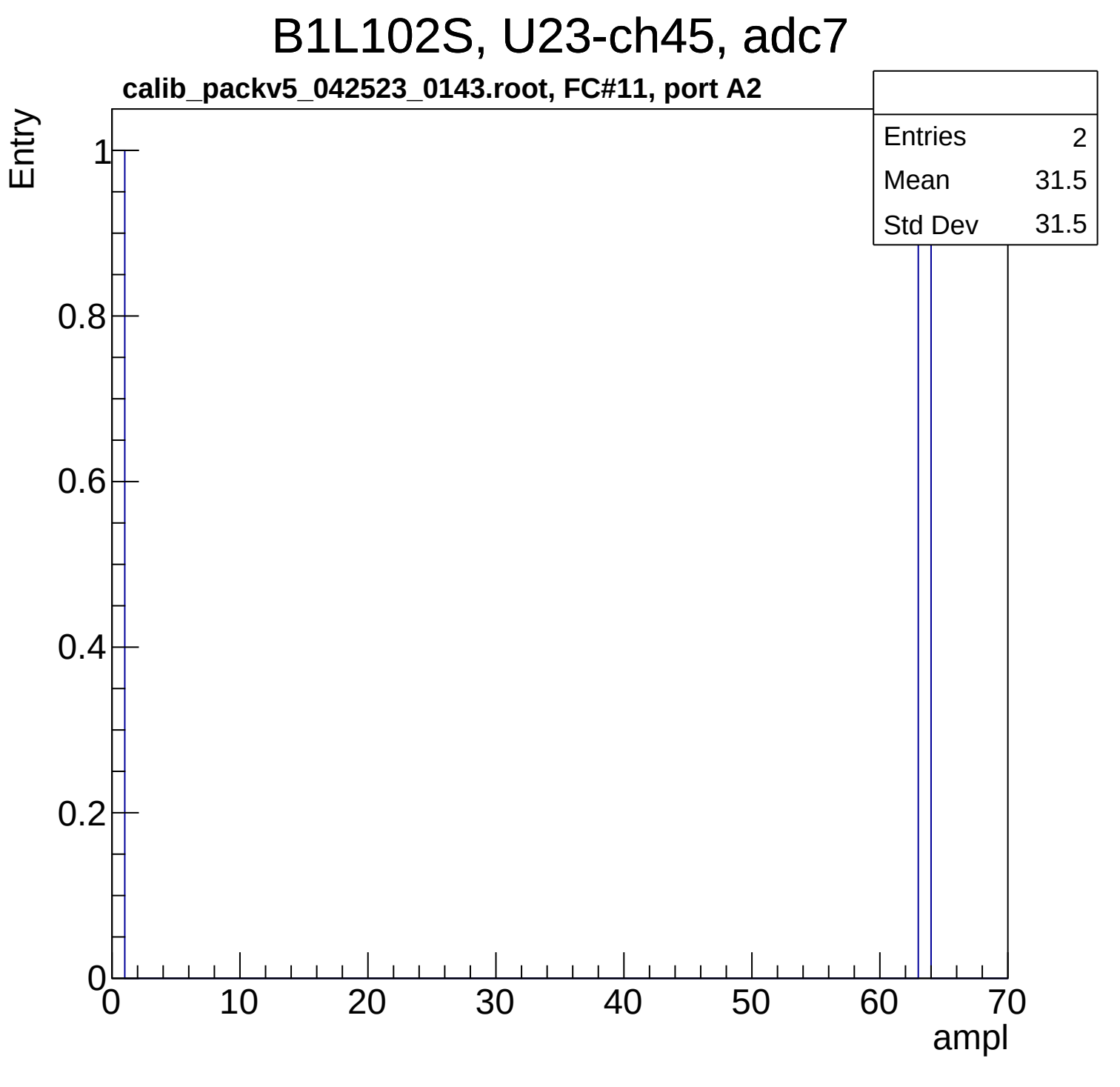

B1L102S, U23-ch45, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 42.06
Std Dev 3.295
Gaus mean : 43.7204
Gaus Width: 5.3714
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

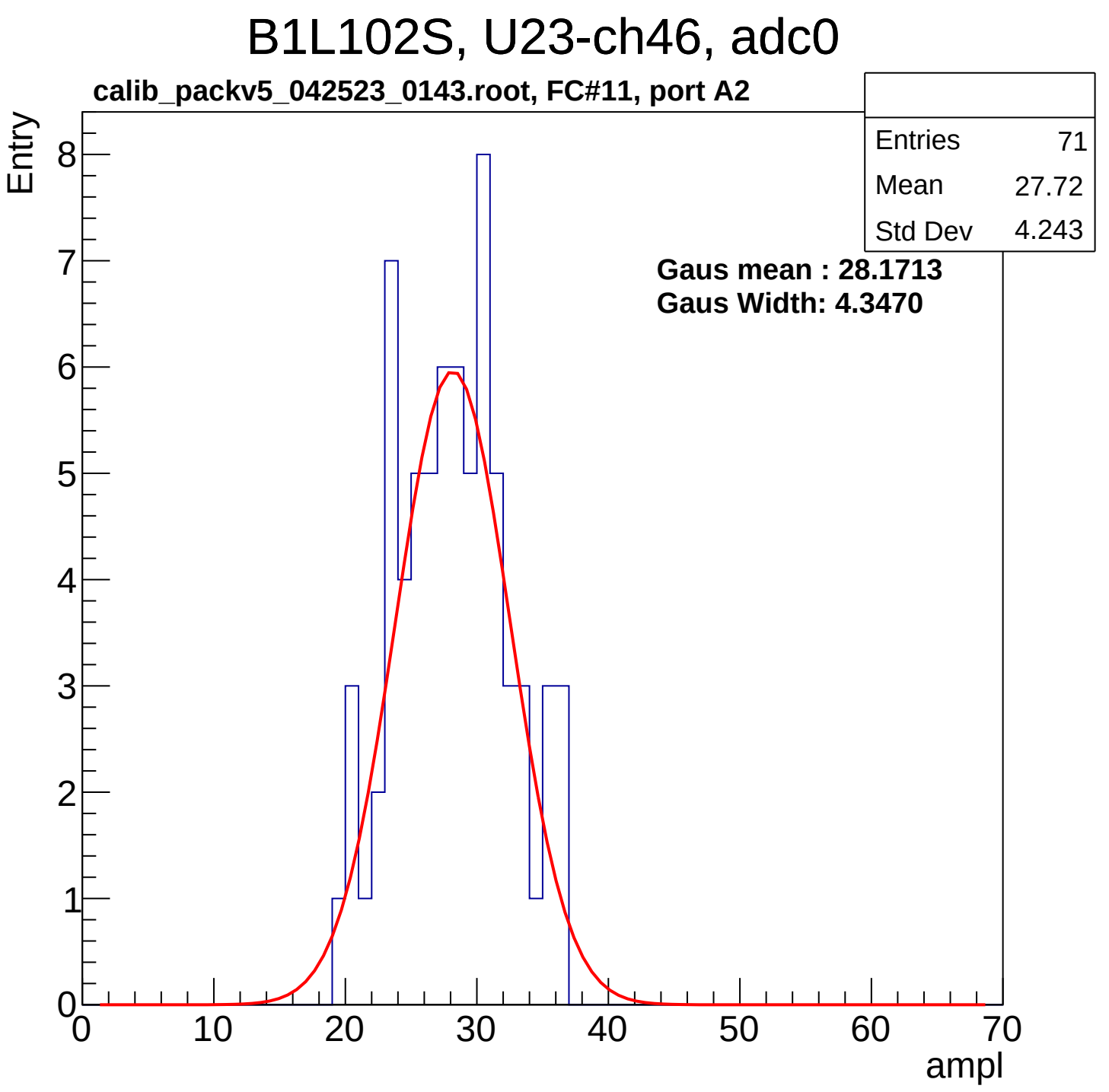

B1L102S, U23-ch45, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 48.3
Std Dev 3.572
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

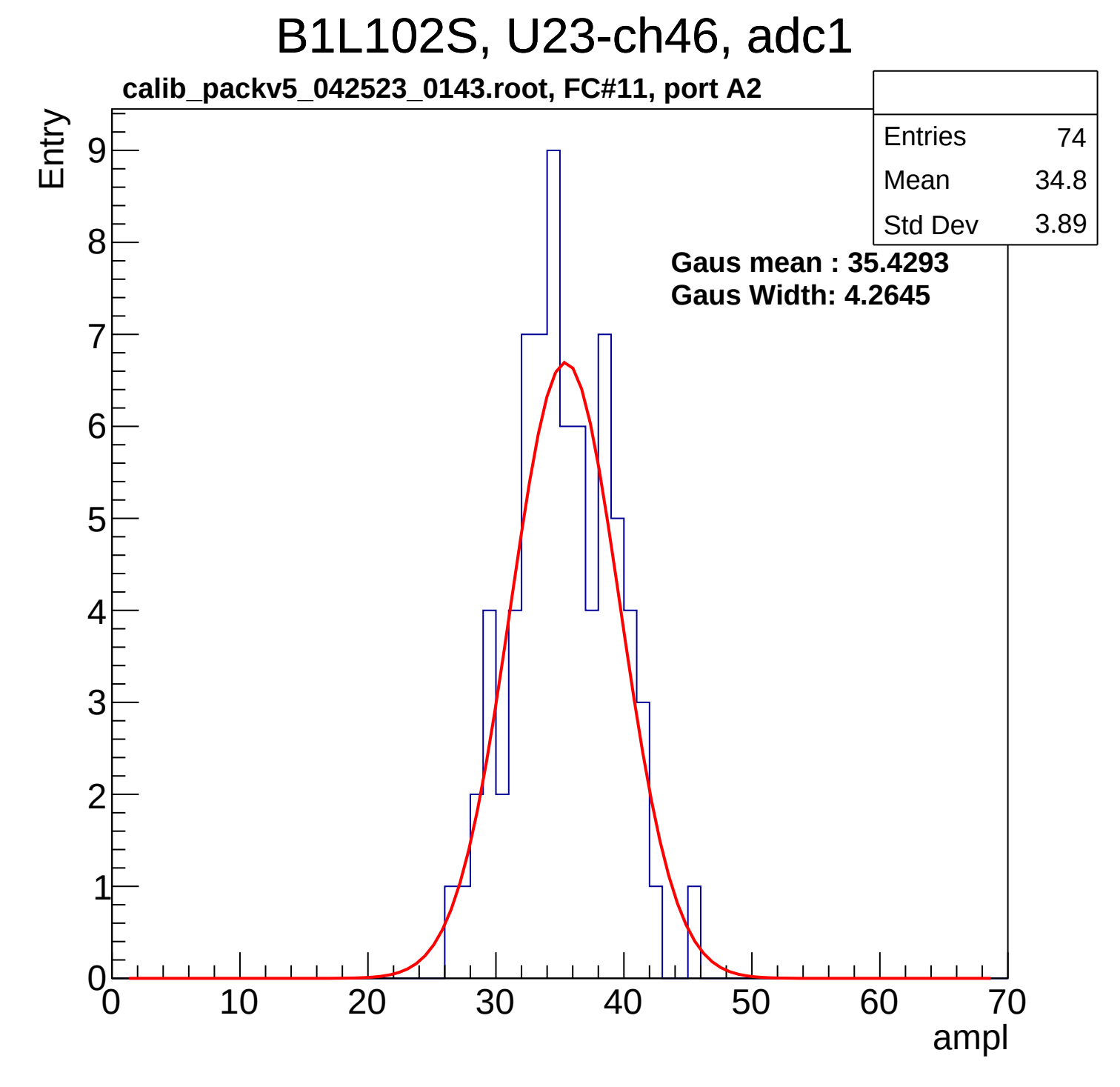

B1L102S, U23-ch45, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 54.79
Std Dev 3.448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

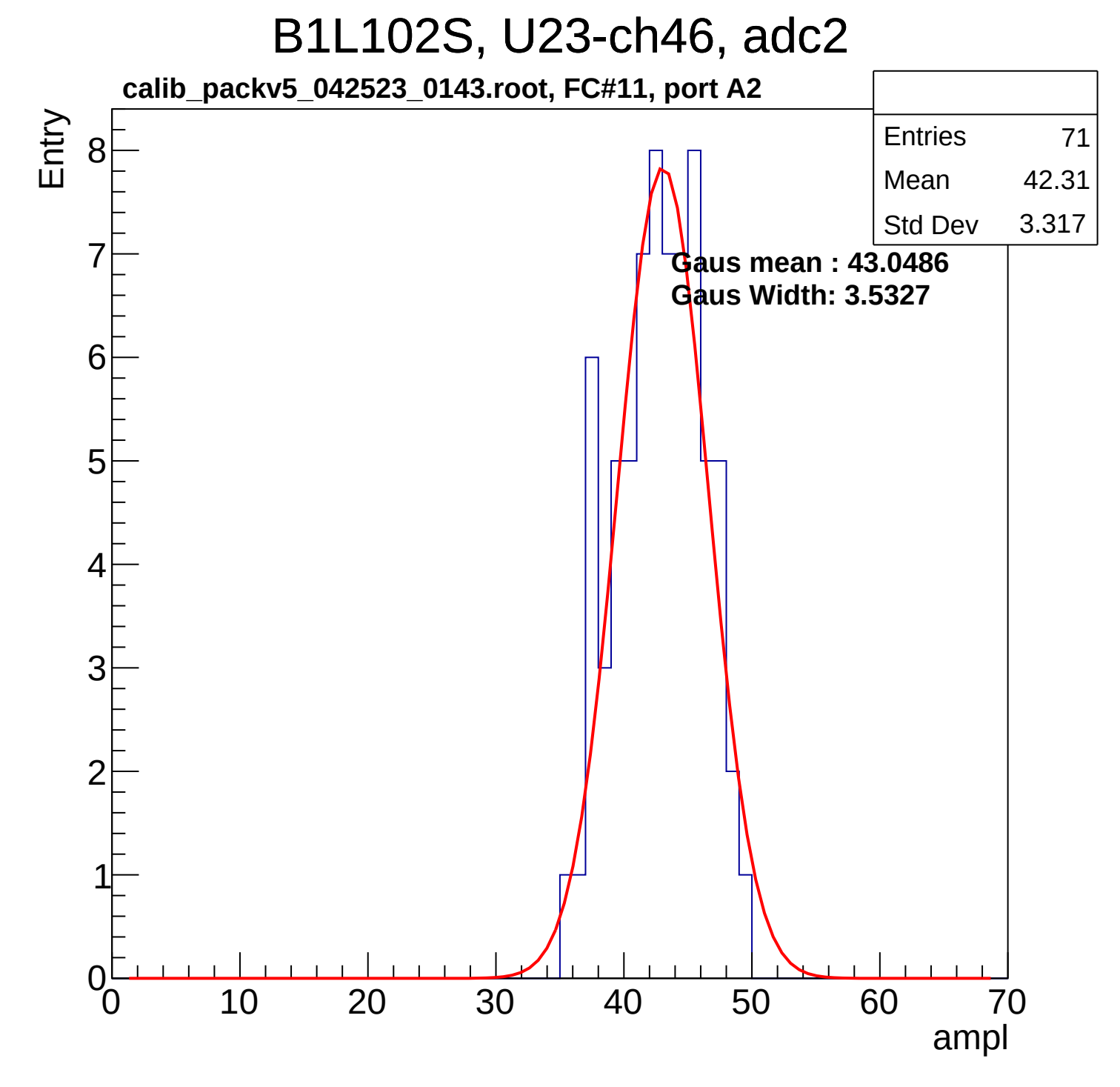

B1L102S, U23-ch45, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.63
Std Dev 9.821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

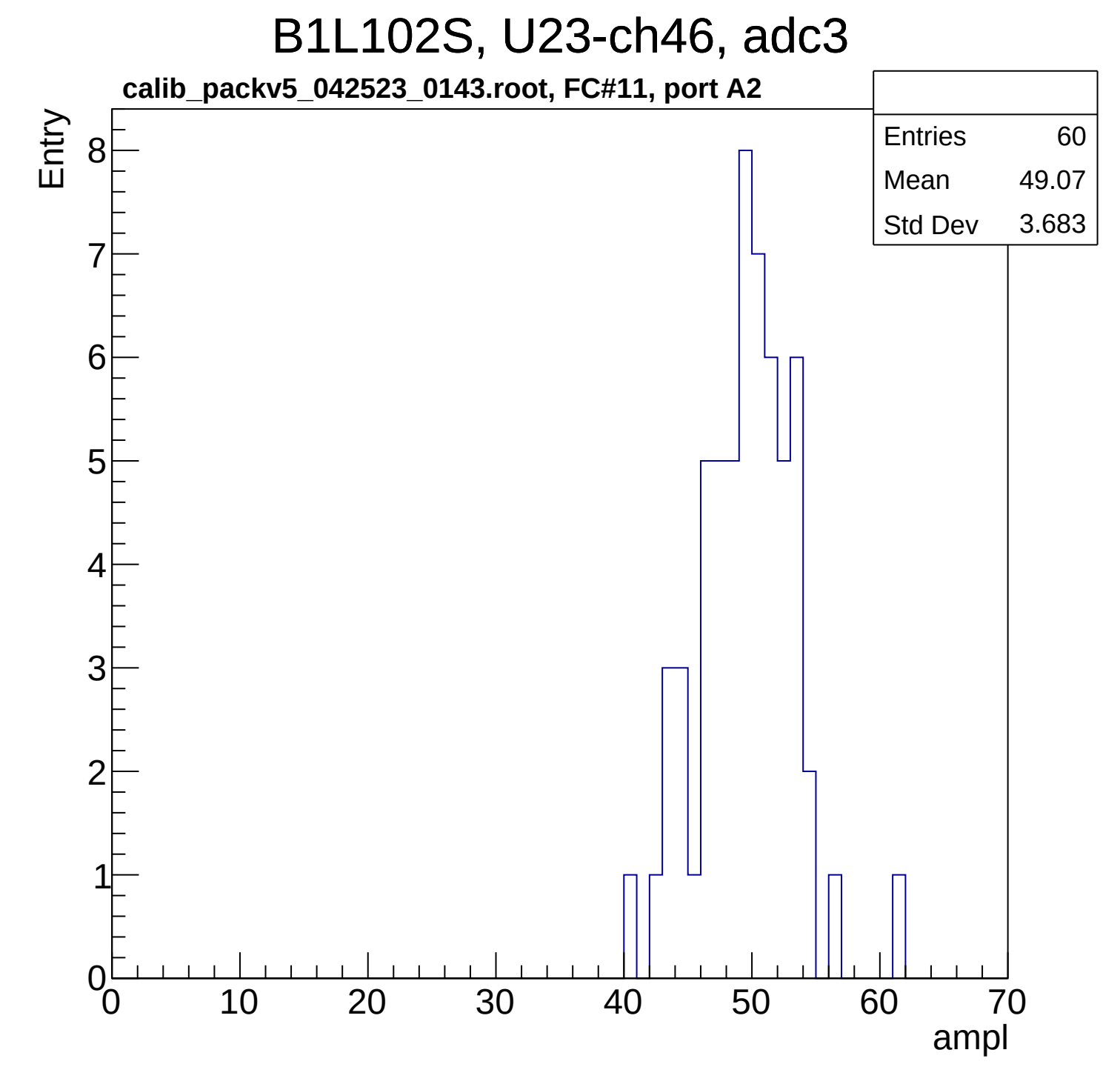

B1L102S, U23-ch45, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62.33
Std Dev 0.6667
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

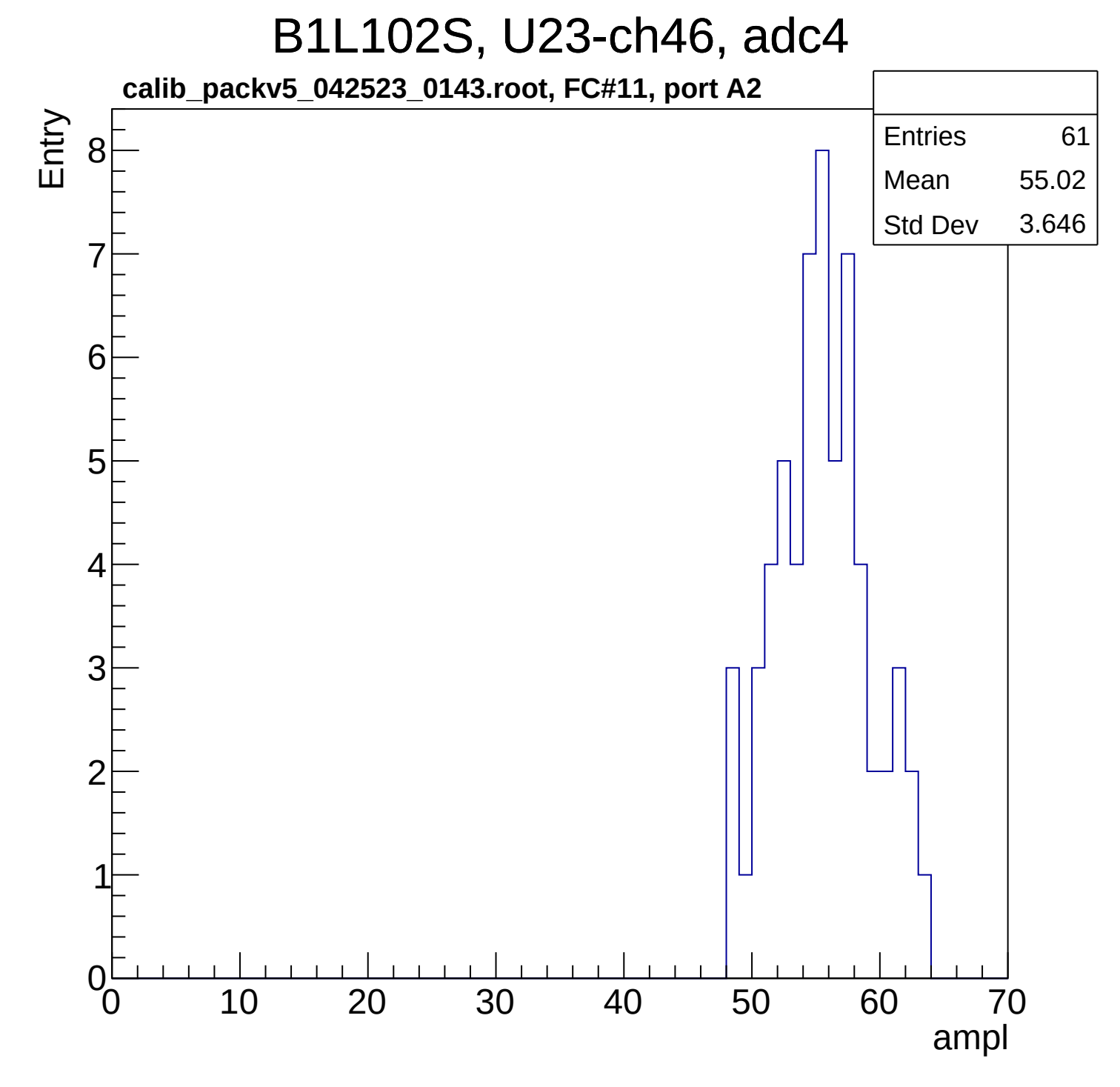

B1L102S, U23-ch45, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

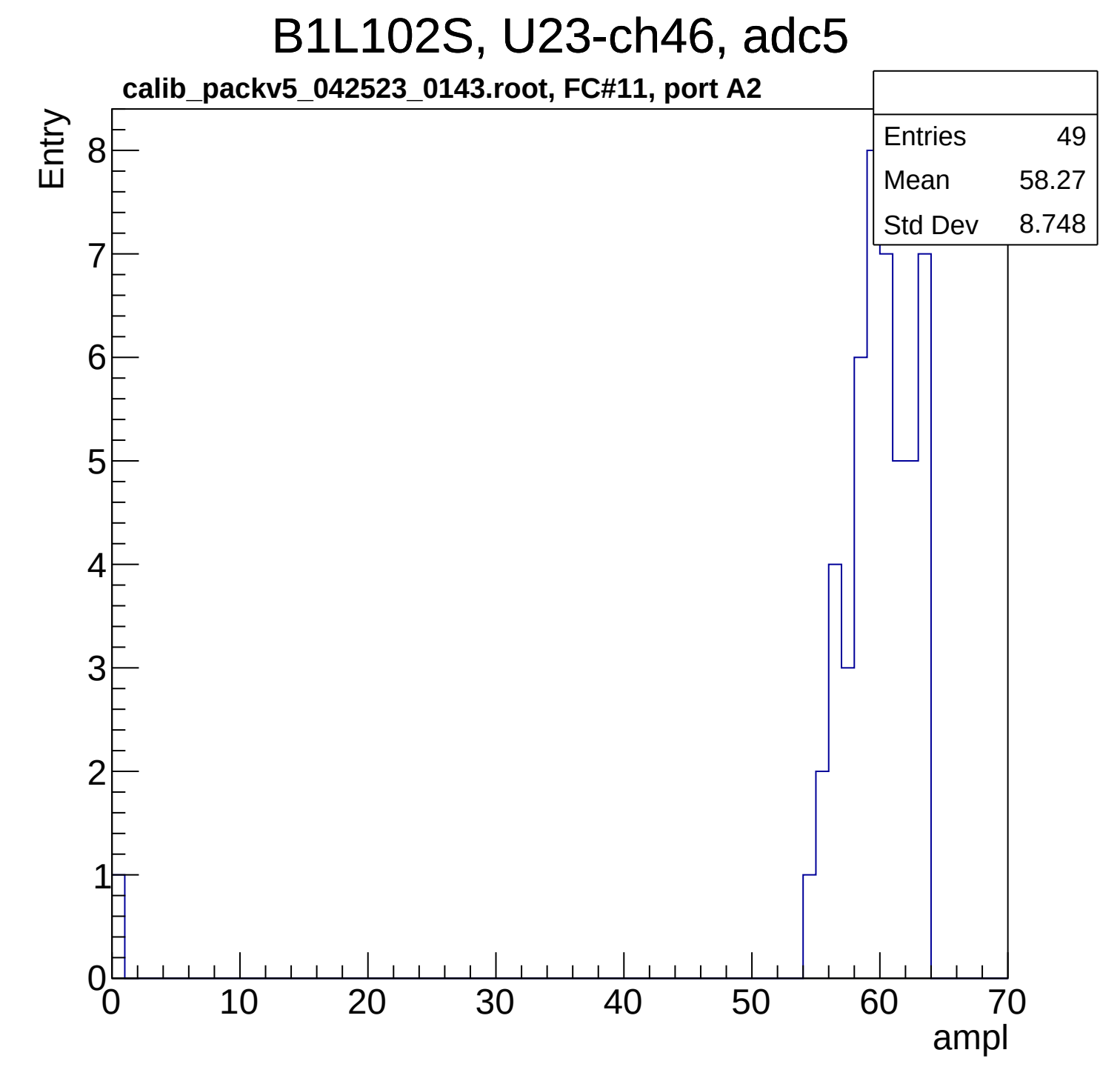

B1L102S, U23-ch46, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.72
Std Dev 4.243
Gaus mean : 28.1713
Gaus Width: 4.3470
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

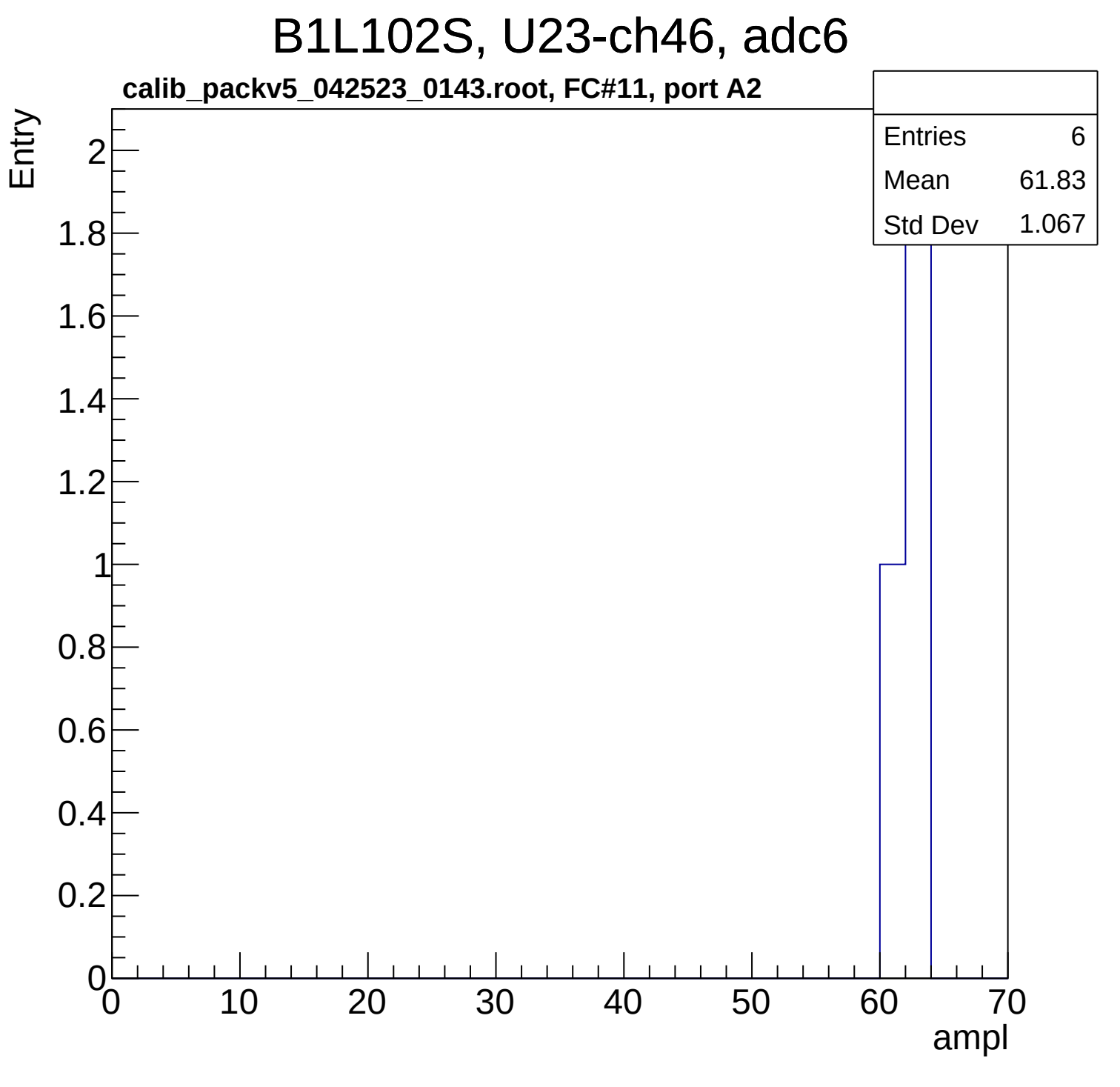

B1L102S, U23-ch46, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 34.8
Std Dev 3.89
Gaus mean : 35.4293
Gaus Width: 4.2645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

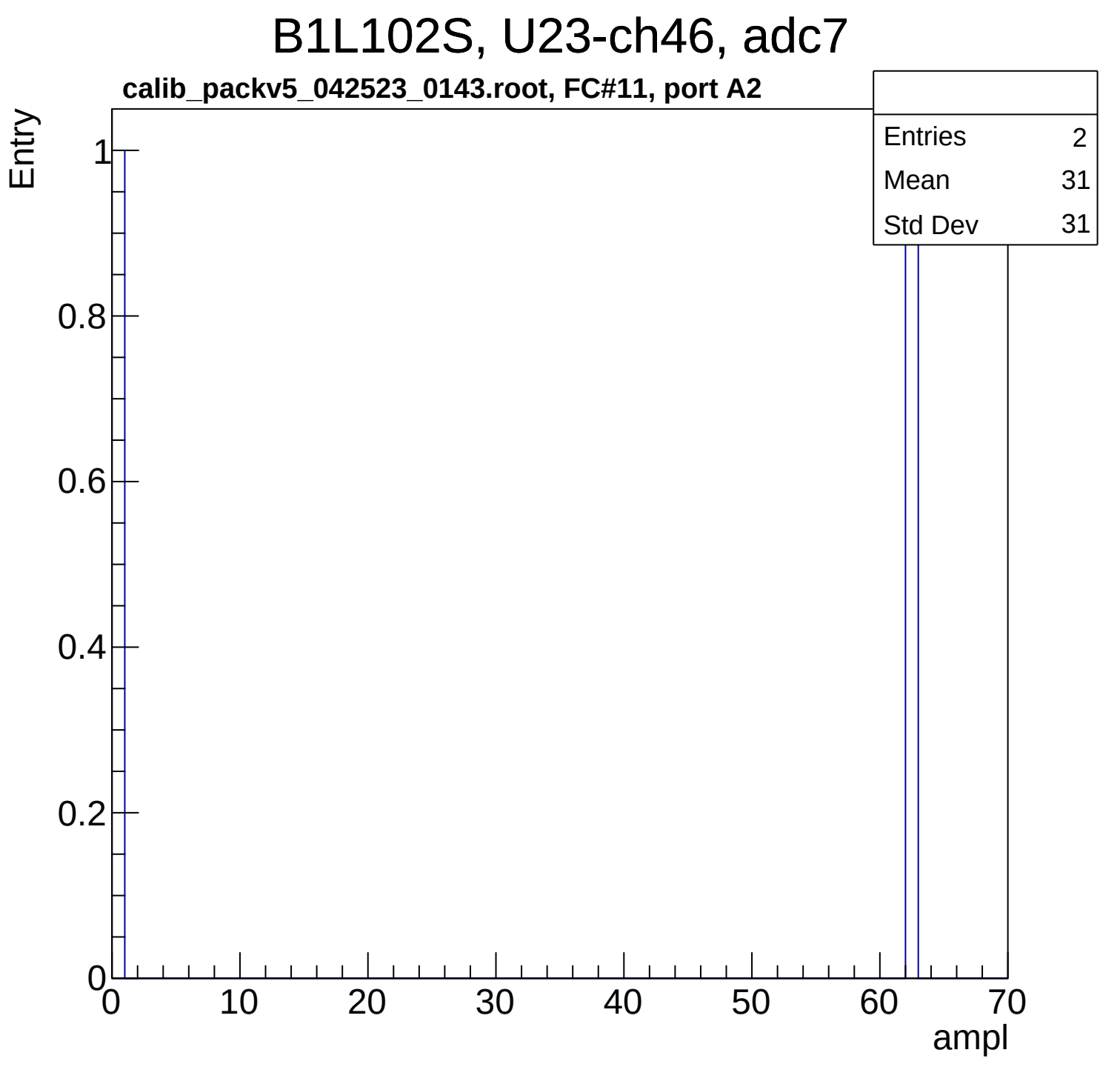

B1L102S, U23-ch46, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.31
Std Dev 3.317
Gaus mean : 43.0486
Gaus Width: 3.5327
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

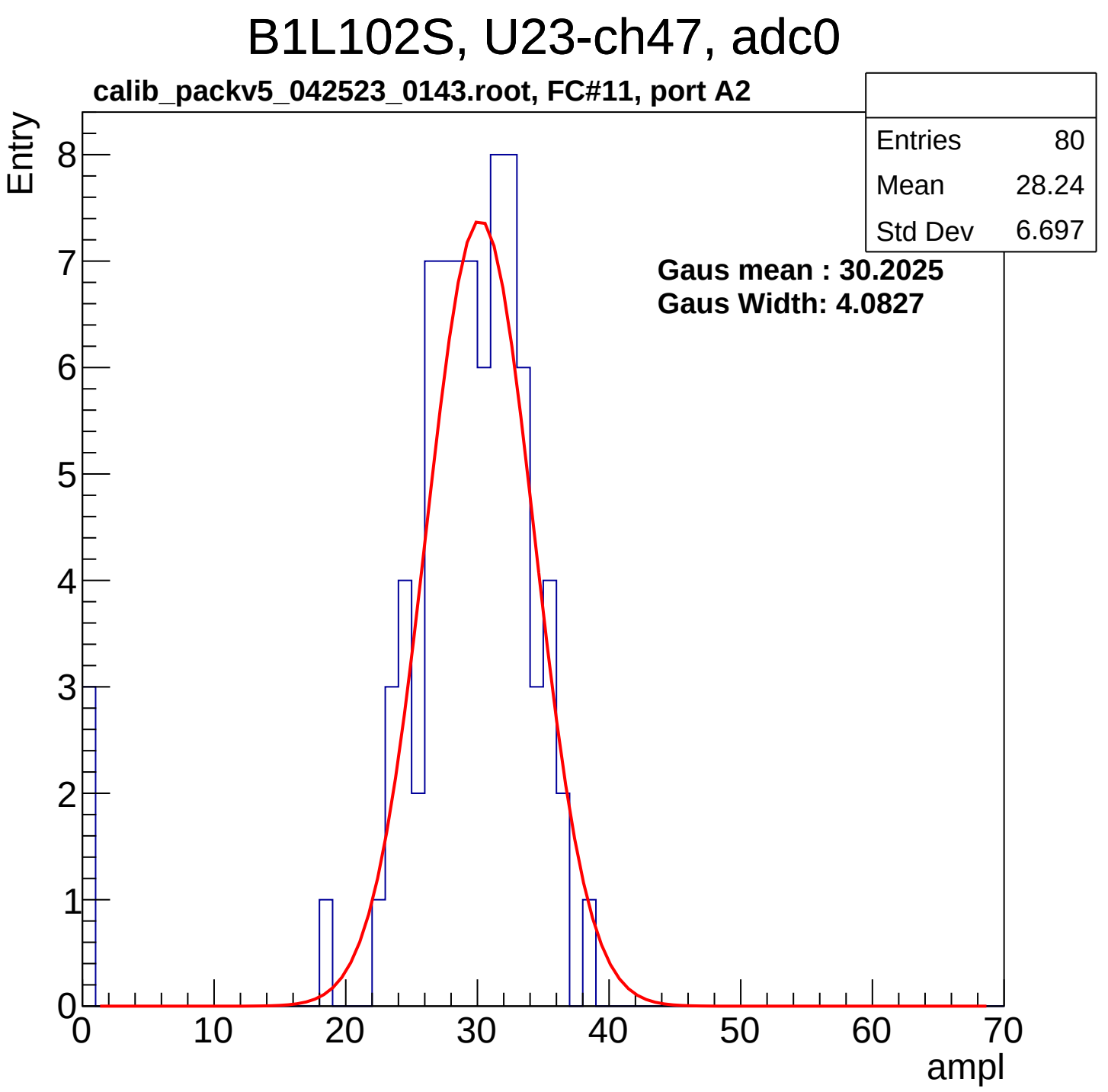

B1L102S, U23-ch46, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 49.07
Std Dev 3.683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

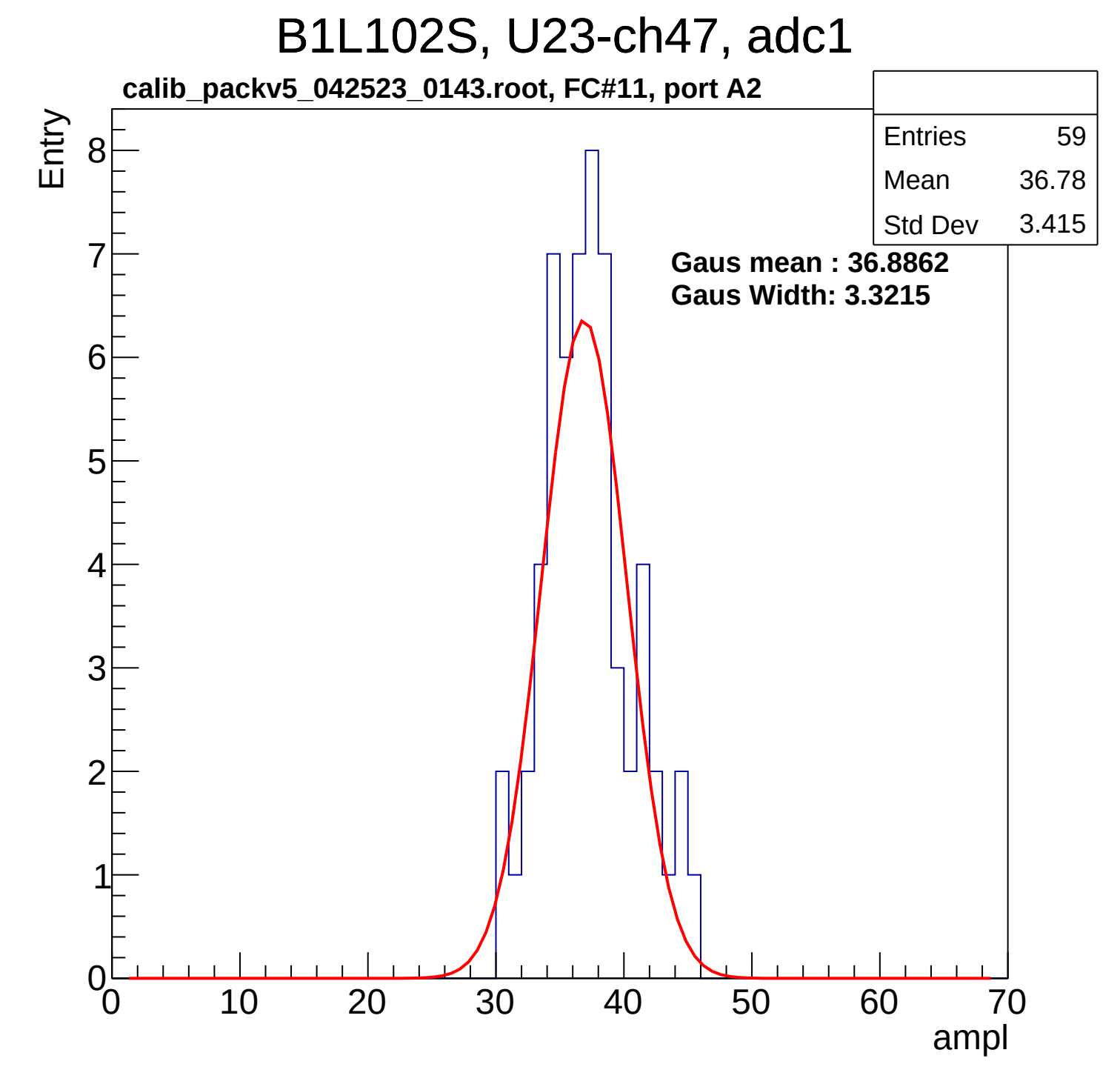

B1L102S, U23-ch46, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 55.02
Std Dev 3.646
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

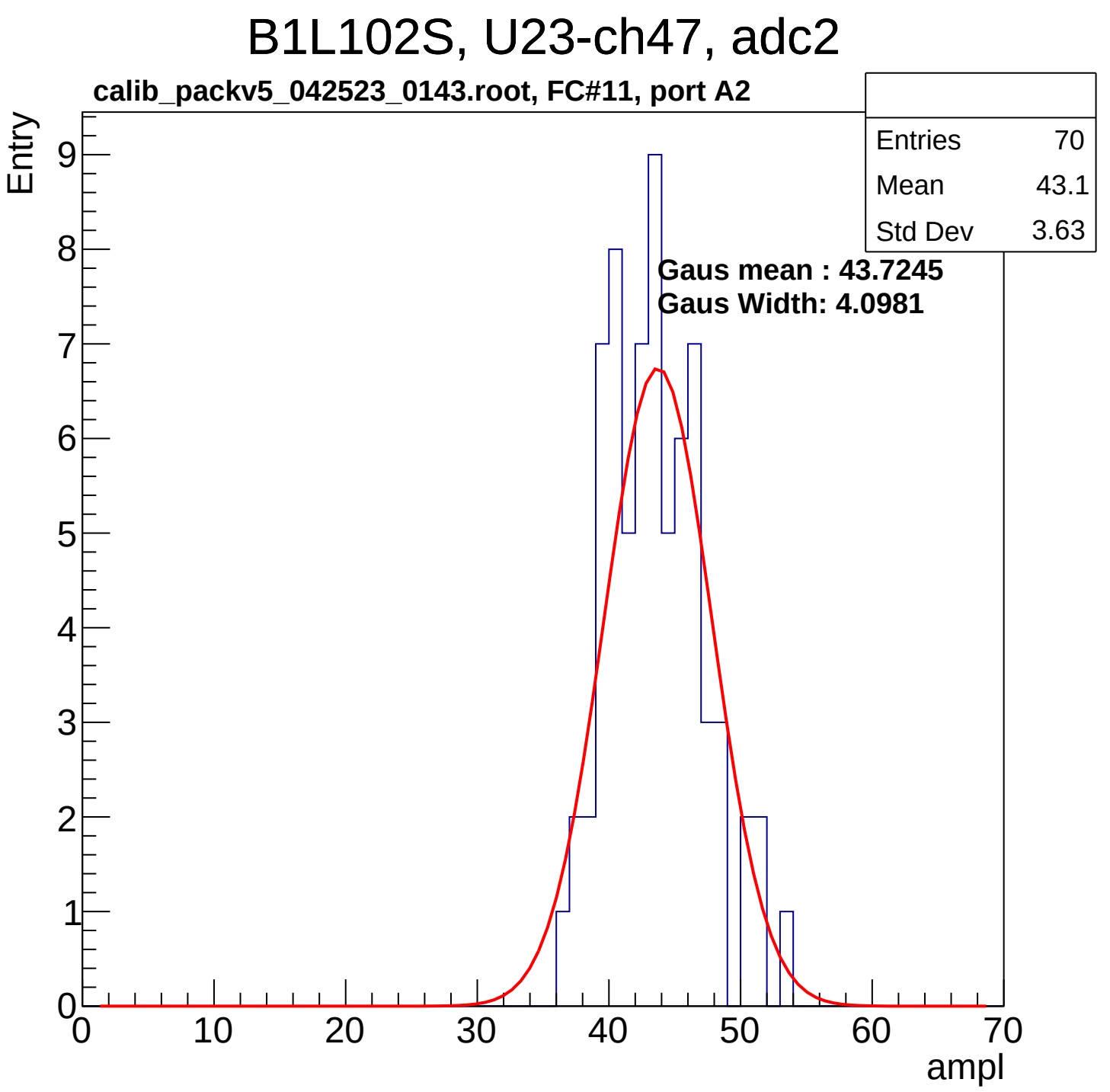

B1L102S, U23-ch46, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.27
Std Dev 8.748
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

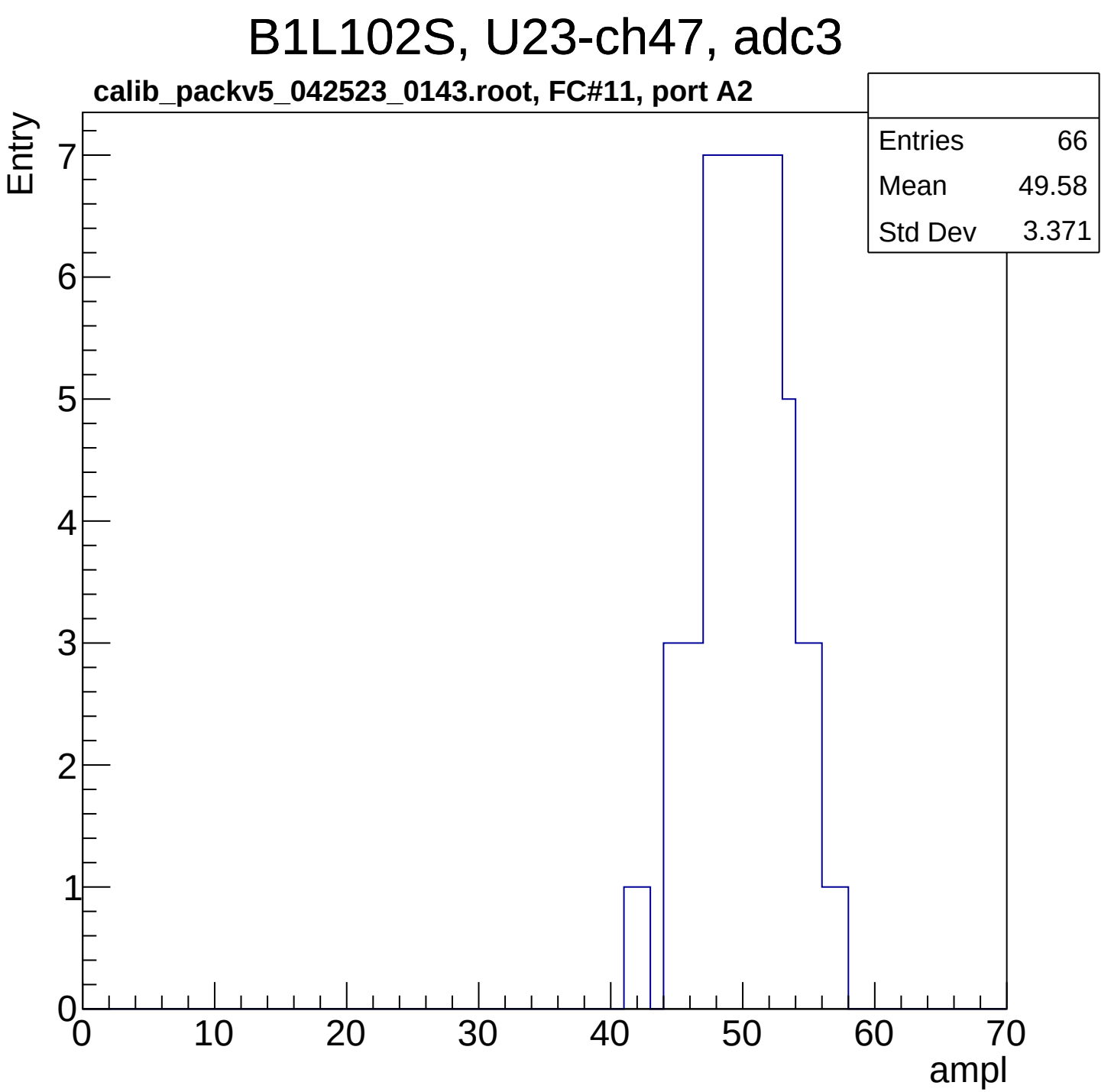

B1L102S, U23-ch46, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

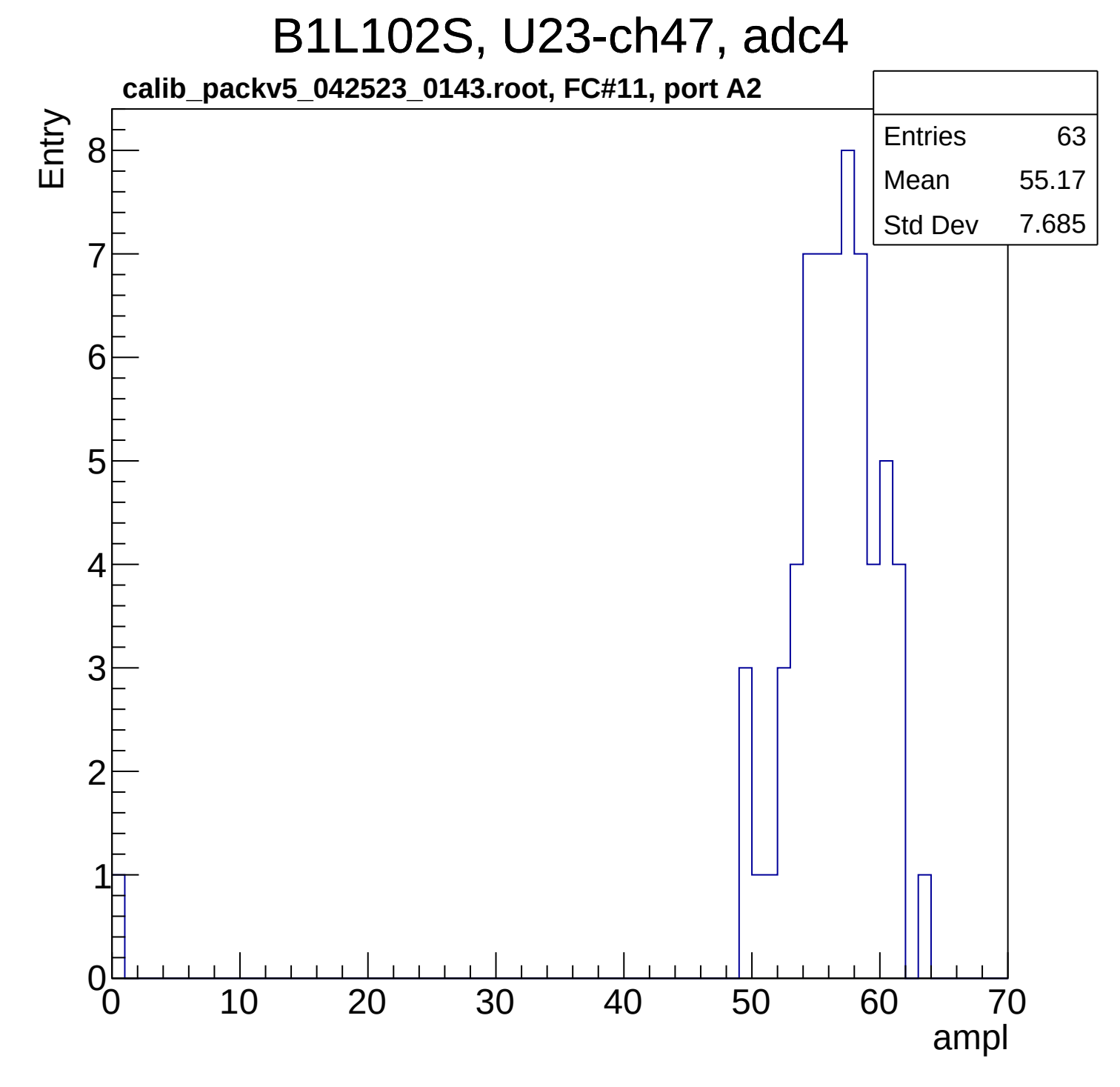

B1L102S, U23-ch46, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

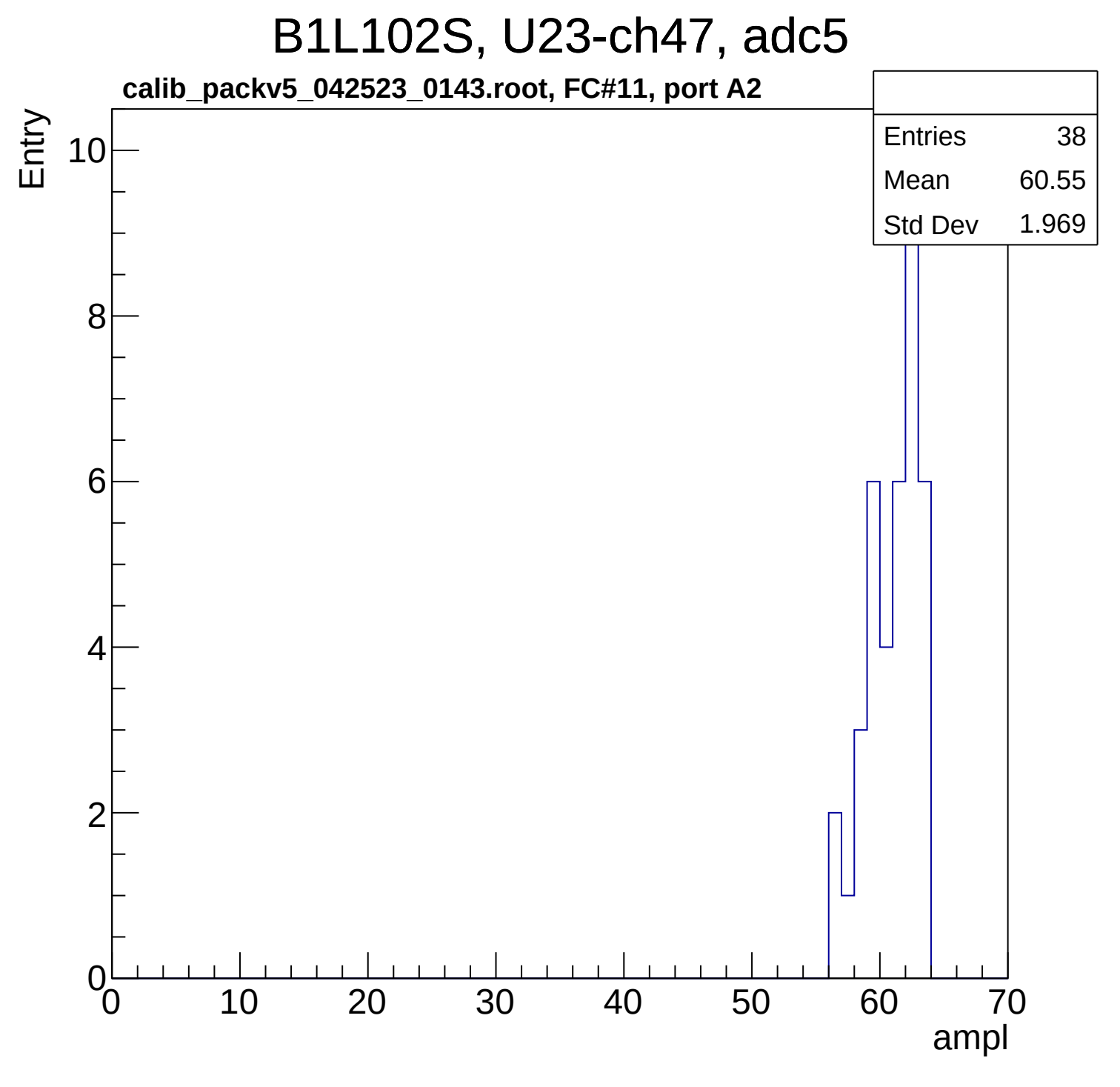

B1L102S, U23-ch47, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 28.24
Std Dev 6.697
Gaus mean : 30.2025
Gaus Width: 4.0827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

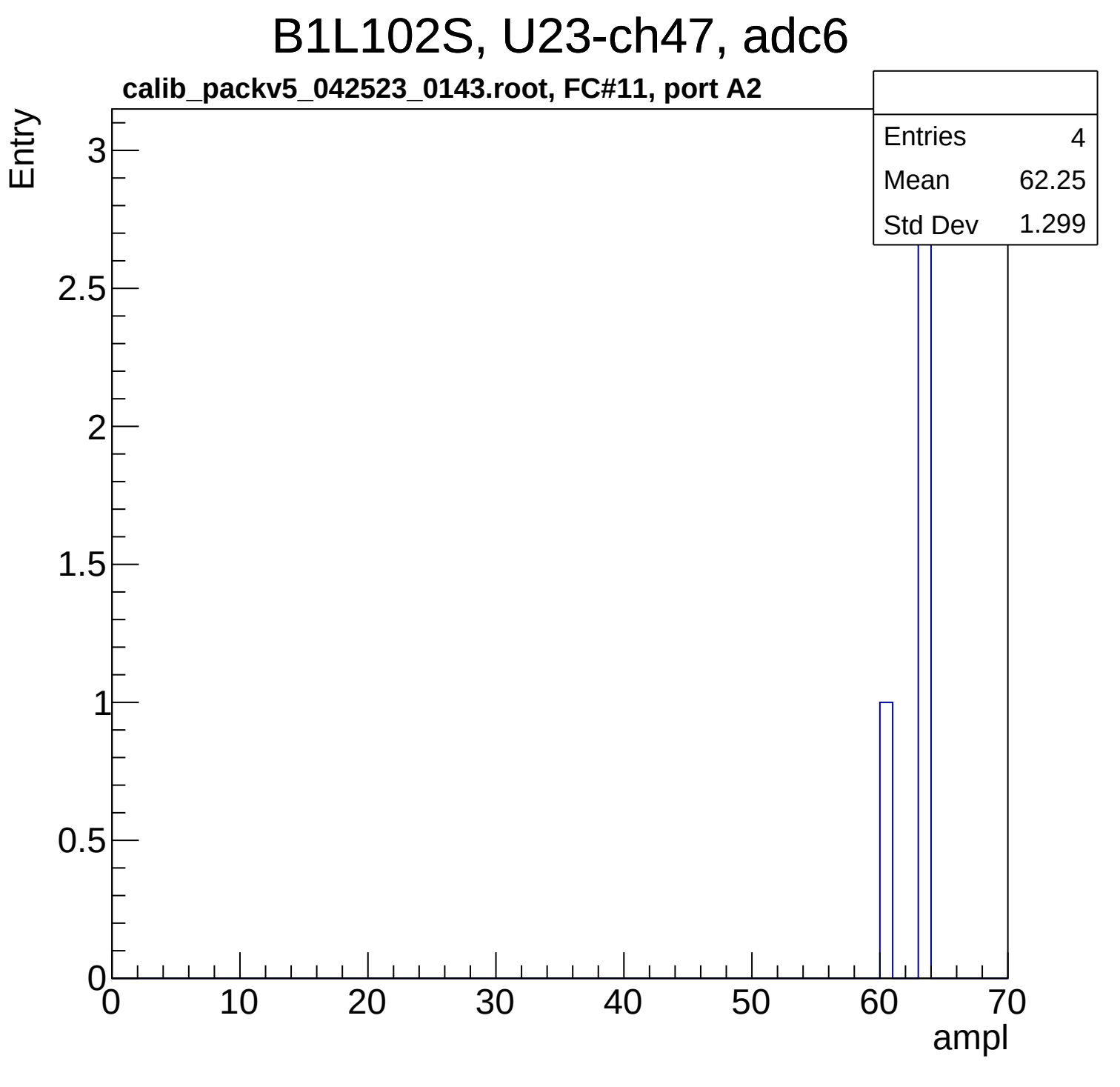

B1L102S, U23-ch47, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 36.78
Std Dev 3.415
Gaus mean : 36.8862
Gaus Width: 3.3215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

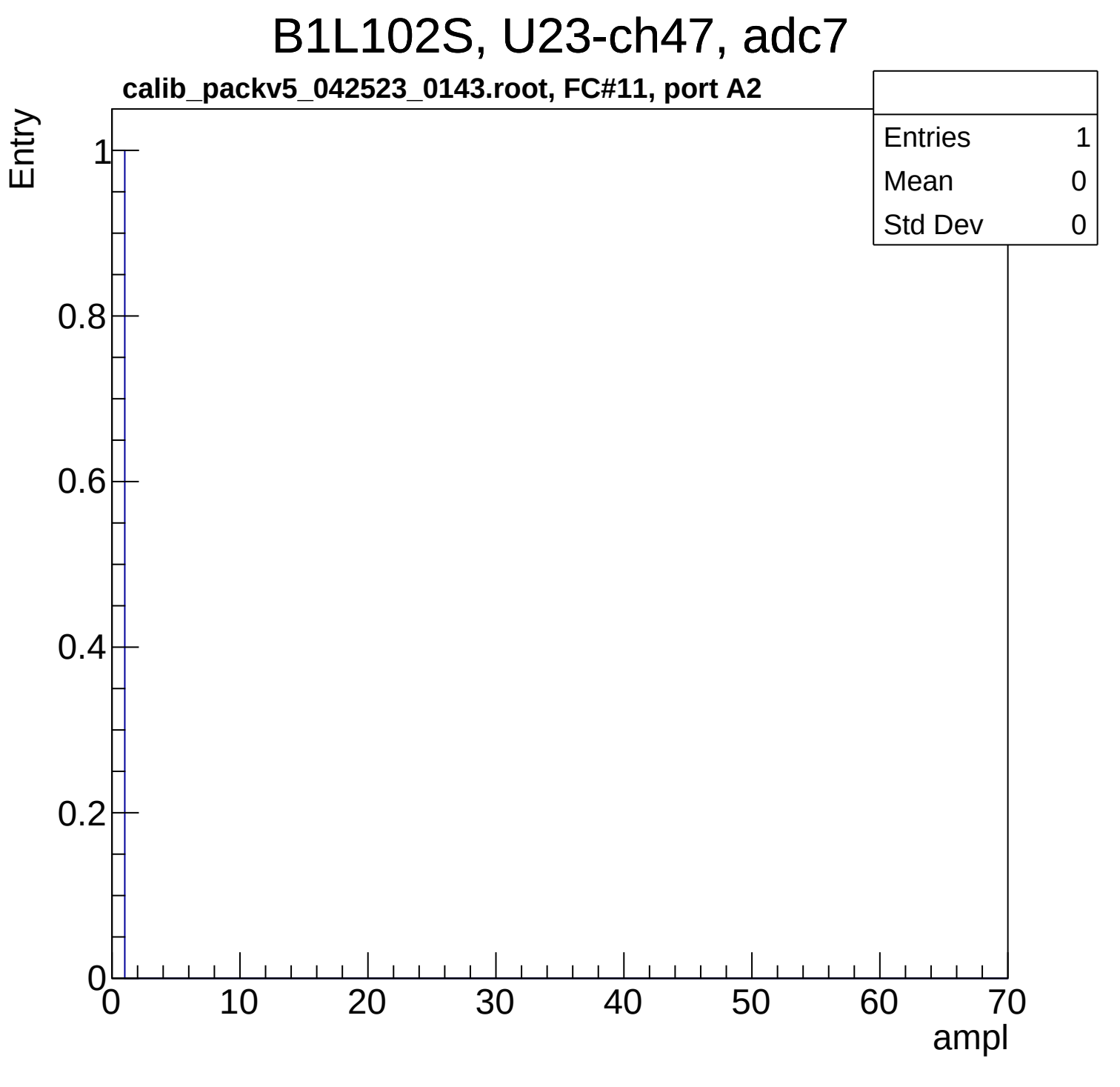

B1L102S, U23-ch47, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 43.1
Std Dev 3.63
Gaus mean : 43.7245
Gaus Width: 4.0981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

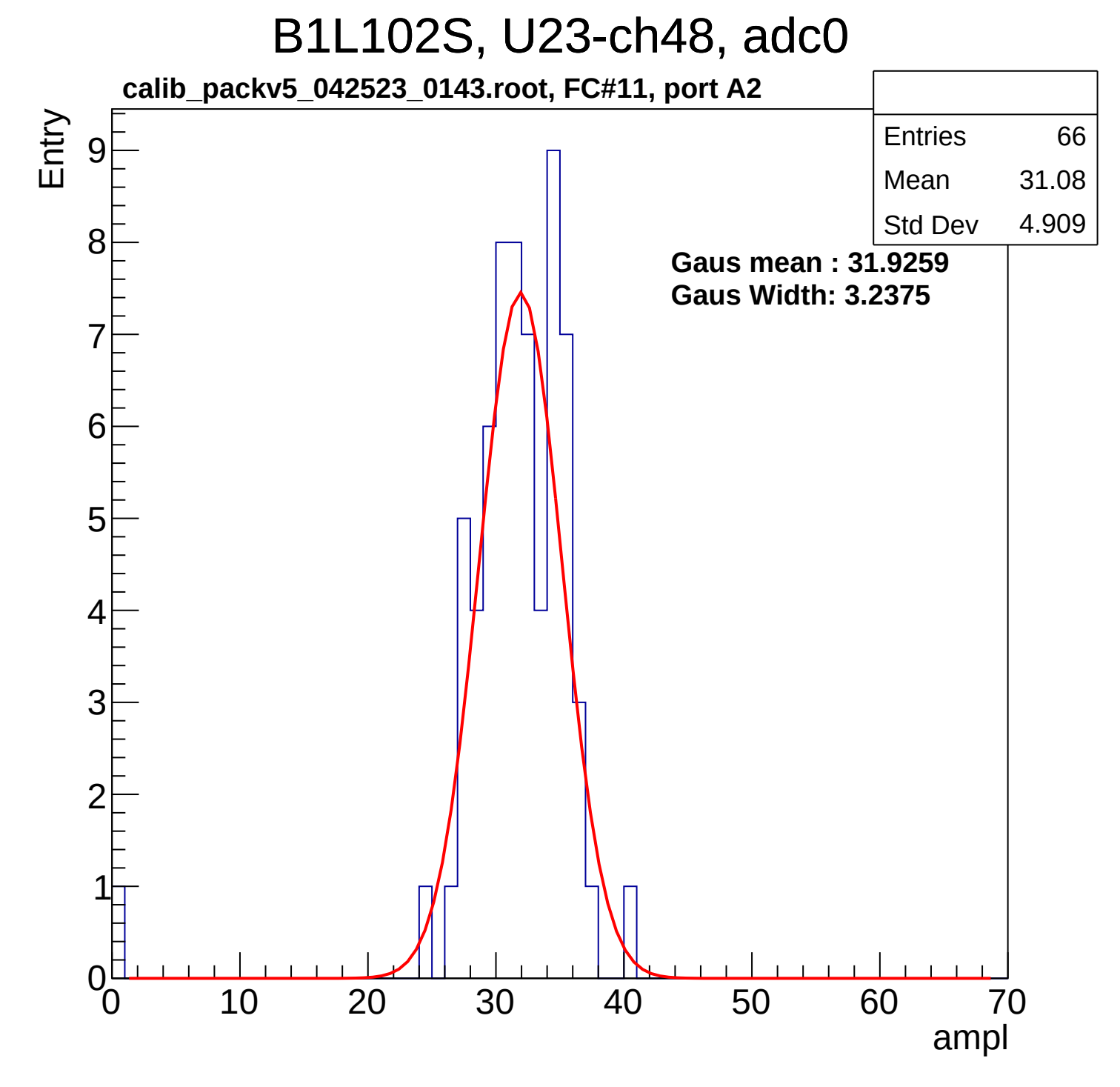

B1L102S, U23-ch47, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 49.58
Std Dev 3.371
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

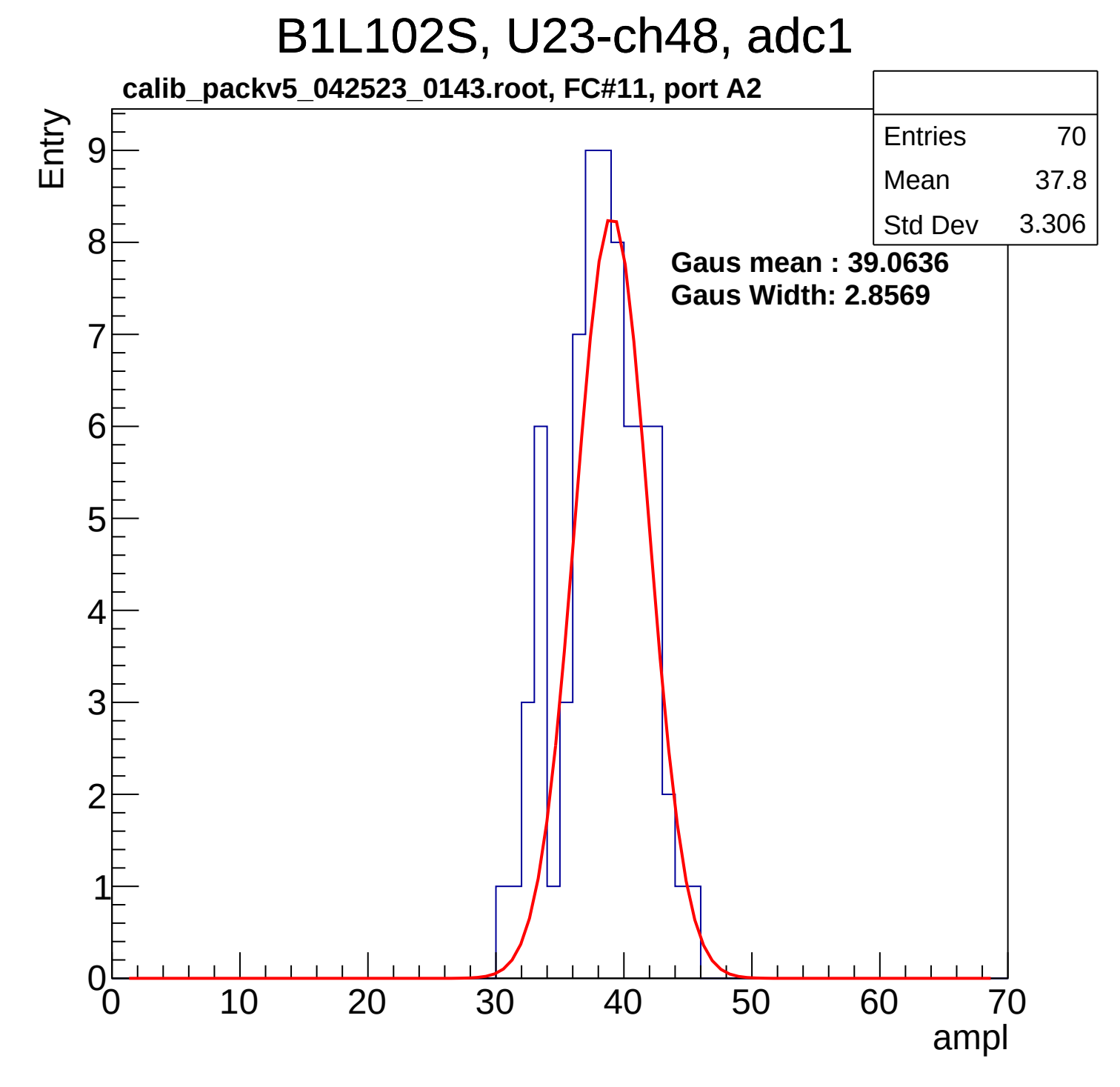

B1L102S, U23-ch47, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 55.17
Std Dev 7.685
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

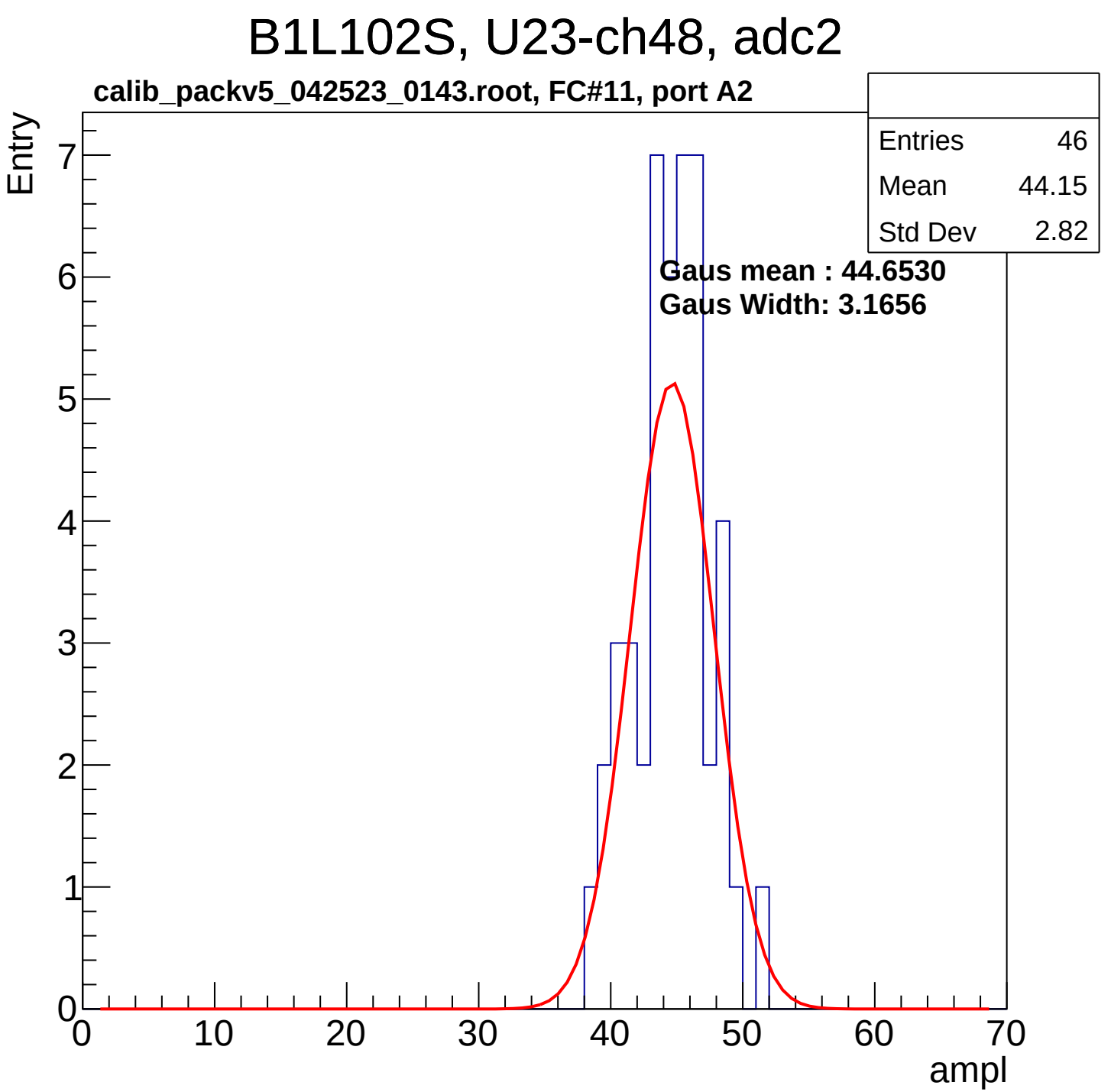

B1L102S, U23-ch47, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 60.55
Std Dev 1.969
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

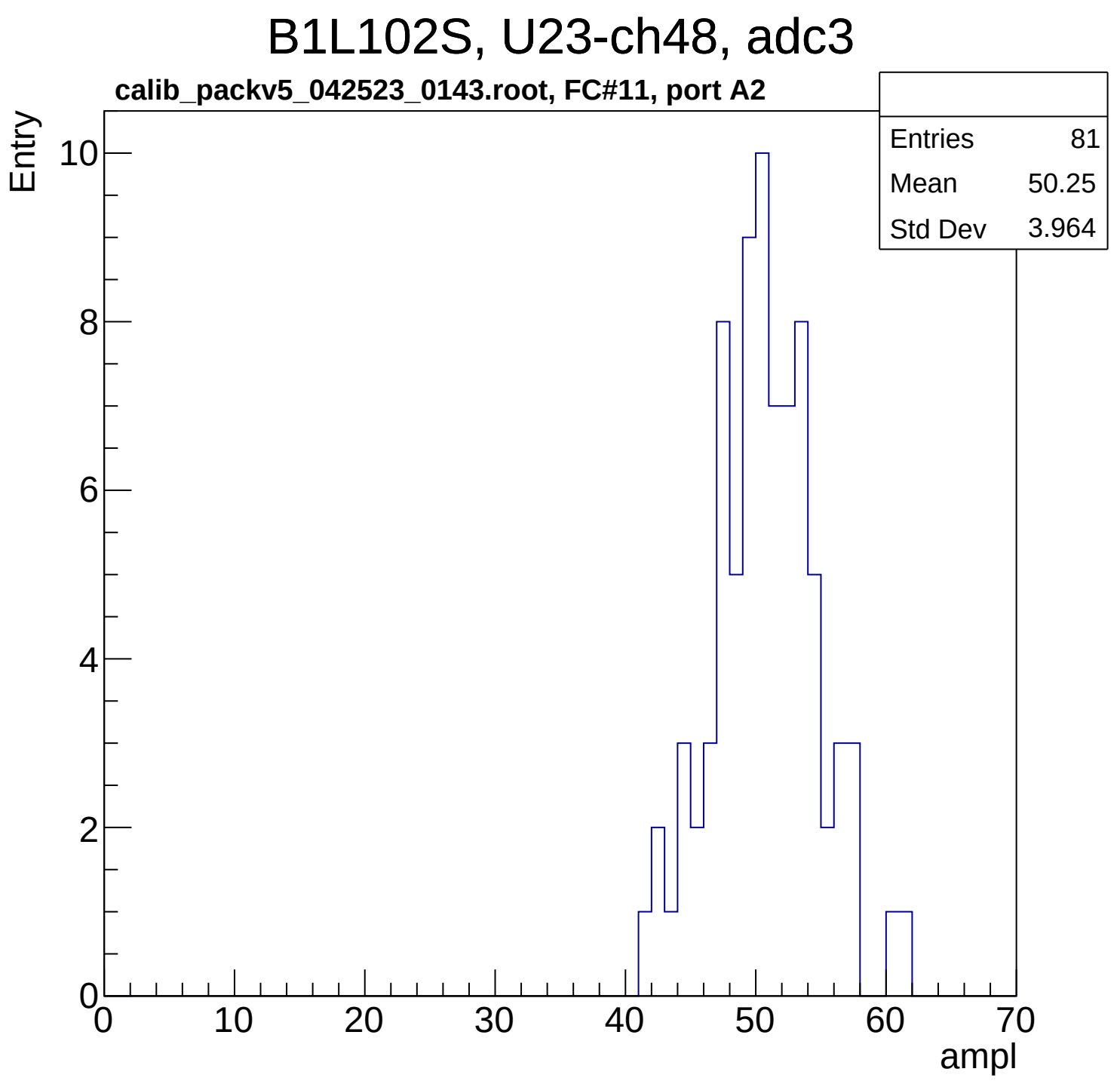

B1L102S, U23-ch47, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

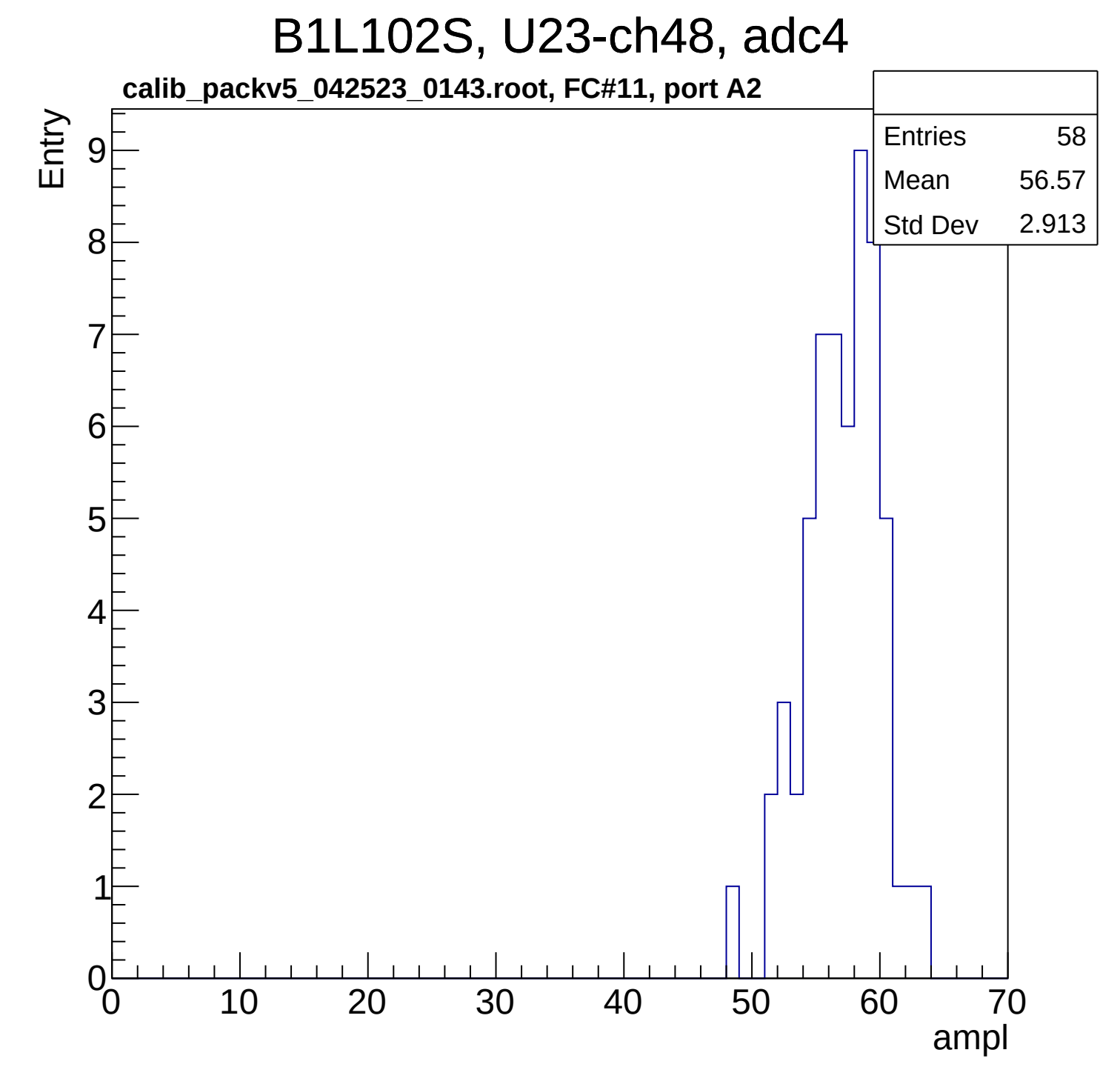

B1L102S, U23-ch47, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

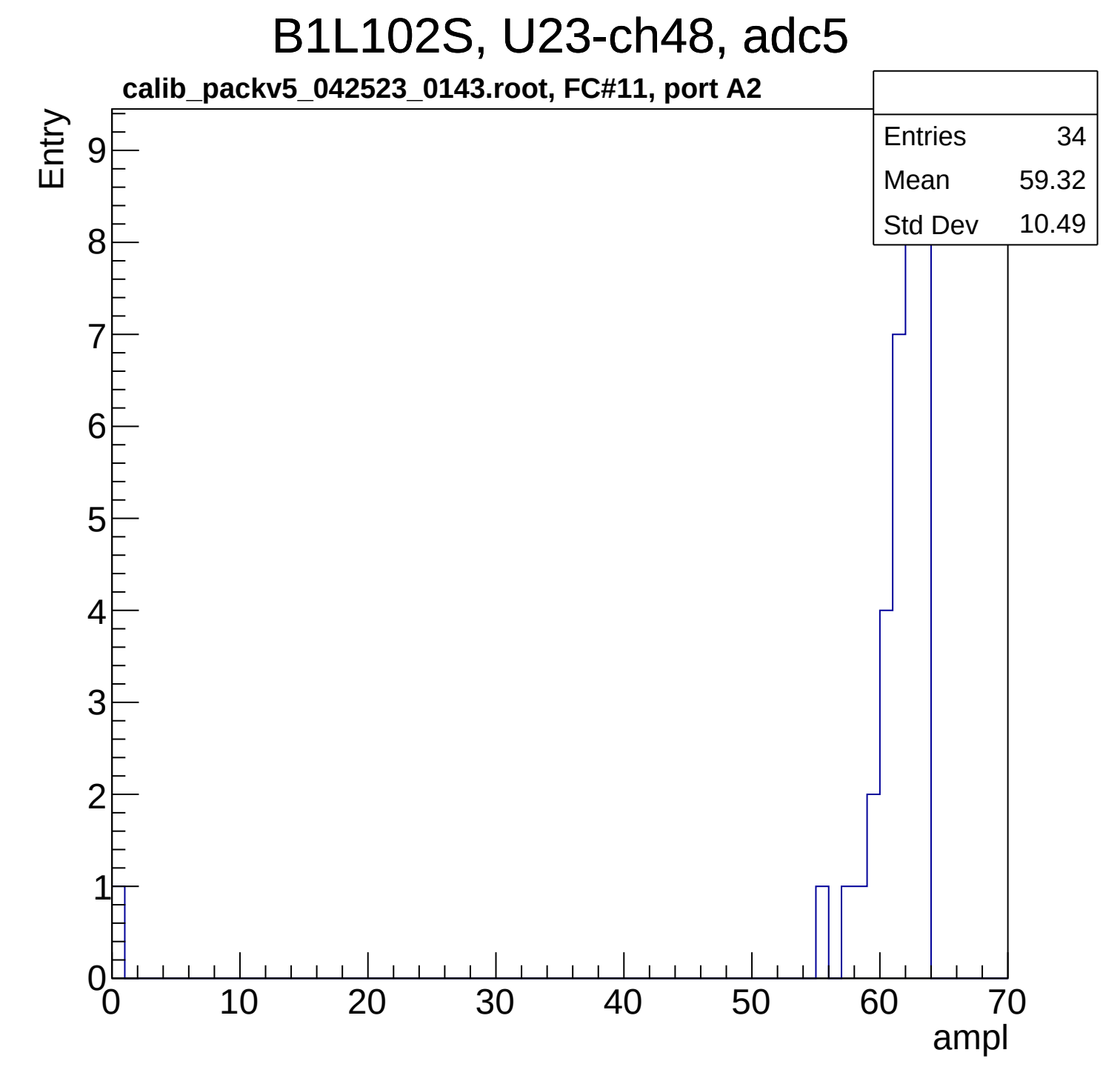

B1L102S, U23-ch48, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 31.08
Std Dev 4.909
Gaus mean : 31.9259
Gaus Width: 3.2375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

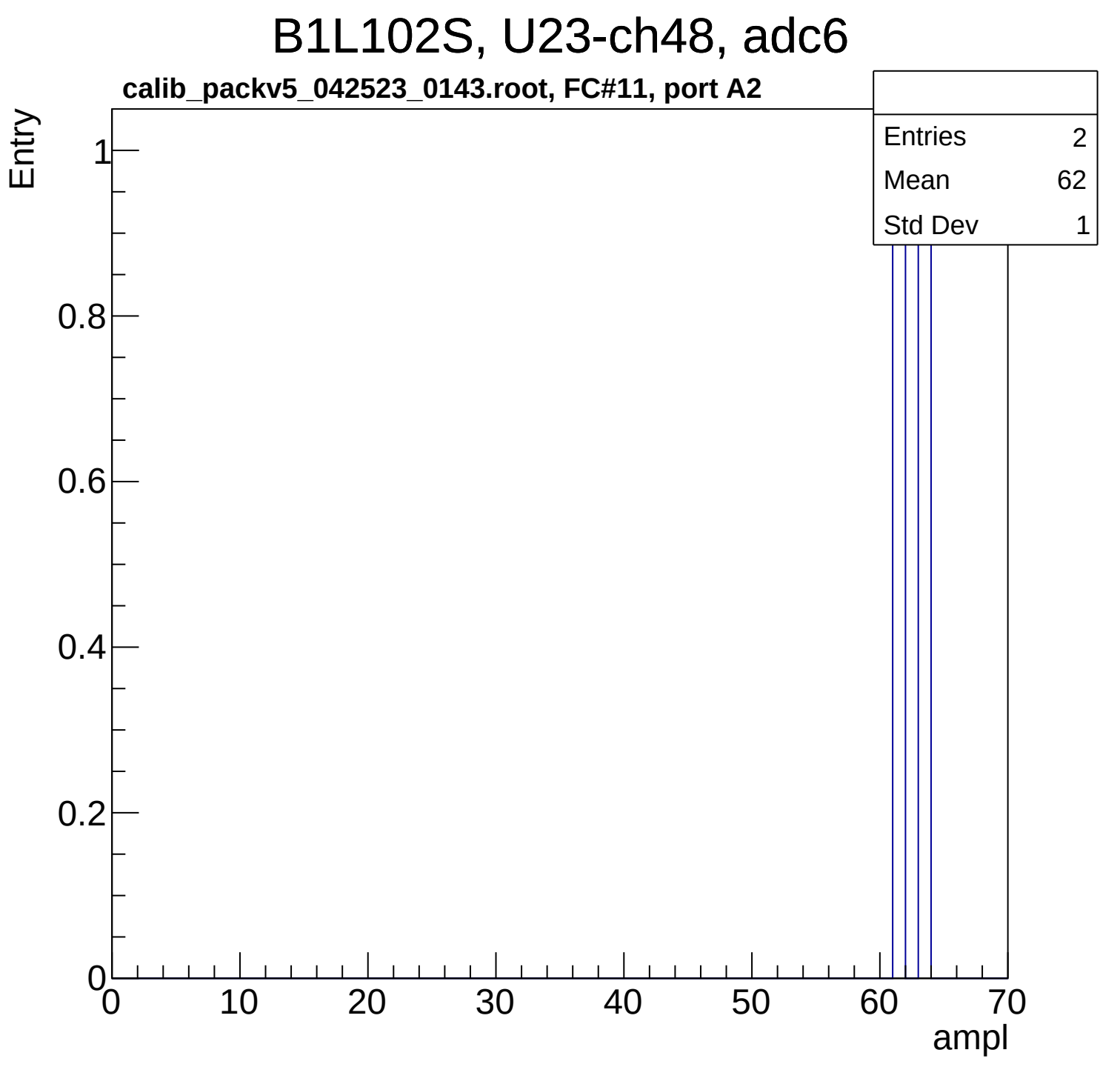

B1L102S, U23-ch48, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 37.8
Std Dev 3.306
Gaus mean : 39.0636
Gaus Width: 2.8569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

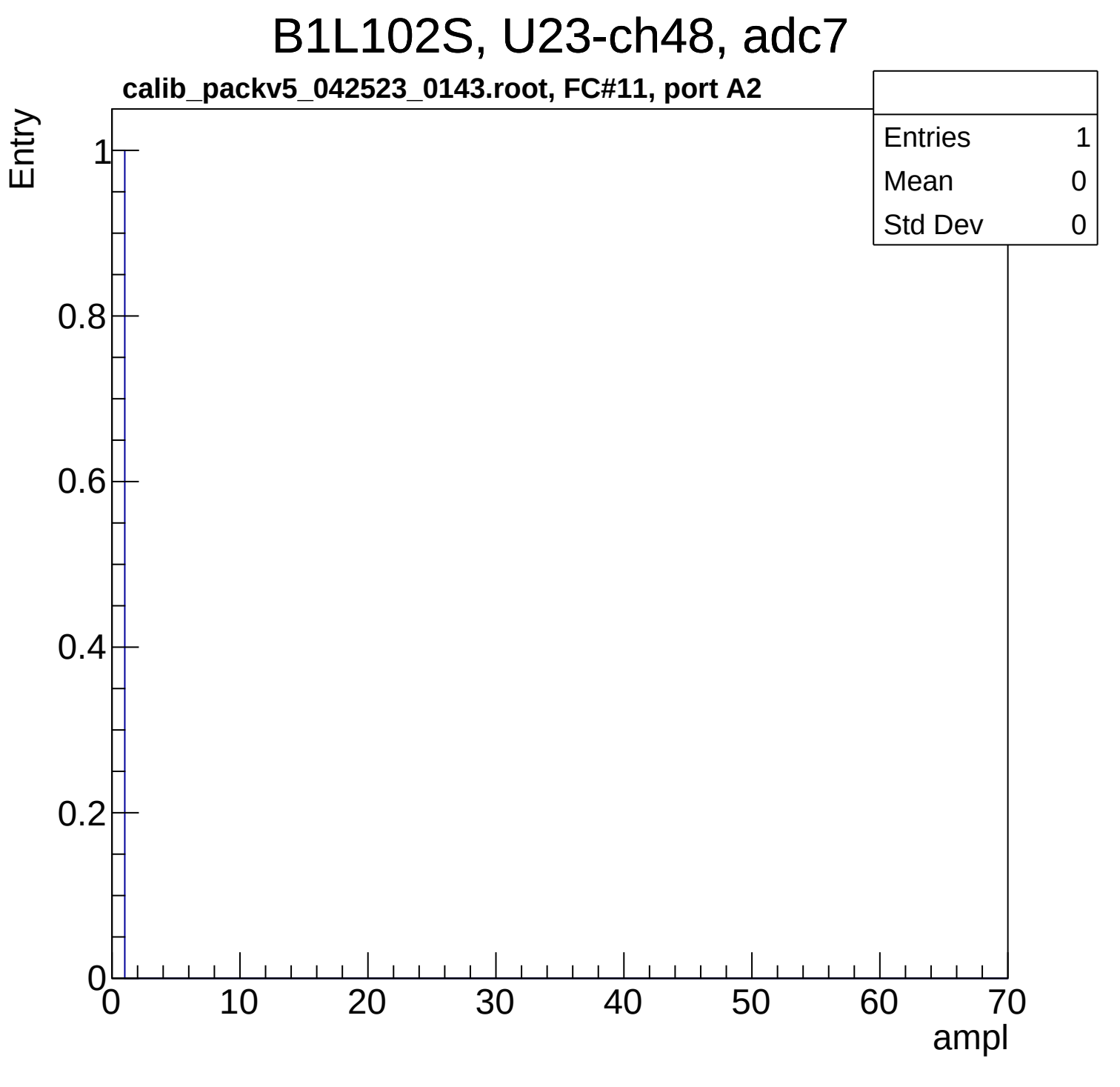

B1L102S, U23-ch48, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 44.15
Std Dev 2.82
Gaus mean : 44.6530
Gaus Width: 3.1656
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

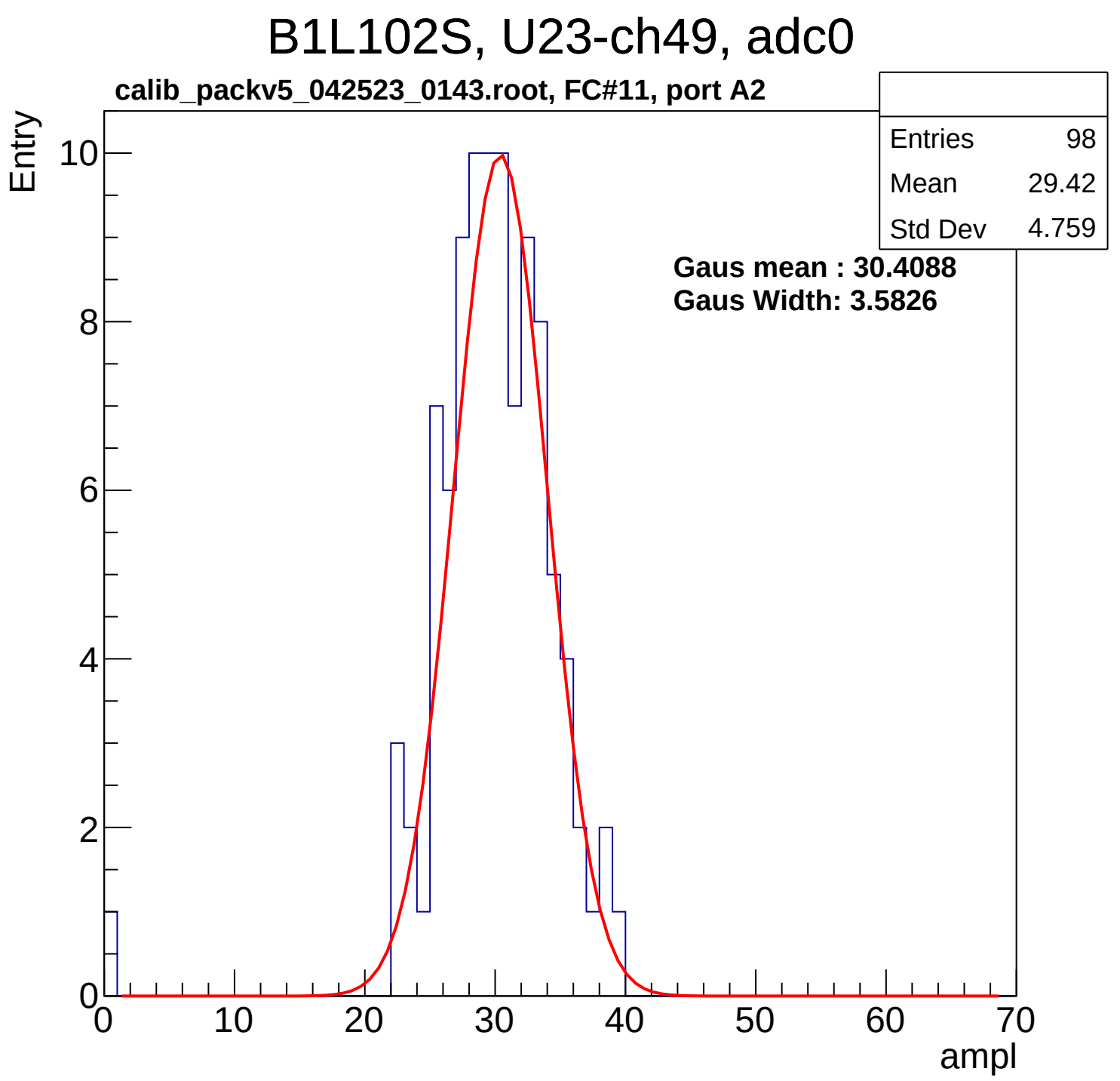

B1L102S, U23-ch48, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 50.25
Std Dev 3.964
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

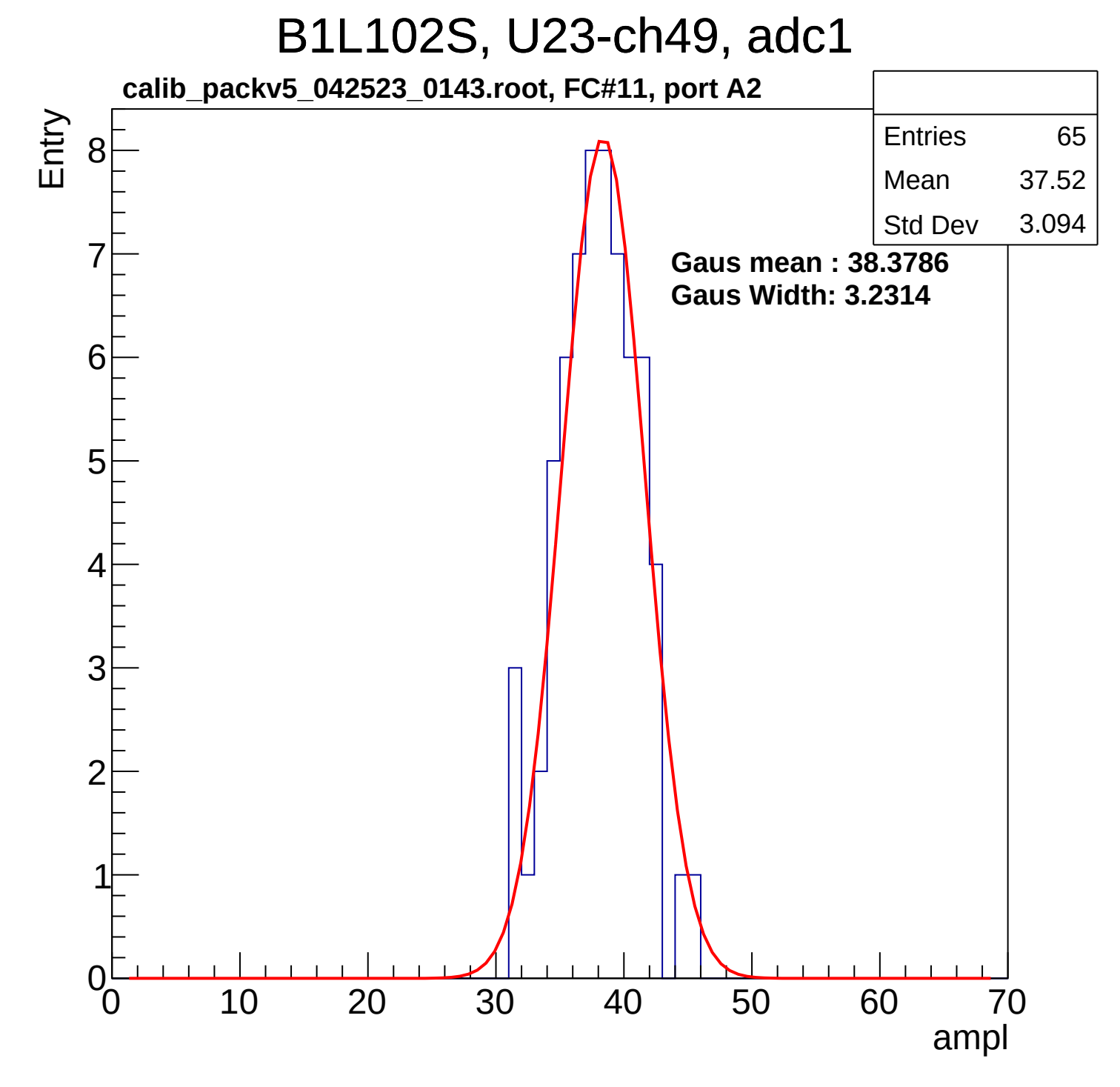

B1L102S, U23-ch48, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 56.57
Std Dev 2.913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

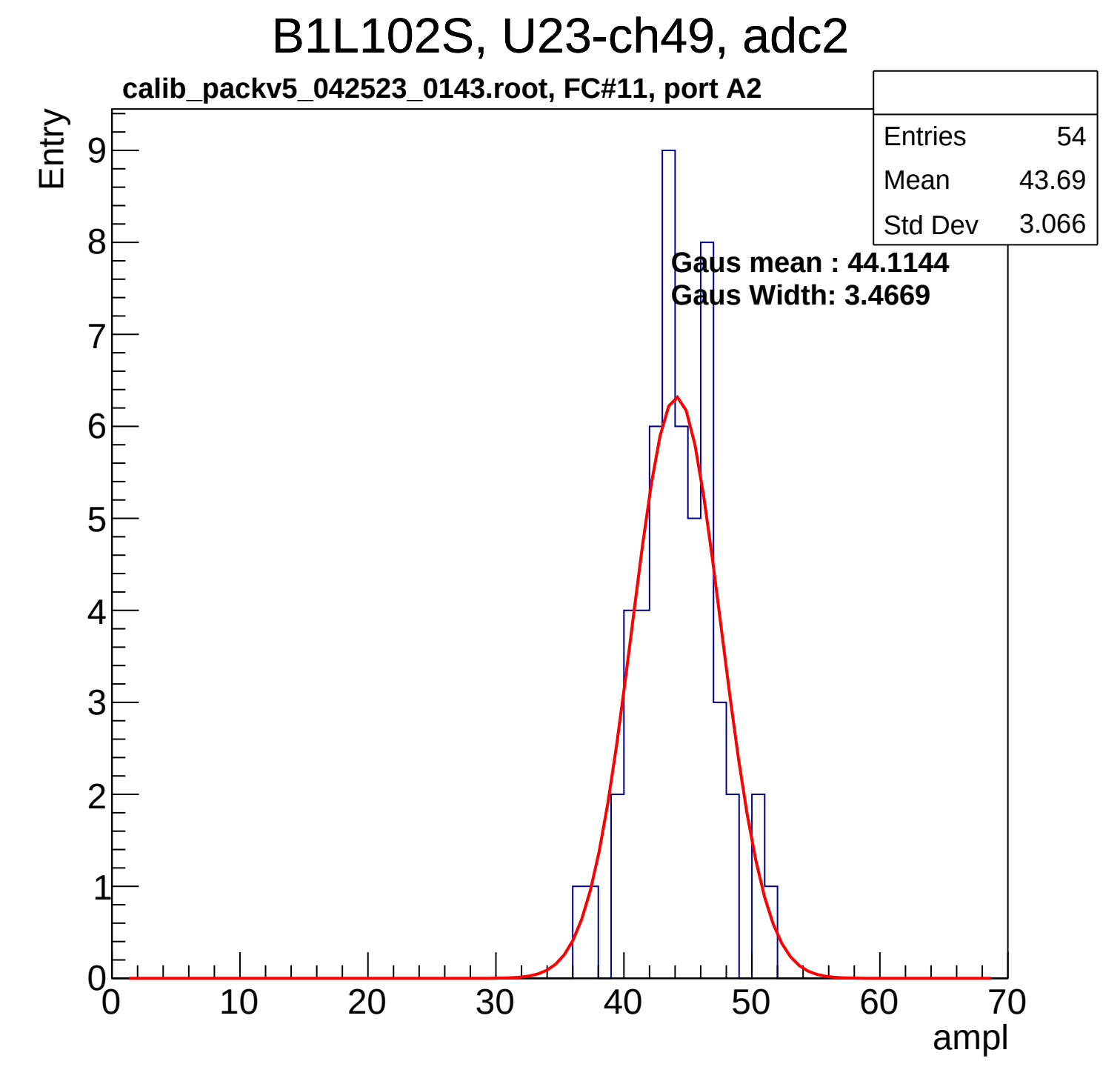

B1L102S, U23-ch48, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 34
Mean 59.32
Std Dev 10.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

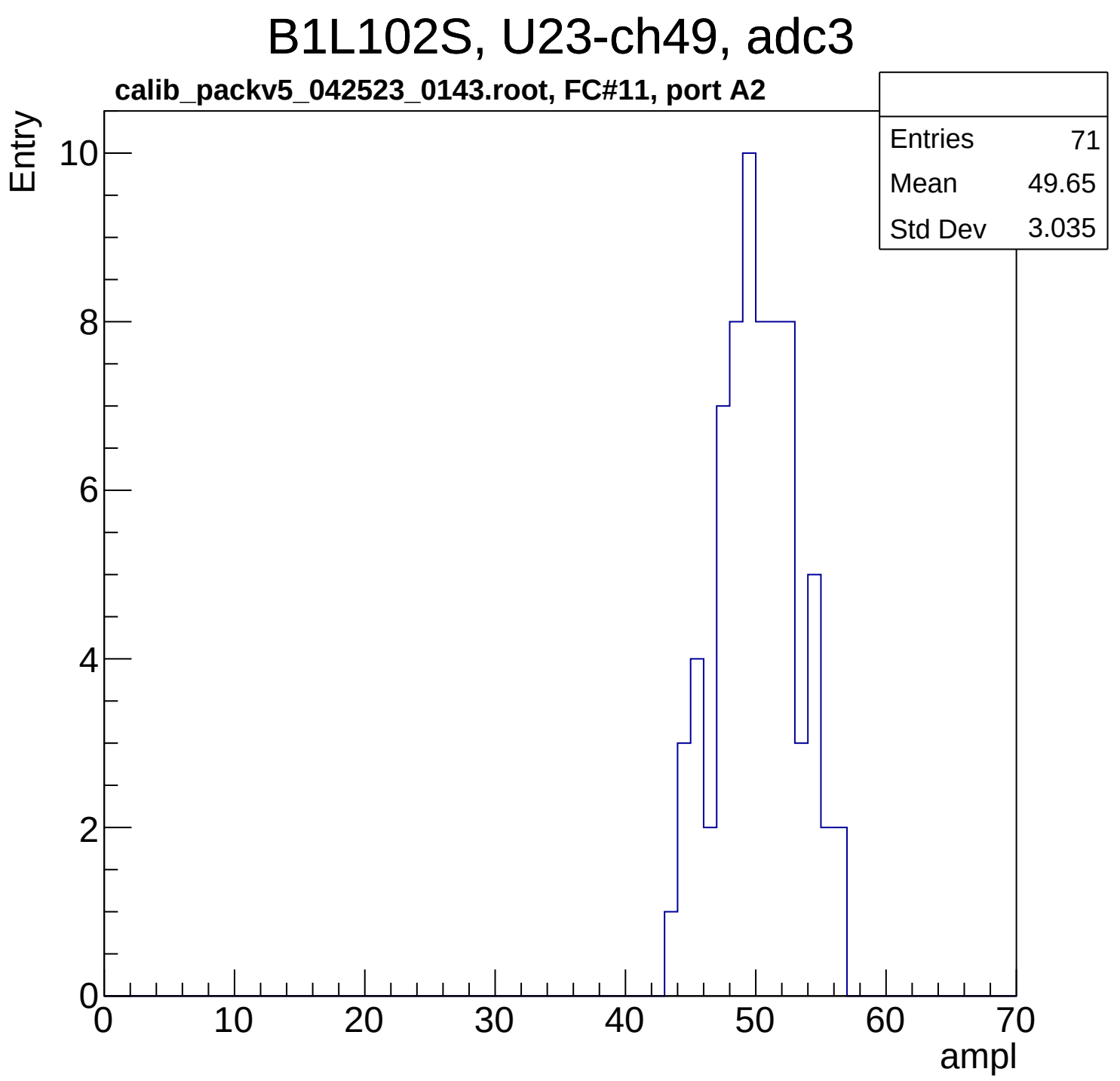

B1L102S, U23-ch48, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

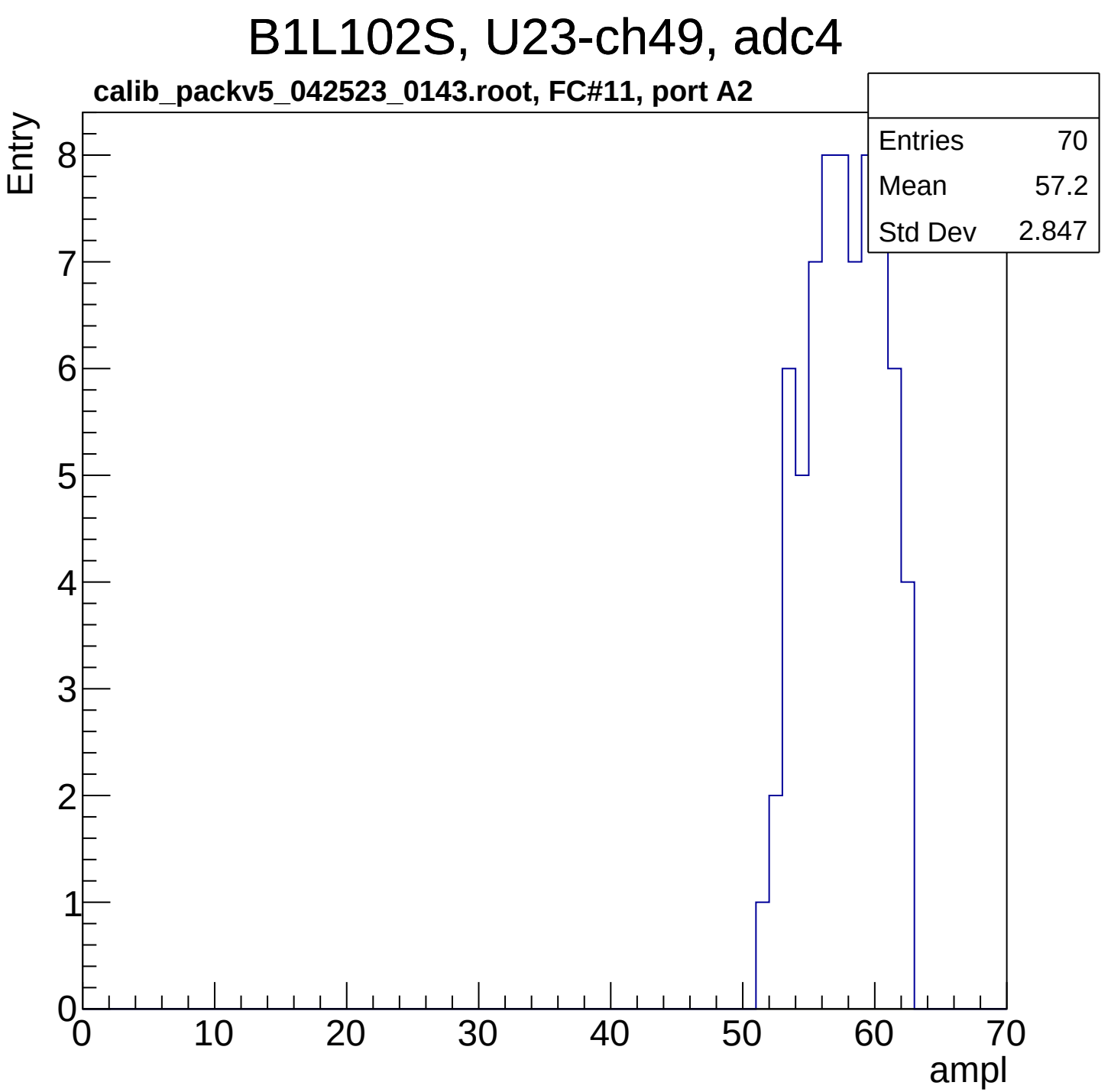

B1L102S, U23-ch48, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

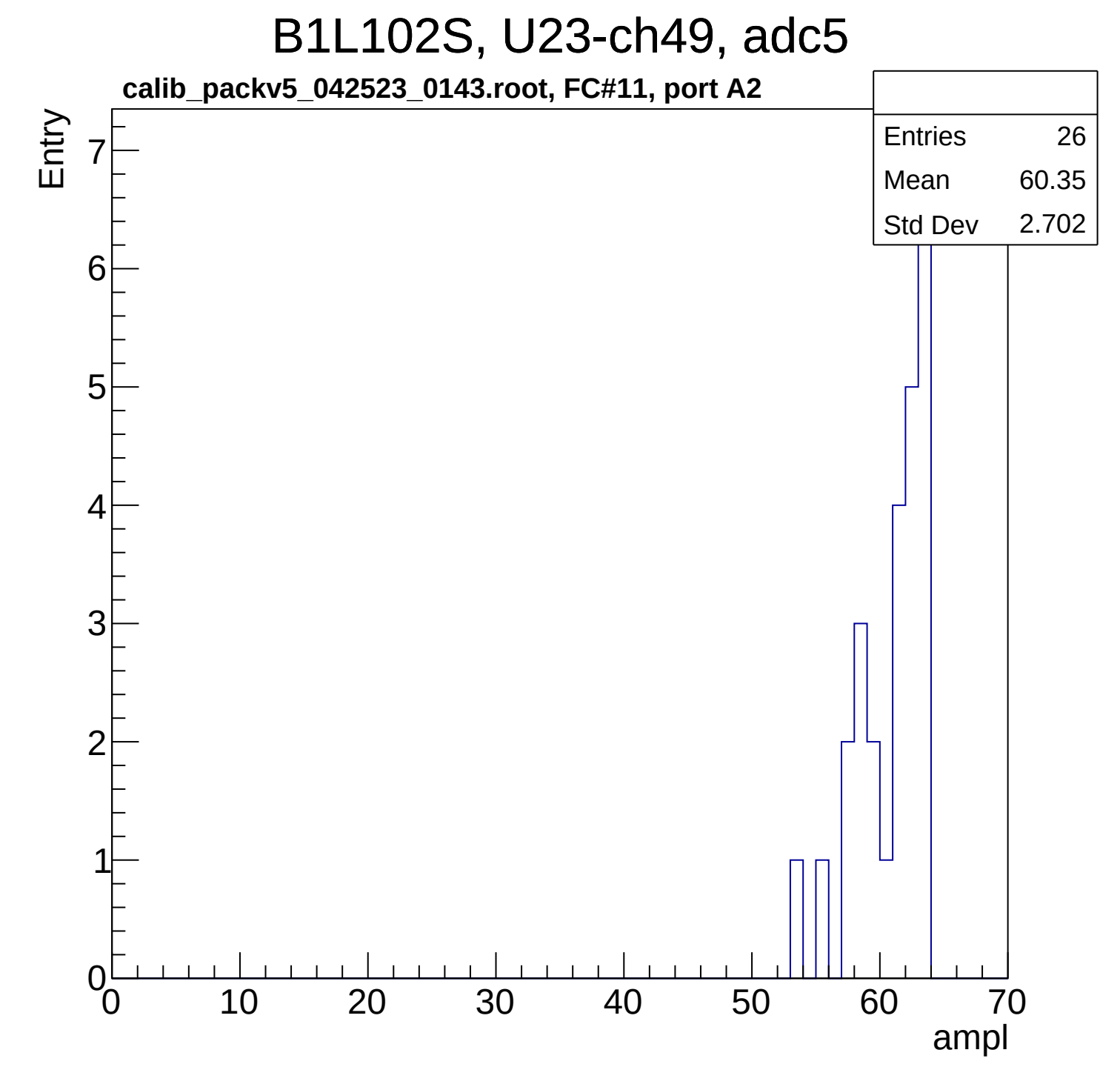

B1L102S, U23-ch49, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 98
Mean 29.42
Std Dev 4.759
Gaus mean : 30.4088
Gaus Width: 3.5826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

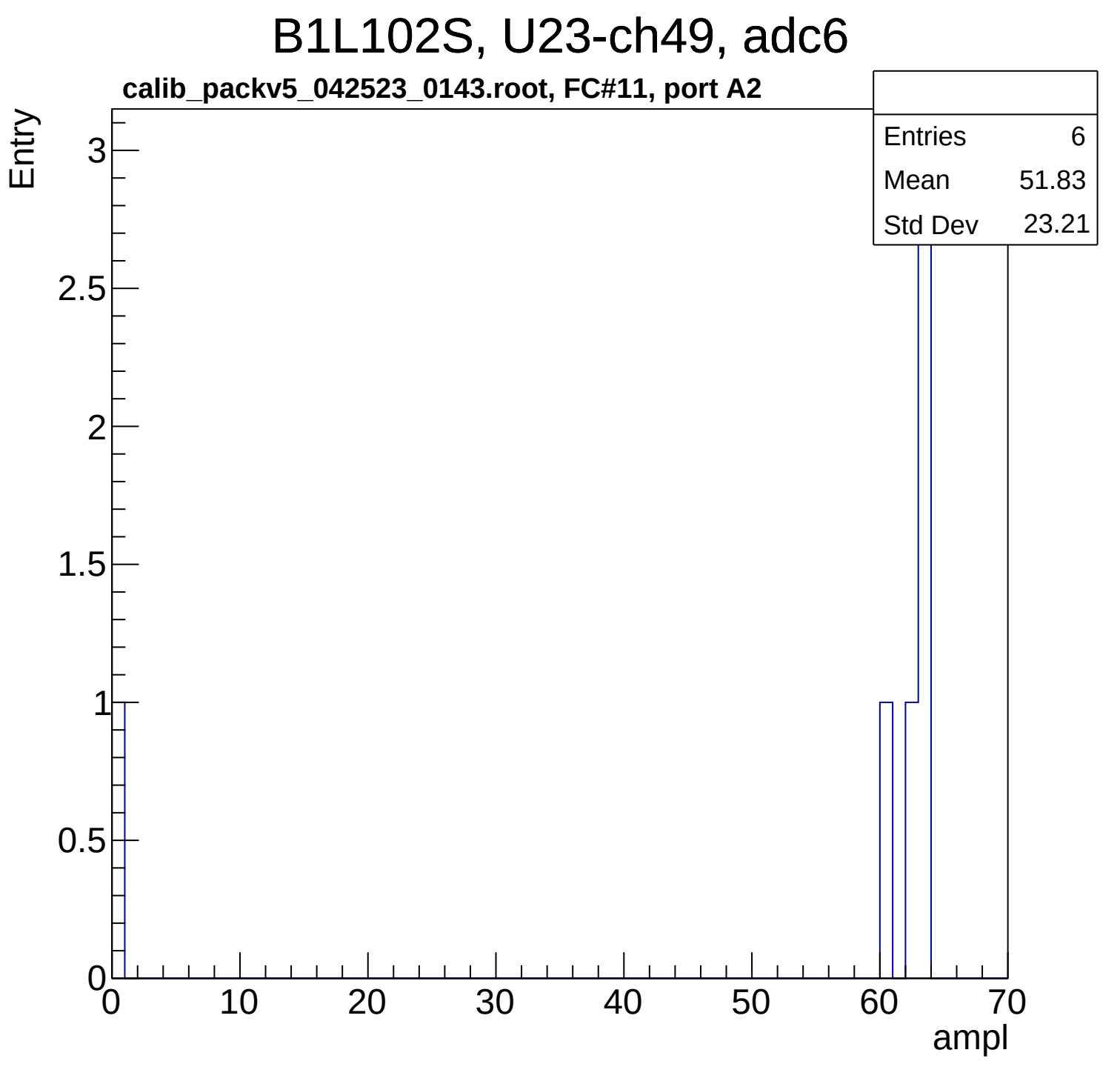

B1L102S, U23-ch49, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 37.52
Std Dev 3.094
Gaus mean : 38.3786
Gaus Width: 3.2314
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

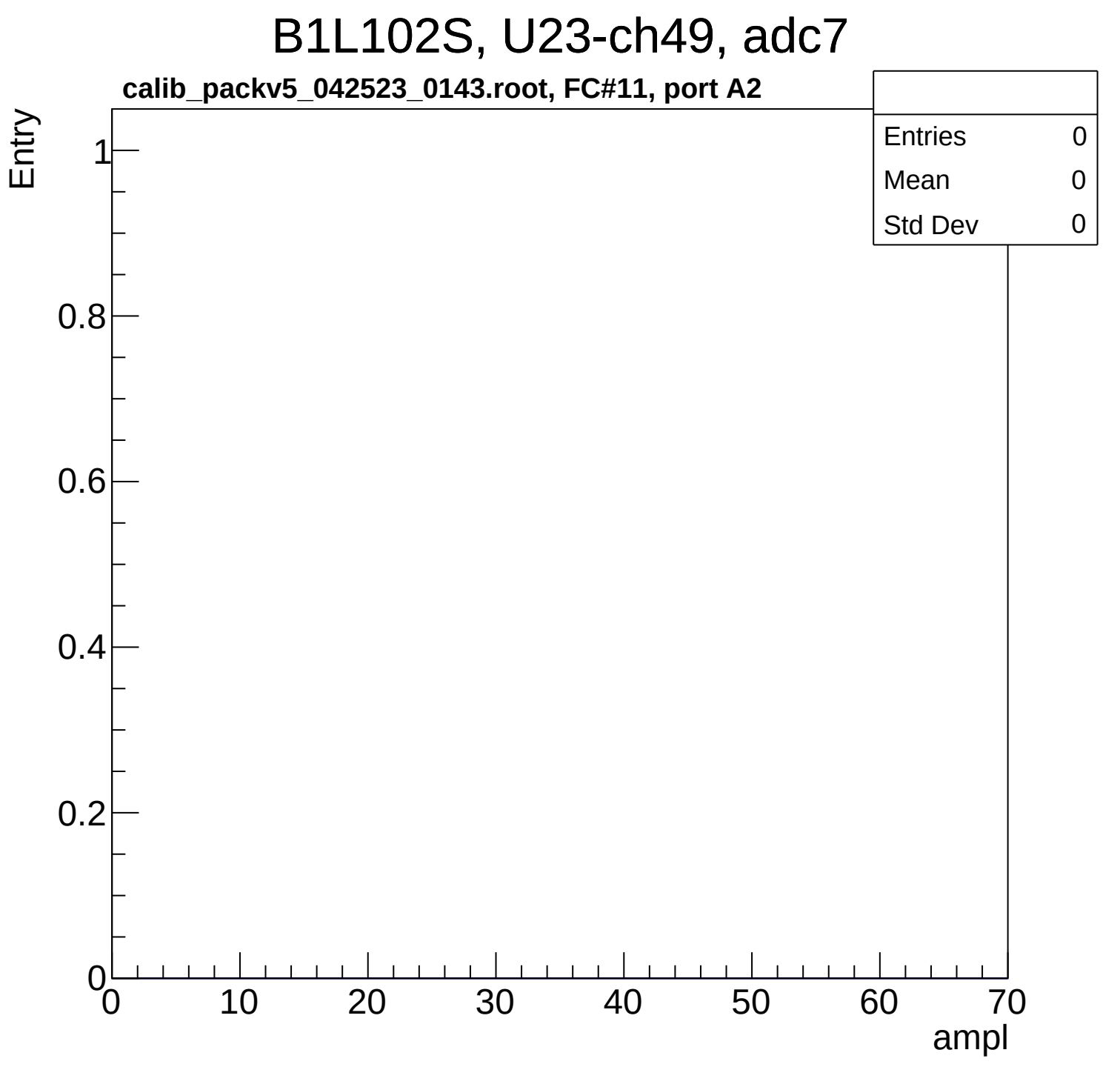

B1L102S, U23-ch49, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 43.69
Std Dev 3.066
Gaus mean : 44.1144
Gaus Width: 3.4669
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

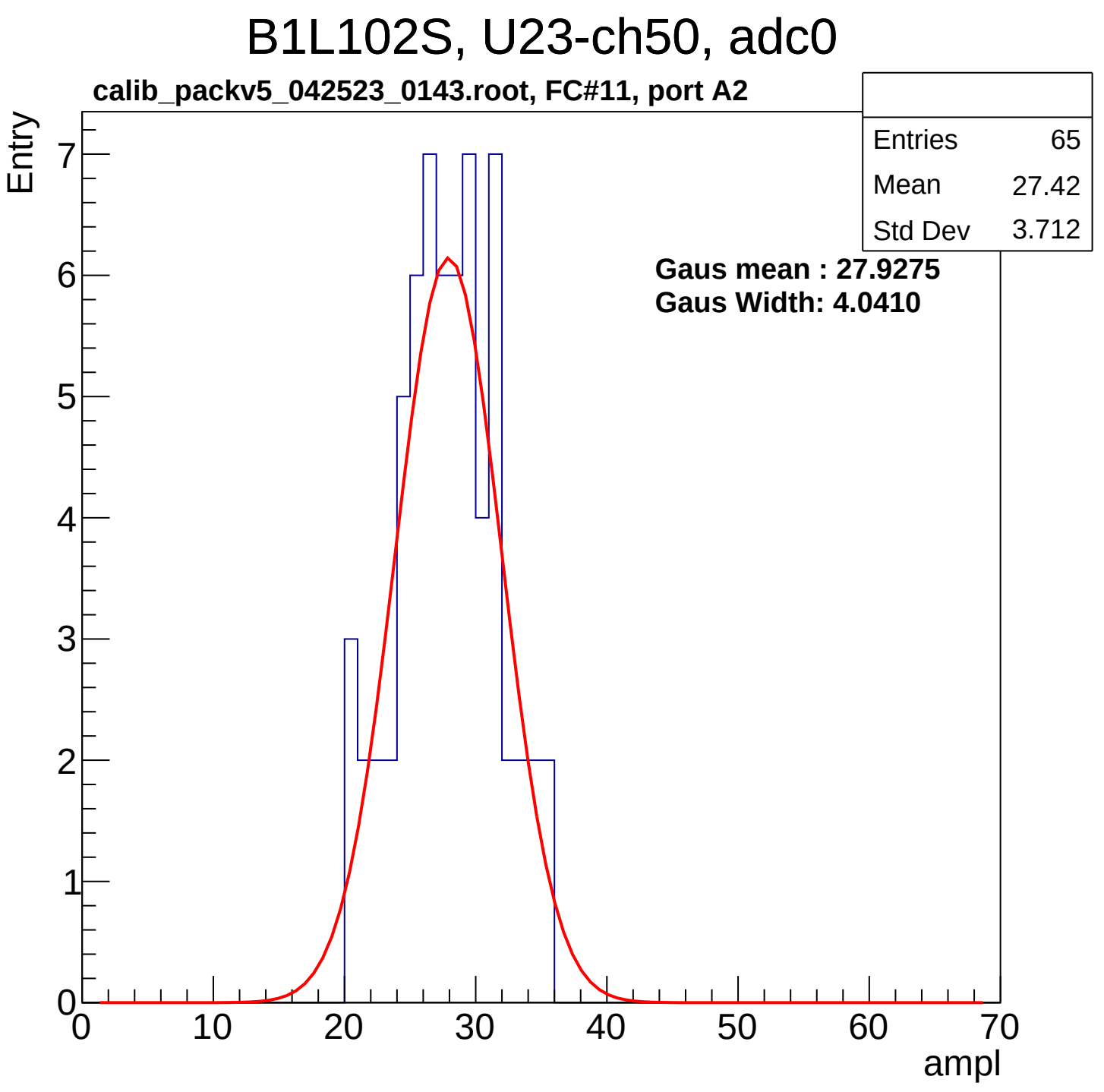

B1L102S, U23-ch49, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 49.65
Std Dev 3.035
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

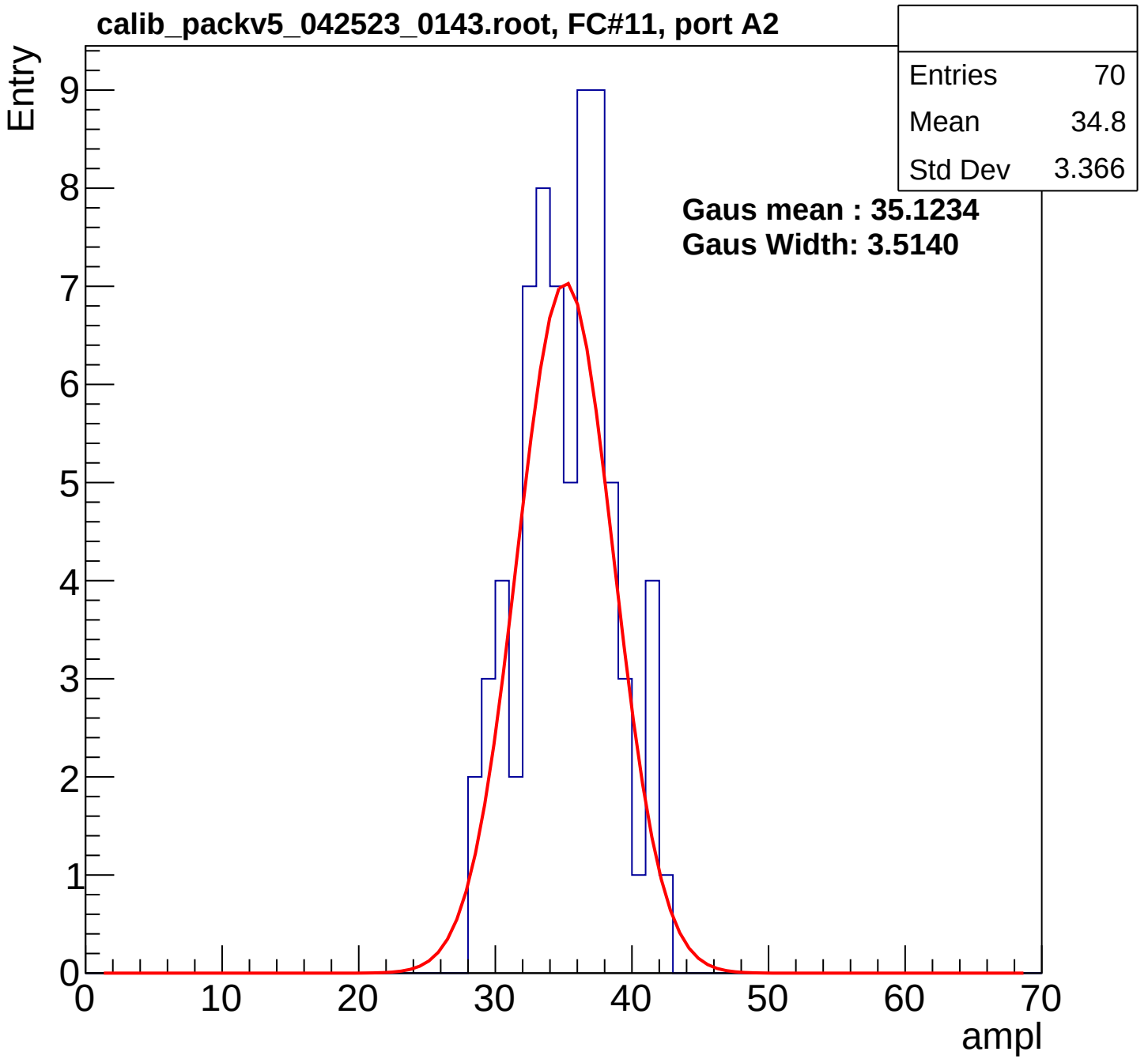

B1L102S, U23-ch49, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 57.2
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

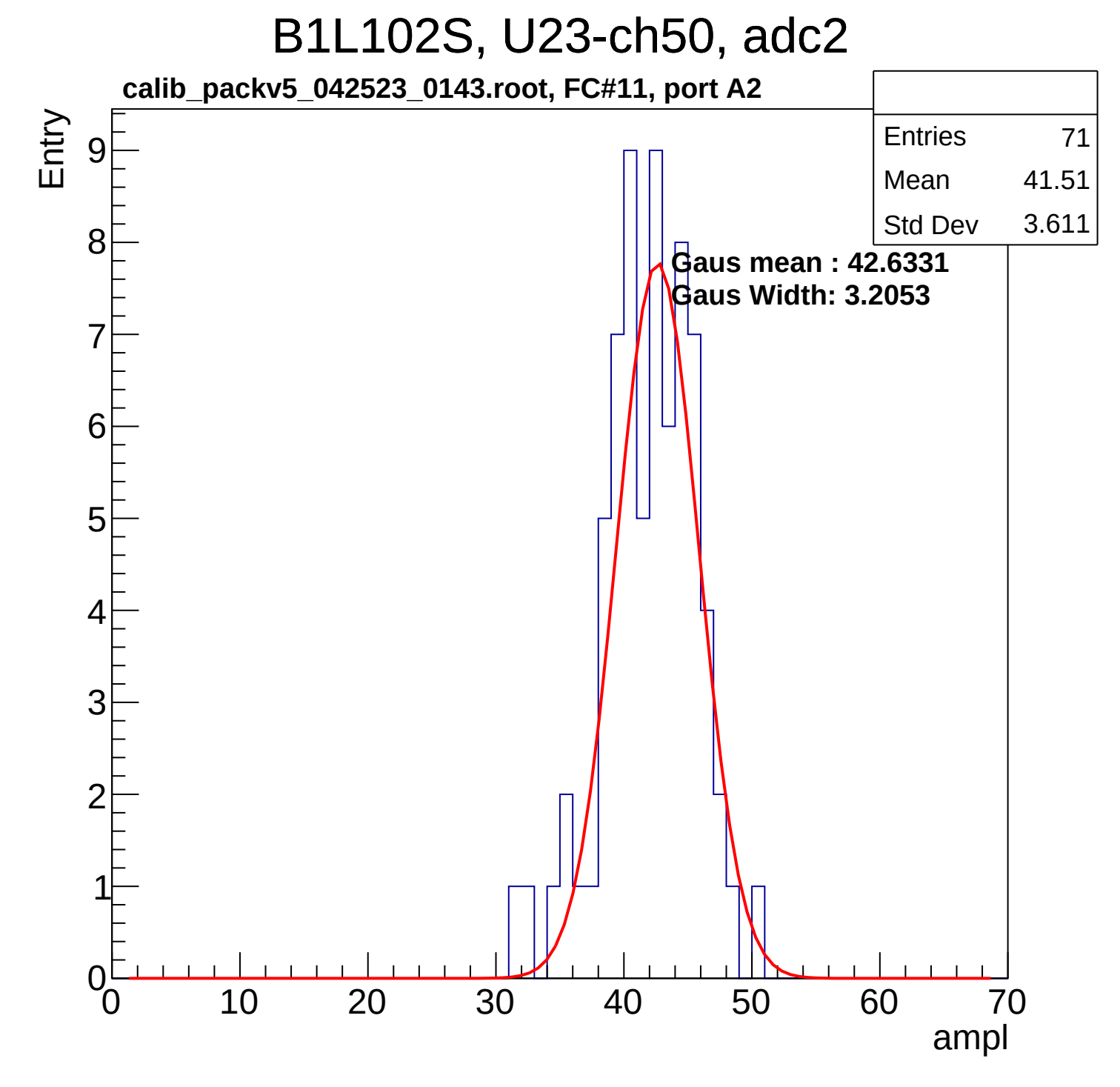

B1L102S, U23-ch49, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 26
Mean 60.35
Std Dev 2.702
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

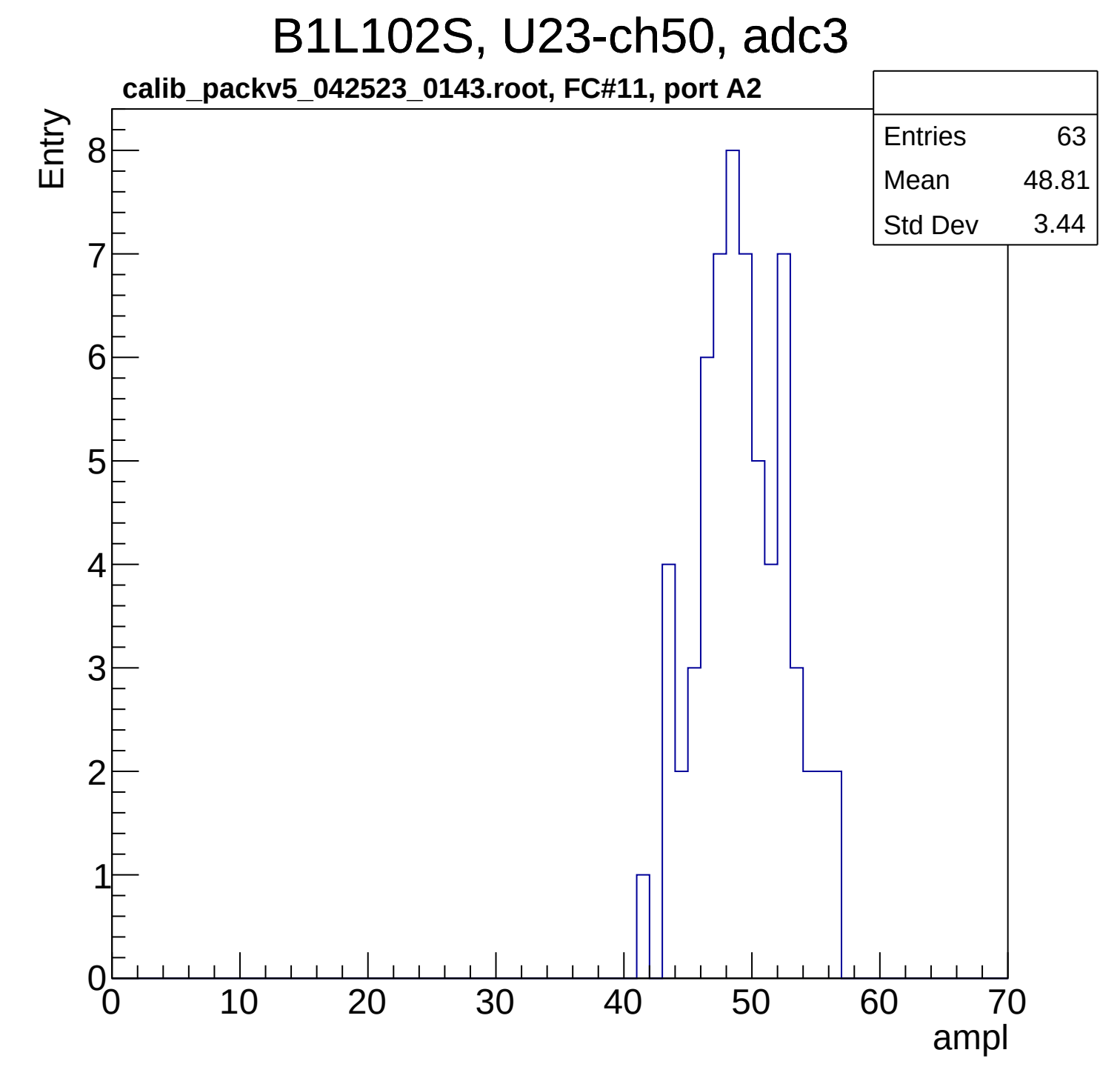

B1L102S, U23-ch49, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

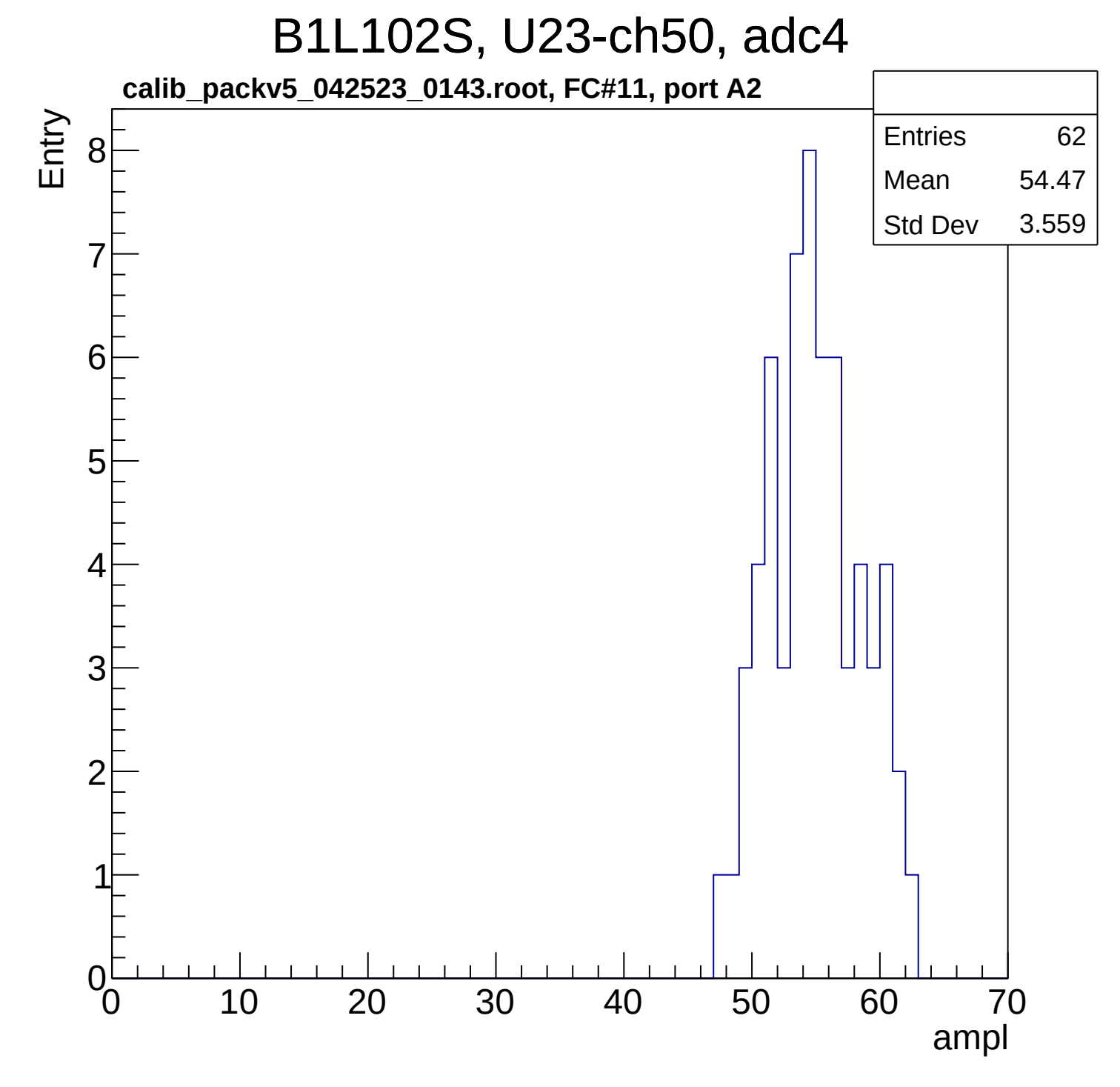

B1L102S, U23-ch49, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

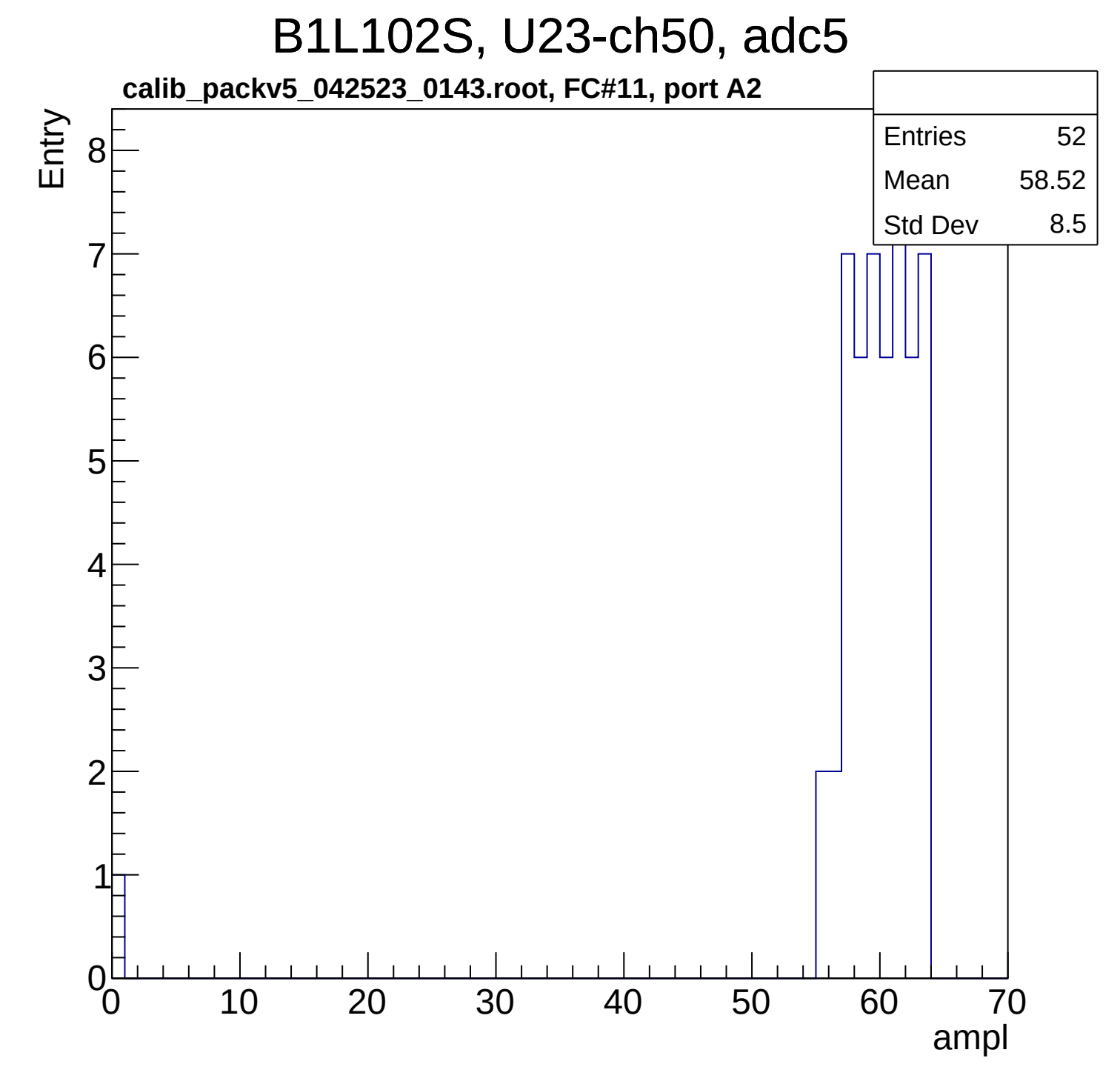

B1L102S, U23-ch50, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 27.42
Std Dev 3.712
Gaus mean : 27.9275
Gaus Width: 4.0410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

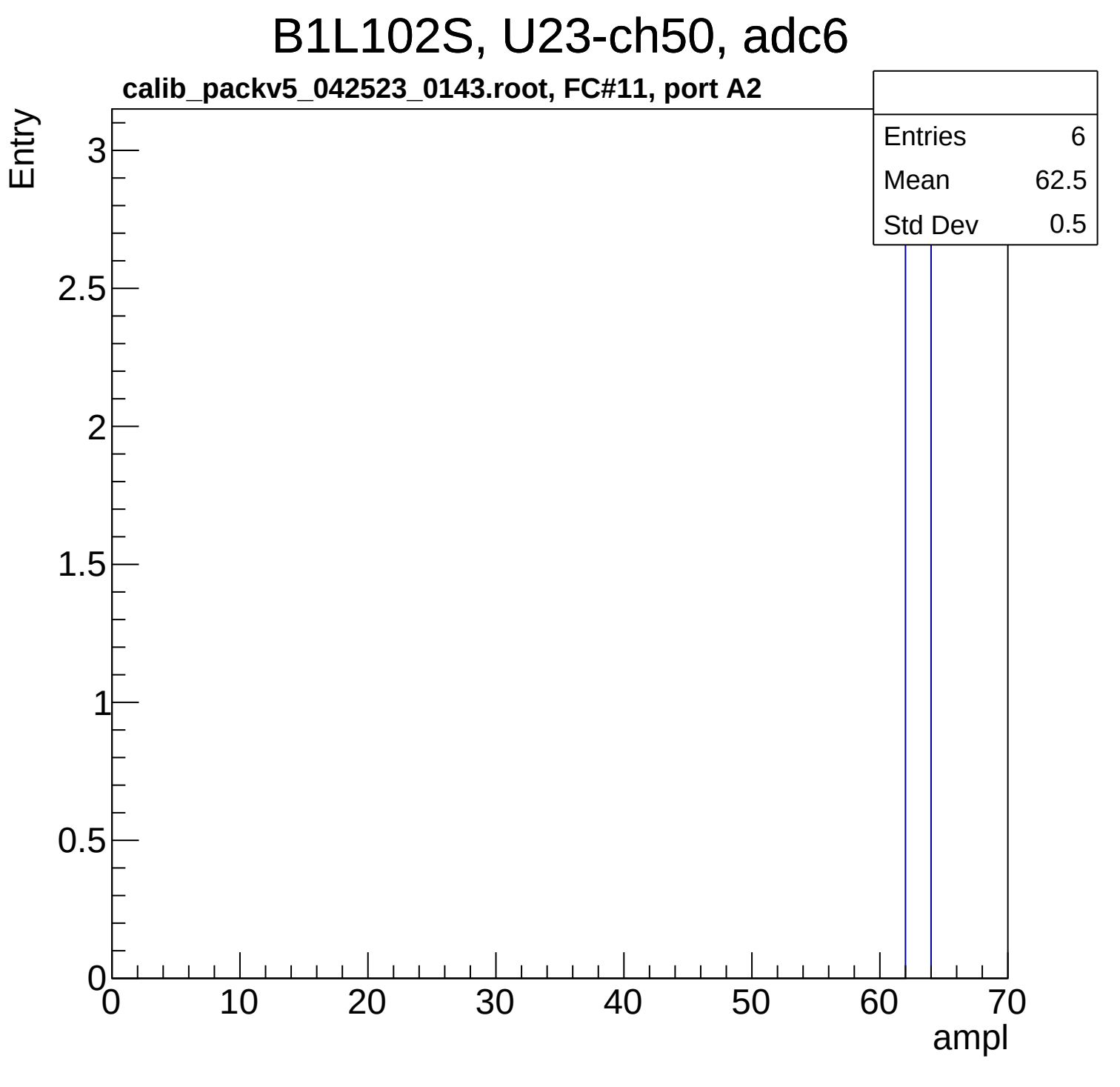

B1L102S, U23-ch50, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.8
Std Dev 3.366
Gaus mean : 35.1234
Gaus Width: 3.5140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

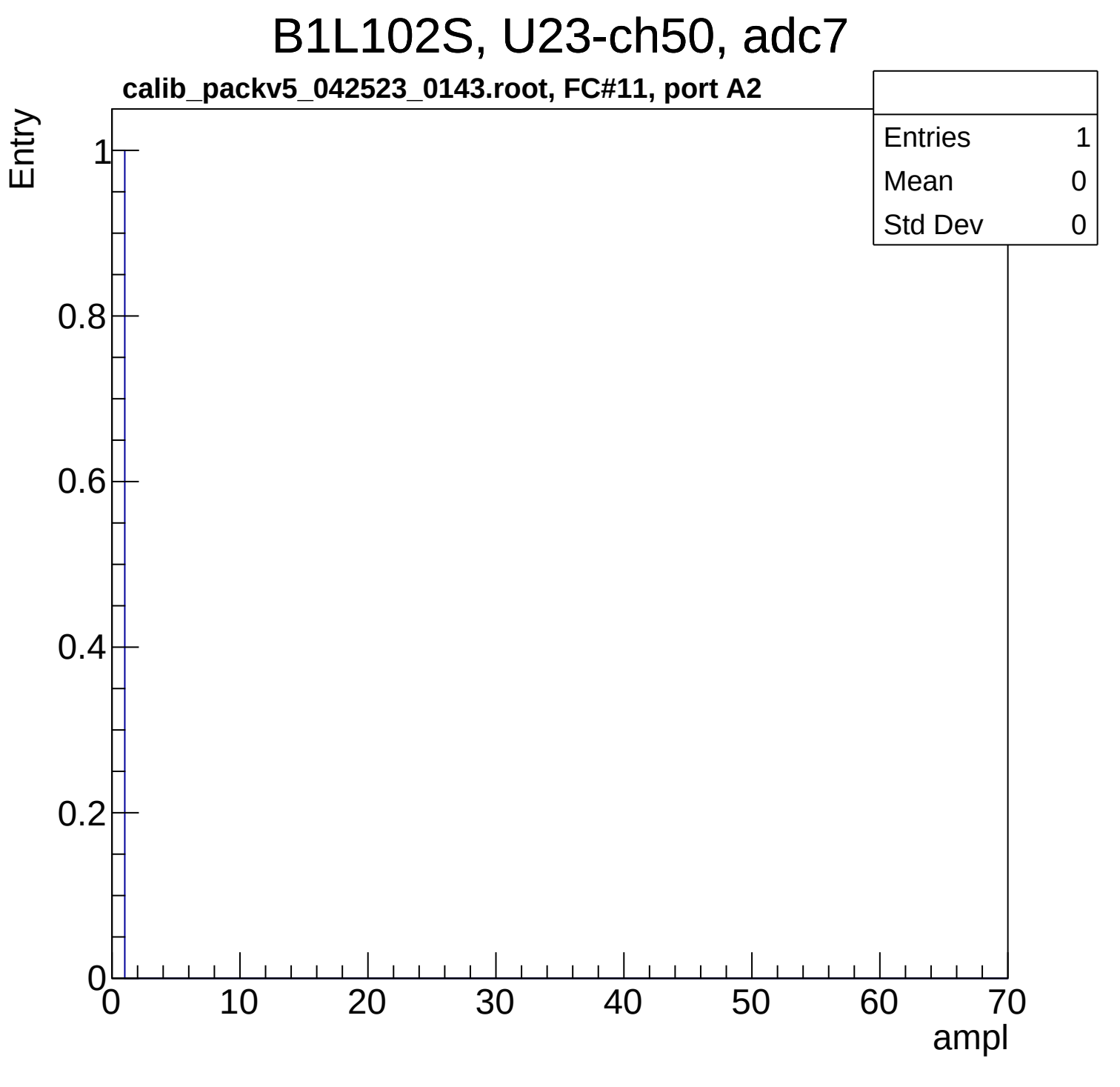

B1L102S, U23-ch50, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 41.51
Std Dev 3.611
Gaus mean : 42.6331
Gaus Width: 3.2053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

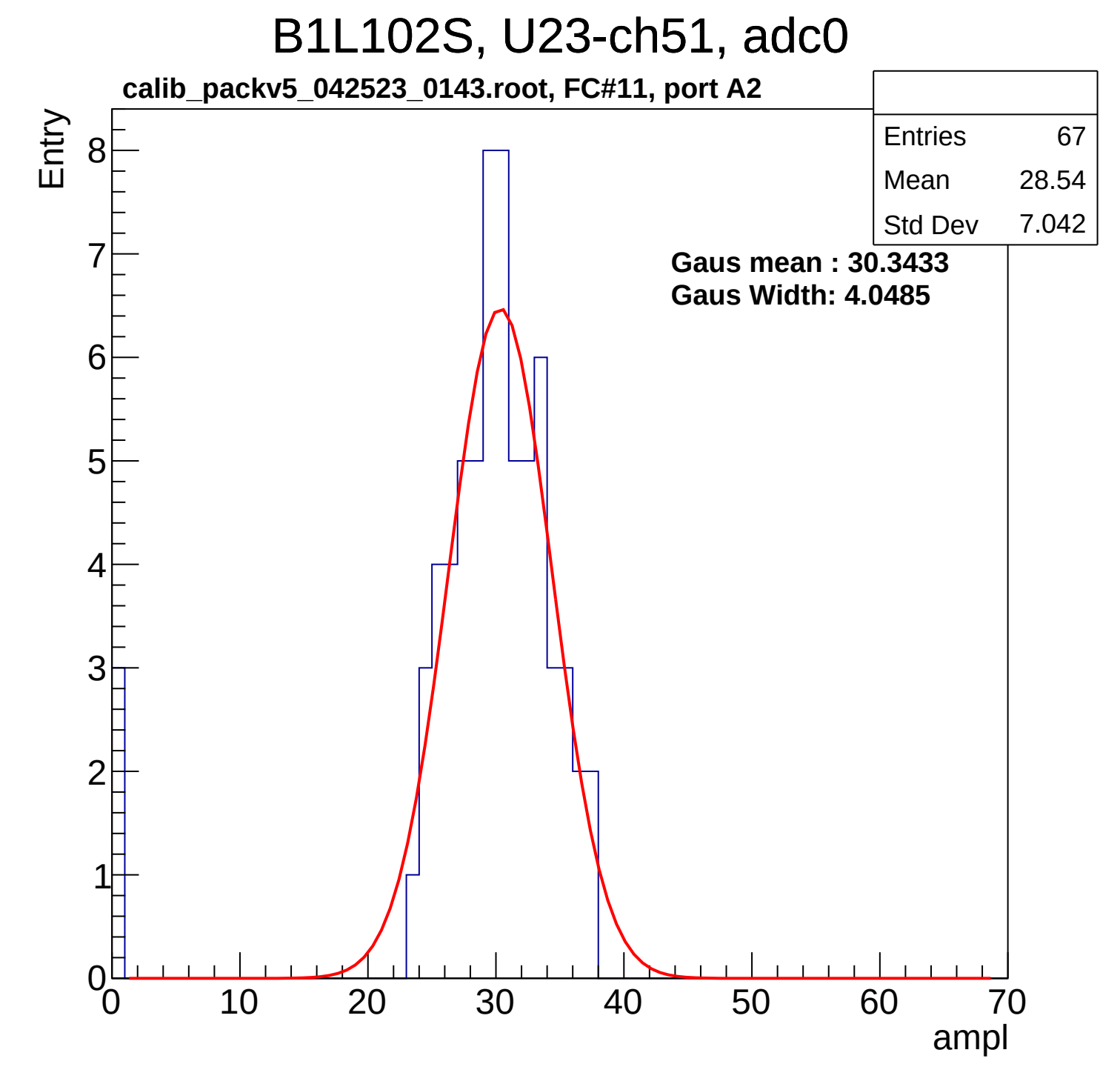

B1L102S, U23-ch50, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 48.81
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

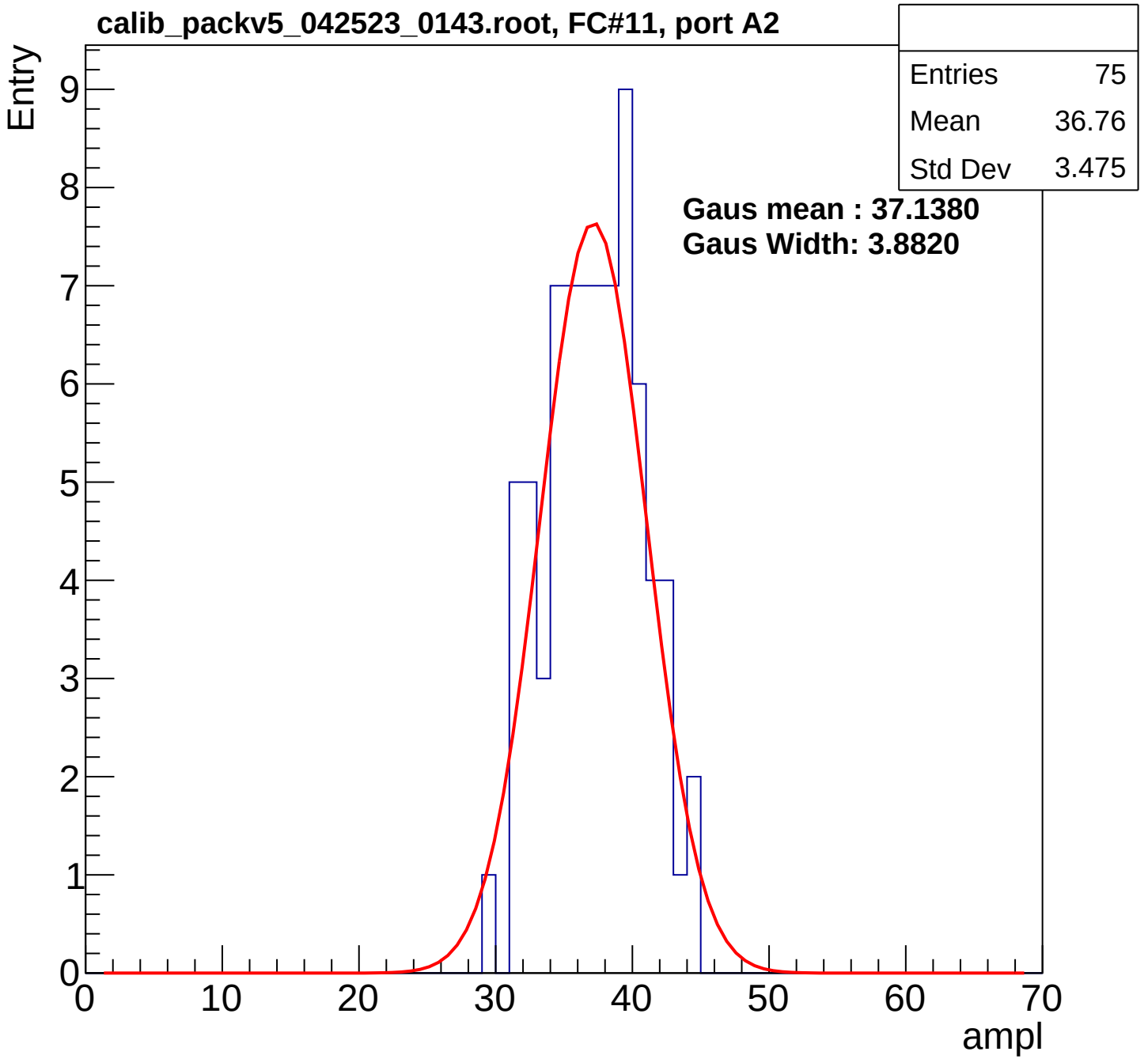

B1L102S, U23-ch50, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 54.47
Std Dev 3.559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

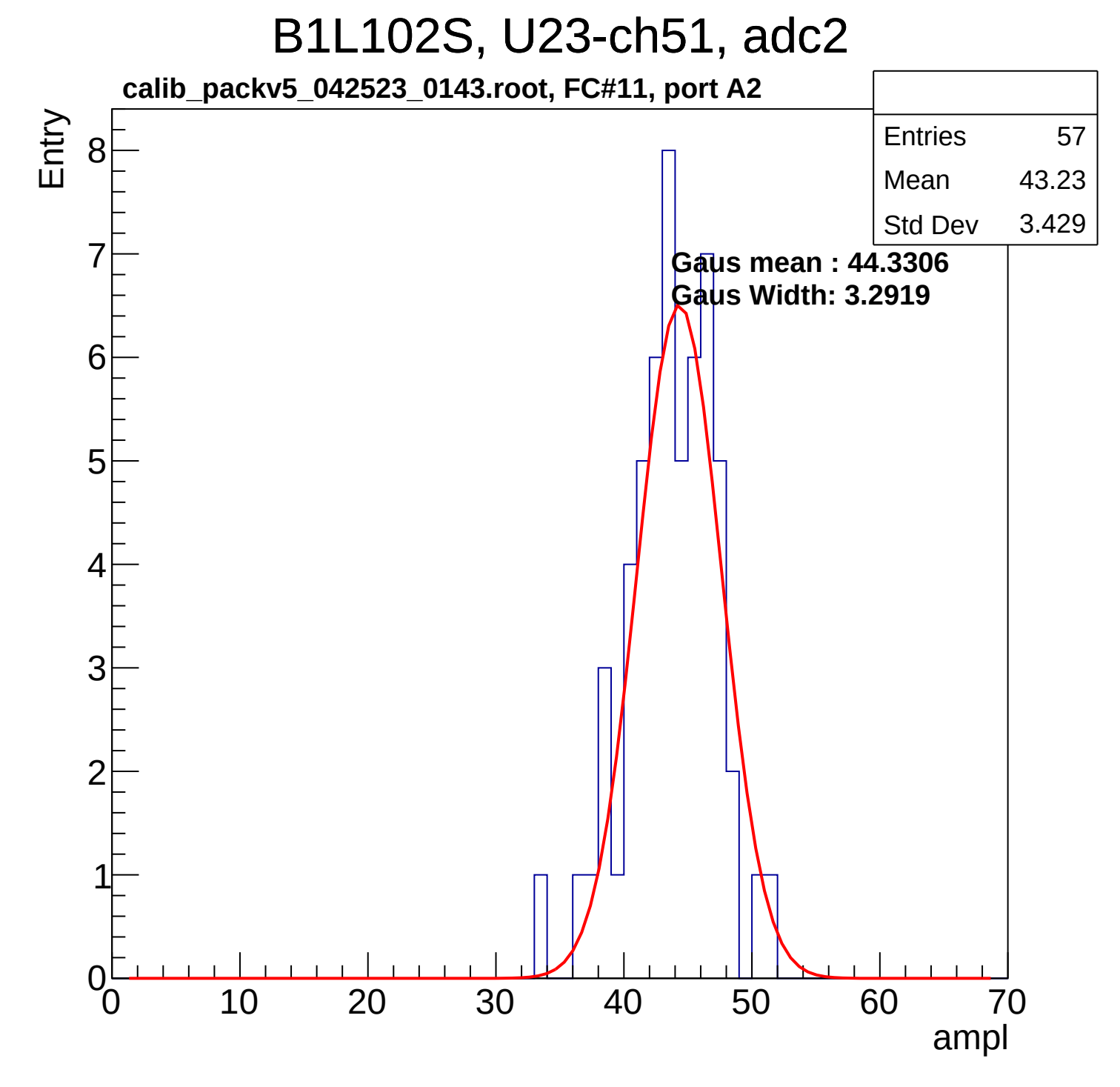

B1L102S, U23-ch50, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.52
Std Dev 8.5
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

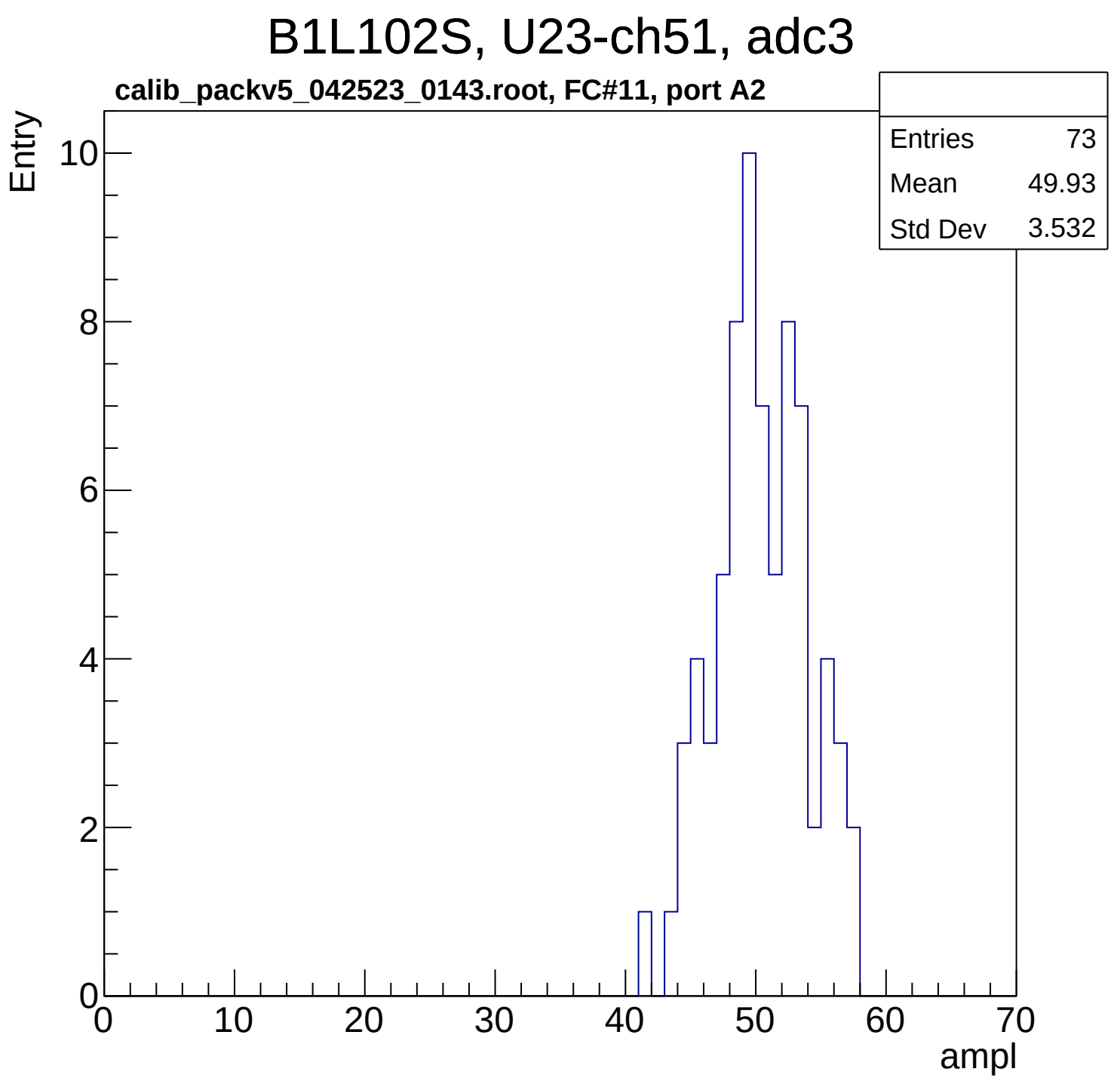

B1L102S, U23-ch50, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

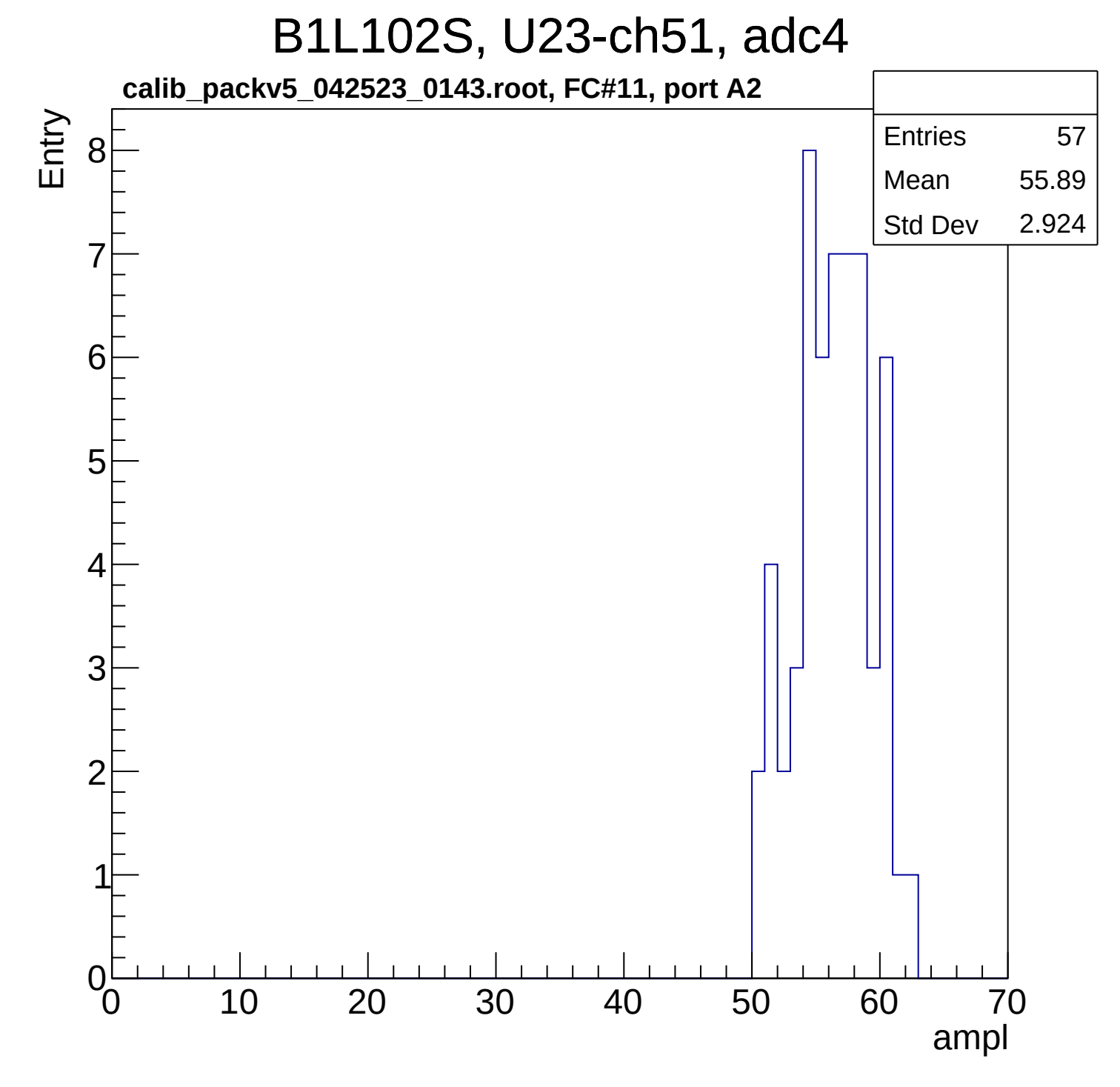

B1L102S, U23-ch50, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

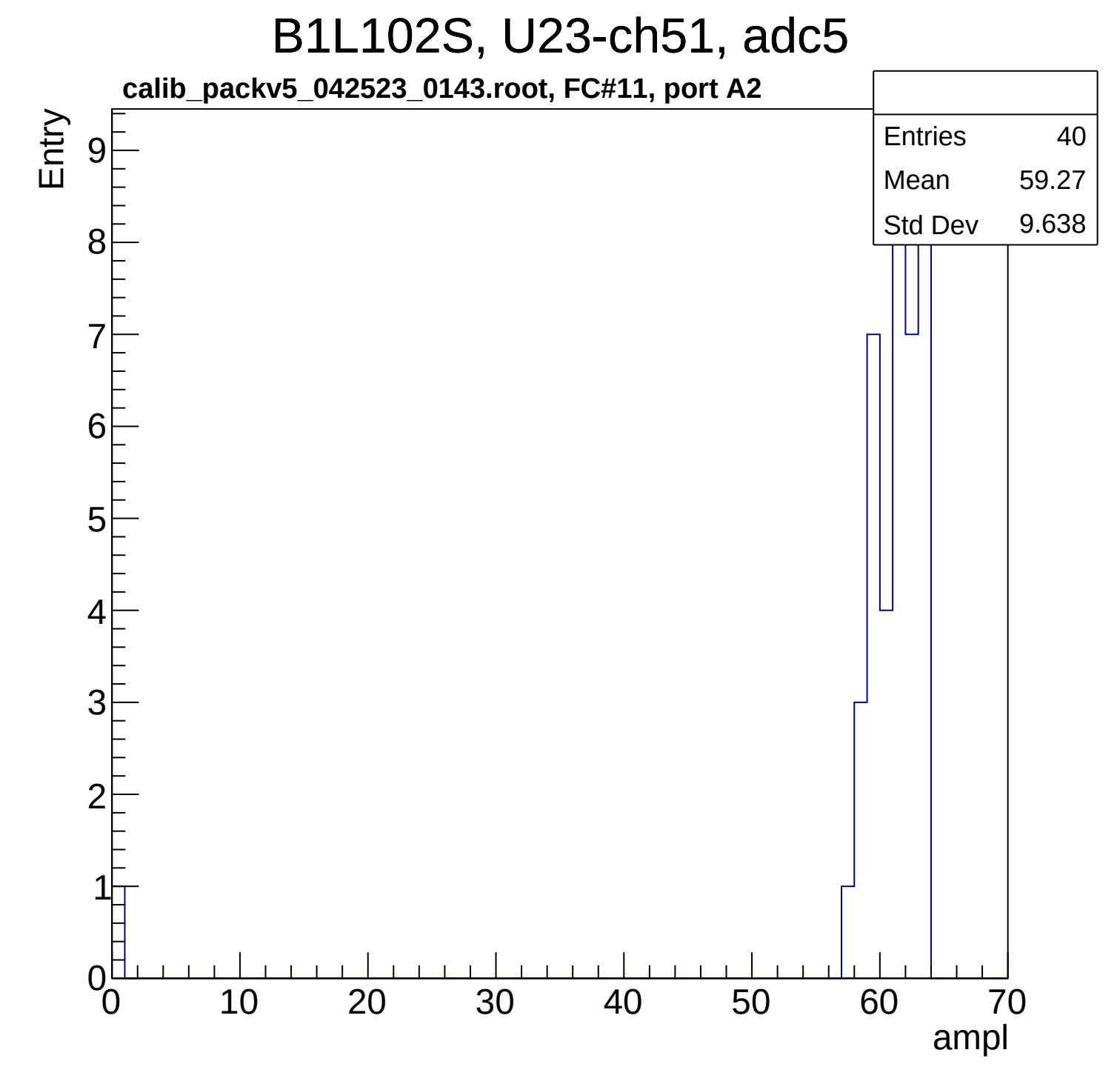

B1L102S, U23-ch51, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 28.54
Std Dev 7.042
Gaus mean : 30.3433
Gaus Width: 4.0485
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

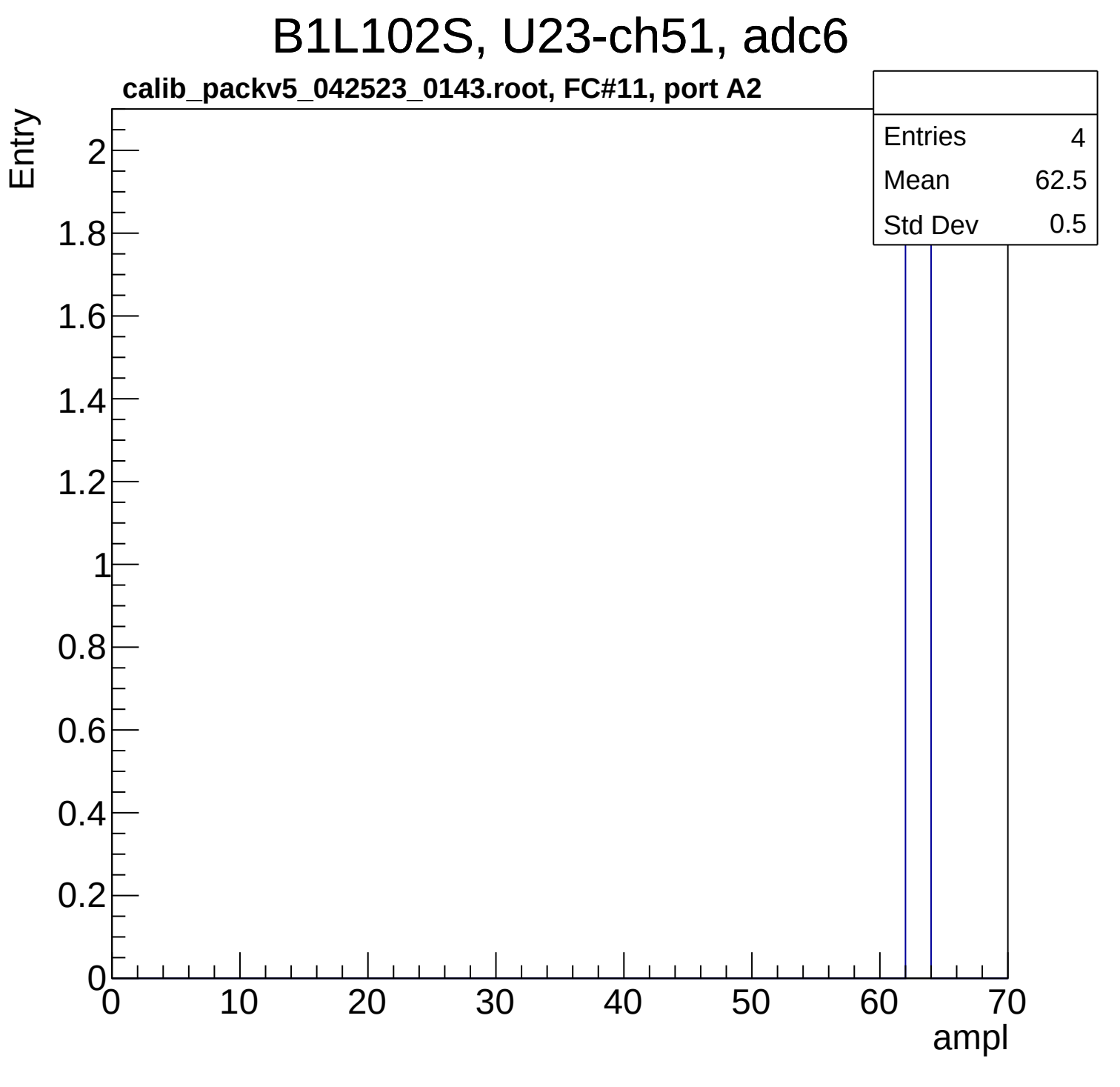

B1L102S, U23-ch51, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 36.76
Std Dev 3.475
Gaus mean : 37.1380
Gaus Width: 3.8820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

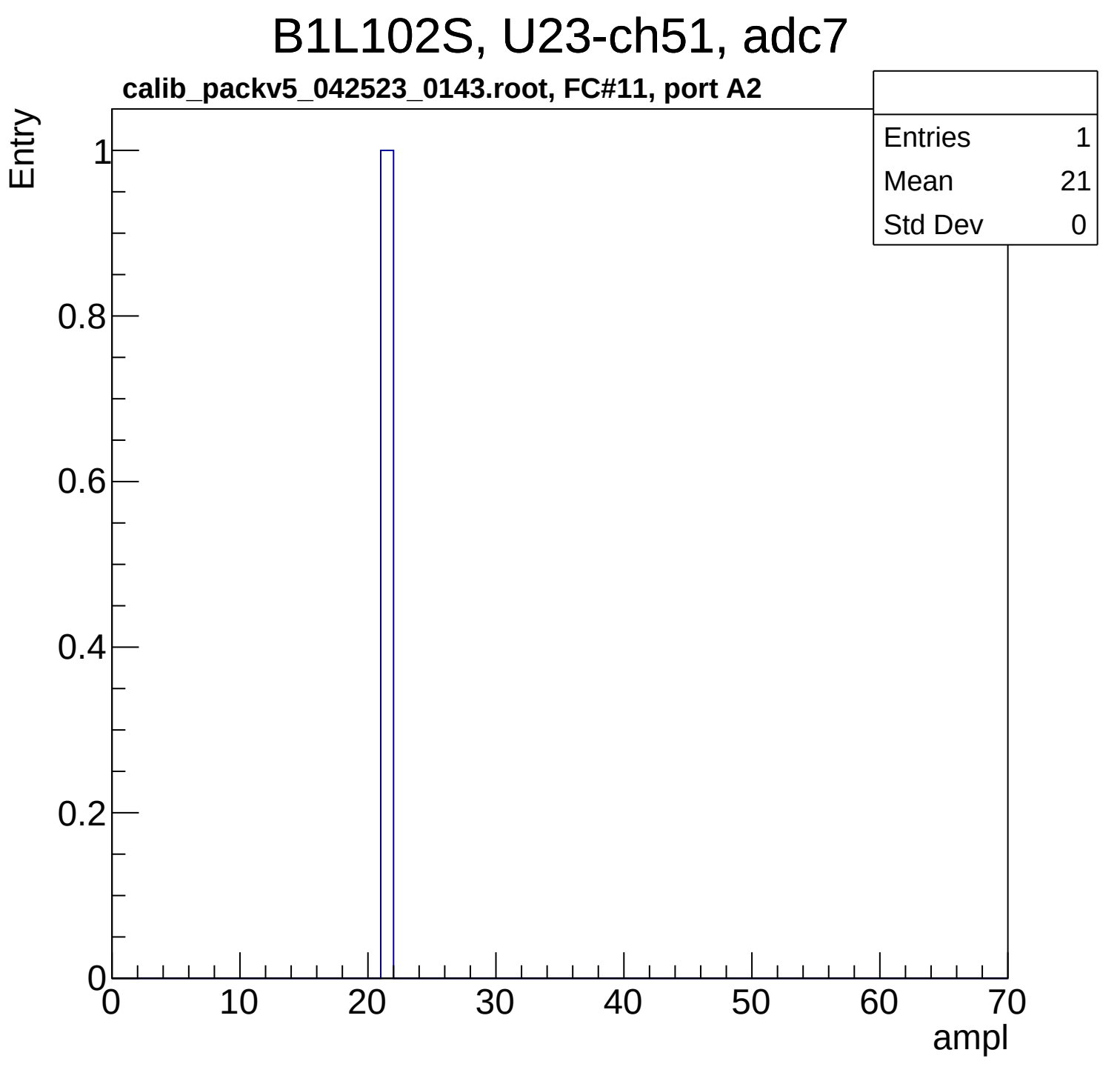

B1L102S, U23-ch51, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 43.23
Std Dev 3.429
Gaus mean : 44.3306
Gaus Width: 3.2919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

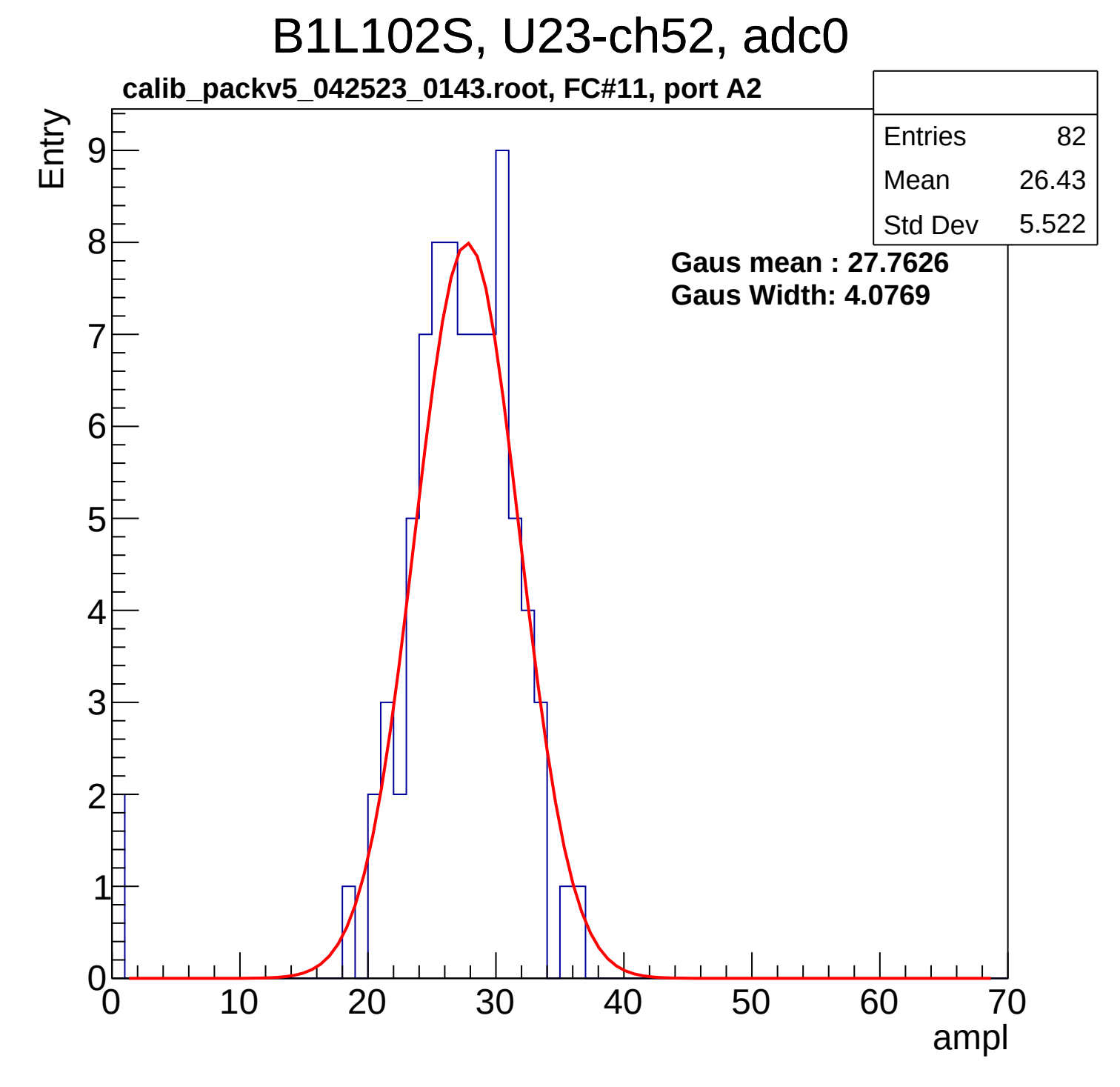

B1L102S, U23-ch51, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 49.93
Std Dev 3.532
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

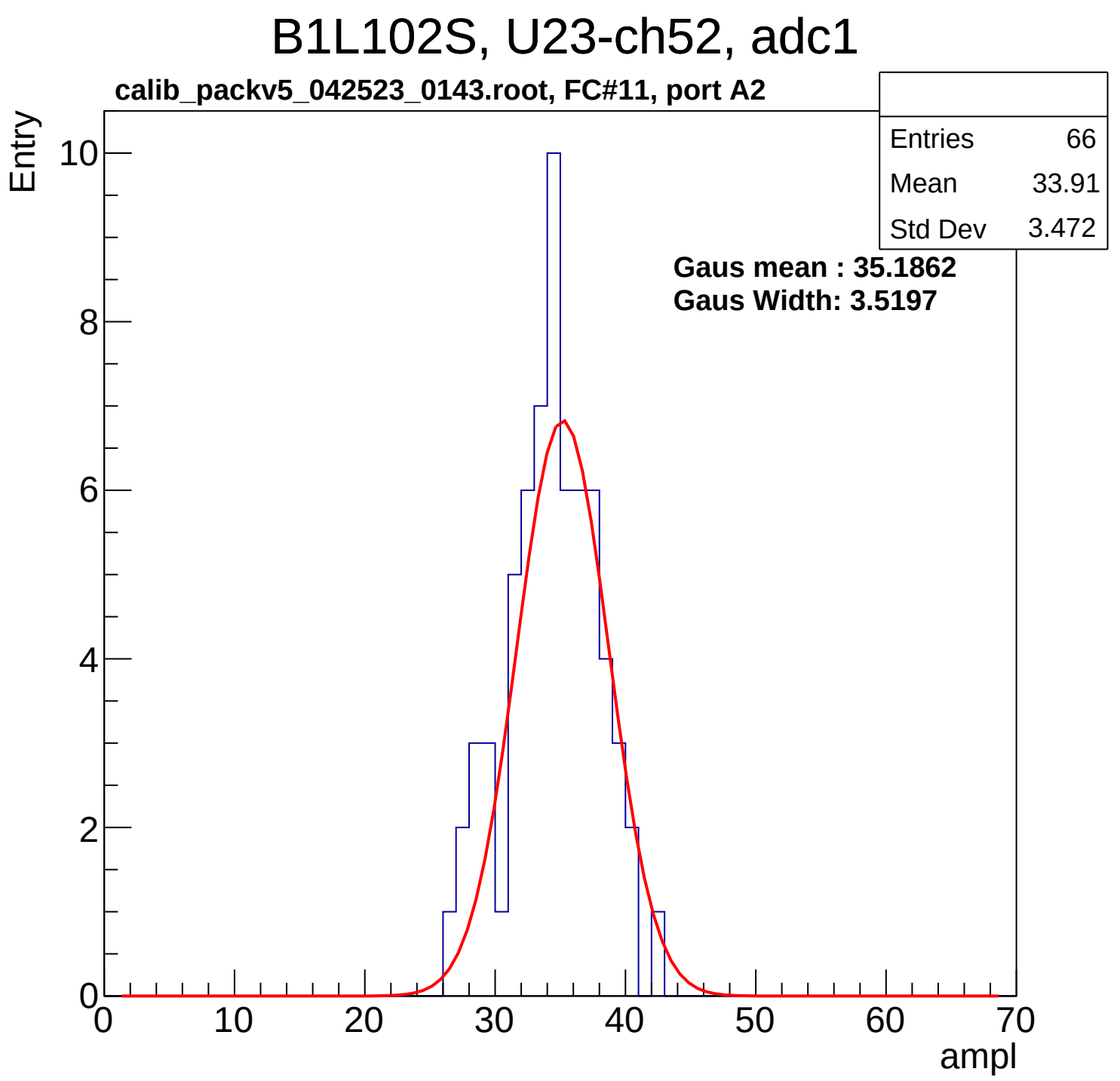

B1L102S, U23-ch51, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.89
Std Dev 2.924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

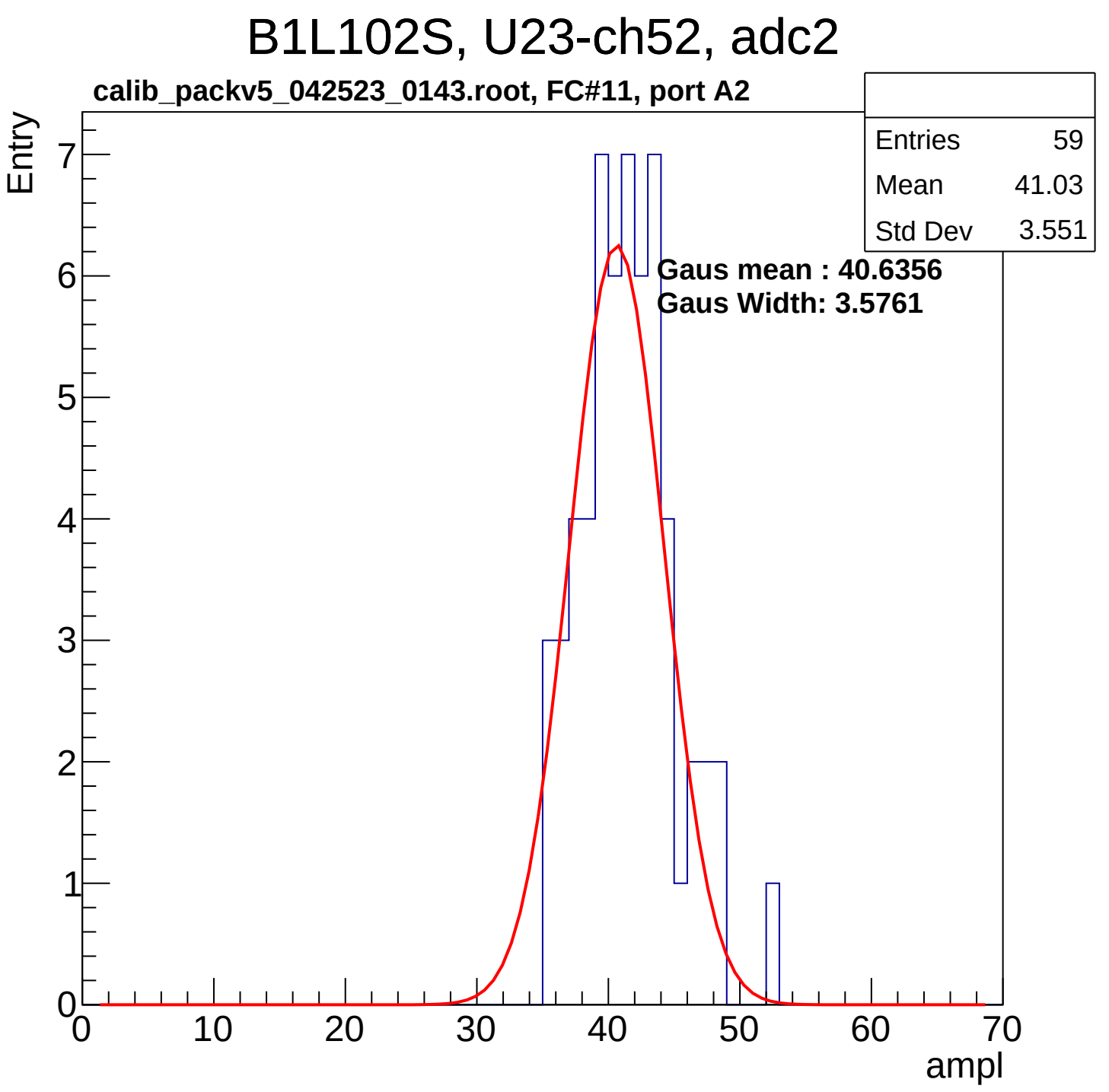

B1L102S, U23-ch51, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 59.27
Std Dev 9.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

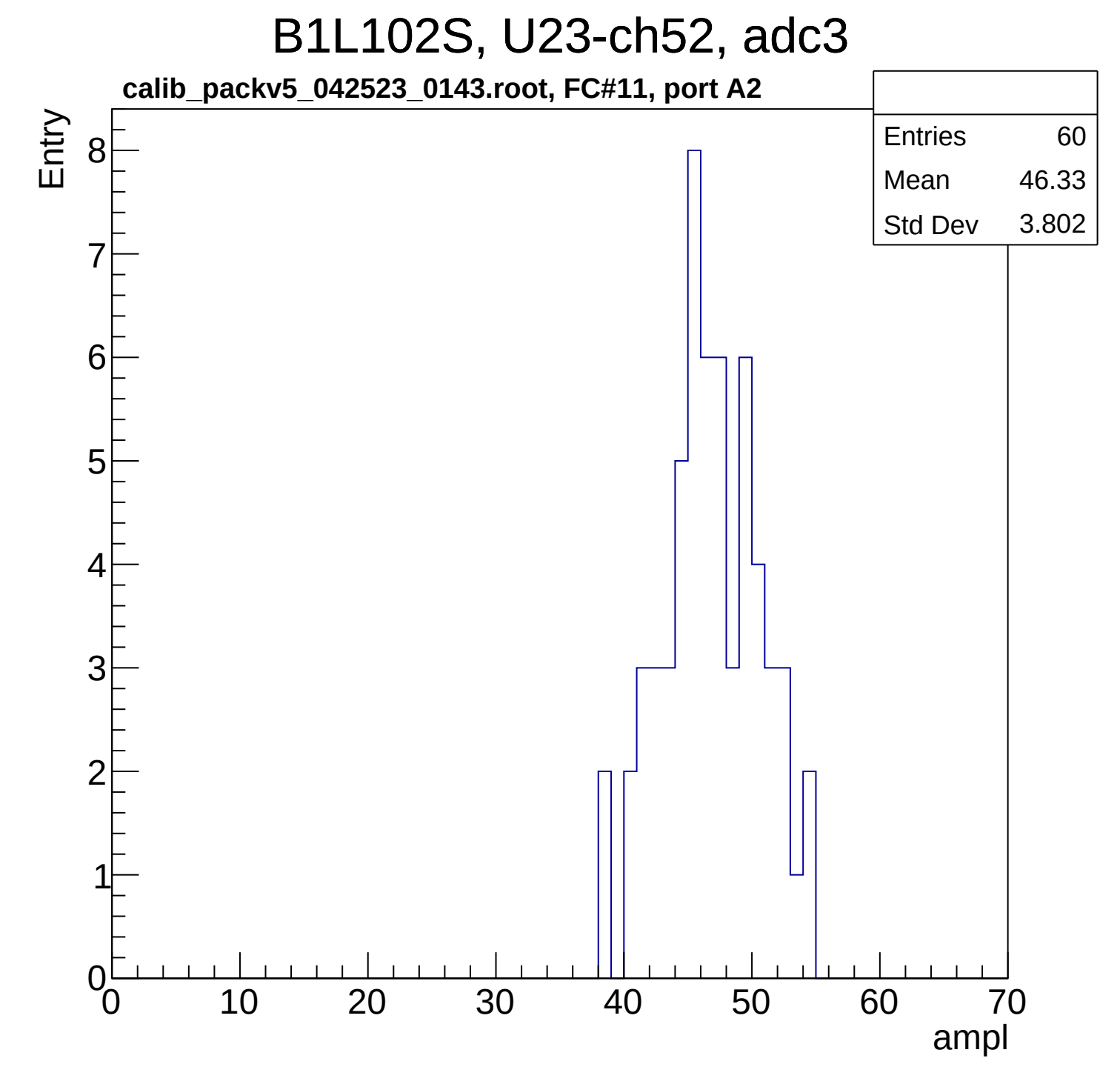

B1L102S, U23-ch51, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

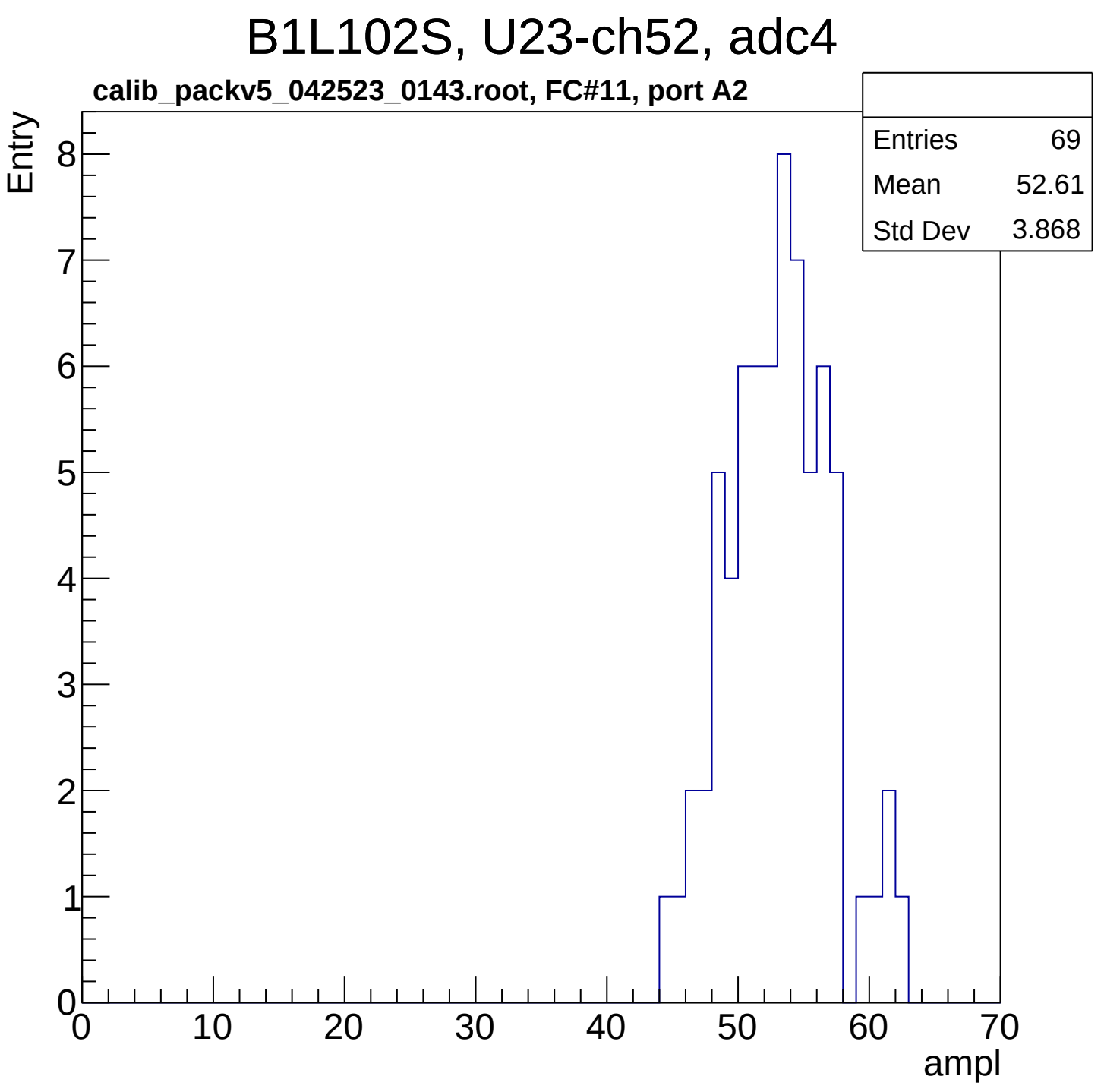

B1L102S, U23-ch51, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

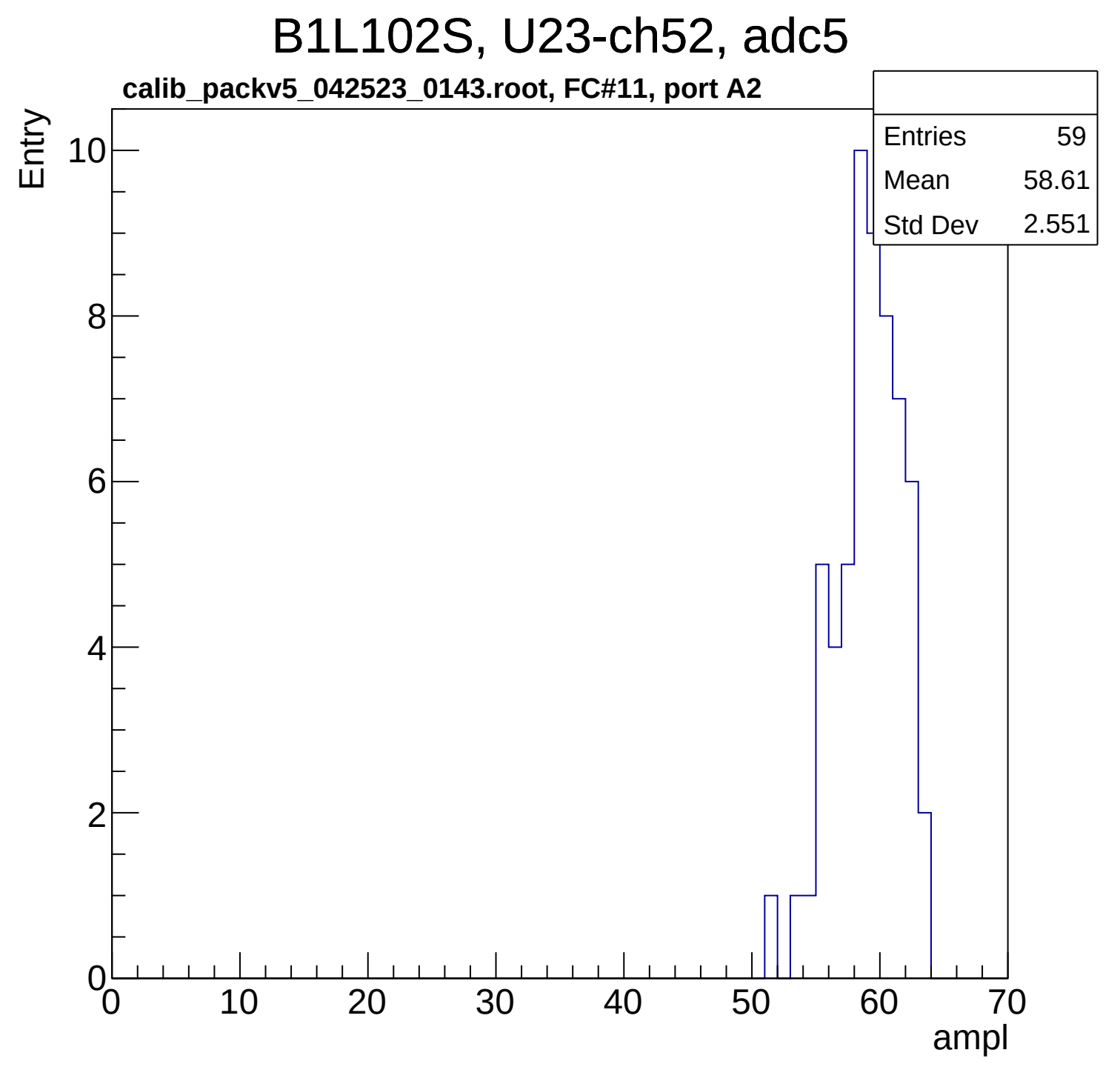

B1L102S, U23-ch52, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 26.43
Std Dev 5.522
Gaus mean : 27.7626
Gaus Width: 4.0769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

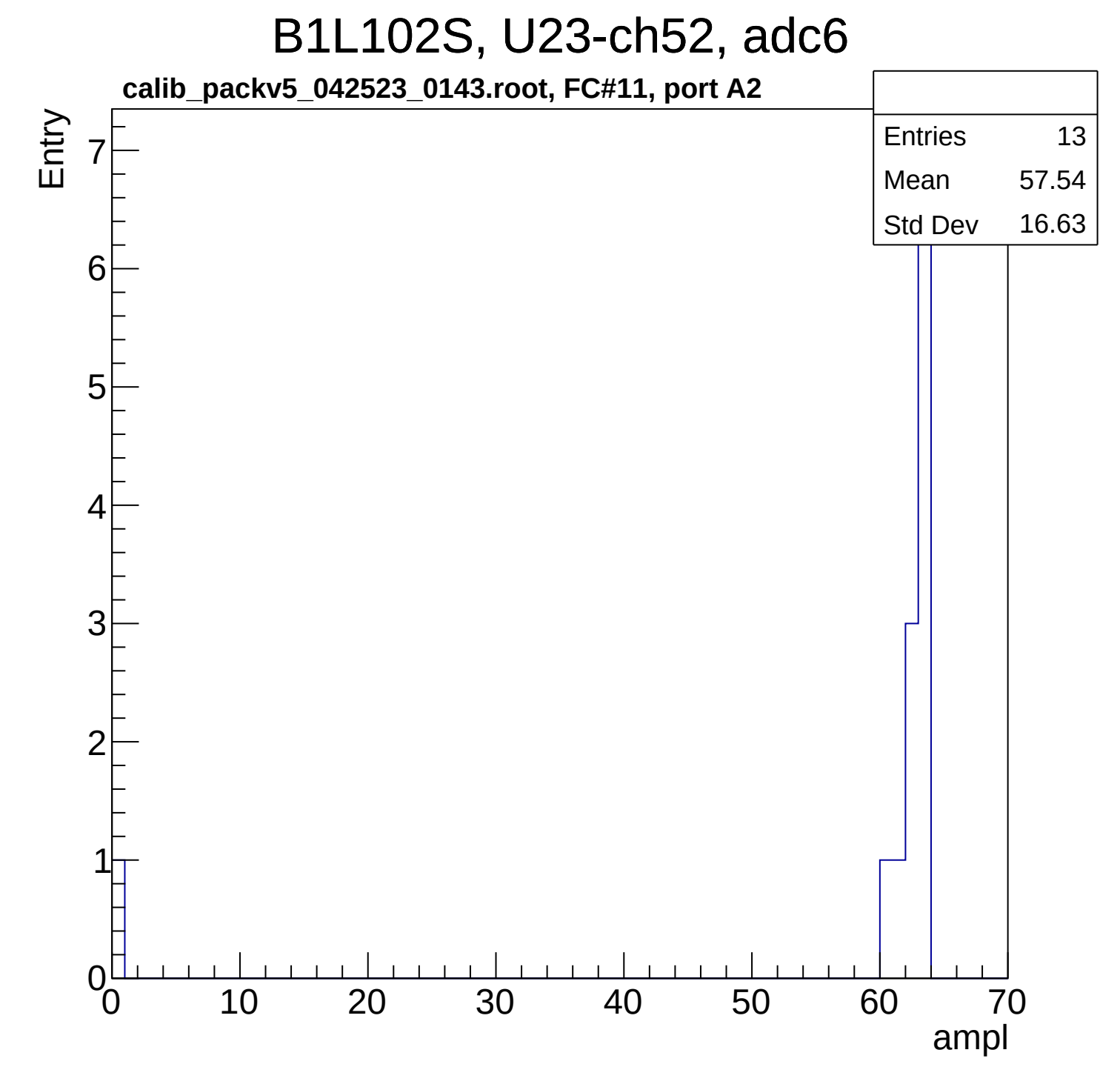

B1L102S, U23-ch52, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 33.91
Std Dev 3.472
Gaus mean : 35.1862
Gaus Width: 3.5197
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

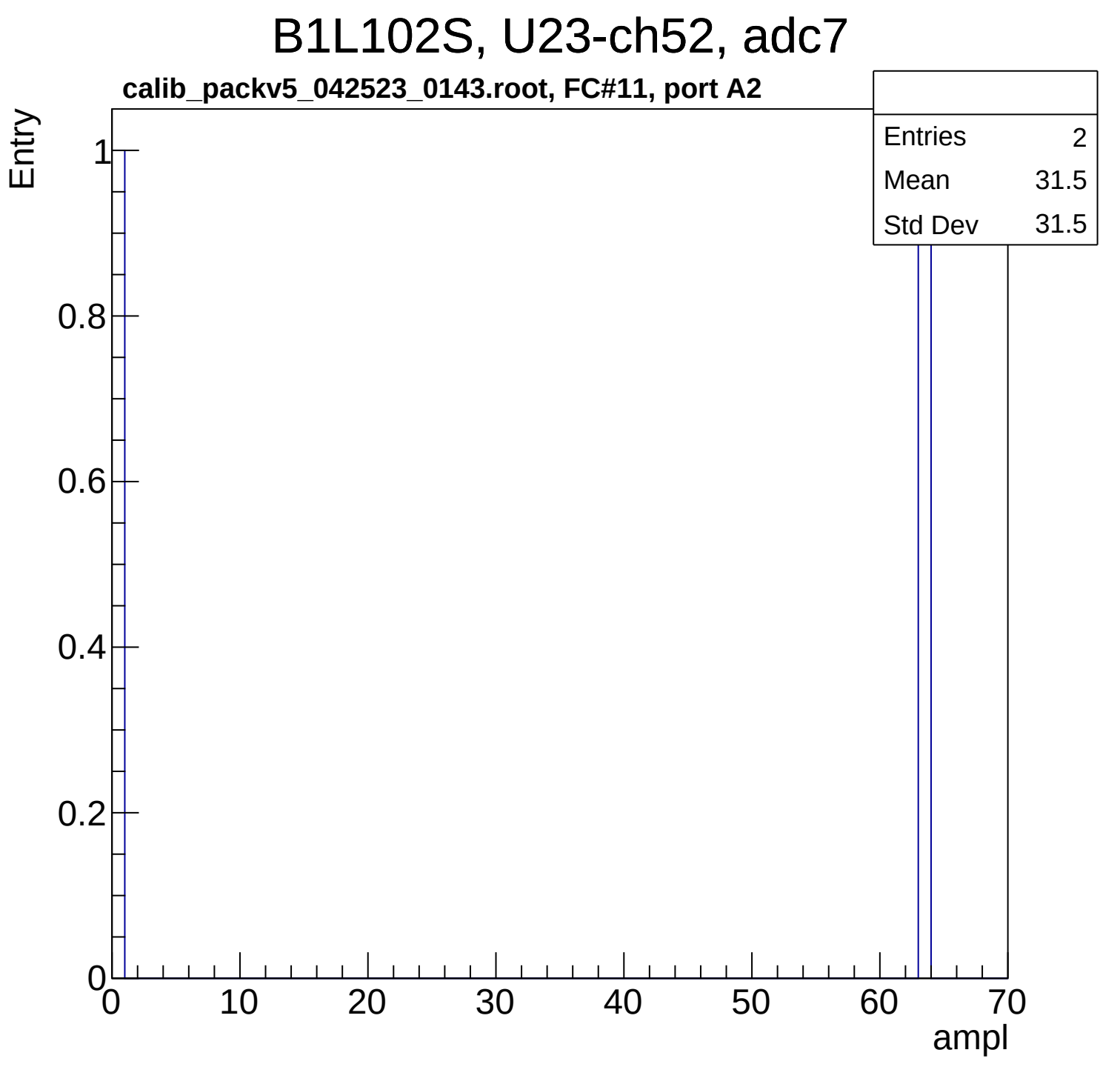

B1L102S, U23-ch52, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 41.03
Std Dev 3.551
Gaus mean : 40.6356
Gaus Width: 3.5761
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

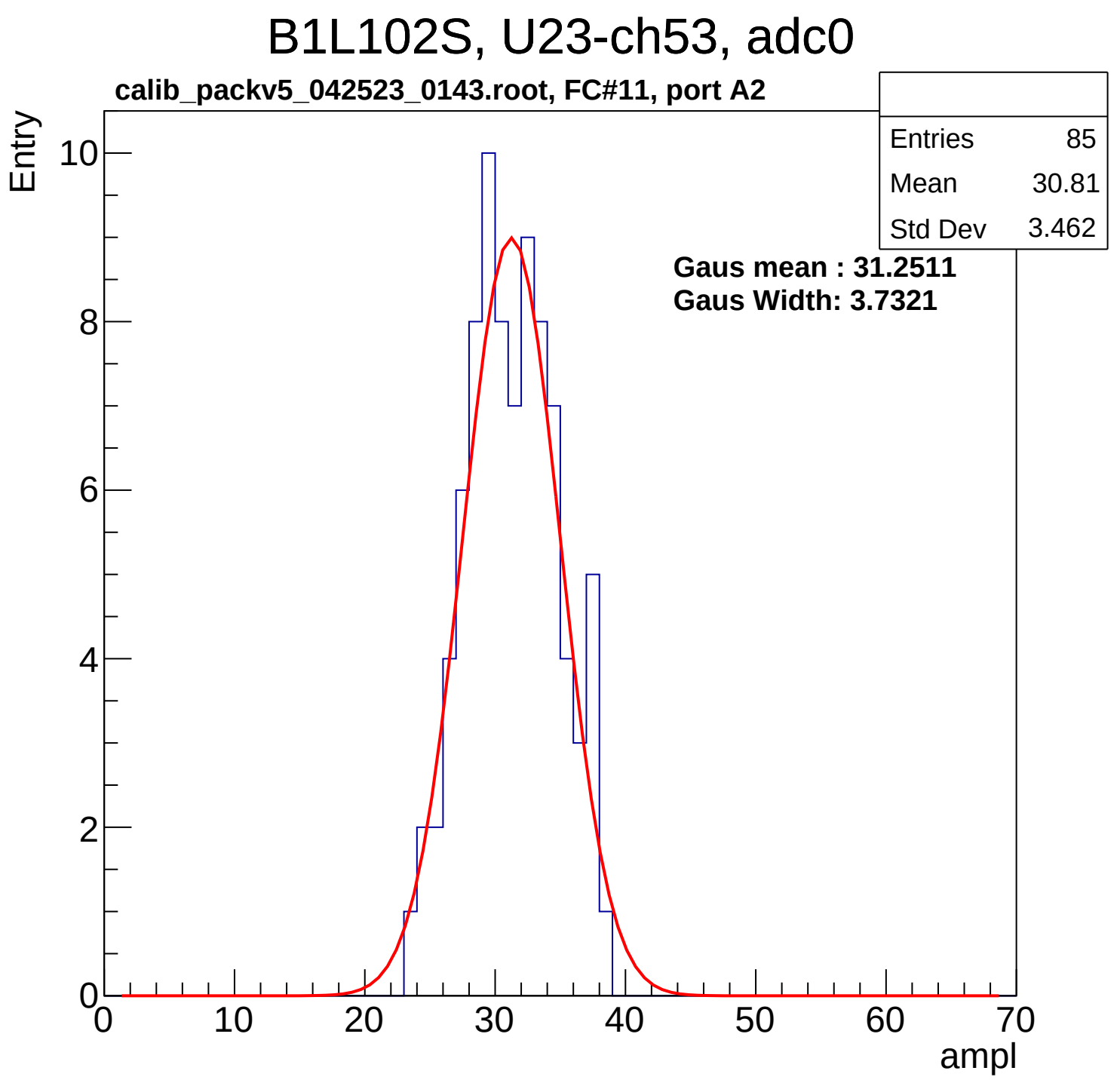

B1L102S, U23-ch52, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 46.33
Std Dev 3.802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

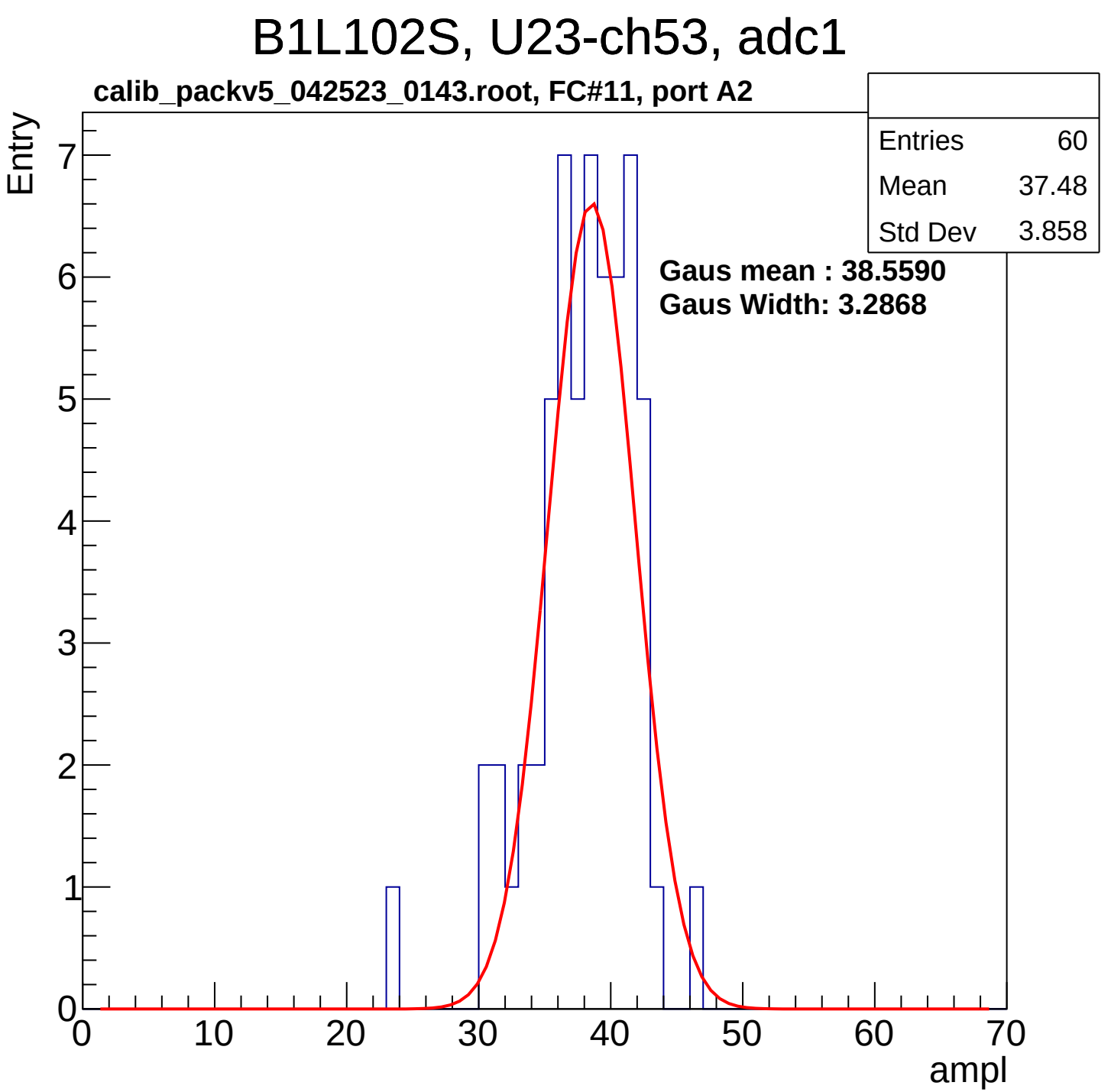

B1L102S, U23-ch52, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 52.61
Std Dev 3.868
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

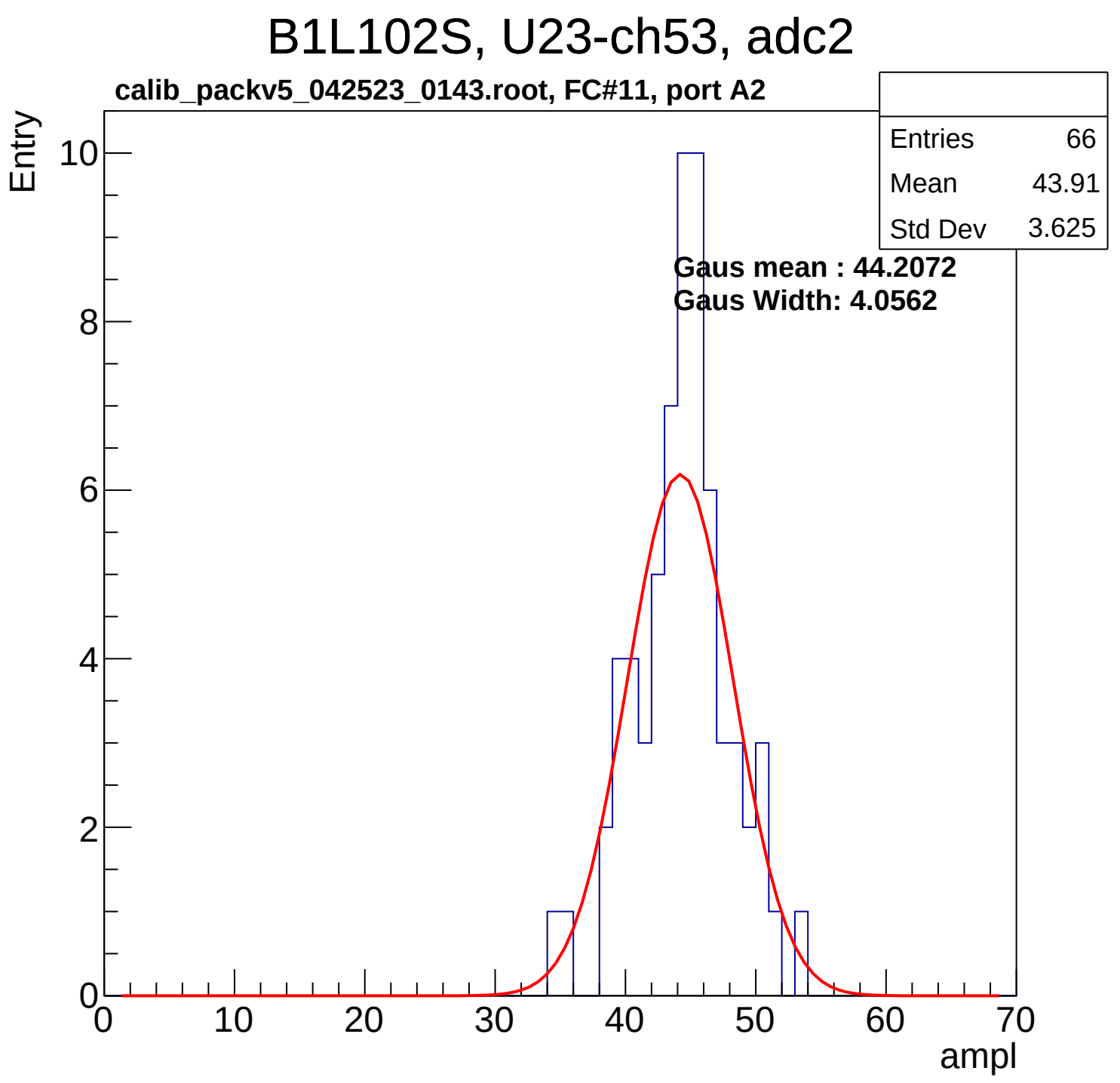

B1L102S, U23-ch52, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 58.61
Std Dev 2.551
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

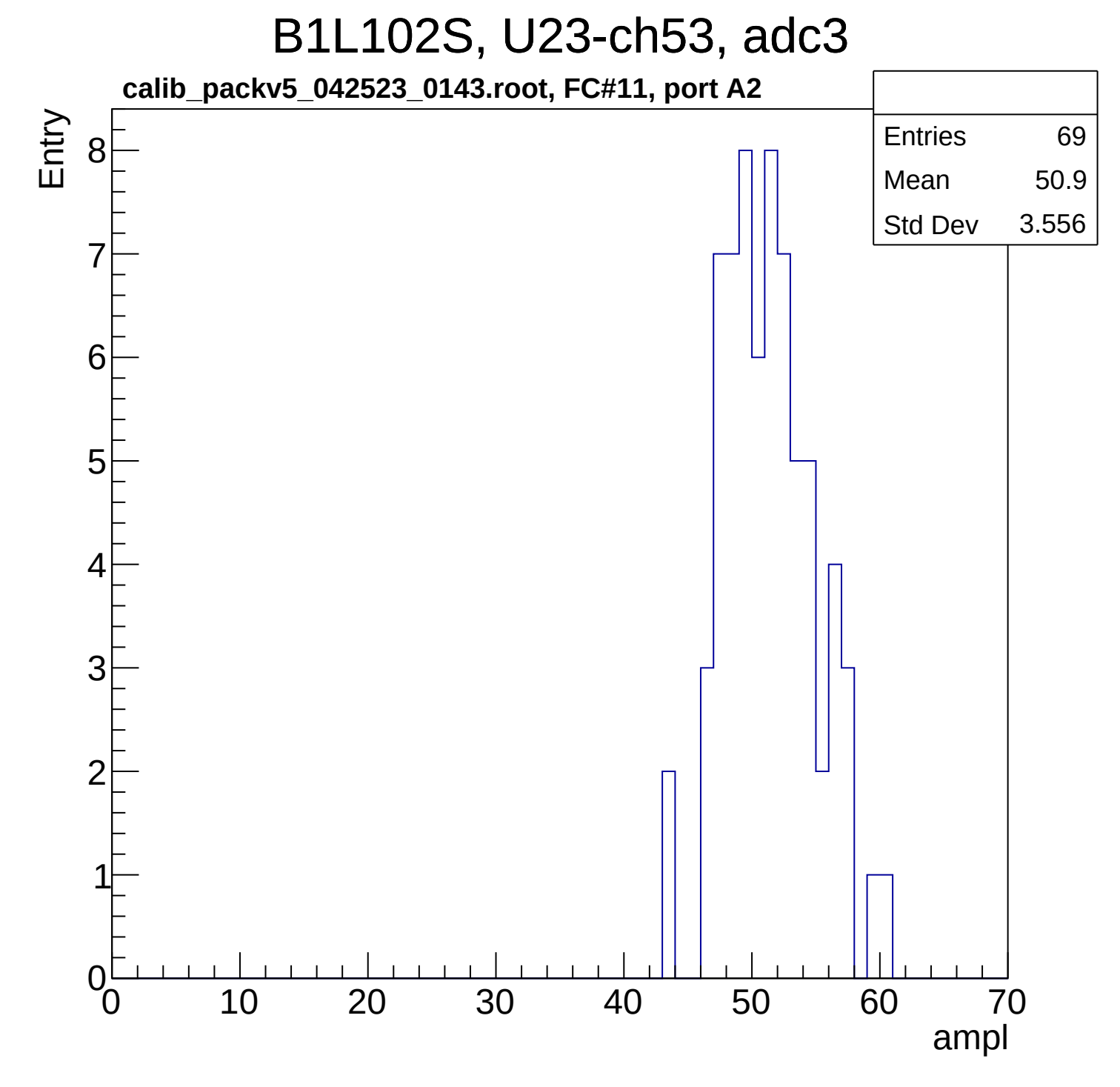

B1L102S, U23-ch52, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 57.54
Std Dev 16.63
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

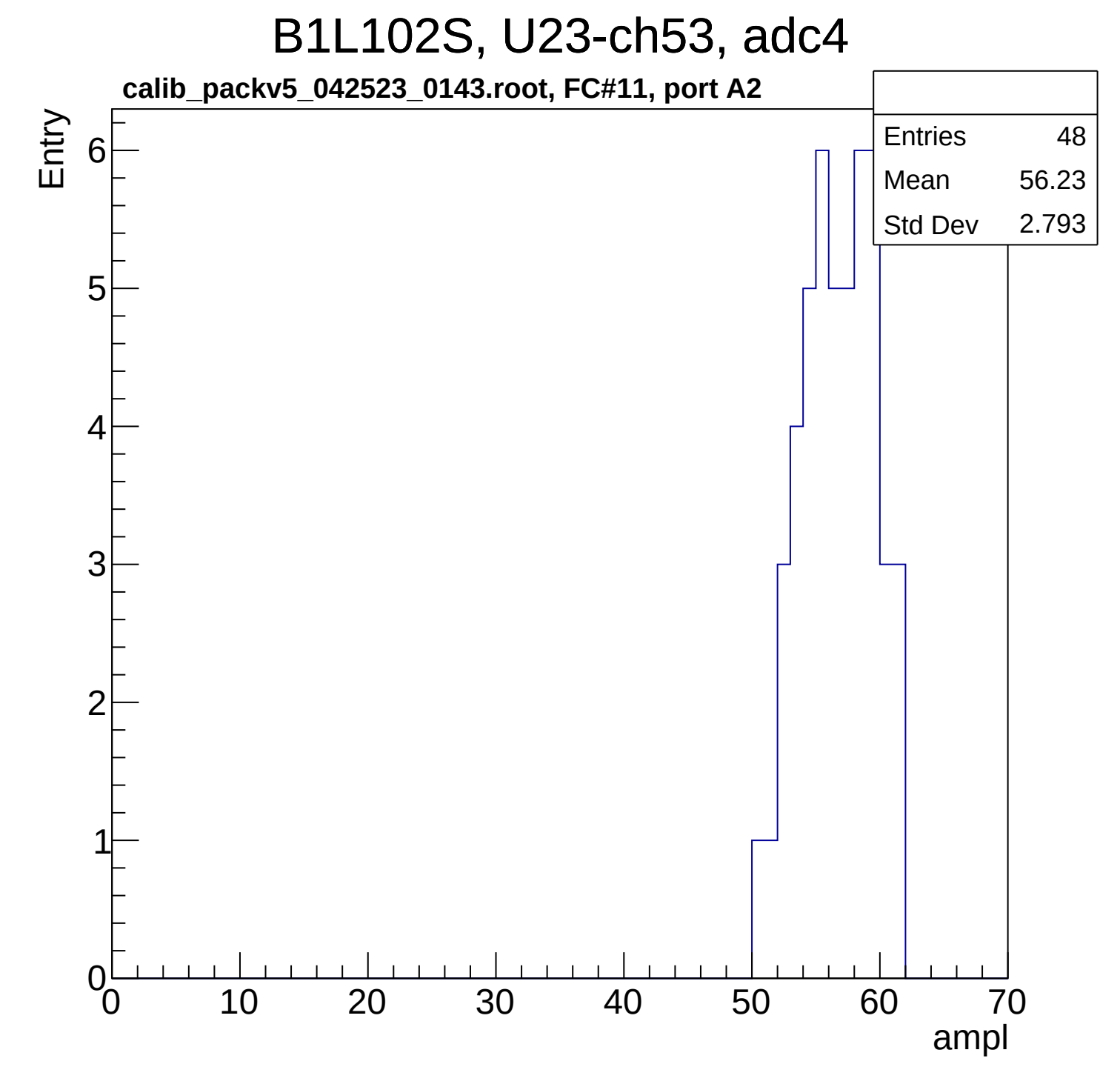

B1L102S, U23-ch52, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

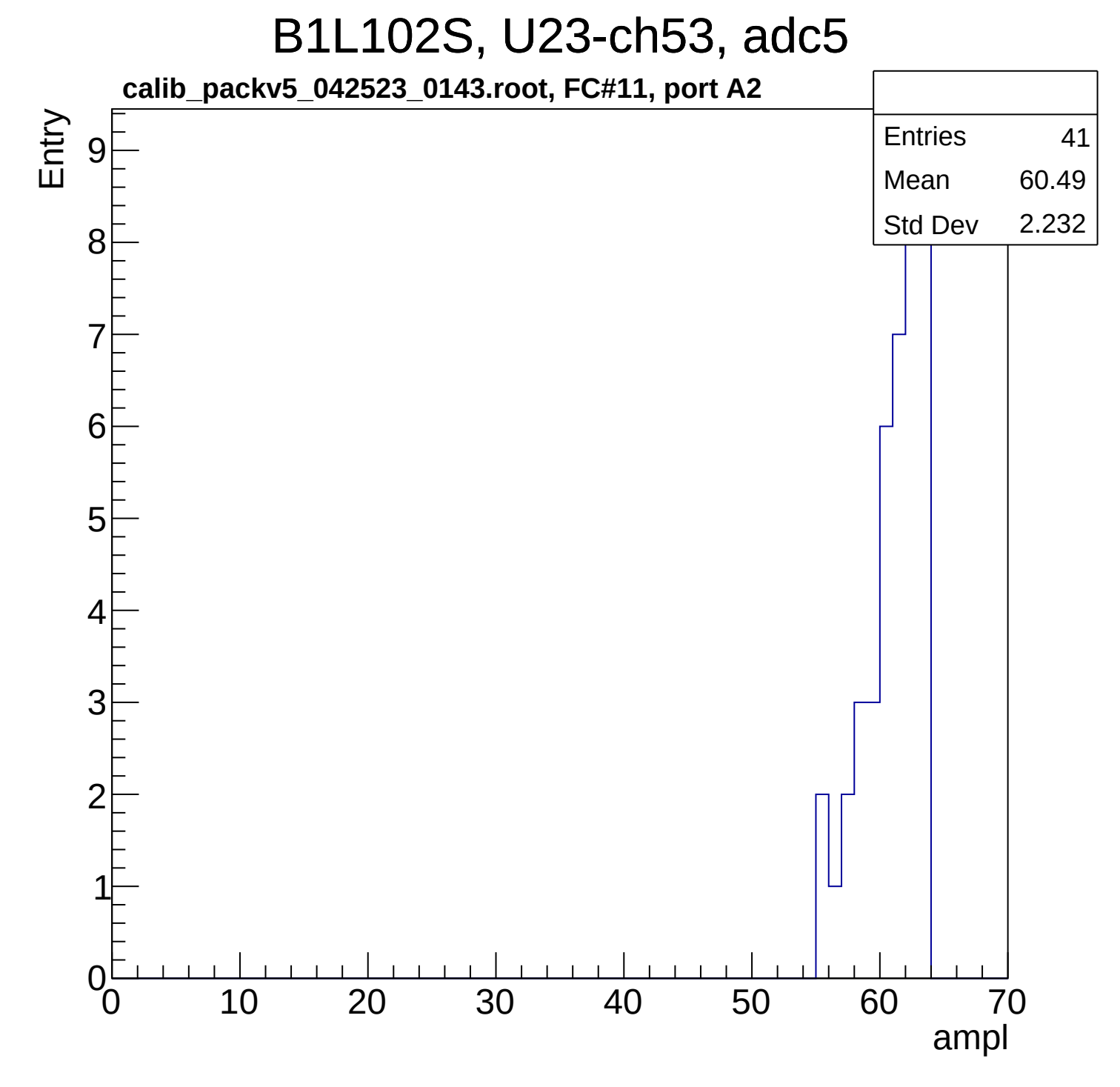

B1L102S, U23-ch53, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 30.81
Std Dev 3.462
Gaus mean : 31.2511
Gaus Width: 3.7321
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

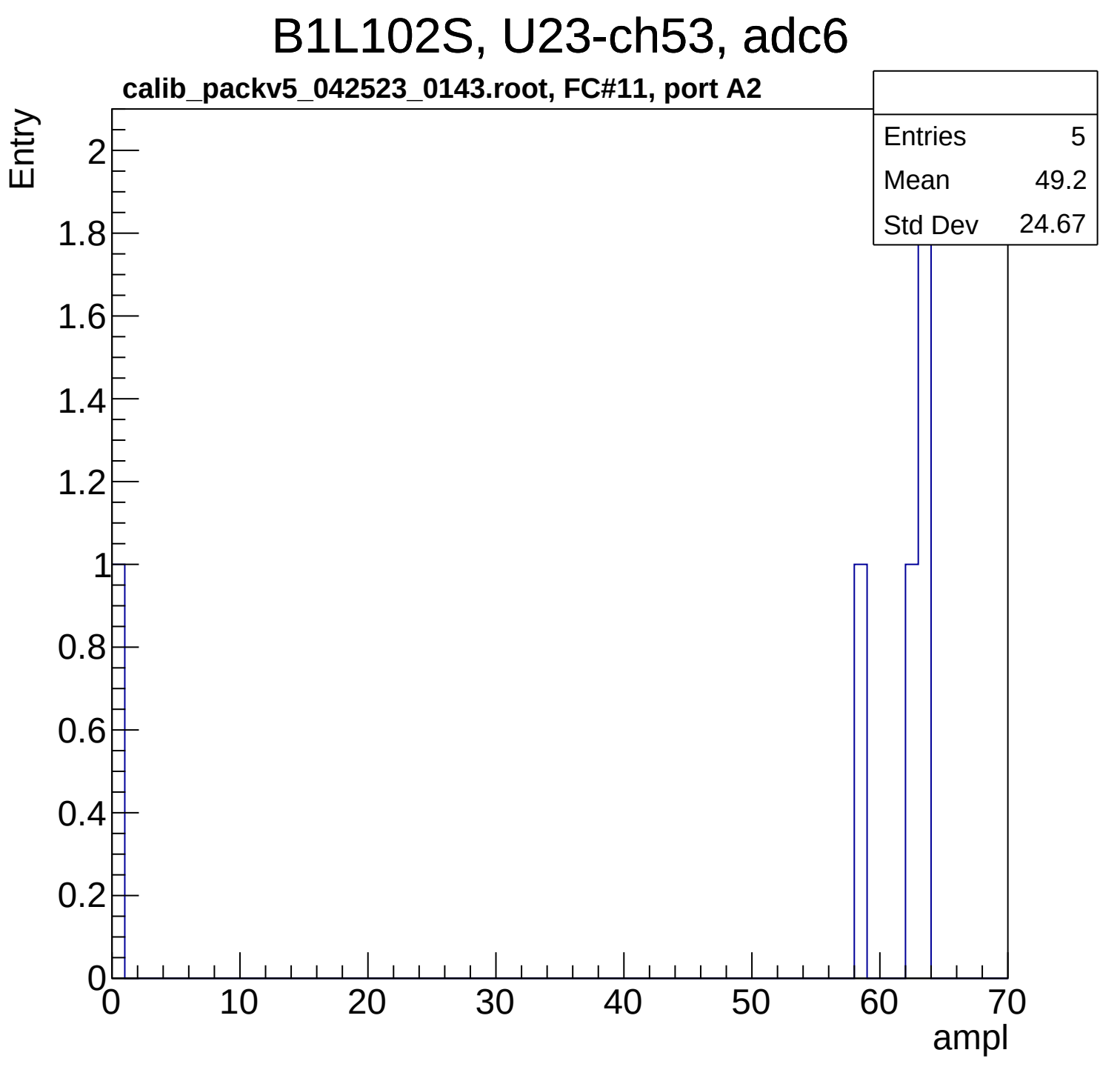

B1L102S, U23-ch53, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 37.48
Std Dev 3.858
Gaus mean : 38.5590
Gaus Width: 3.2868
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

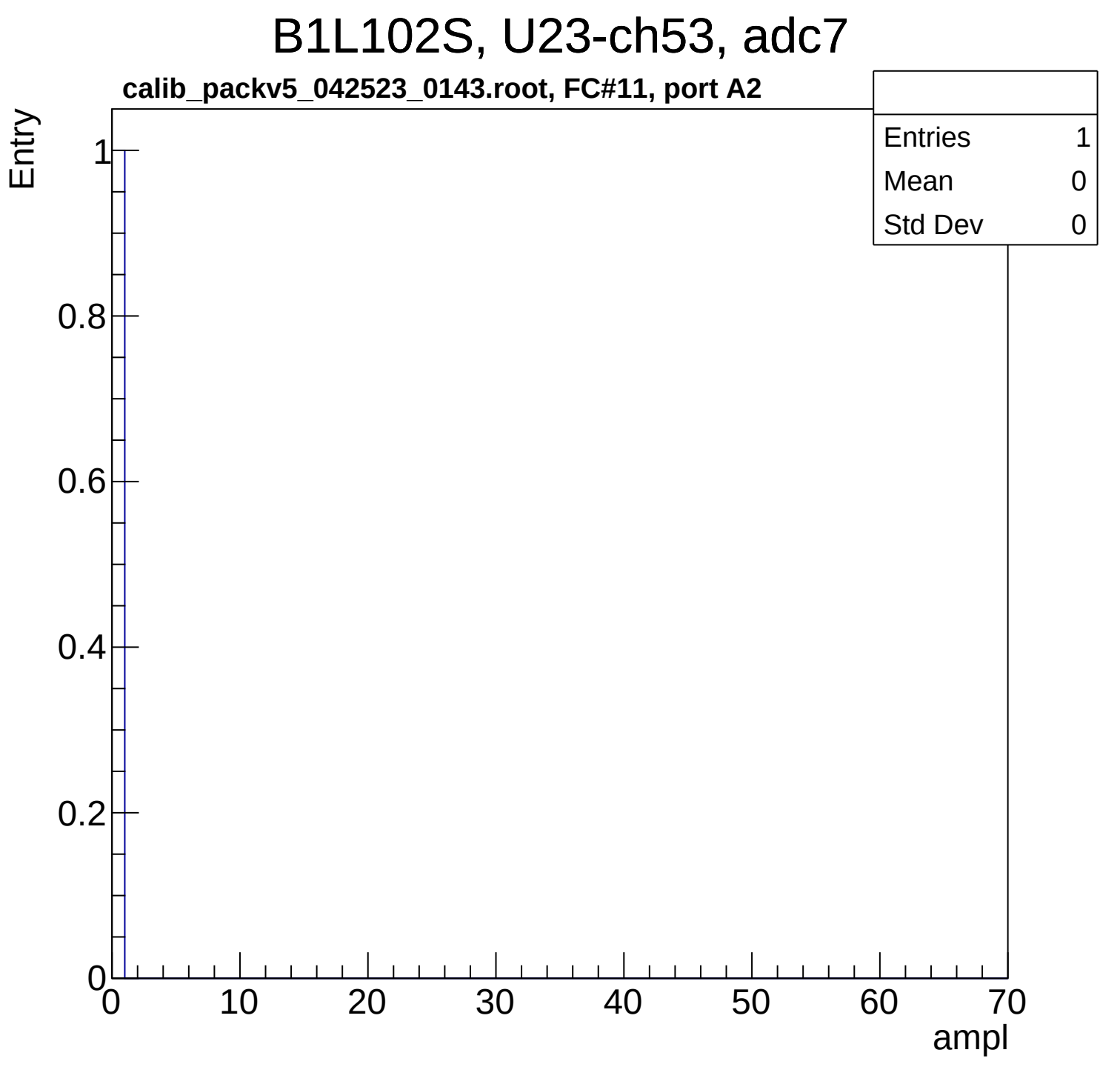

B1L102S, U23-ch53, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 43.91
Std Dev 3.625
Gaus mean : 44.2072
Gaus Width: 4.0562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

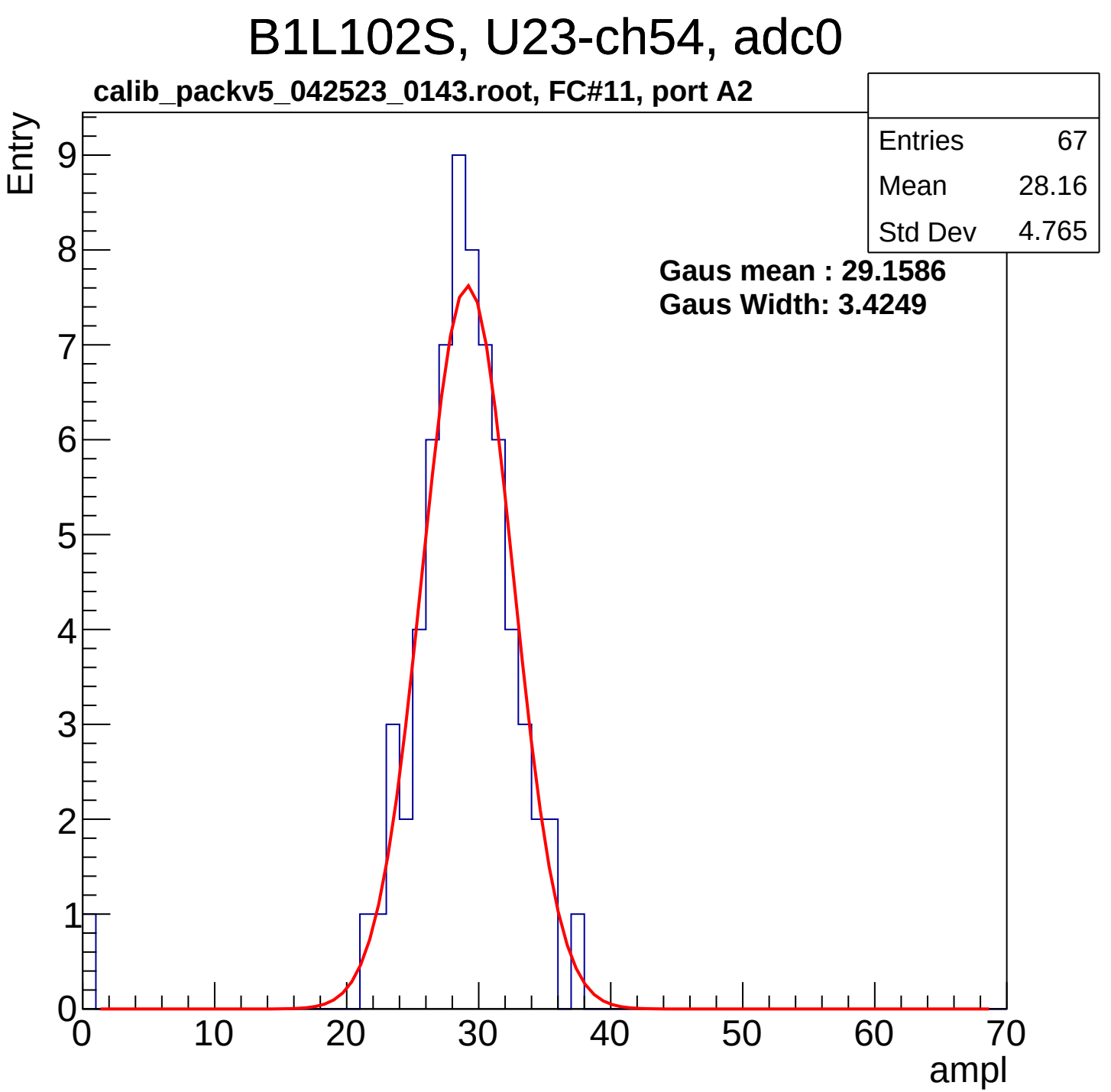

B1L102S, U23-ch53, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 50.9
Std Dev 3.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

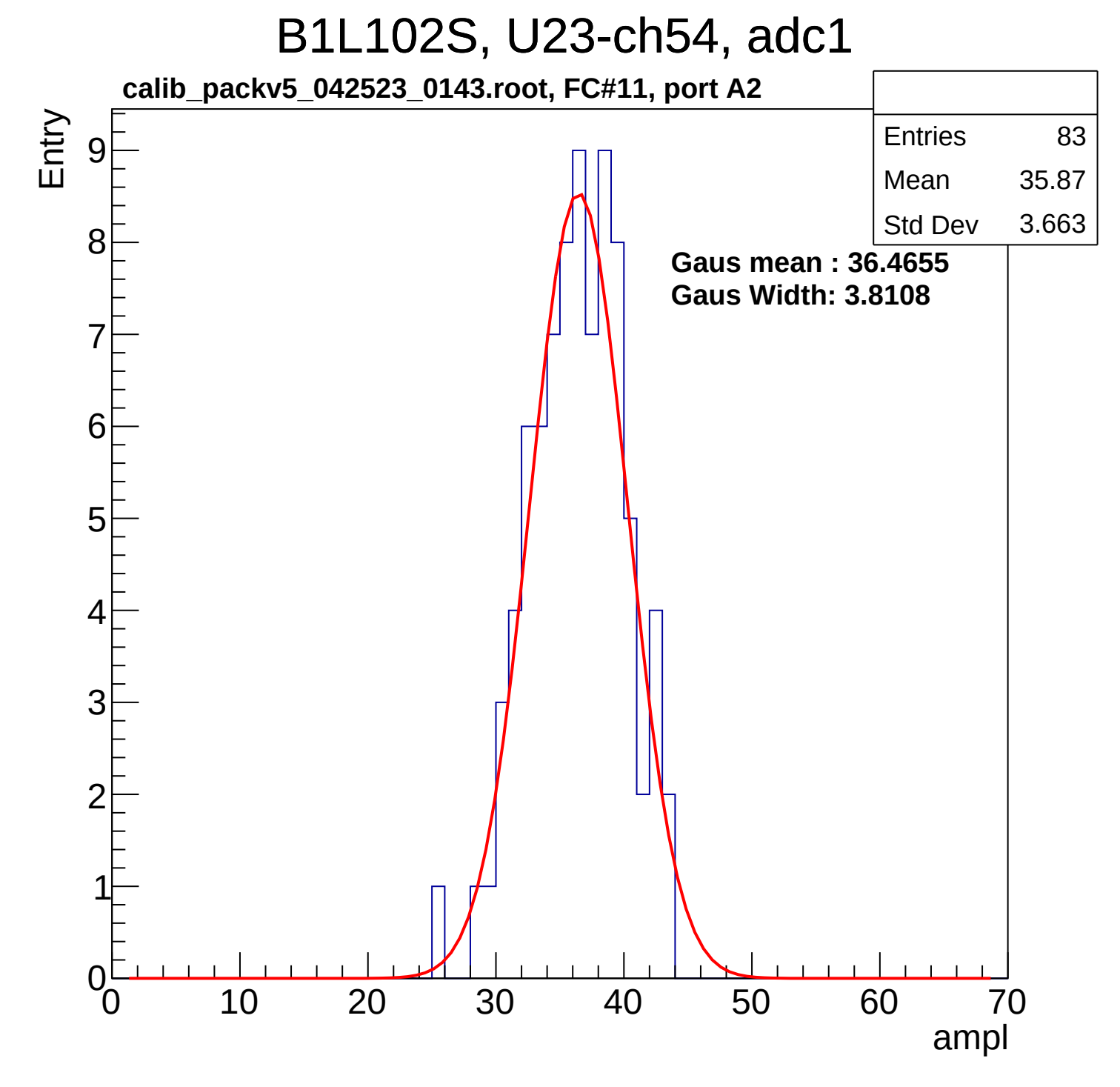

B1L102S, U23-ch53, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 56.23
Std Dev 2.793
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

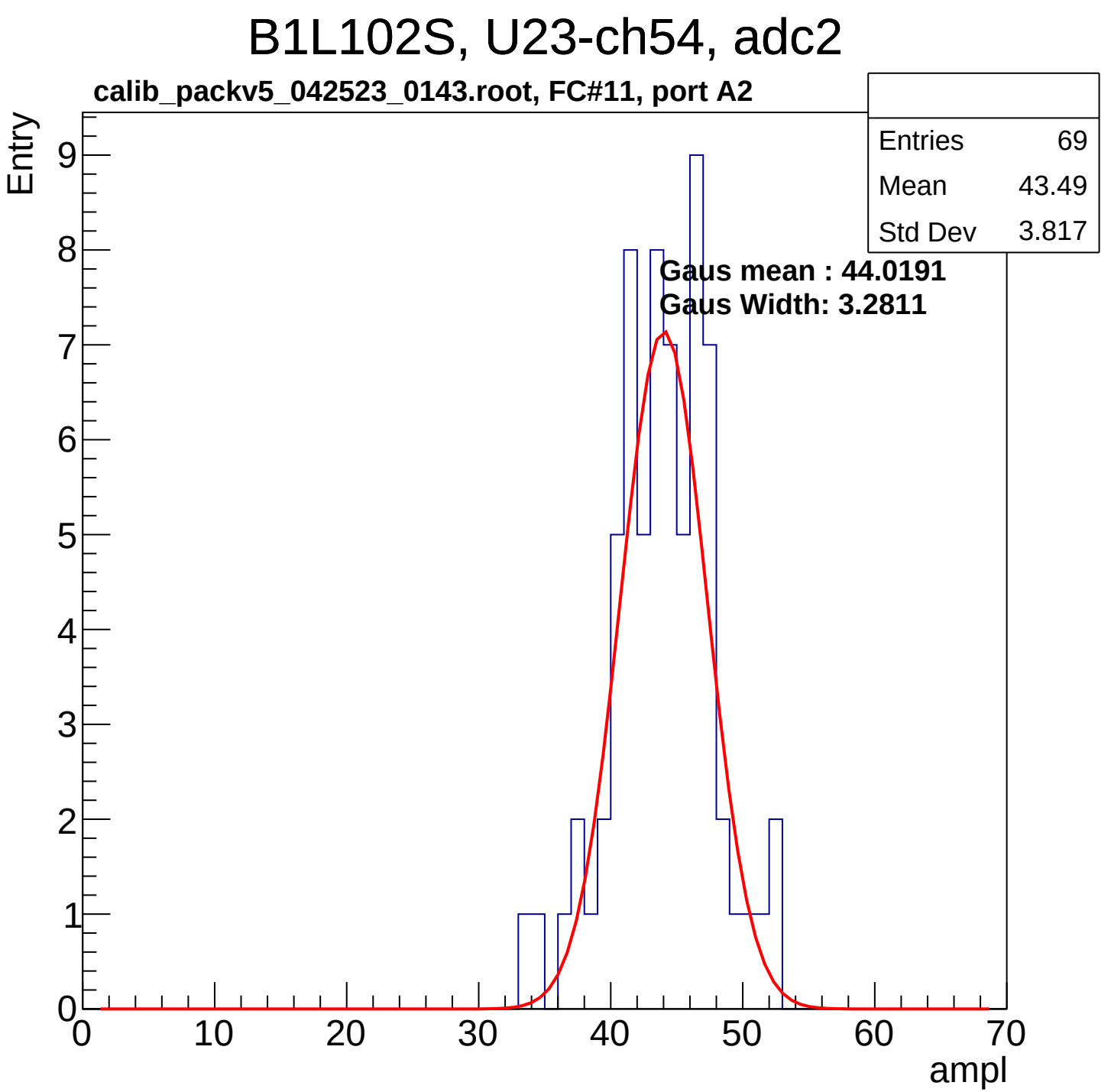

B1L102S, U23-ch53, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 60.49
Std Dev 2.232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

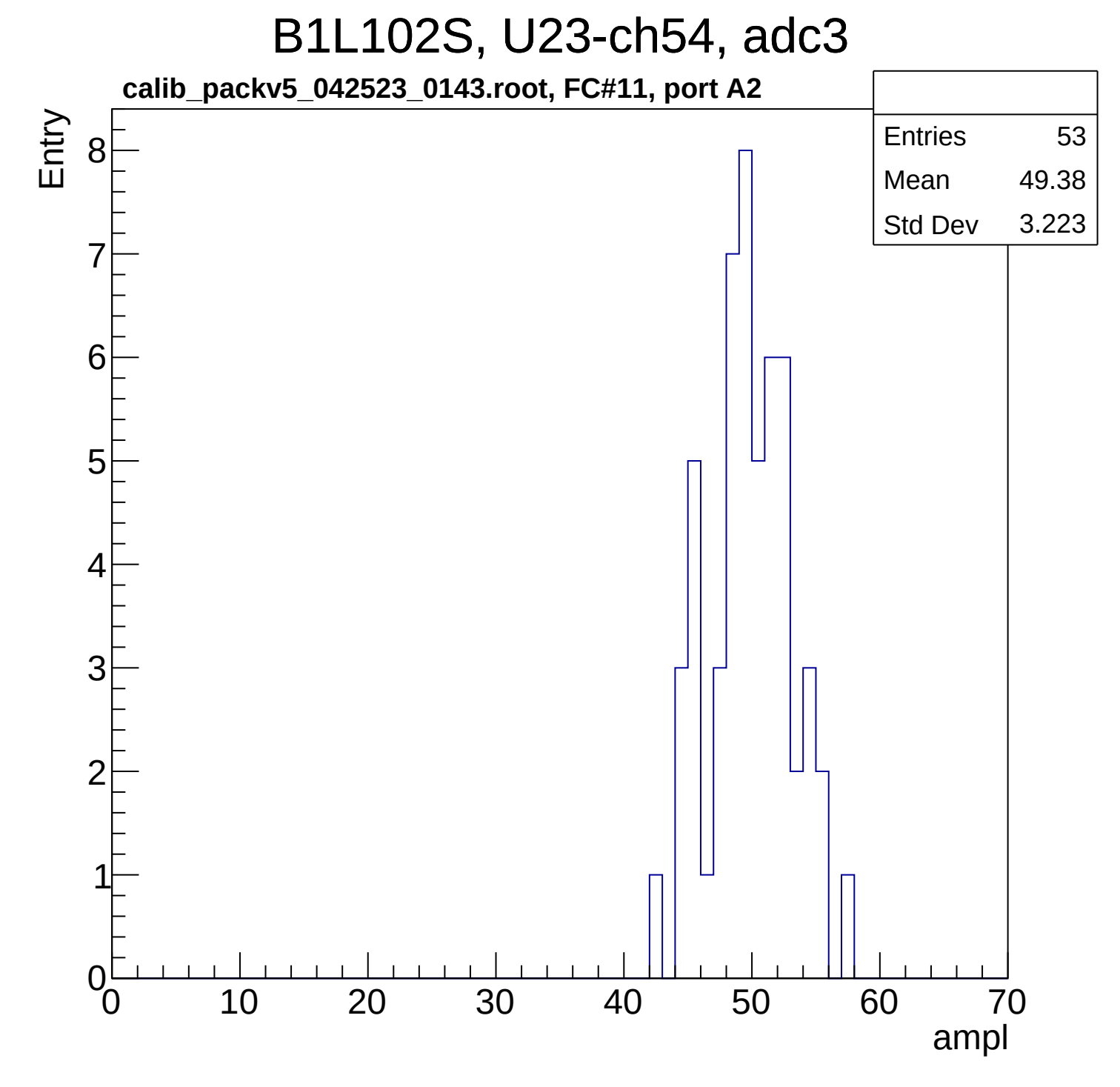

B1L102S, U23-ch53, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 49.2
Std Dev 24.67
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

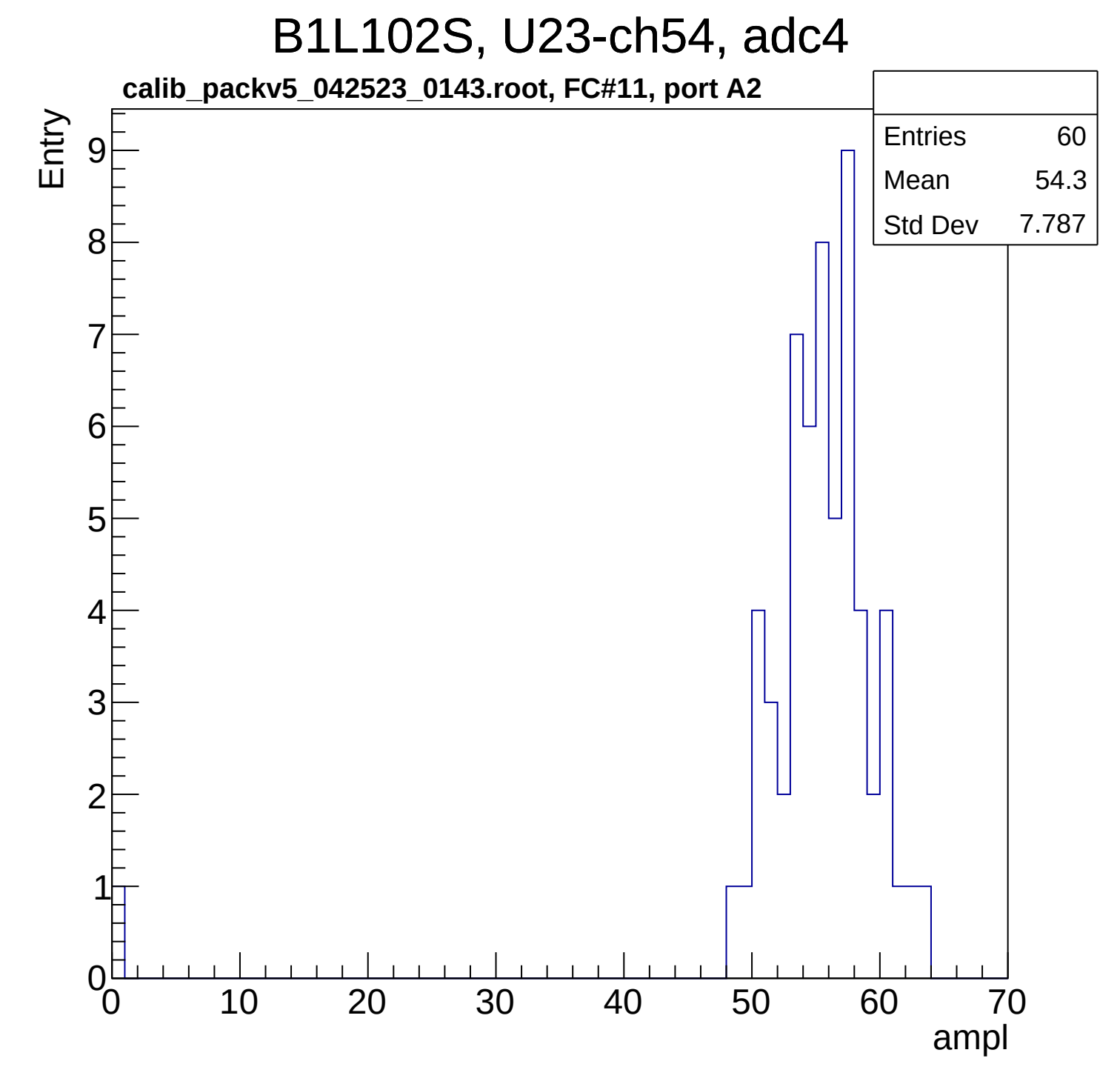

B1L102S, U23-ch53, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

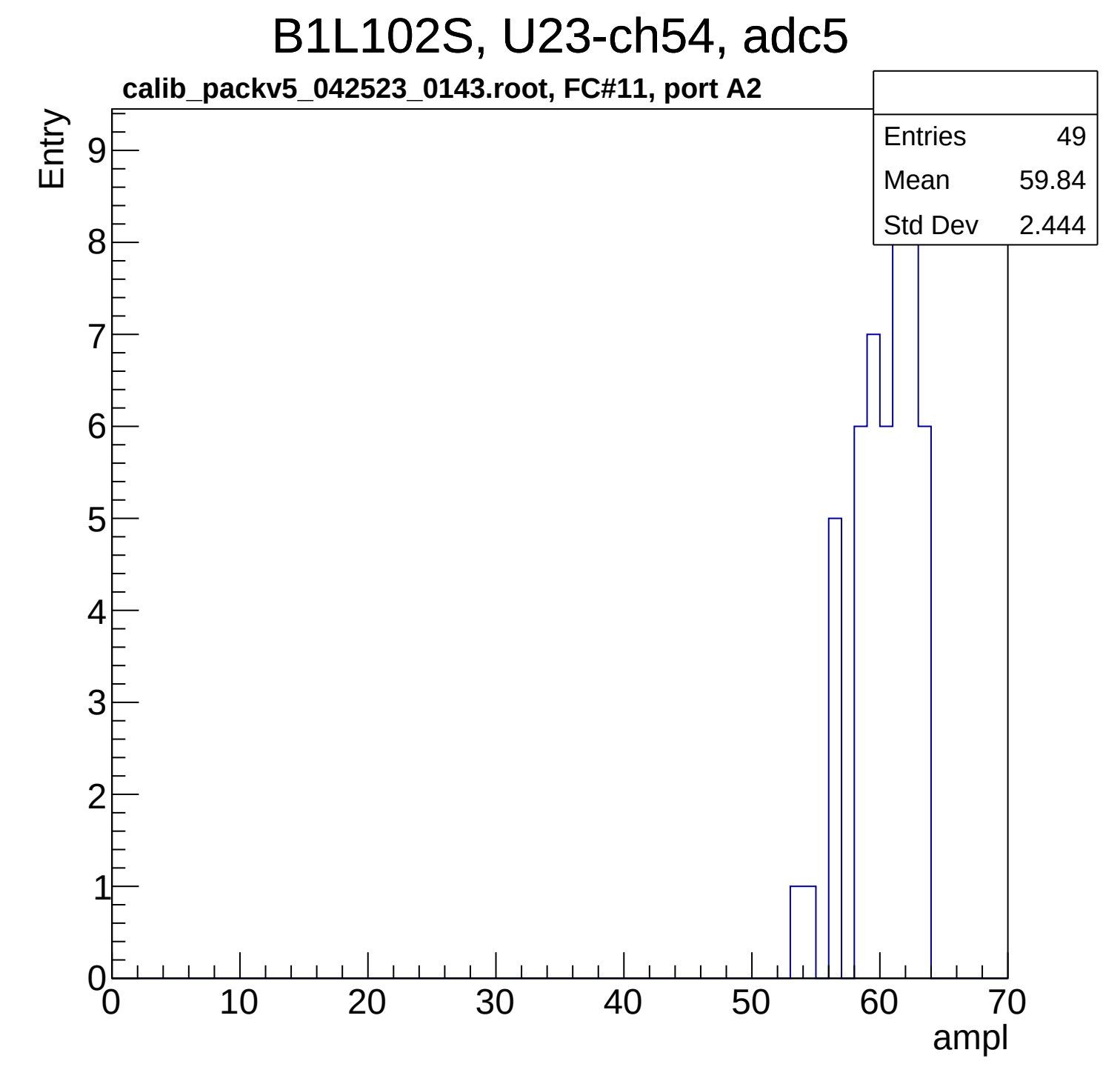

B1L102S, U23-ch54, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 28.16
Std Dev 4.765
Gaus mean : 29.1586
Gaus Width: 3.4249
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

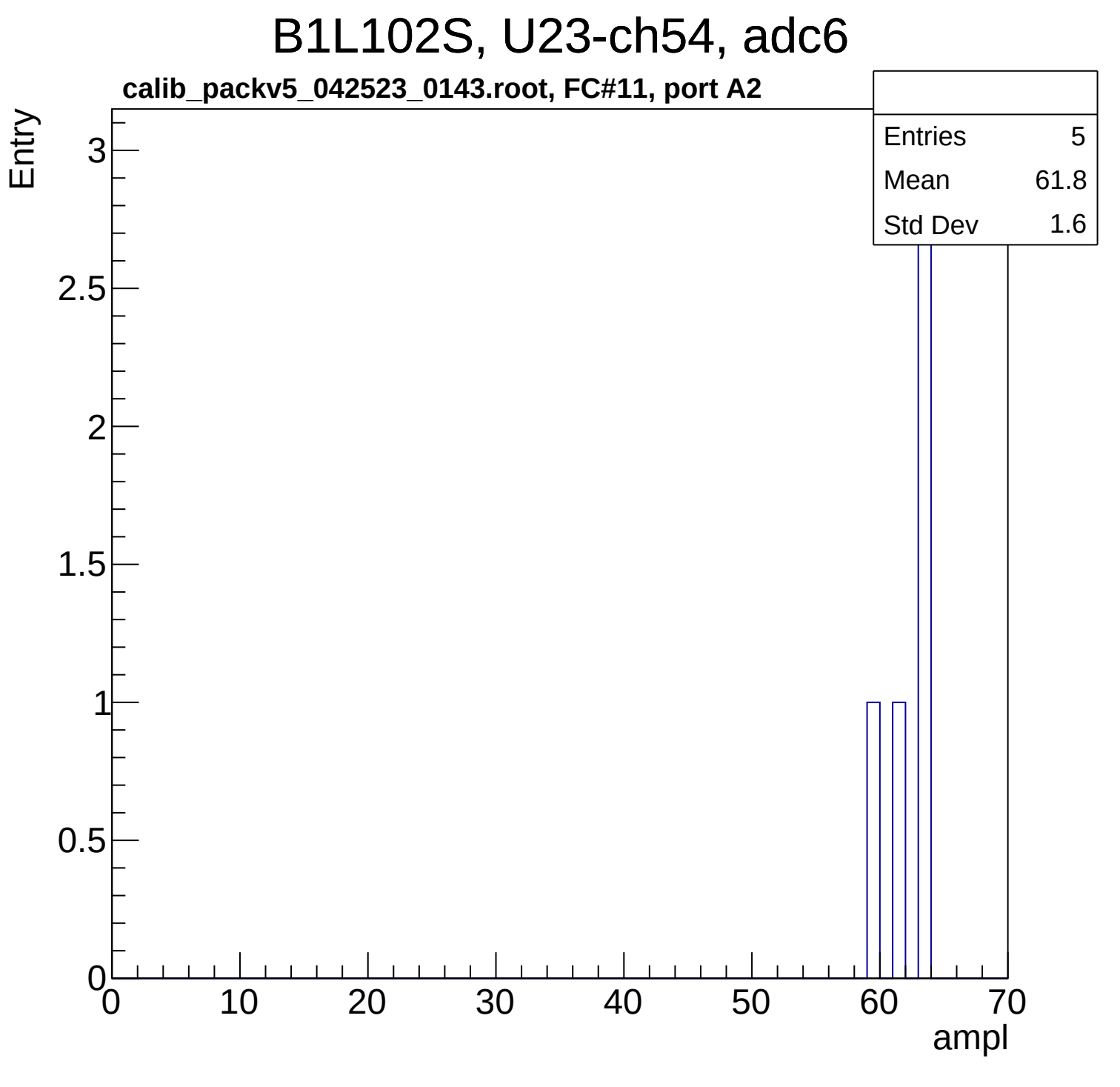

B1L102S, U23-ch54, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 35.87
Std Dev 3.663
Gaus mean : 36.4655
Gaus Width: 3.8108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

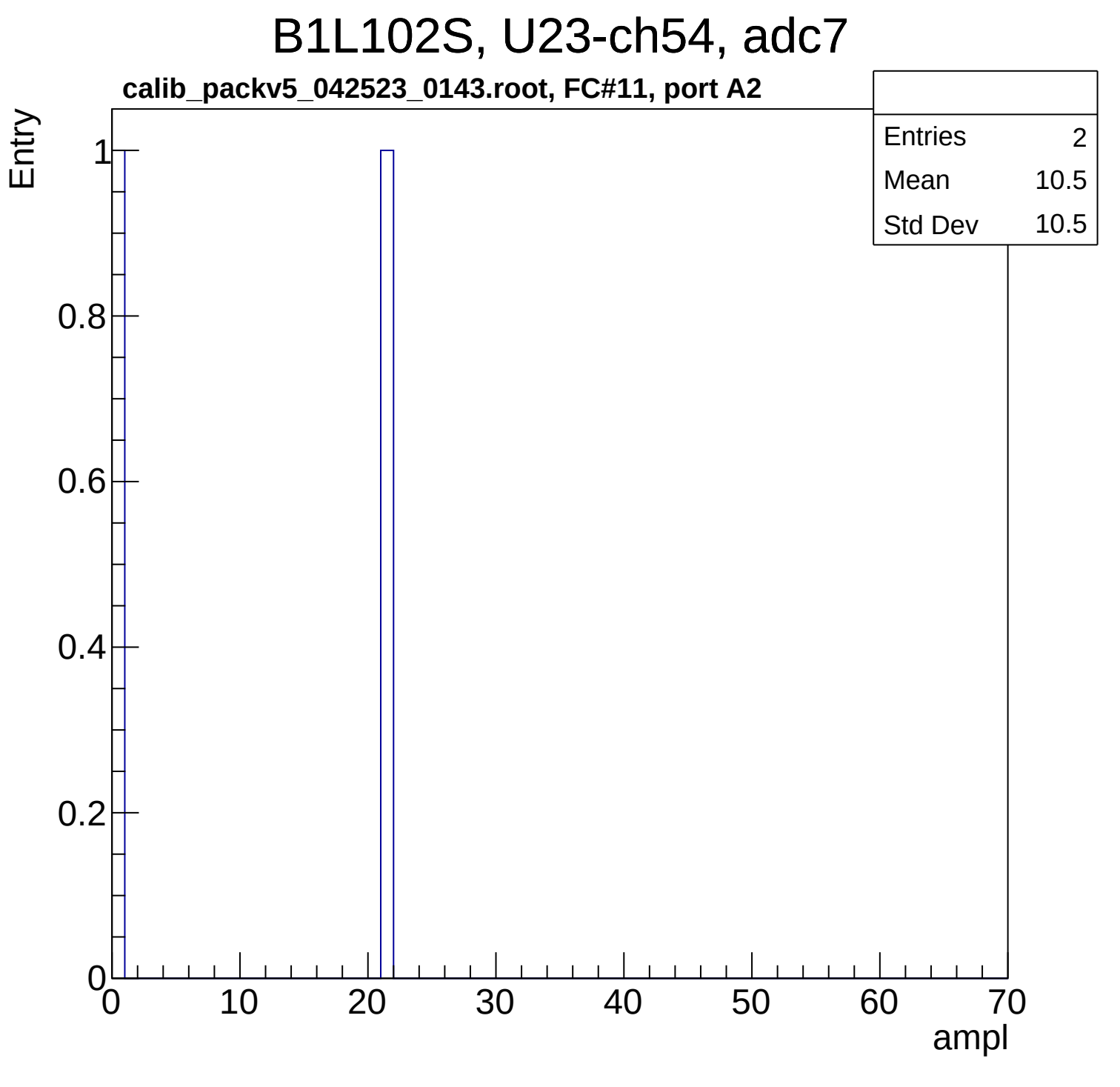

B1L102S, U23-ch54, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 43.49
Std Dev 3.817
Gaus mean : 44.0191
Gaus Width: 3.2811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

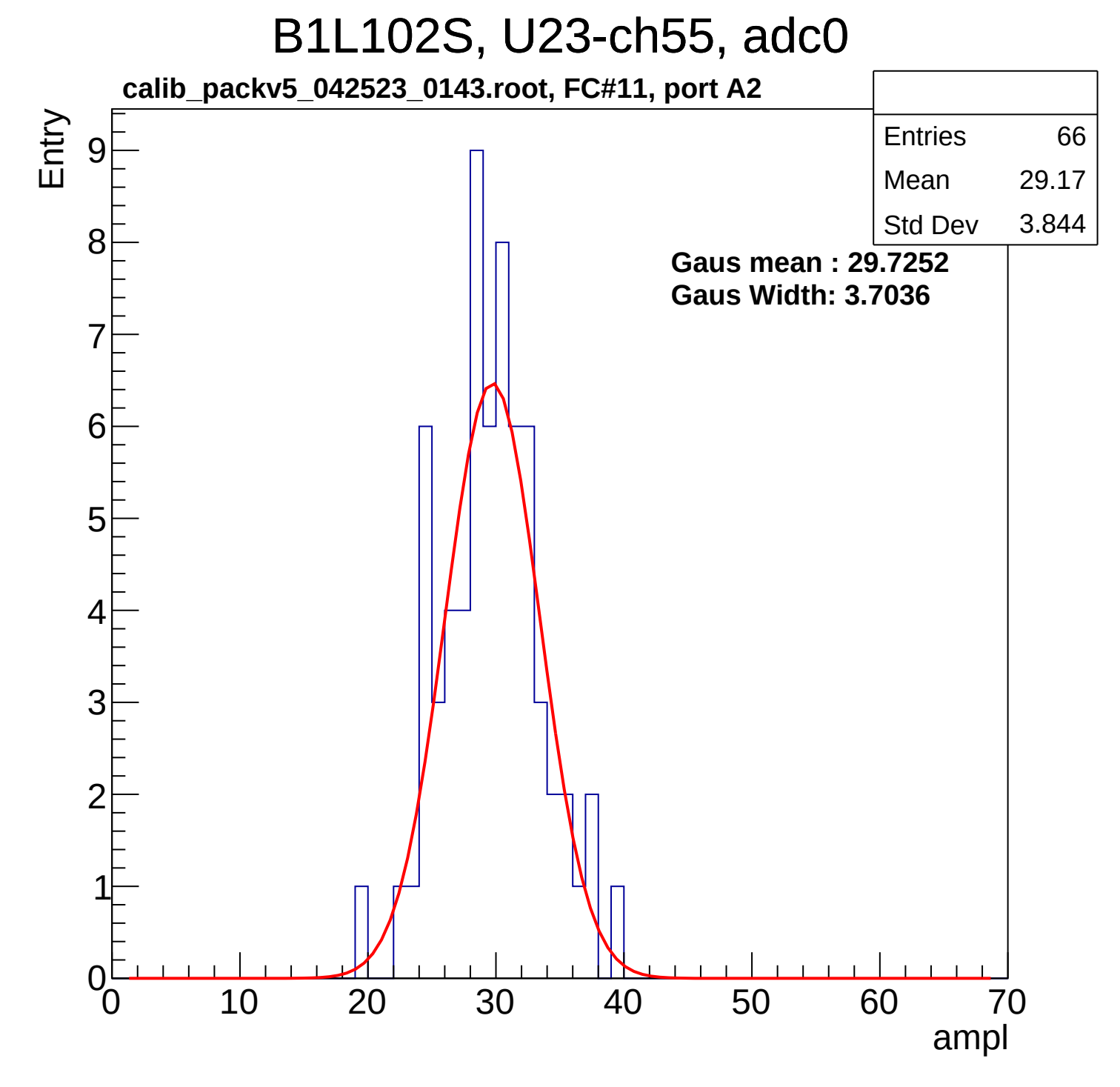

B1L102S, U23-ch54, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 49.38
Std Dev 3.223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

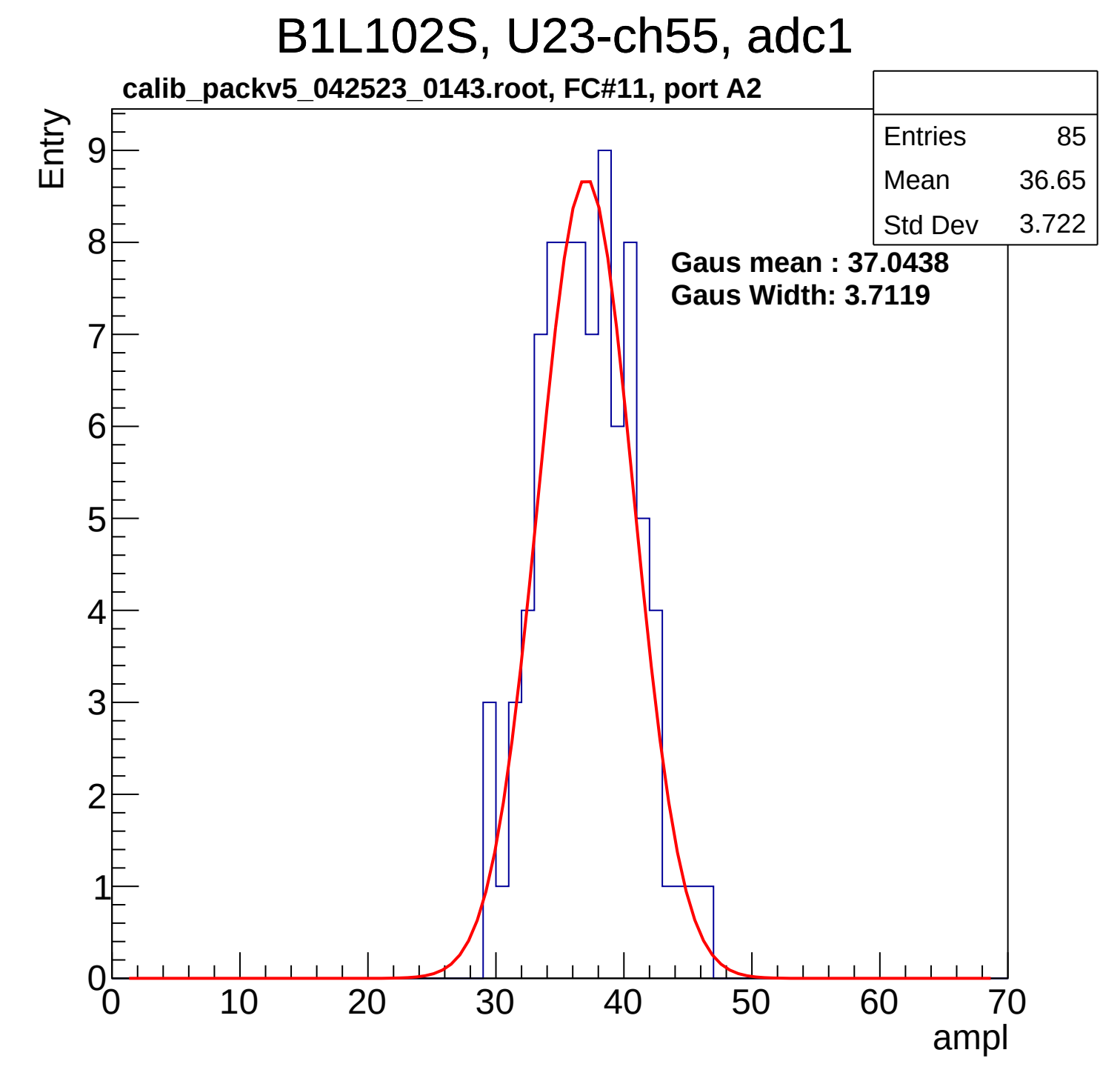

B1L102S, U23-ch54, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 54.3
Std Dev 7.787
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

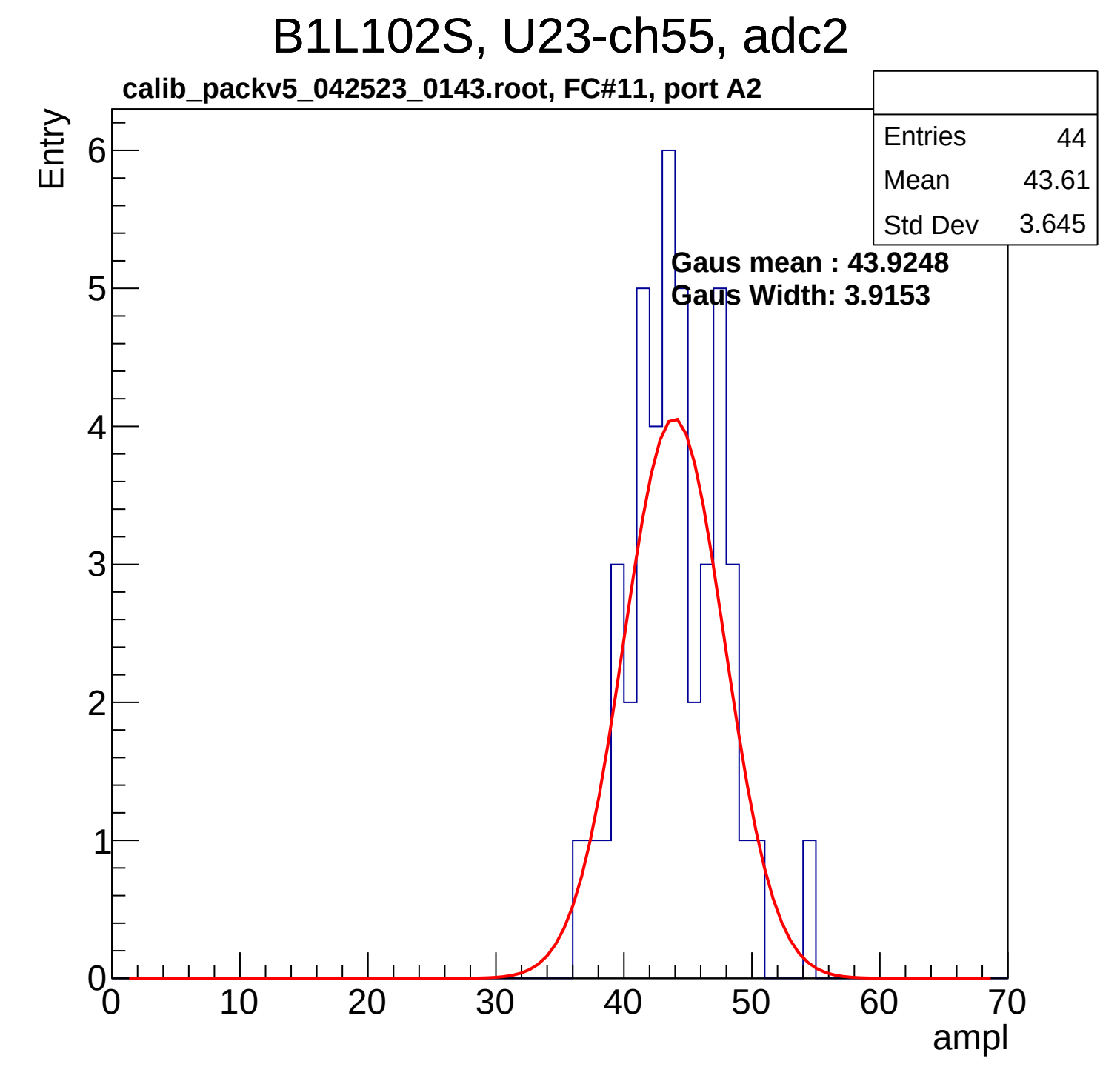

B1L102S, U23-ch54, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.84
Std Dev 2.444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

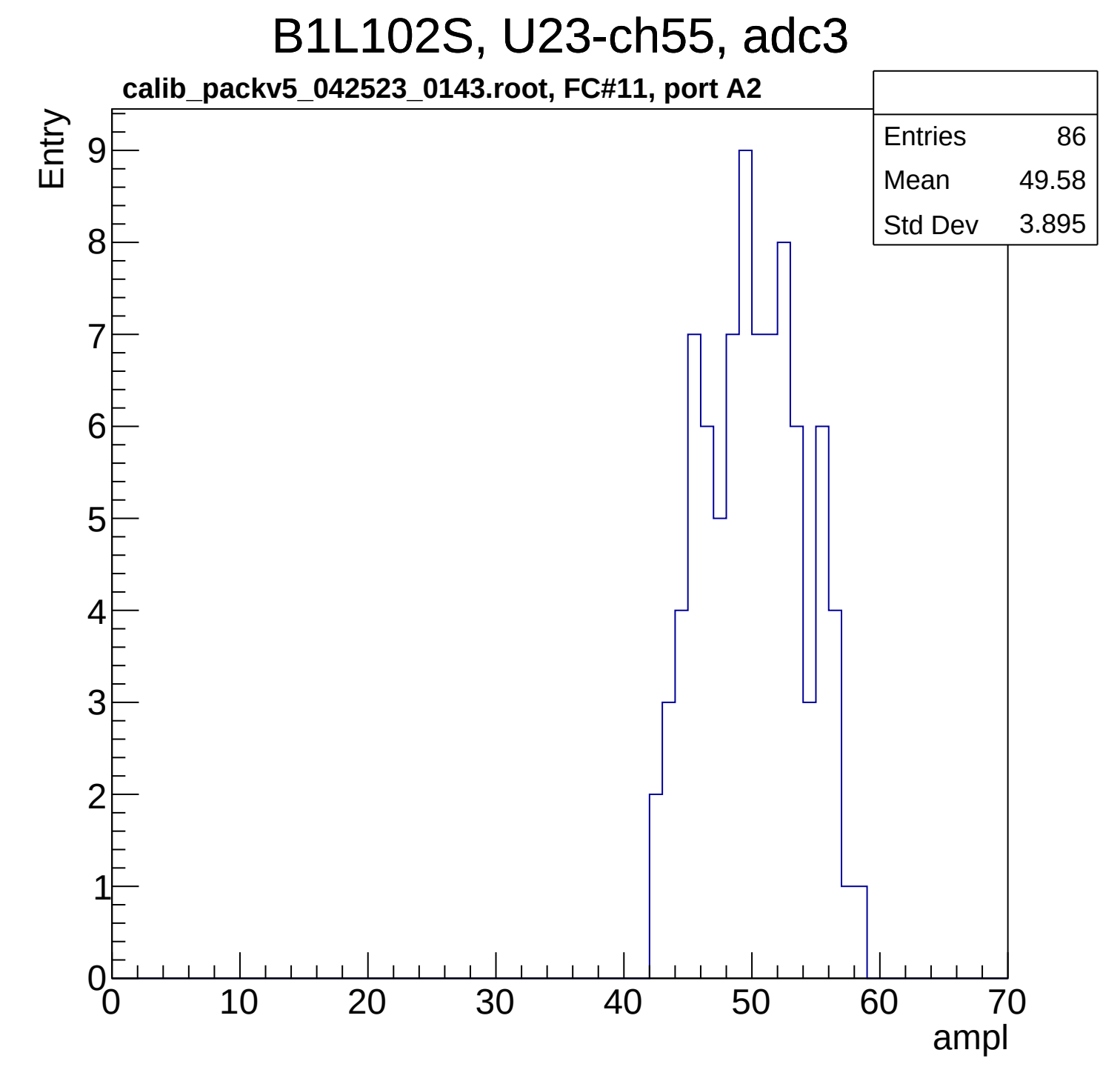

B1L102S, U23-ch54, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61.8
Std Dev 1.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

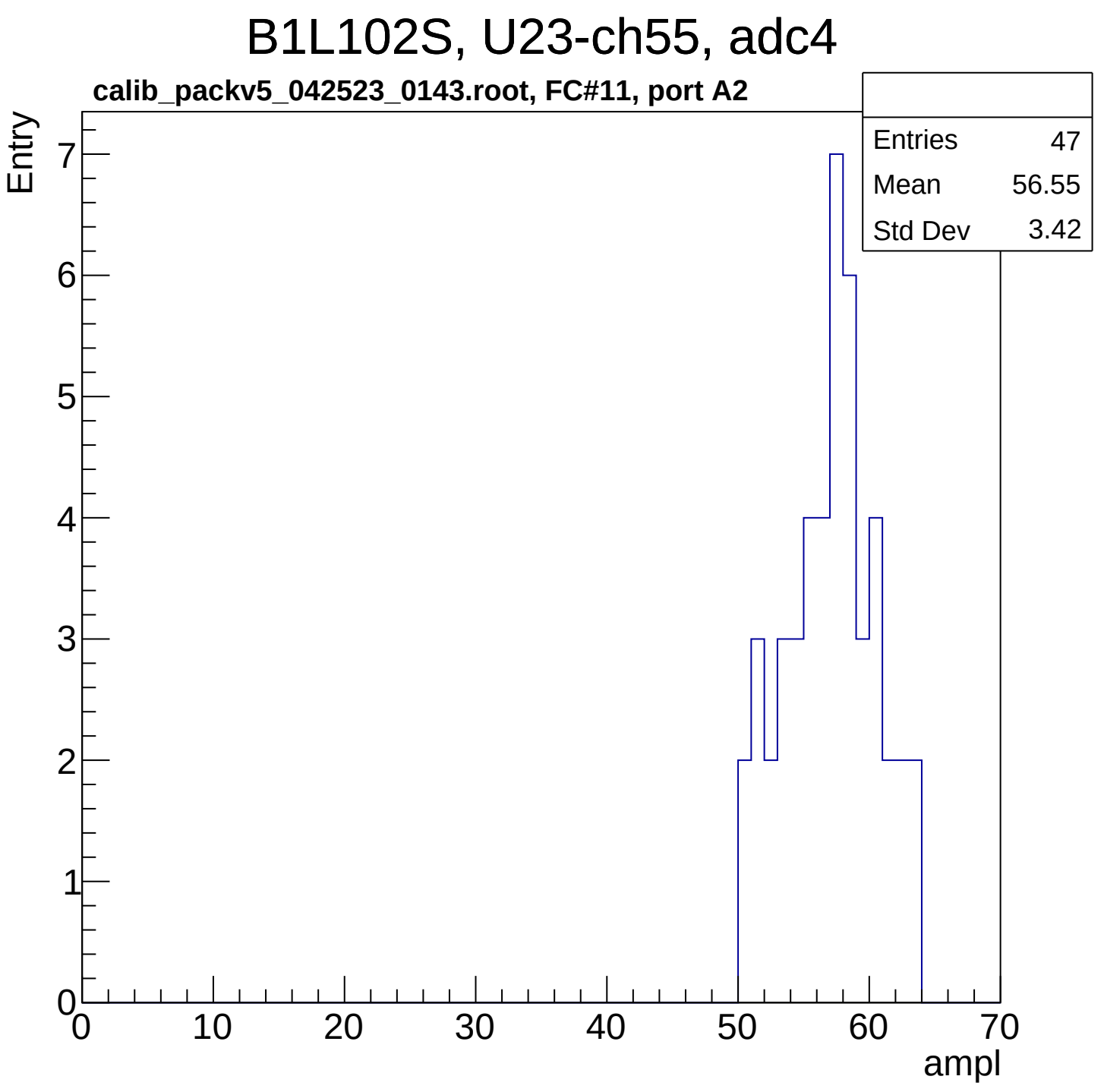

B1L102S, U23-ch54, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

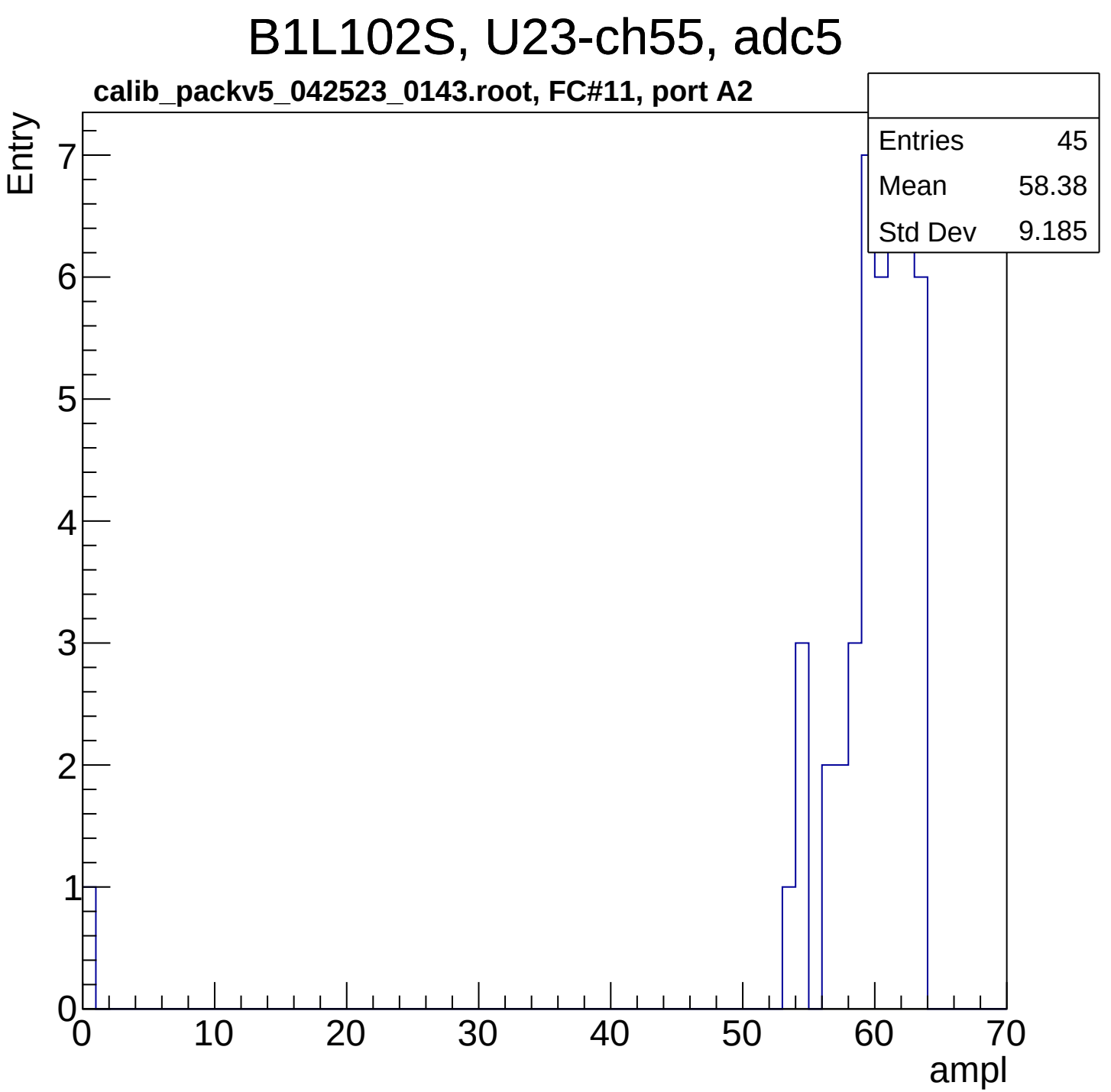

B1L102S, U23-ch55, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 29.17
Std Dev 3.844
Gaus mean : 29.7252
Gaus Width: 3.7036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

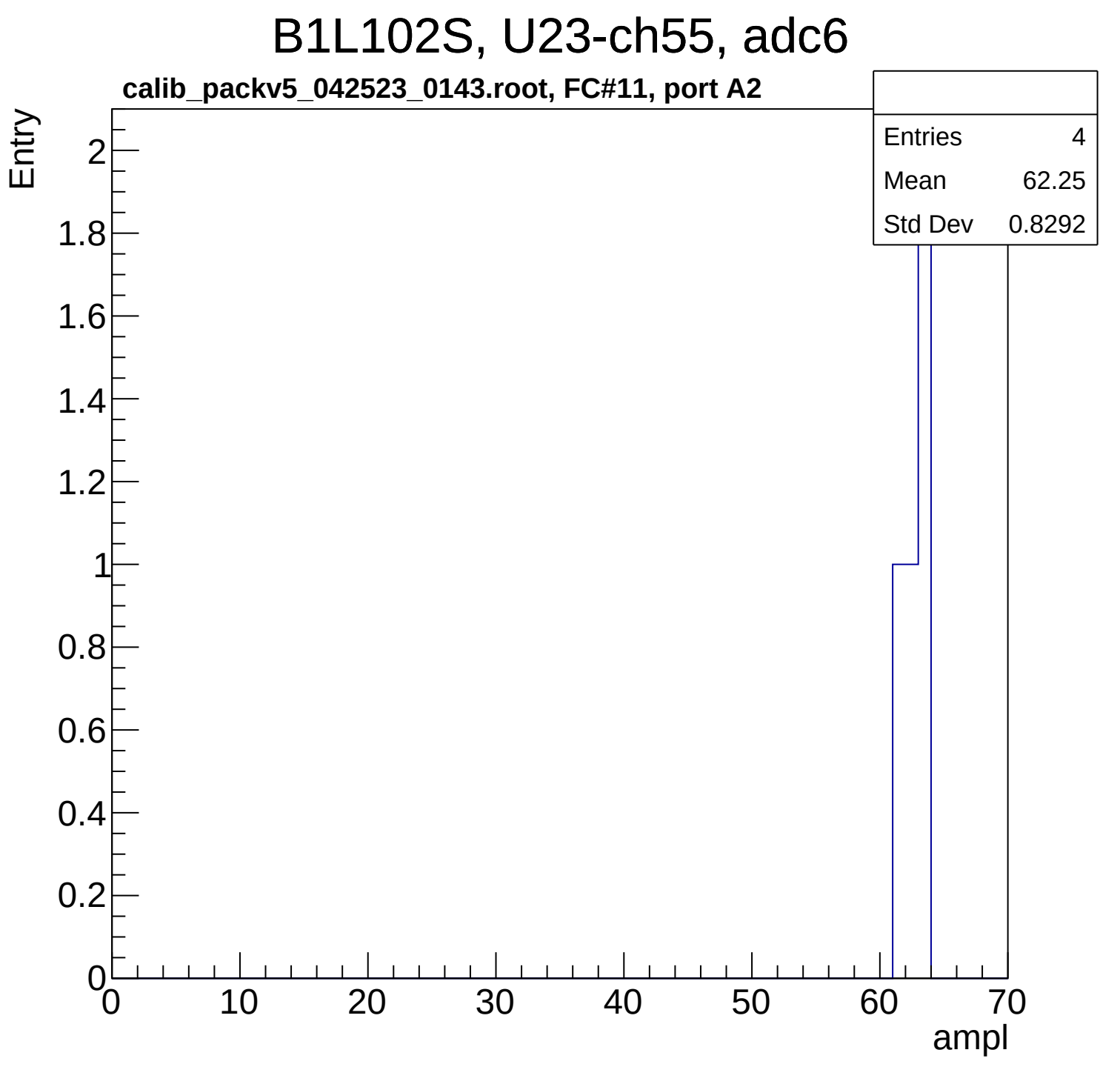

B1L102S, U23-ch55, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 36.65
Std Dev 3.722
Gaus mean : 37.0438
Gaus Width: 3.7119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

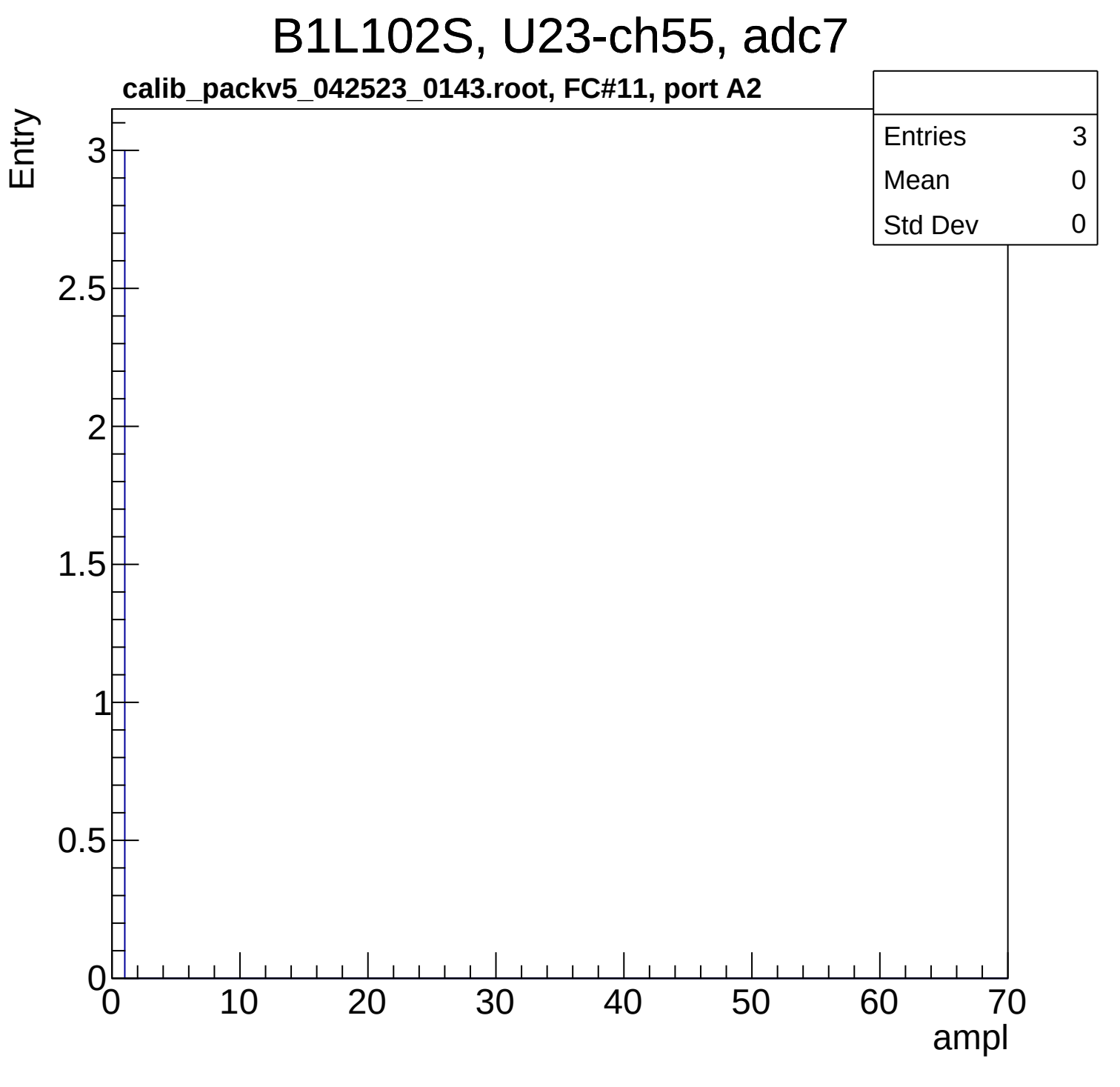

B1L102S, U23-ch55, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 43.61
Std Dev 3.645
Gaus mean : 43.9248
Gaus Width: 3.9153
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

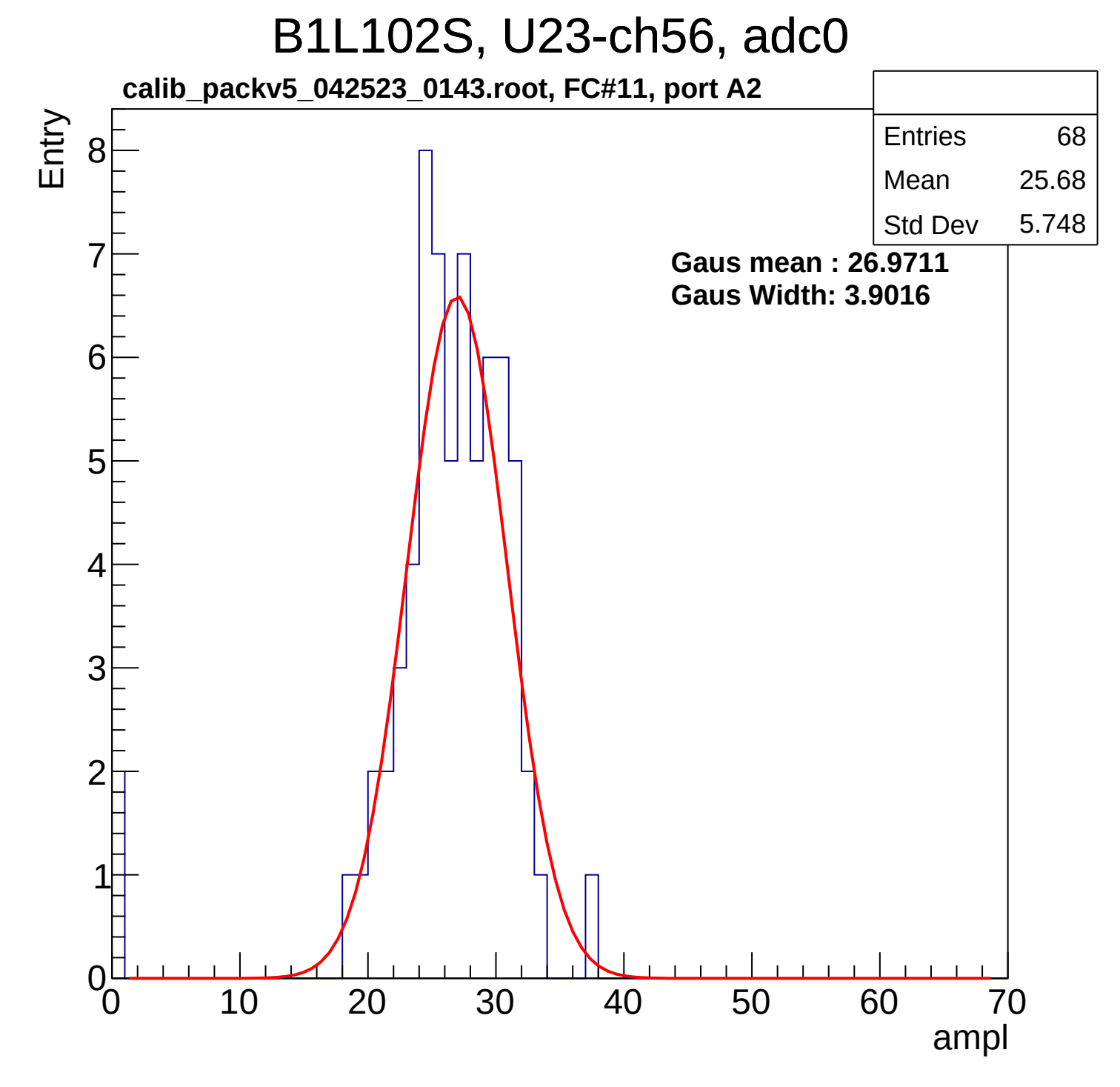

B1L102S, U23-ch55, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 49.58
Std Dev 3.895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

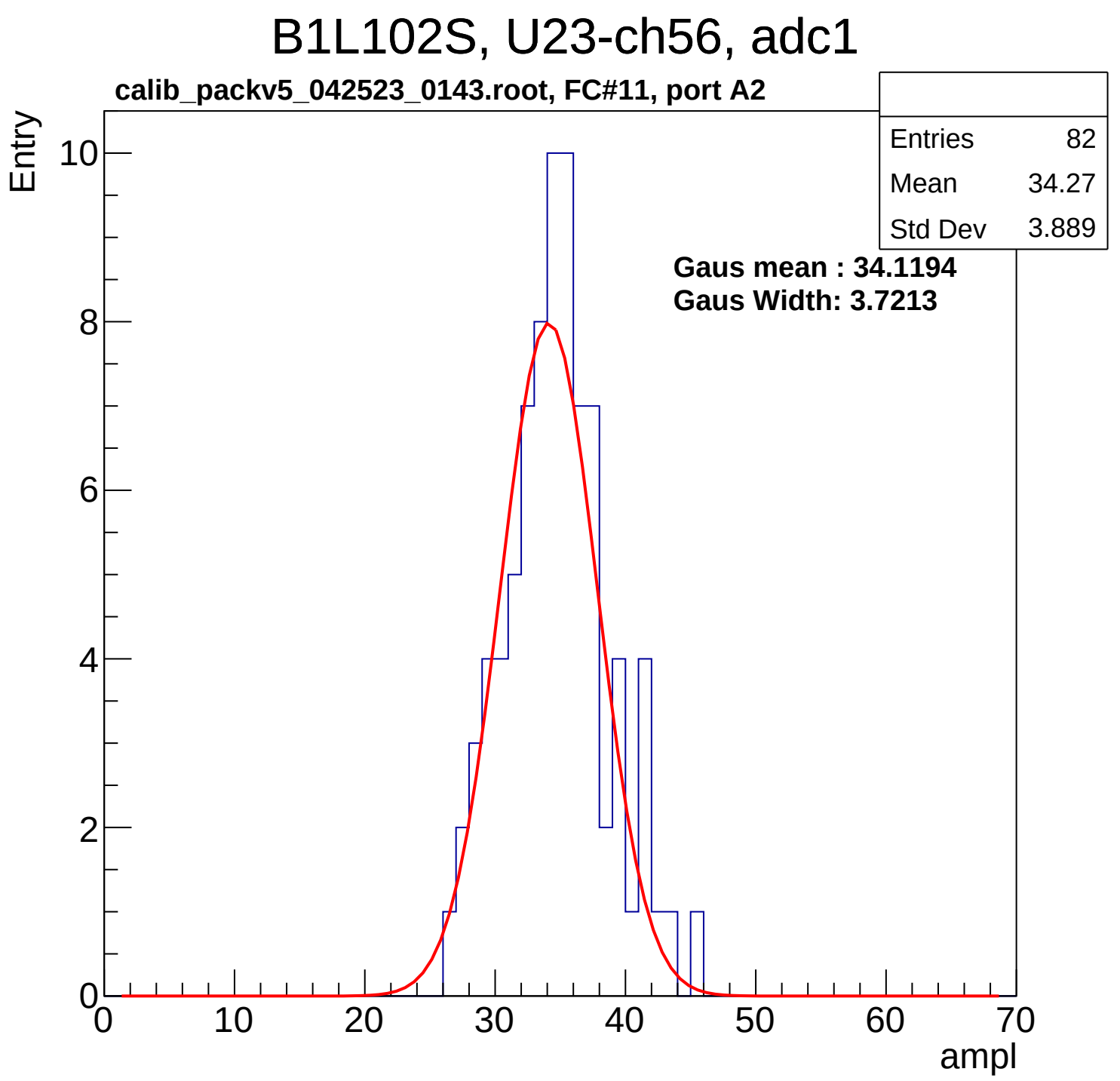

B1L102S, U23-ch55, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 56.55
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

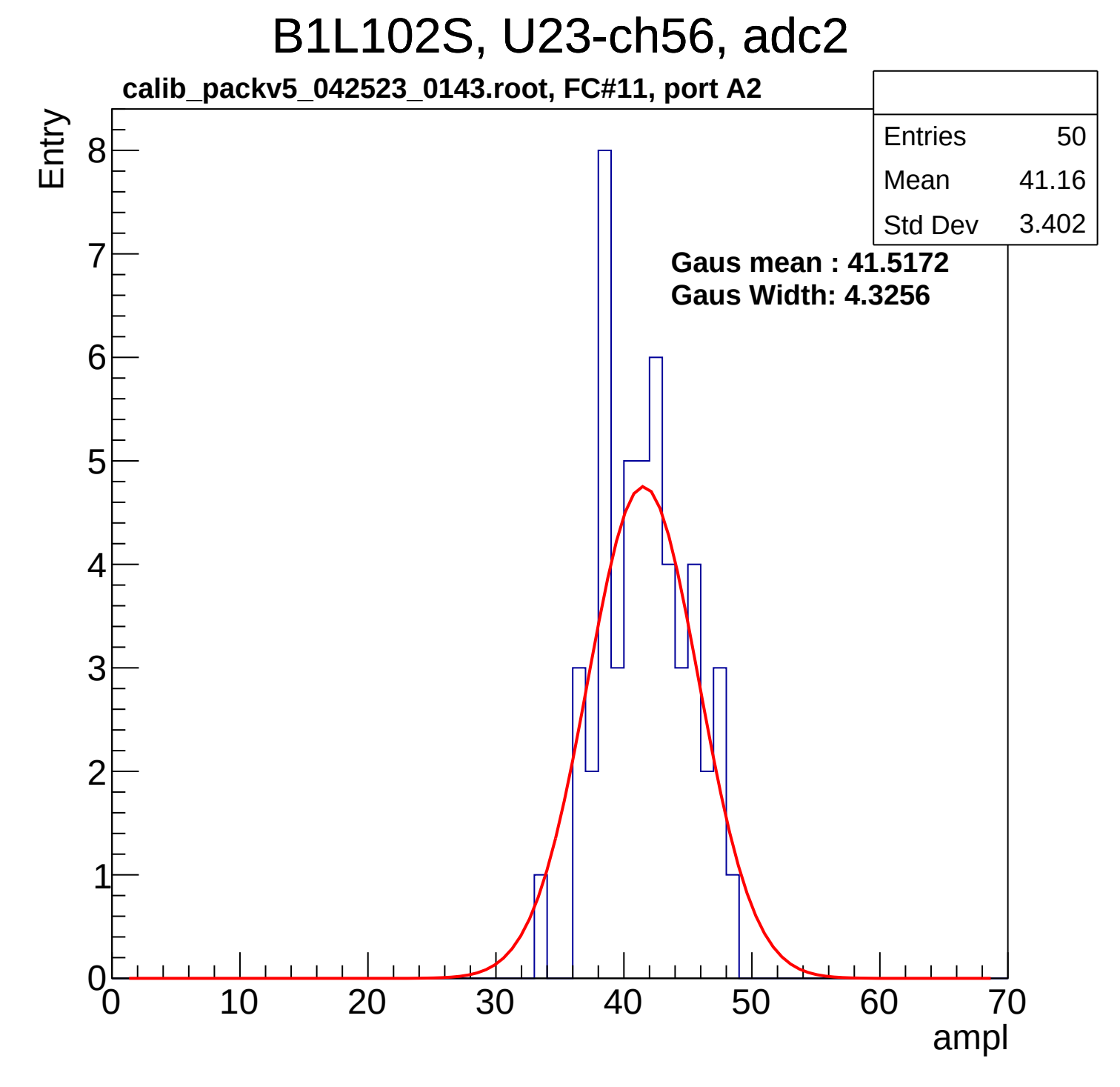

B1L102S, U23-ch55, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.38
Std Dev 9.185
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

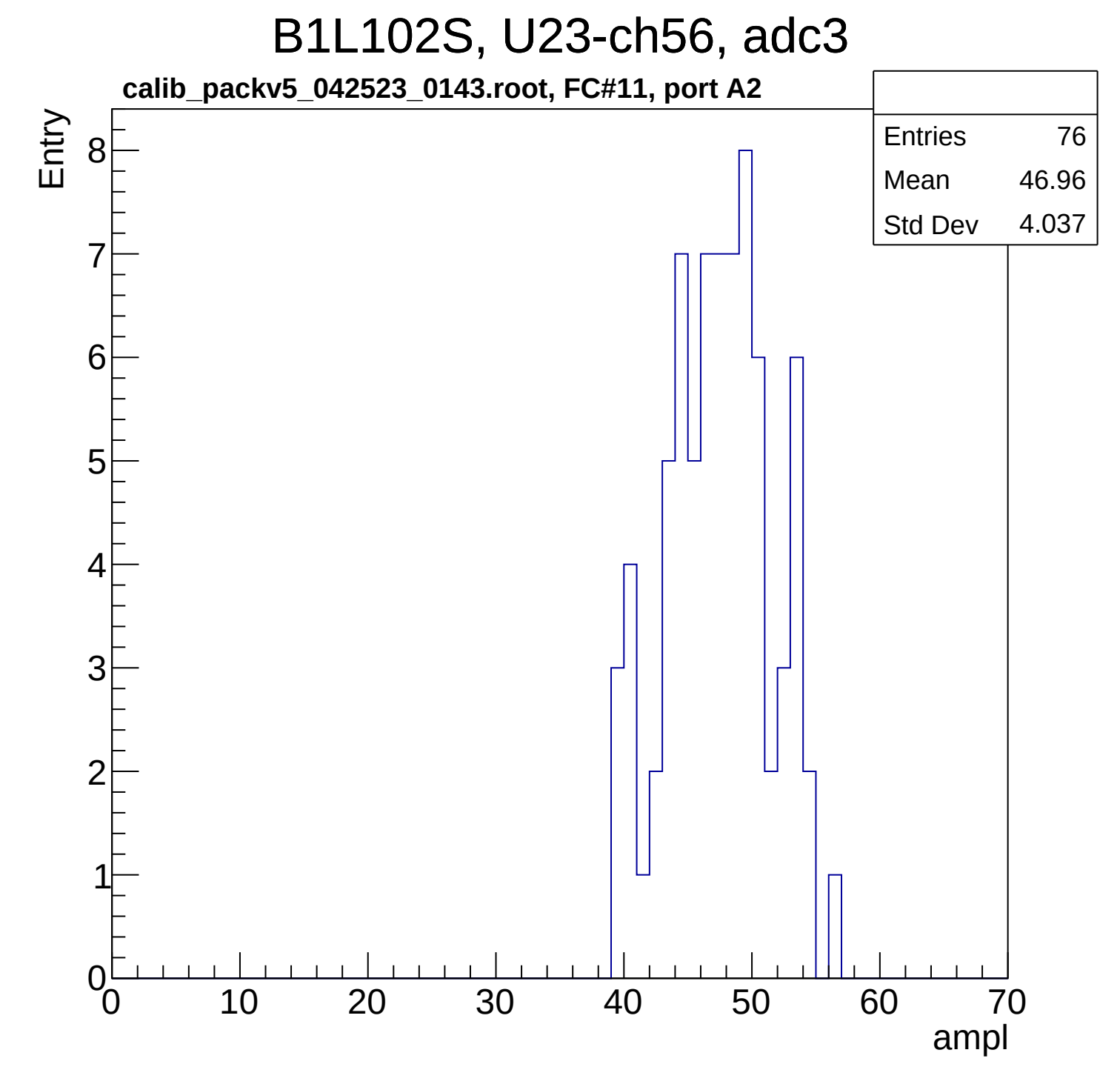

B1L102S, U23-ch55, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

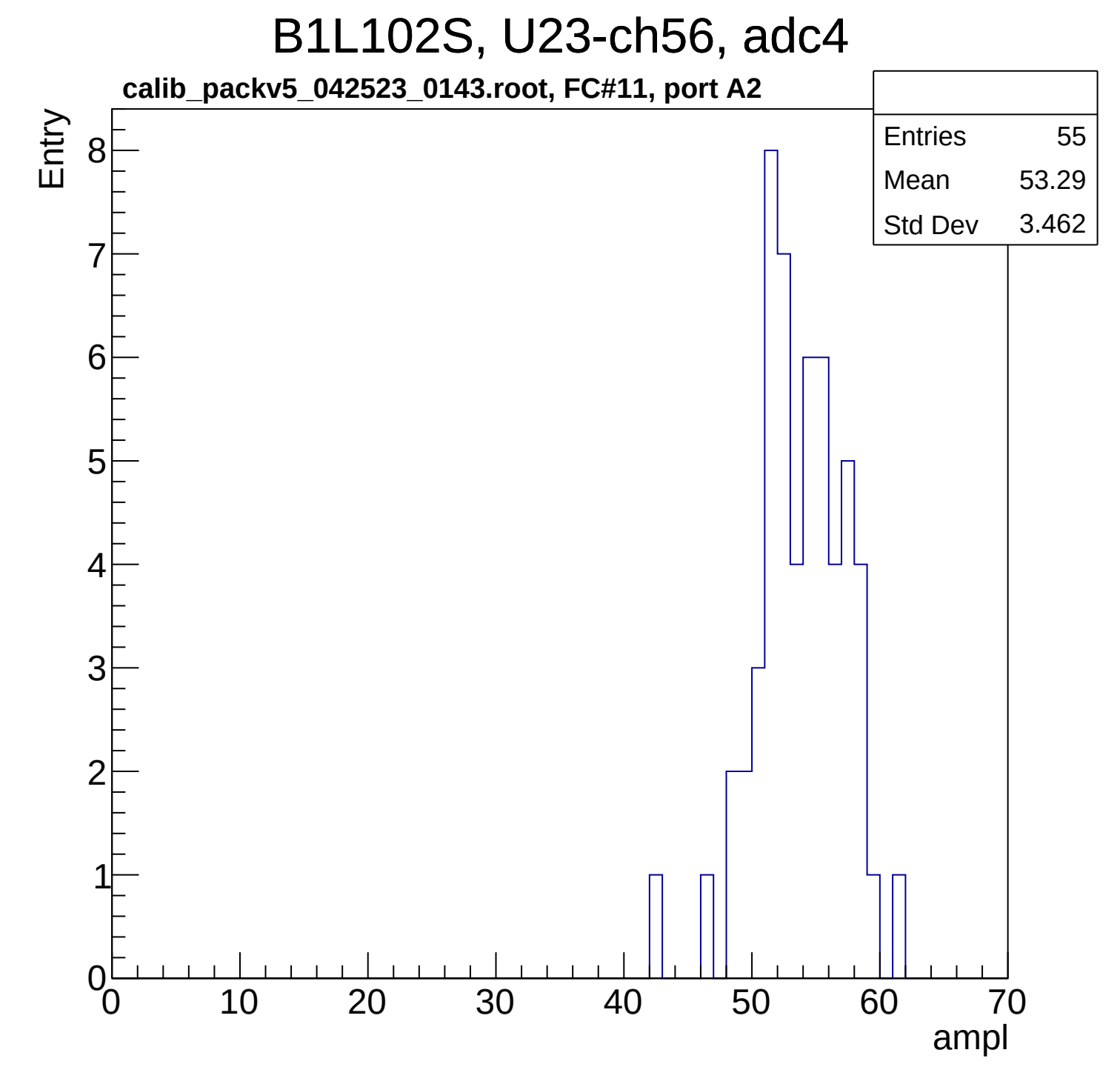

B1L102S, U23-ch55, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

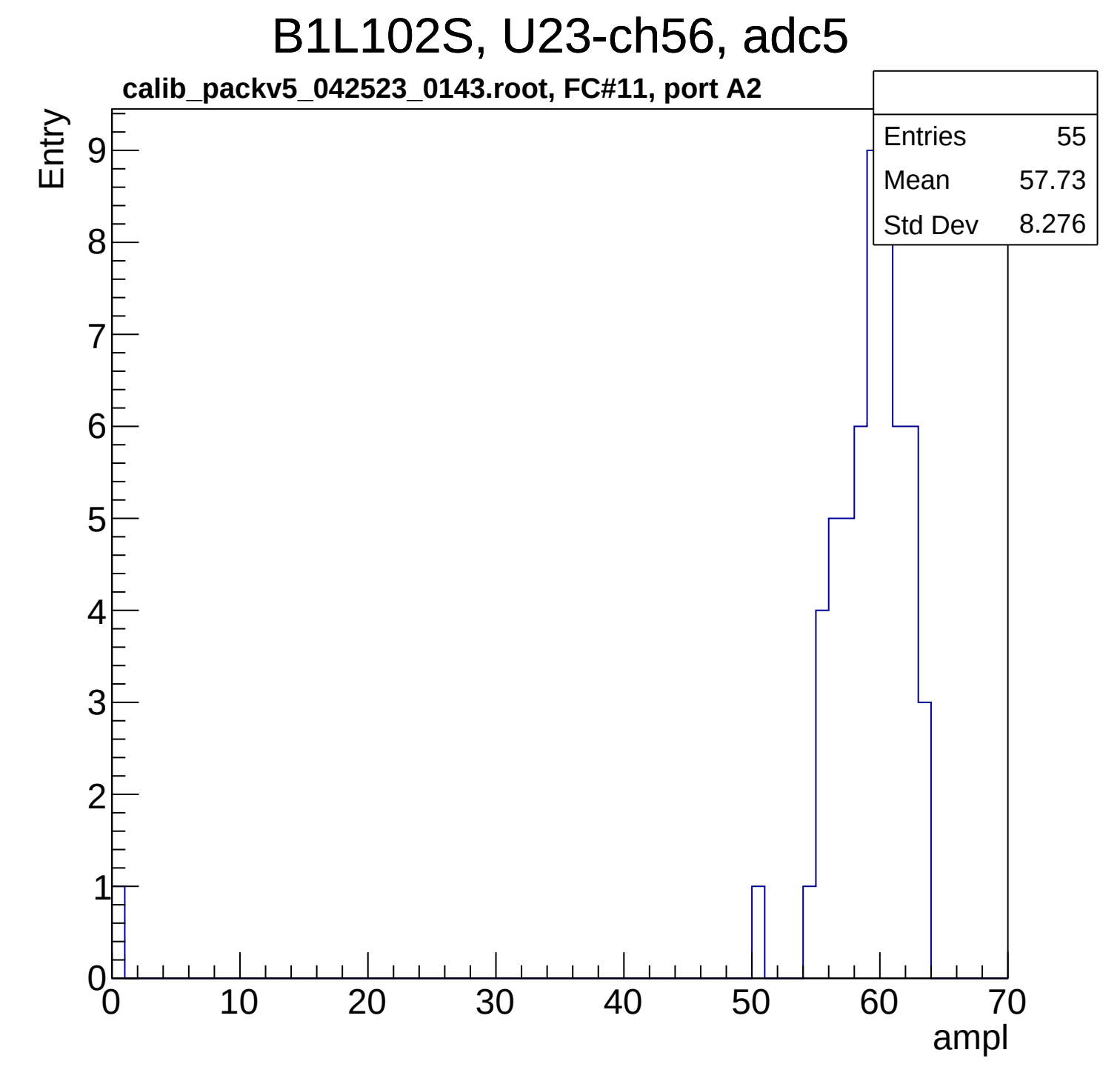

B1L102S, U23-ch56, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 25.68
Std Dev 5.748
Gaus mean : 26.9711
Gaus Width: 3.9016
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

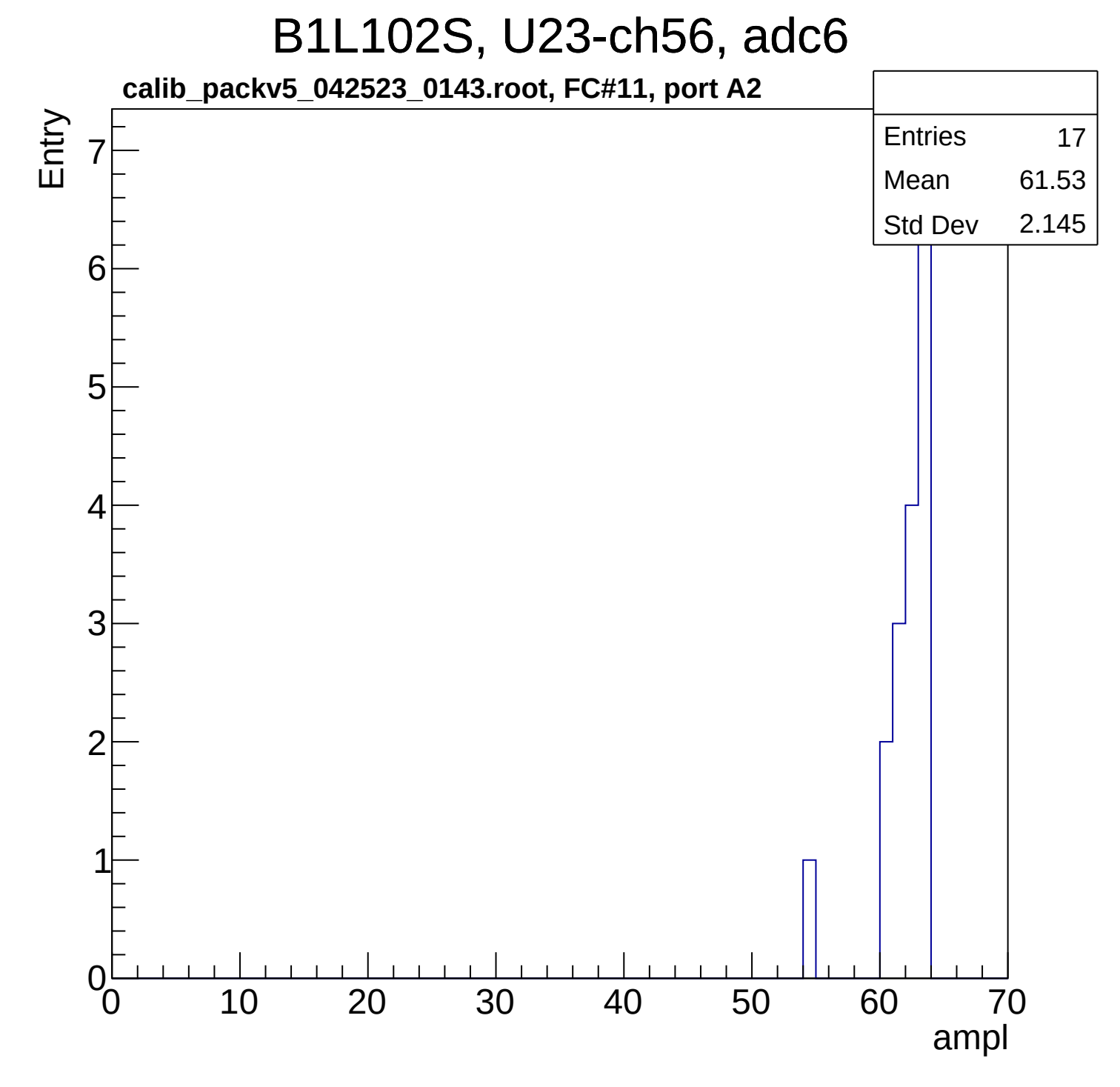

B1L102S, U23-ch56, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 34.27
Std Dev 3.889
Gaus mean : 34.1194
Gaus Width: 3.7213
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

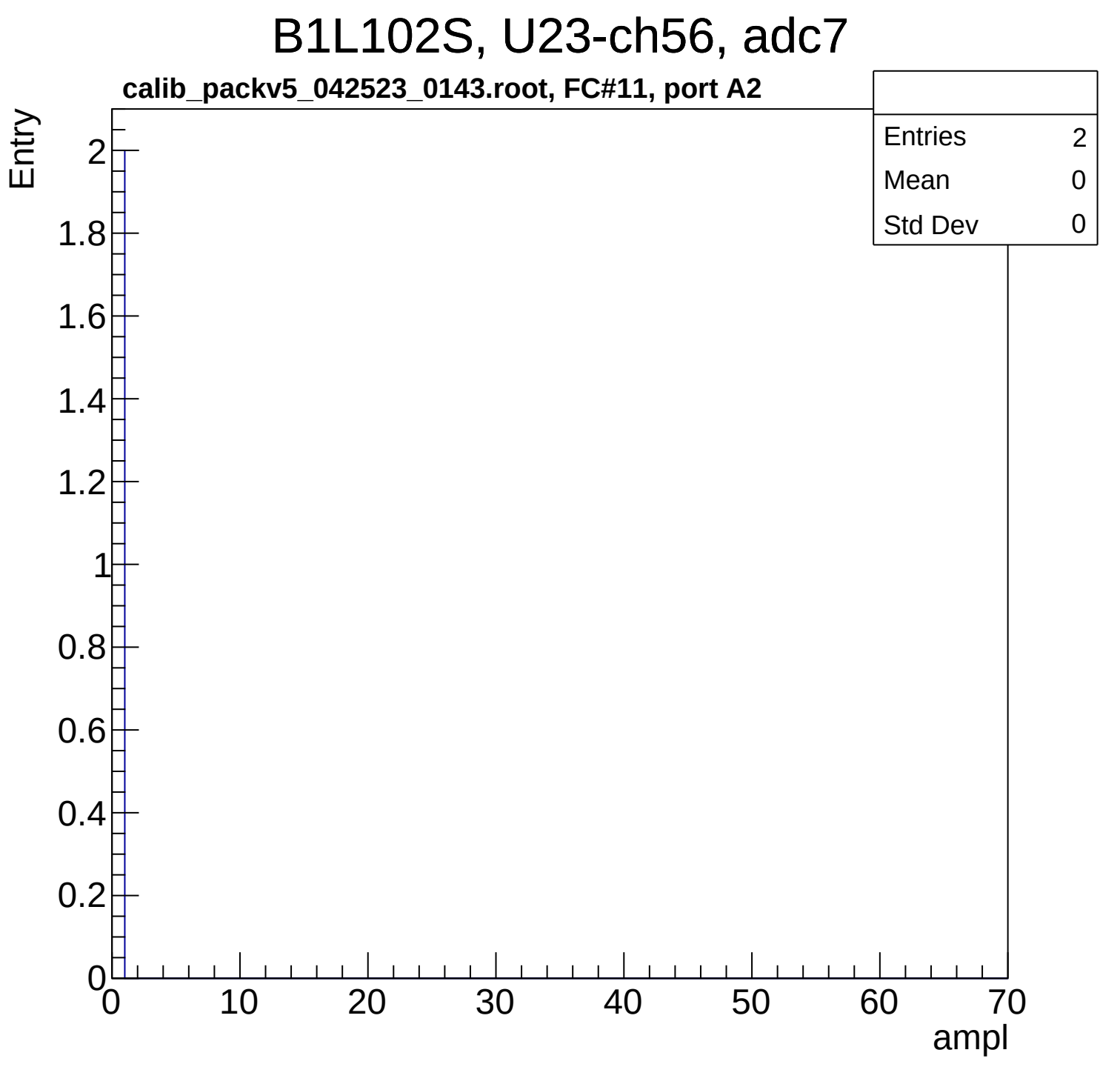

B1L102S, U23-ch56, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 41.16
Std Dev 3.402
Gaus mean : 41.5172
Gaus Width: 4.3256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

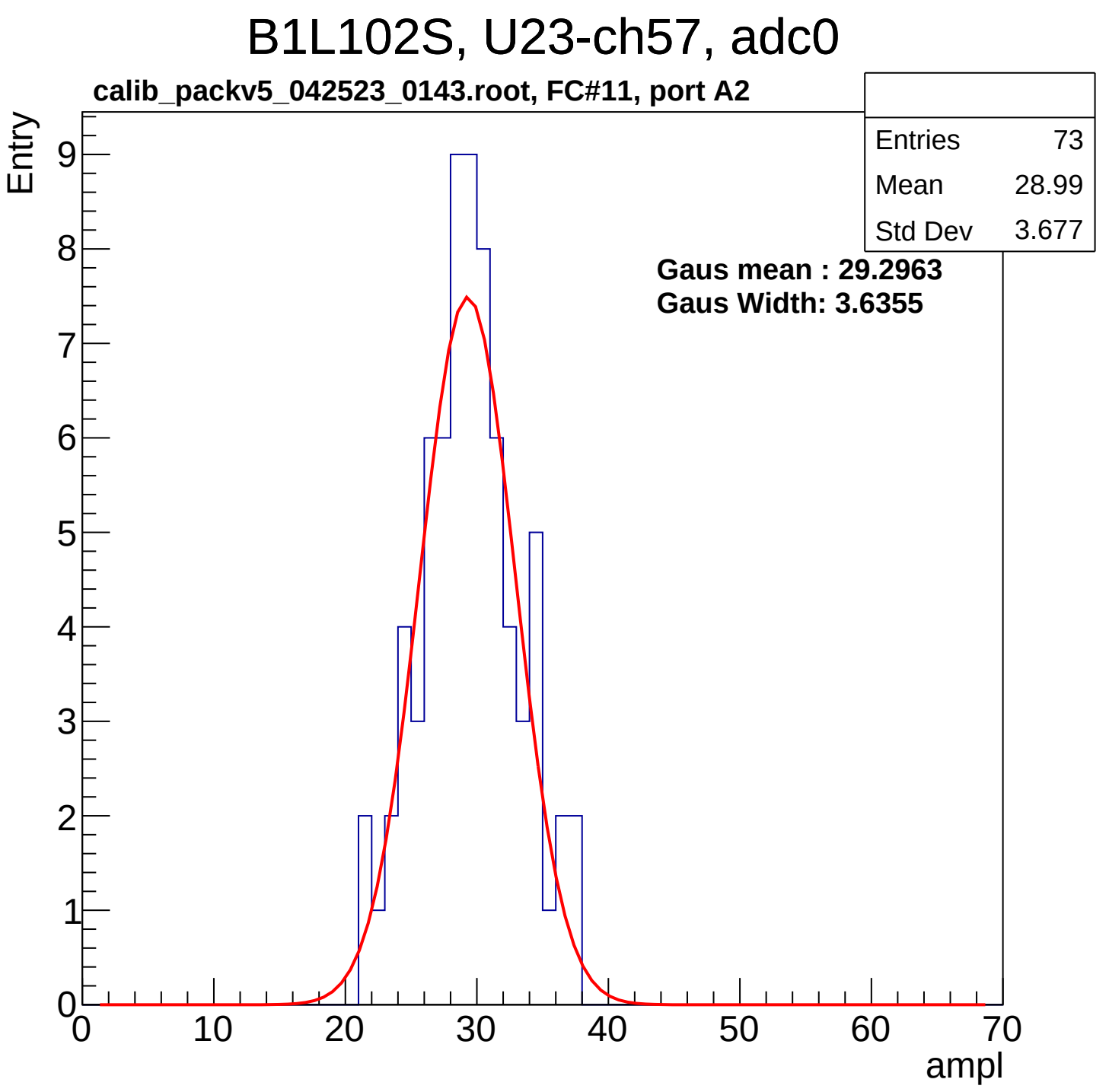

B1L102S, U23-ch56, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 46.96
Std Dev 4.037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

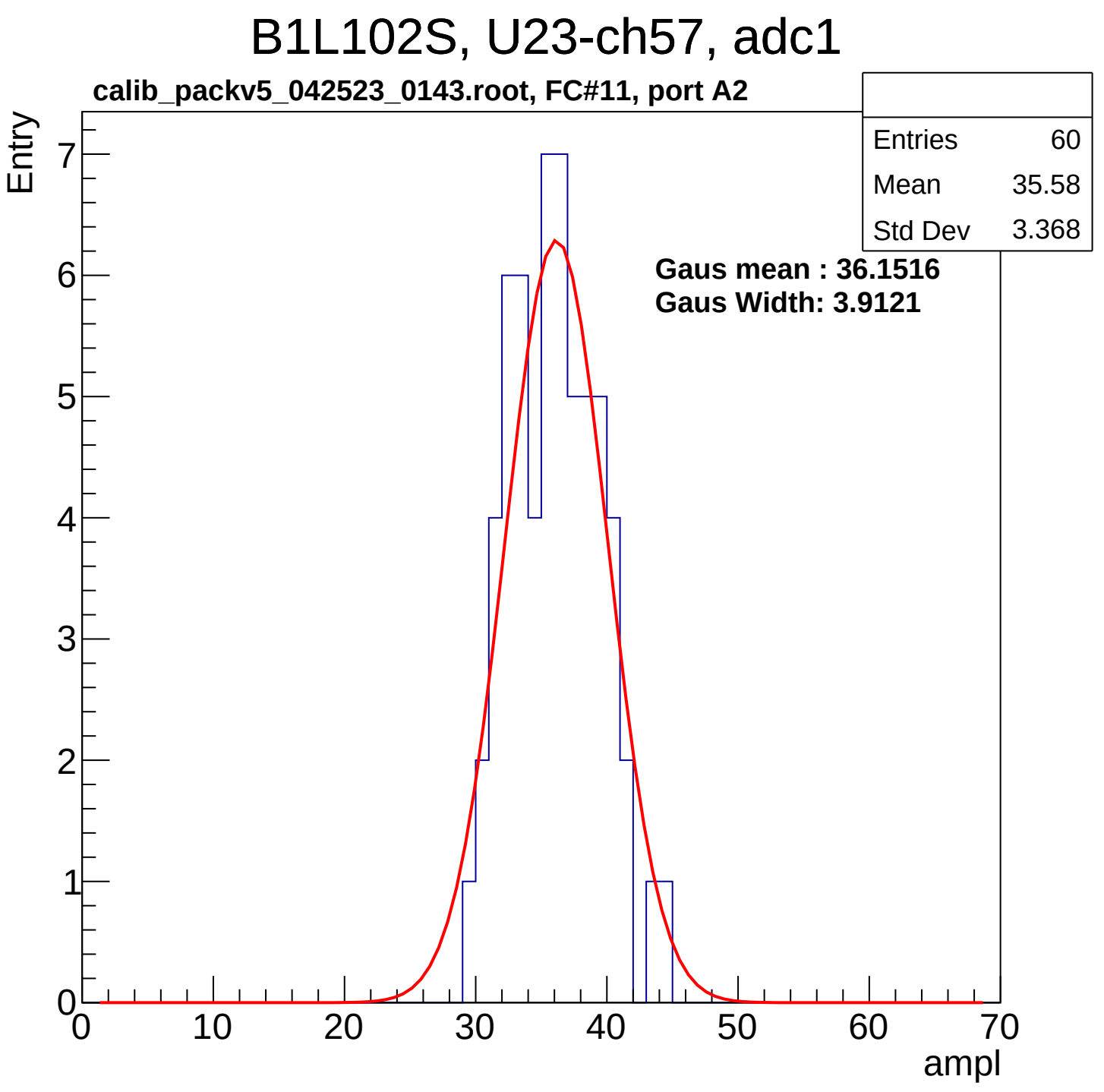

B1L102S, U23-ch56, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.29
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

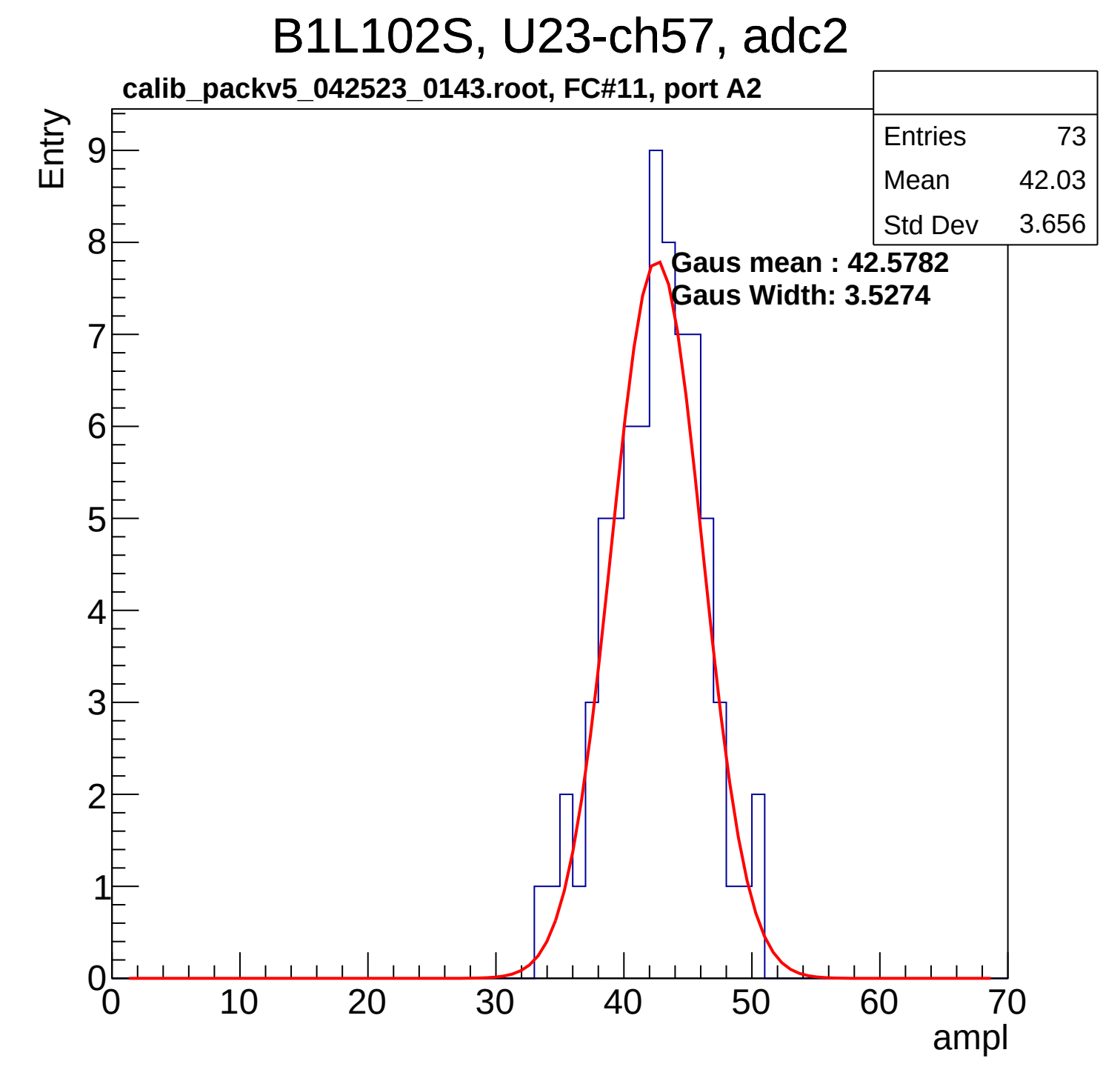

B1L102S, U23-ch56, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 57.73
Std Dev 8.276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

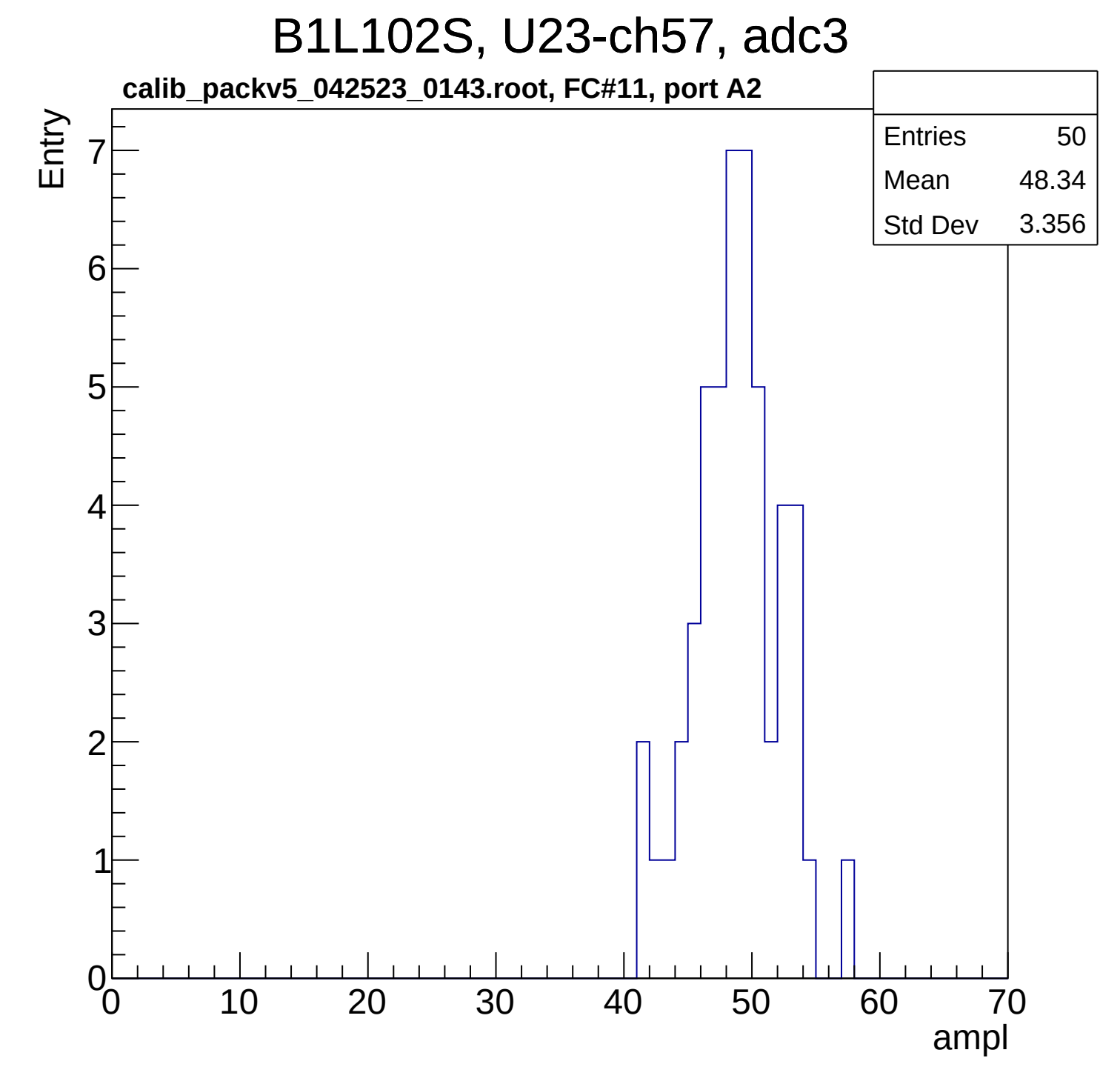

B1L102S, U23-ch56, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 17
Mean 61.53
Std Dev 2.145
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

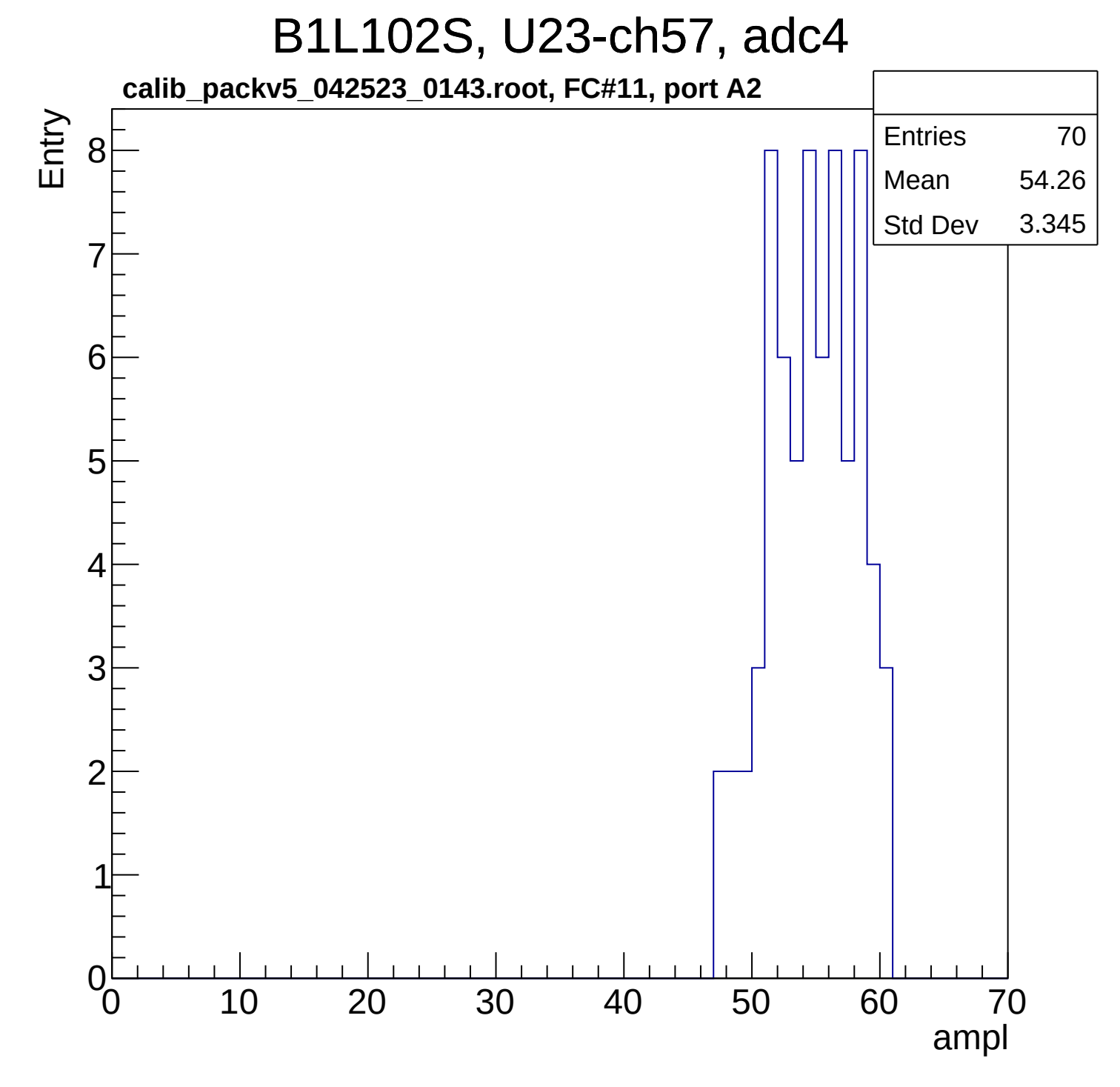

B1L102S, U23-ch56, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

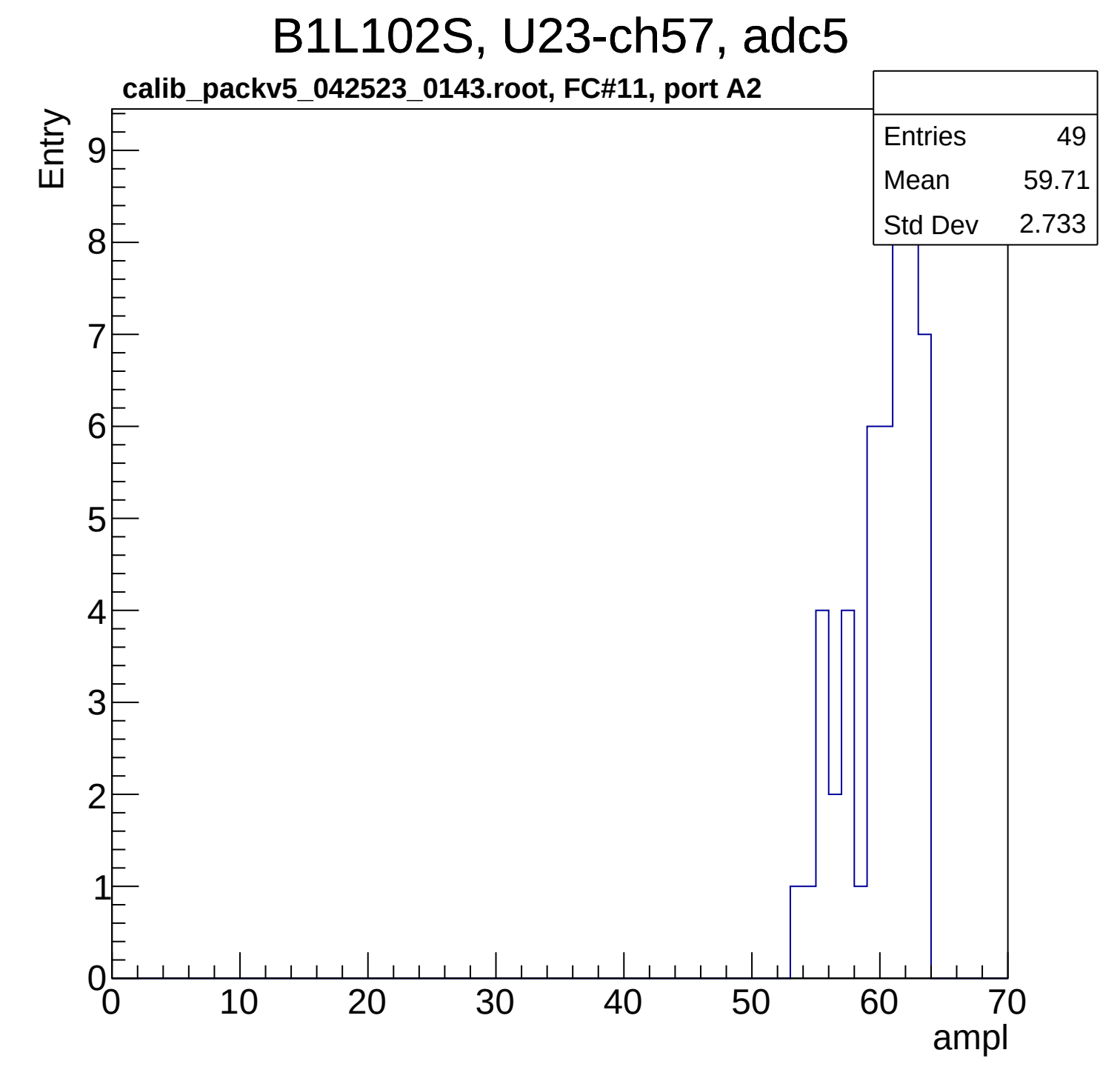

B1L102S, U23-ch57, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 28.99
Std Dev 3.677
Gaus mean : 29.2963
Gaus Width: 3.6355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

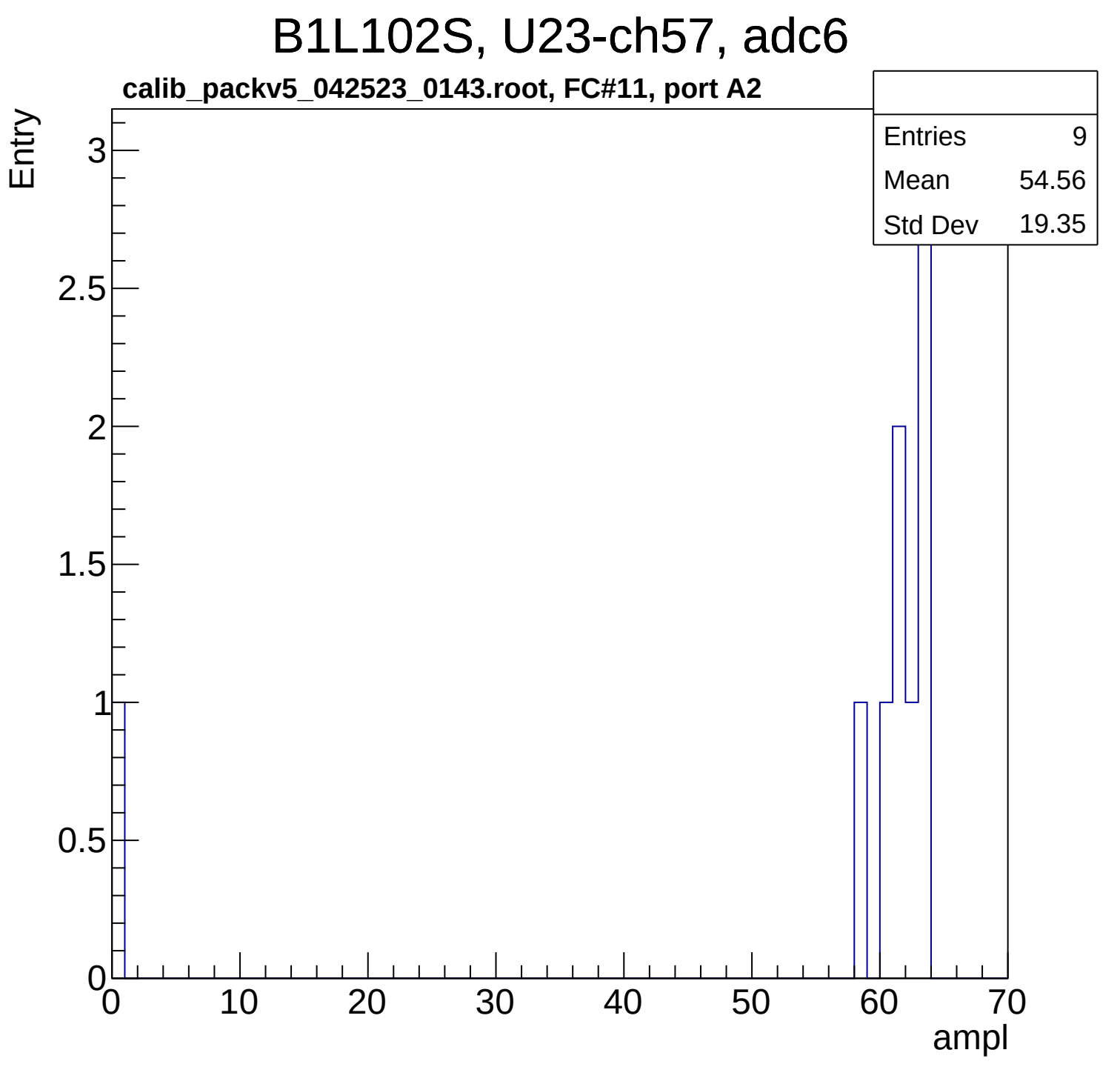

B1L102S, U23-ch57, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 35.58
Std Dev 3.368
Gaus mean : 36.1516
Gaus Width: 3.9121
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

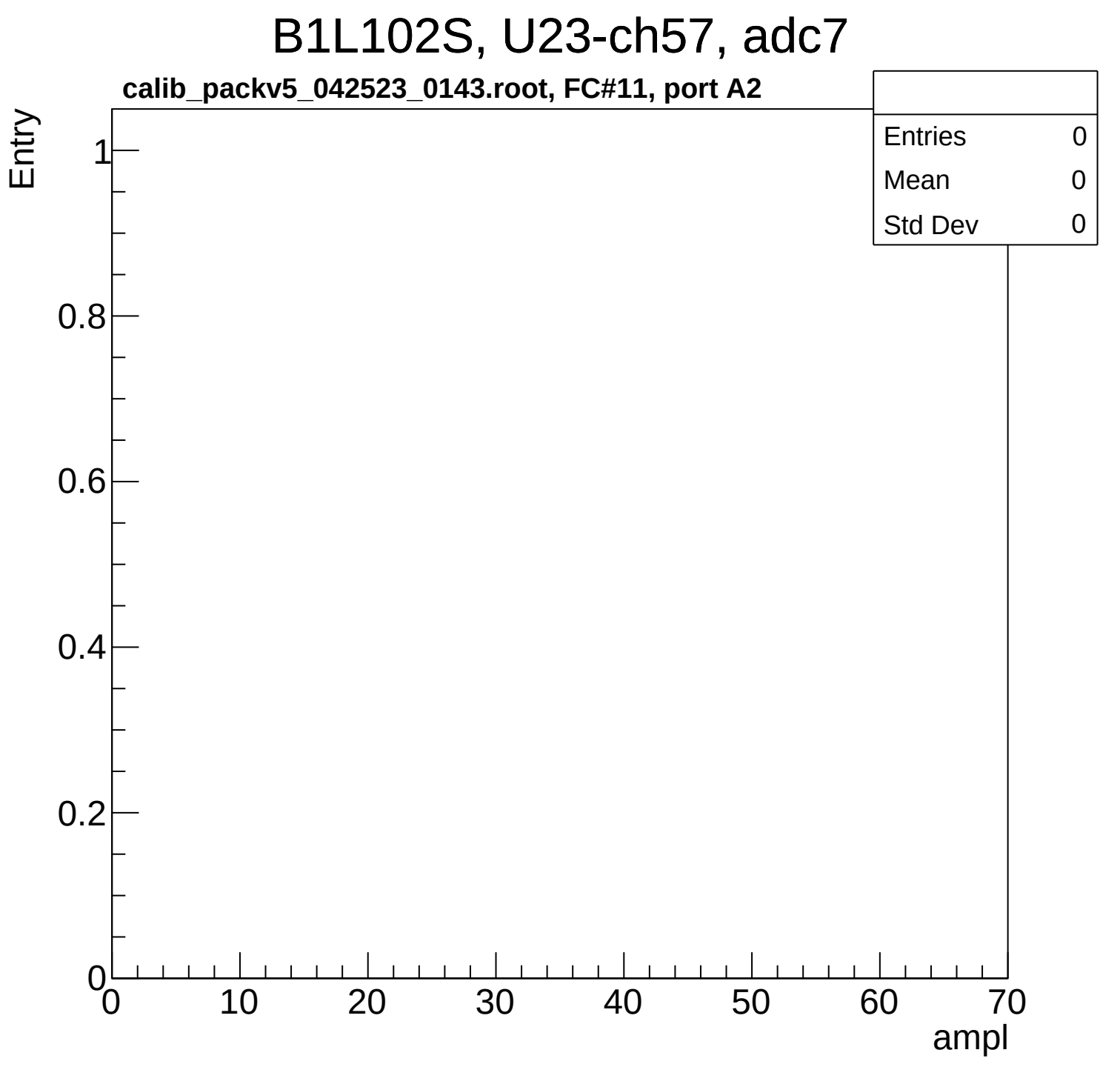

B1L102S, U23-ch57, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 42.03
Std Dev 3.656
Gaus mean : 42.5782
Gaus Width: 3.5274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

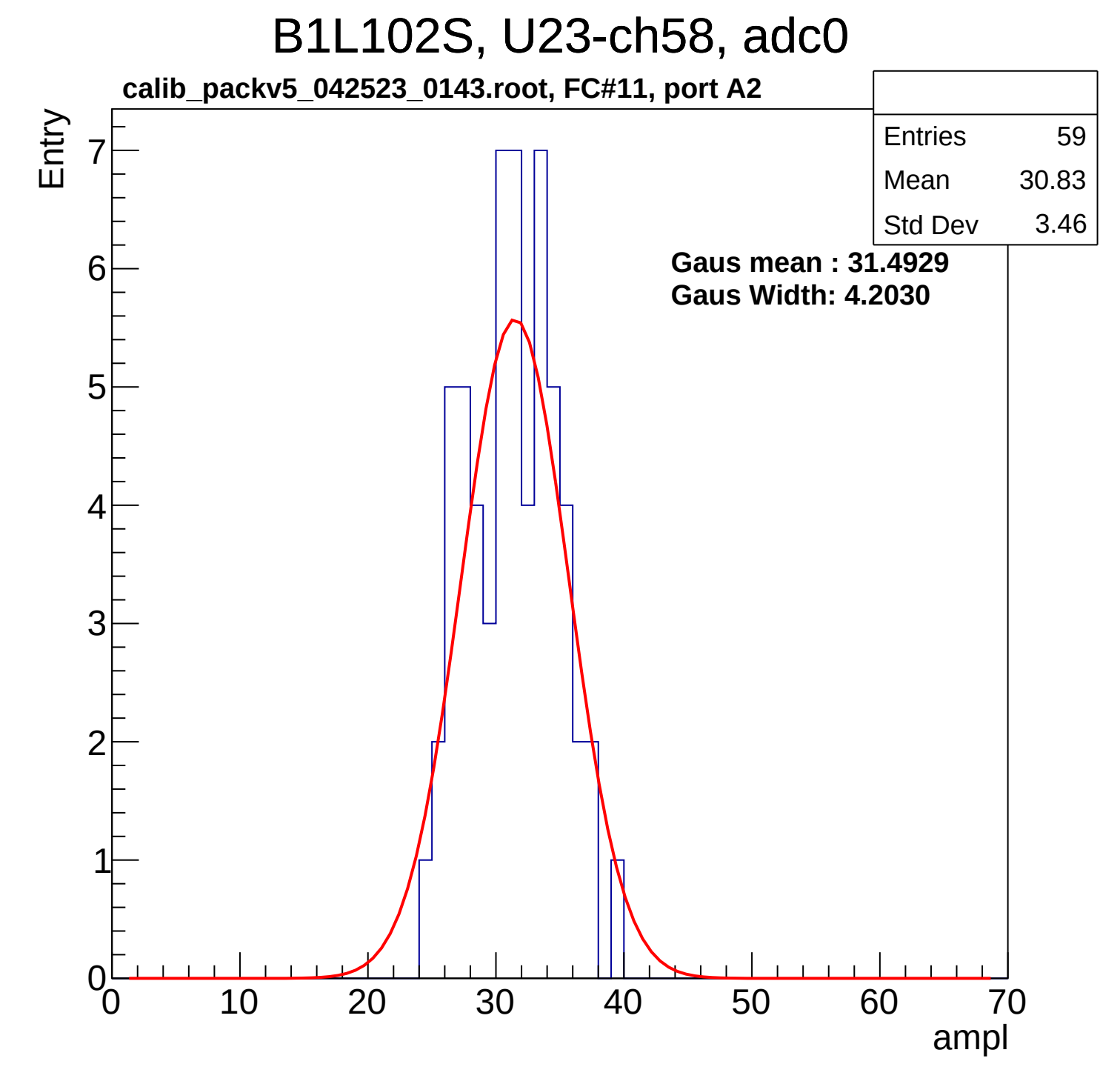

B1L102S, U23-ch57, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 48.34
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

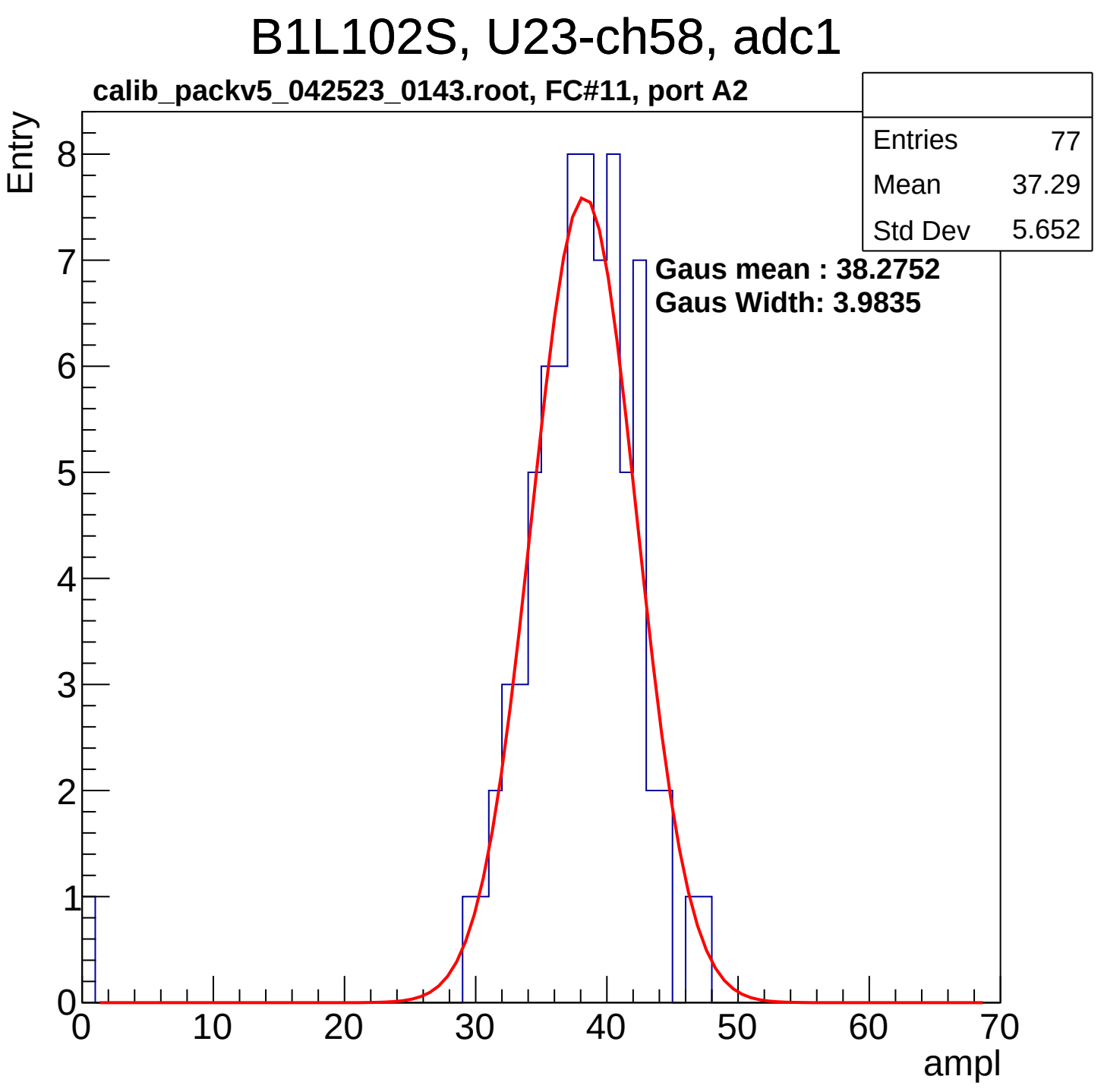

B1L102S, U23-ch57, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 54.26
Std Dev 3.345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

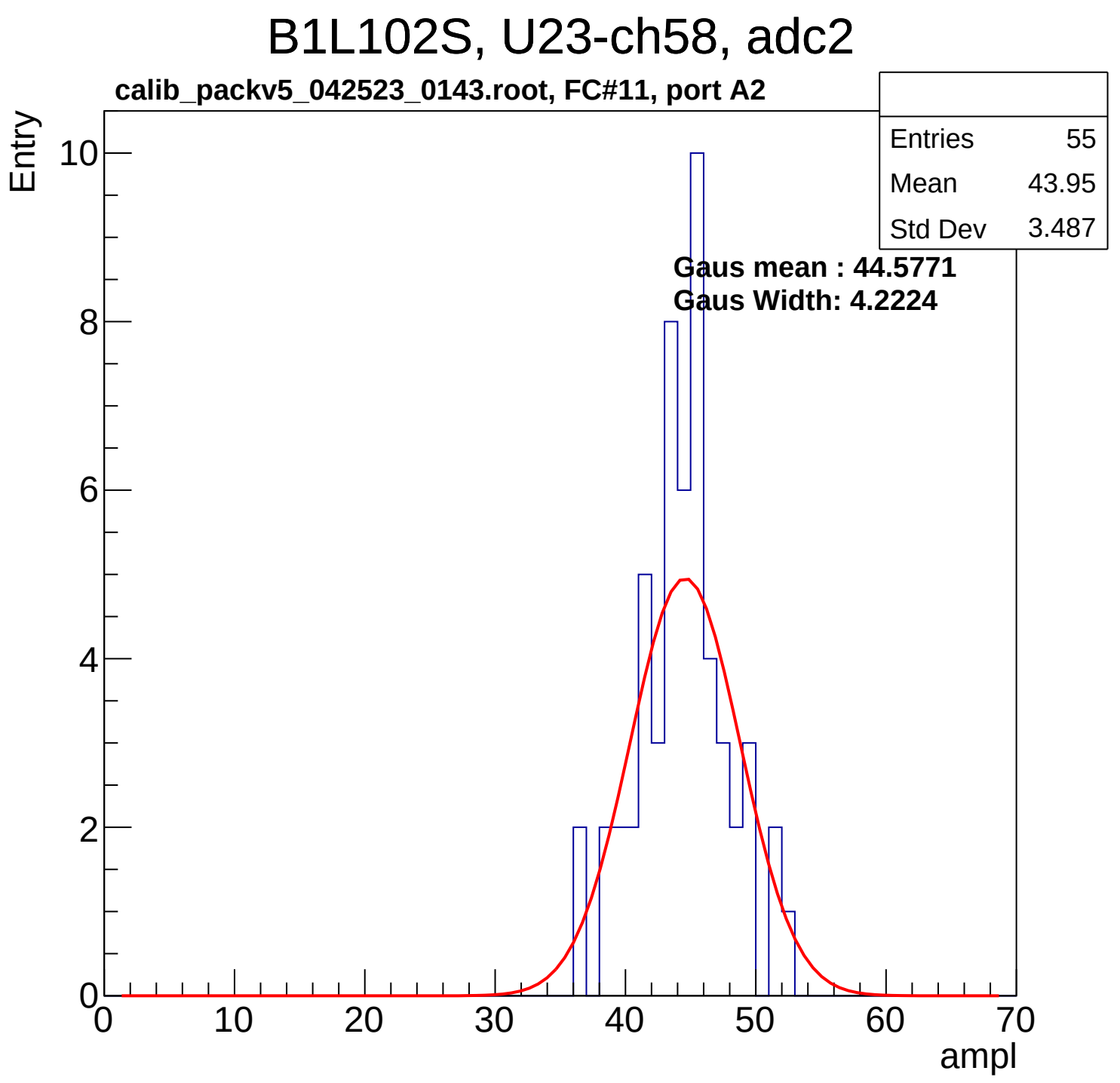

B1L102S, U23-ch57, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.71
Std Dev 2.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

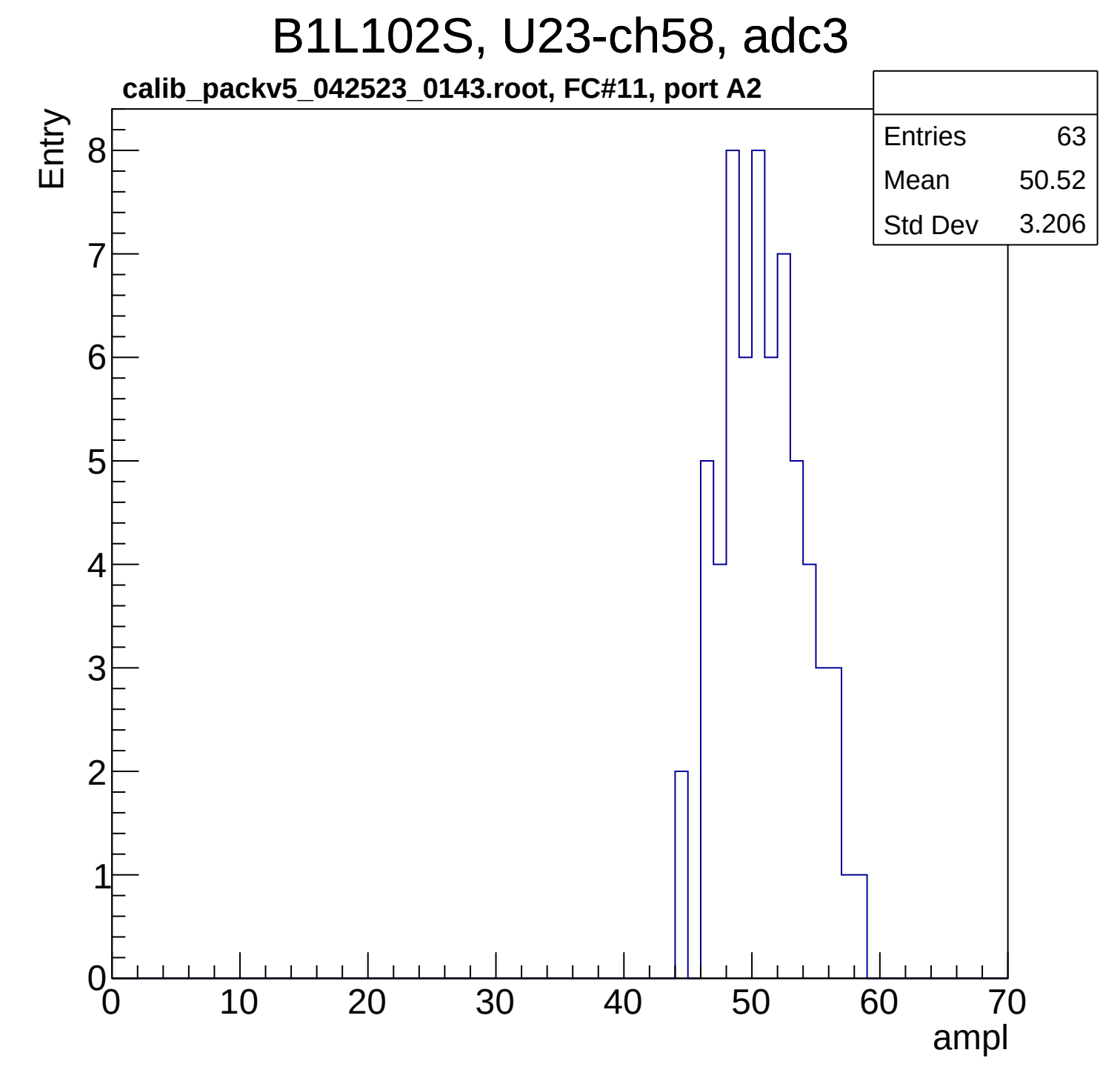

B1L102S, U23-ch57, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 54.56
Std Dev 19.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

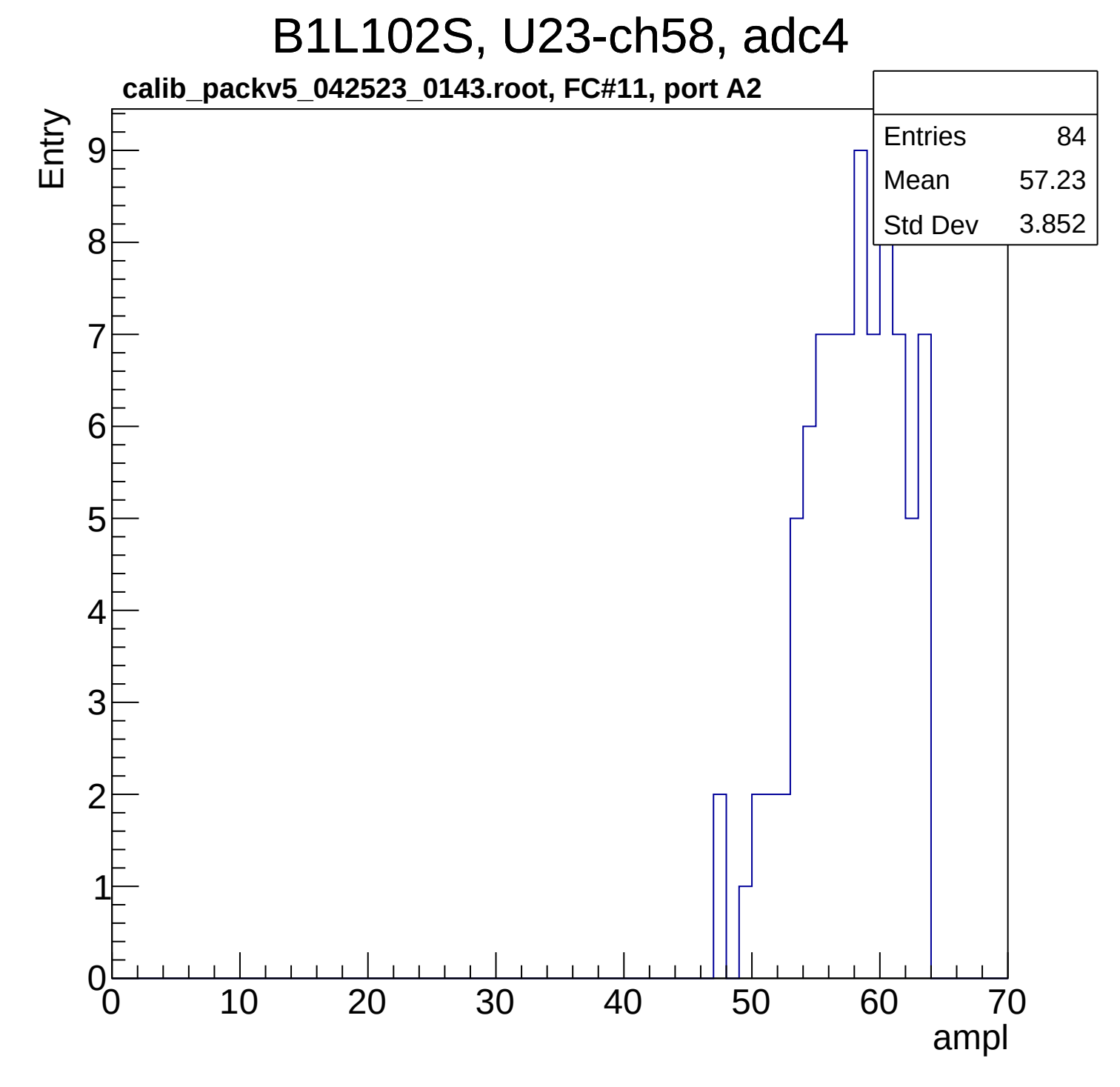

B1L102S, U23-ch57, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

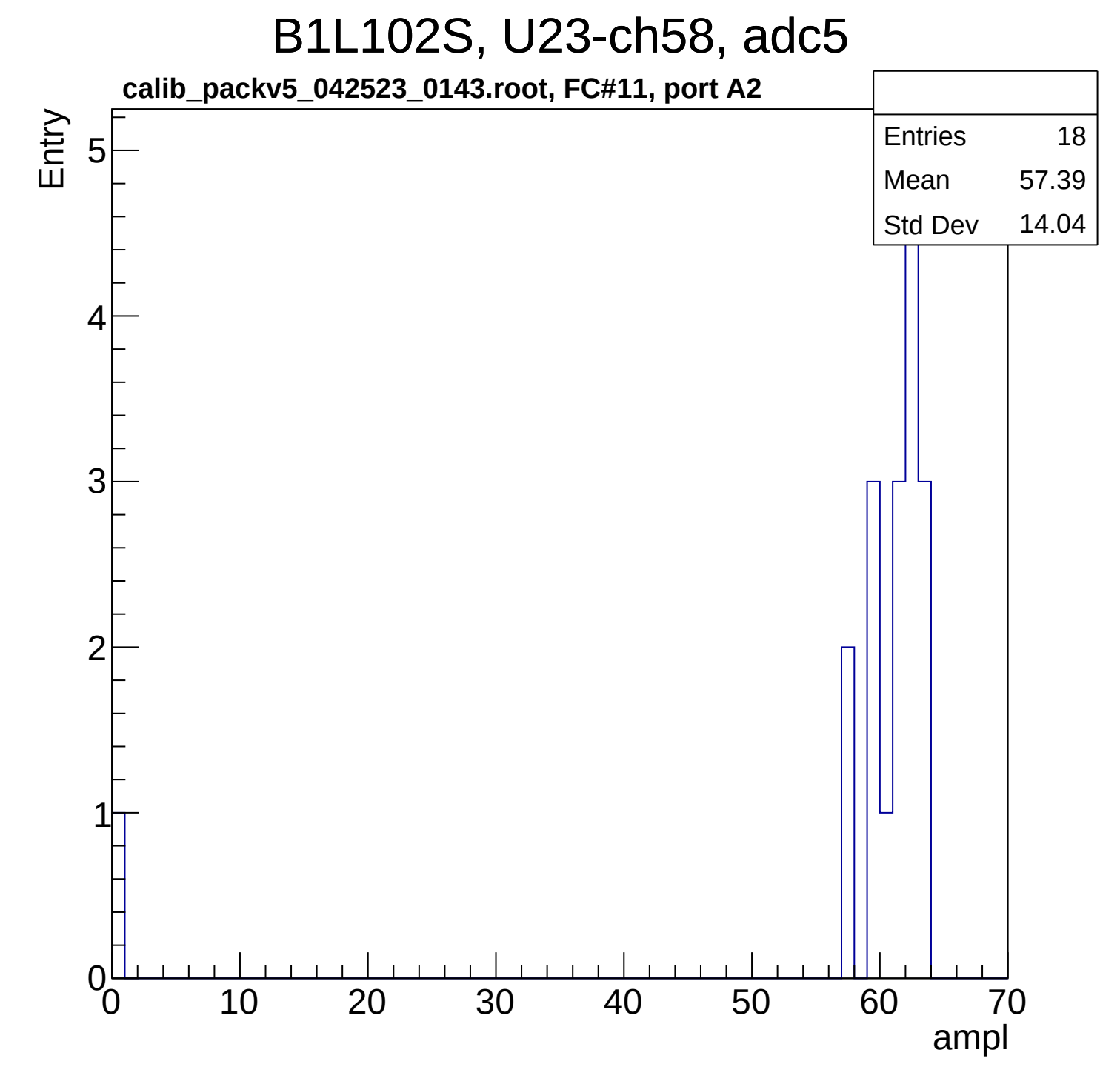

B1L102S, U23-ch58, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 30.83
Std Dev 3.46
Gaus mean : 31.4929
Gaus Width: 4.2030
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

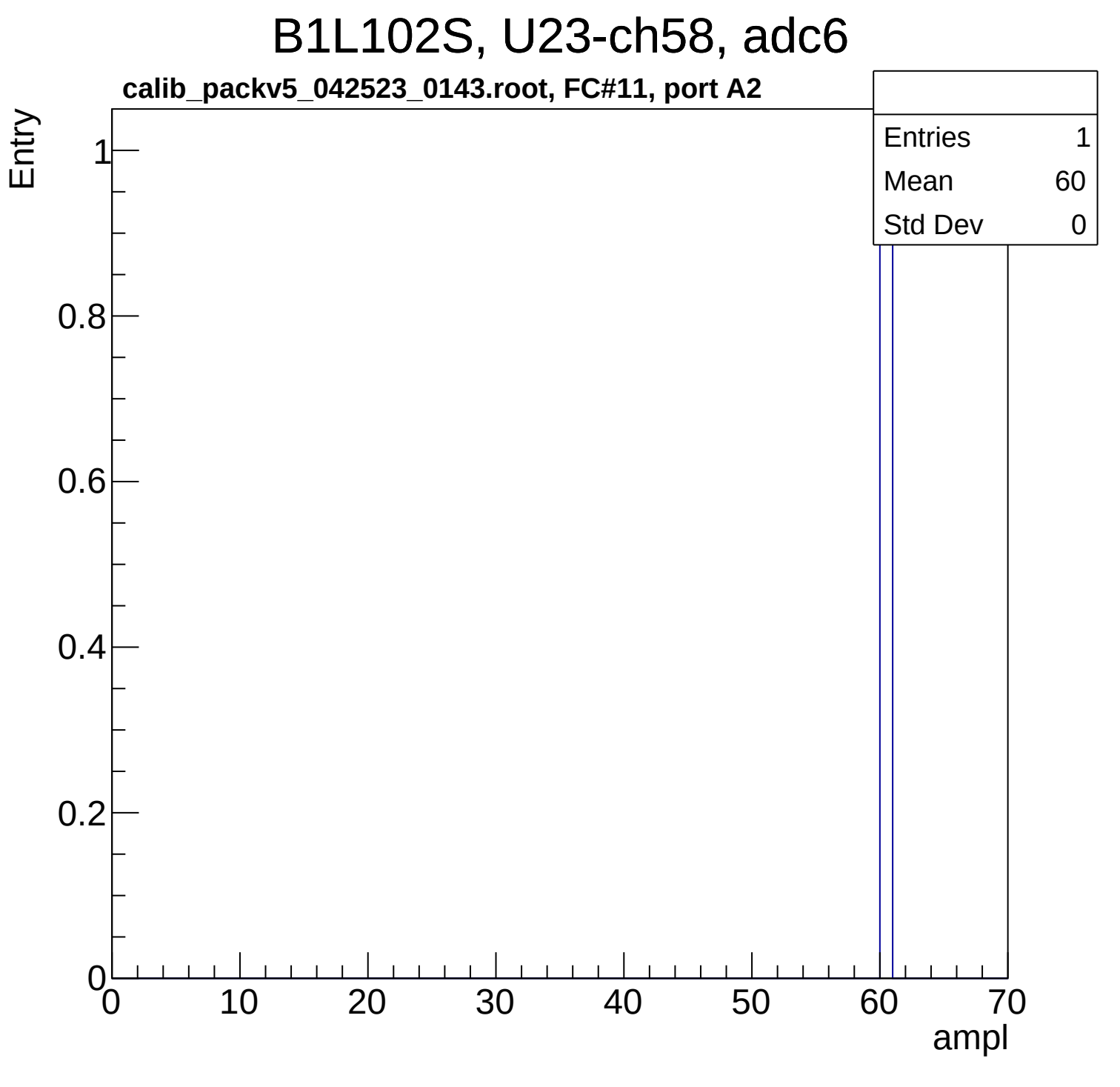

B1L102S, U23-ch58, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 37.29
Std Dev 5.652
Gaus mean : 38.2752
Gaus Width: 3.9835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

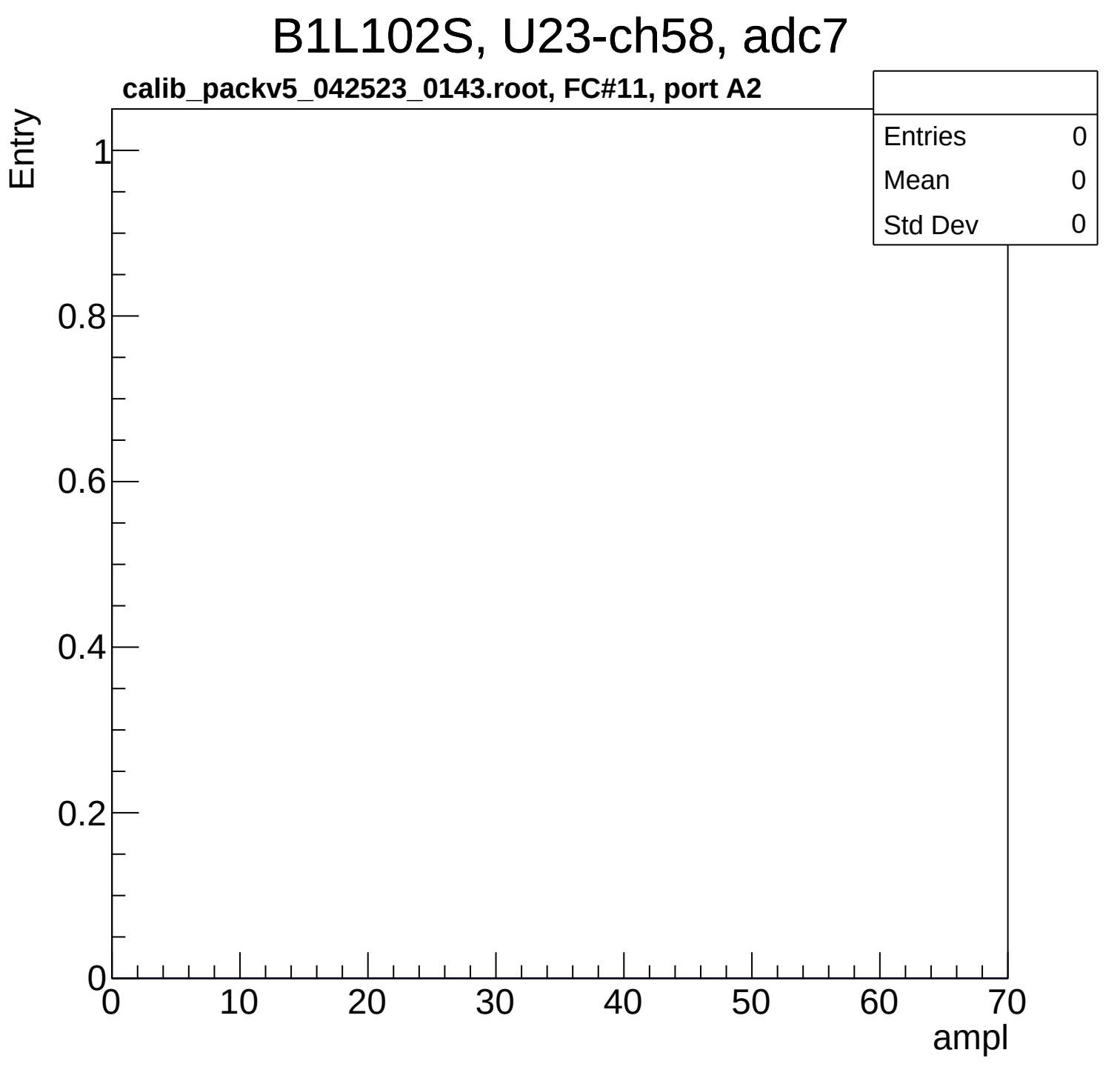

B1L102S, U23-ch58, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 43.95
Std Dev 3.487
Gaus mean : 44.5771
Gaus Width: 4.2224
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

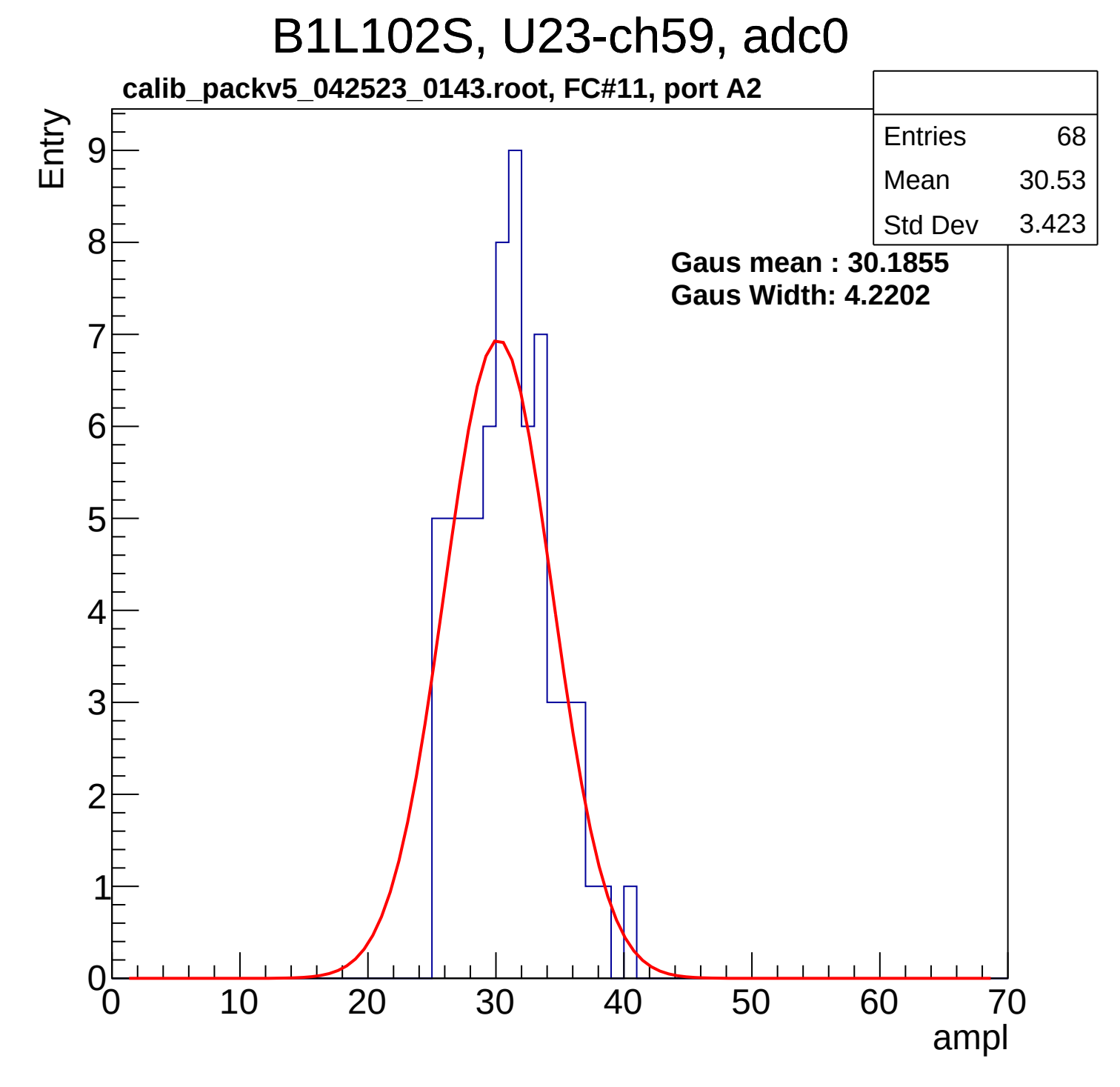

B1L102S, U23-ch58, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 50.52
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

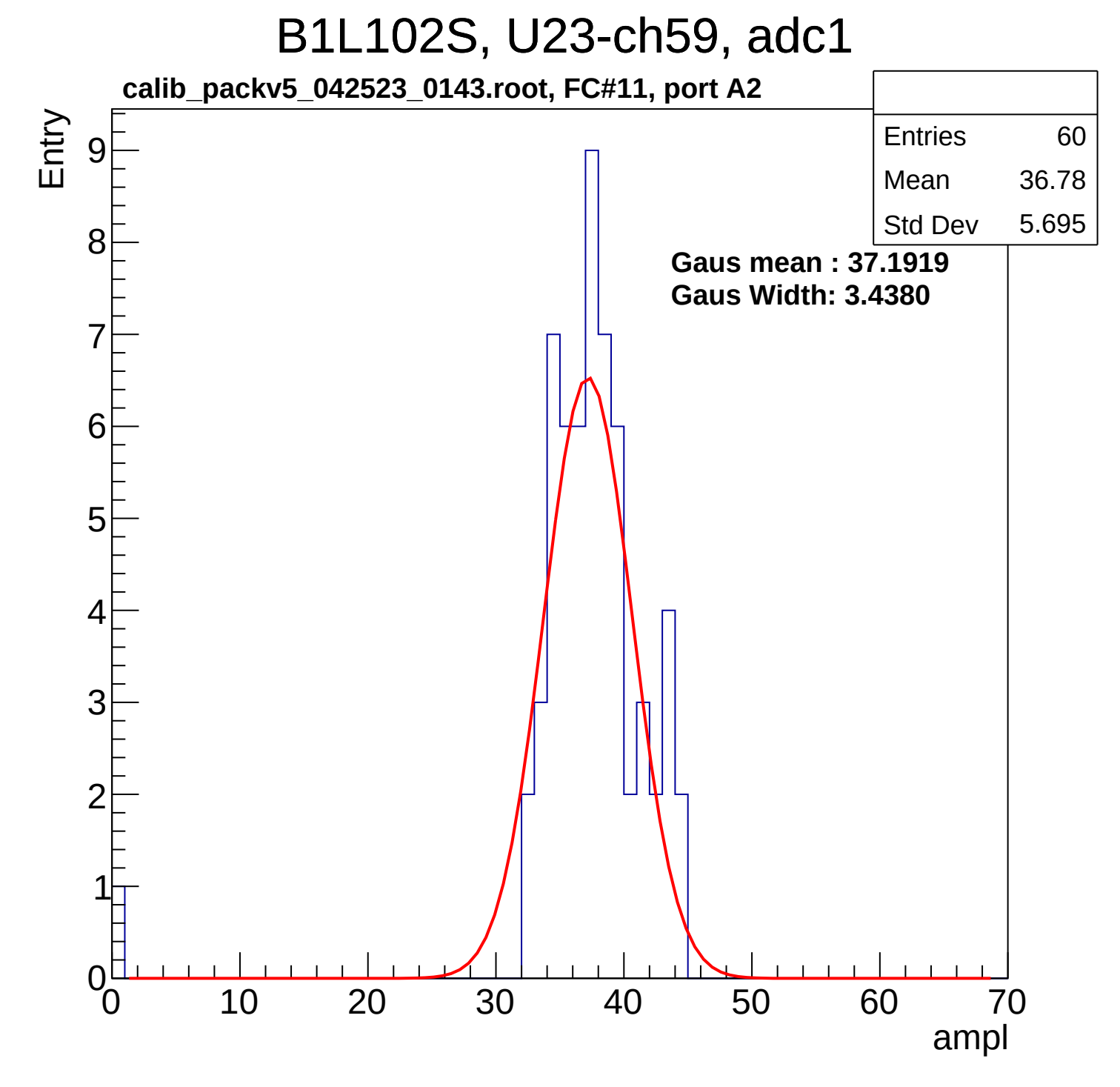

B1L102S, U23-ch58, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 57.23
Std Dev 3.852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

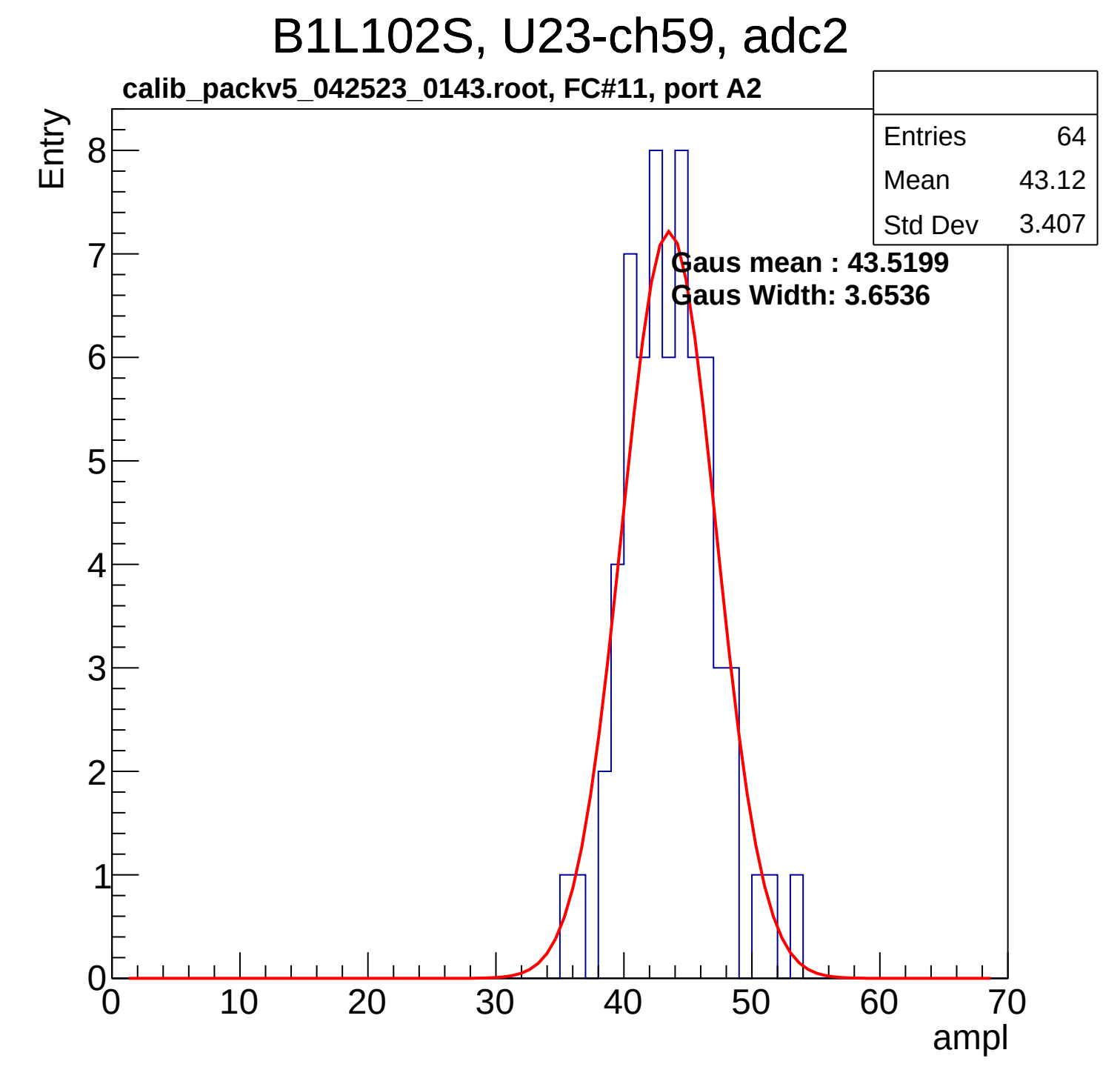

B1L102S, U23-ch58, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 57.39
Std Dev 14.04
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

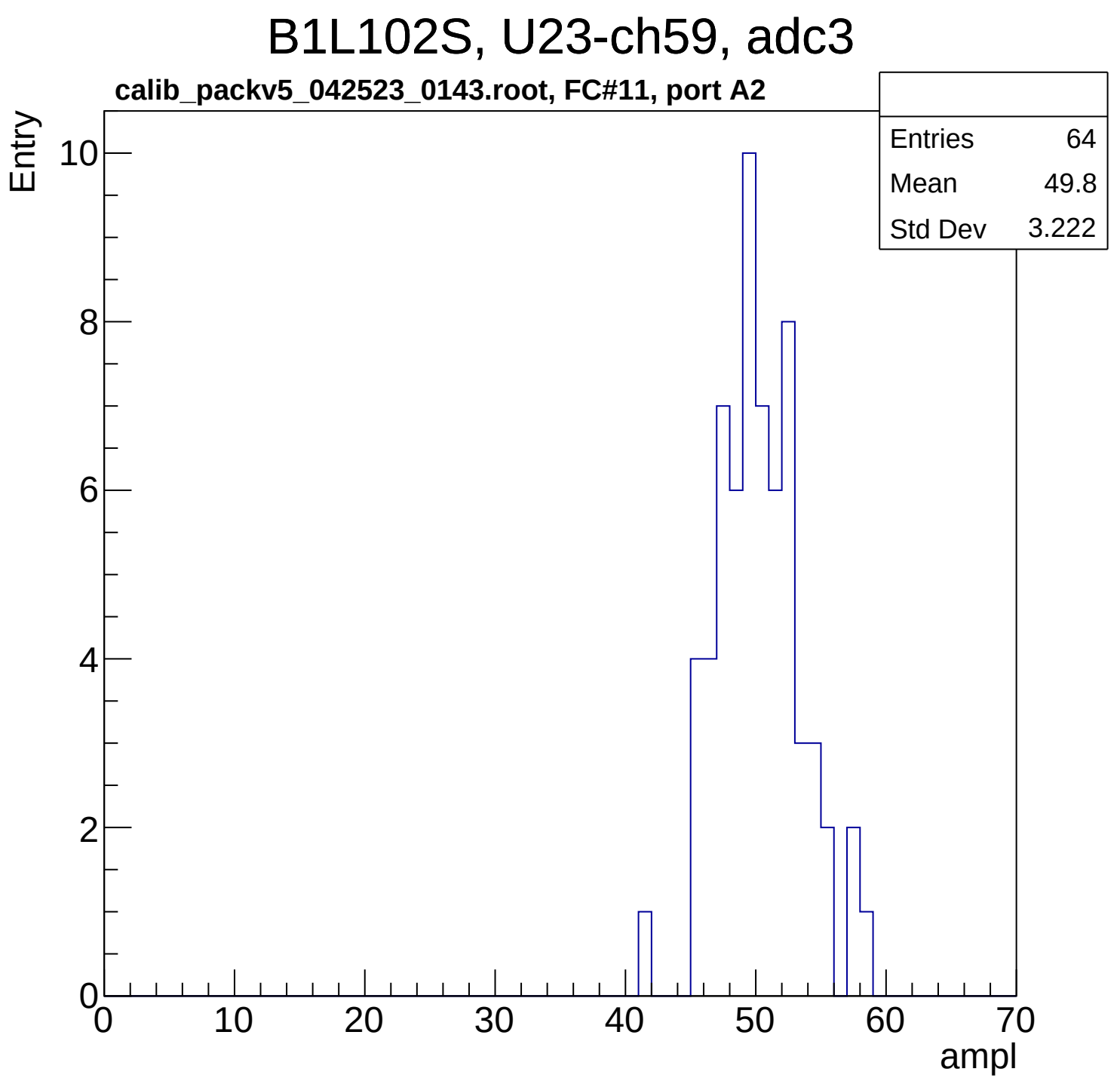

B1L102S, U23-ch58, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

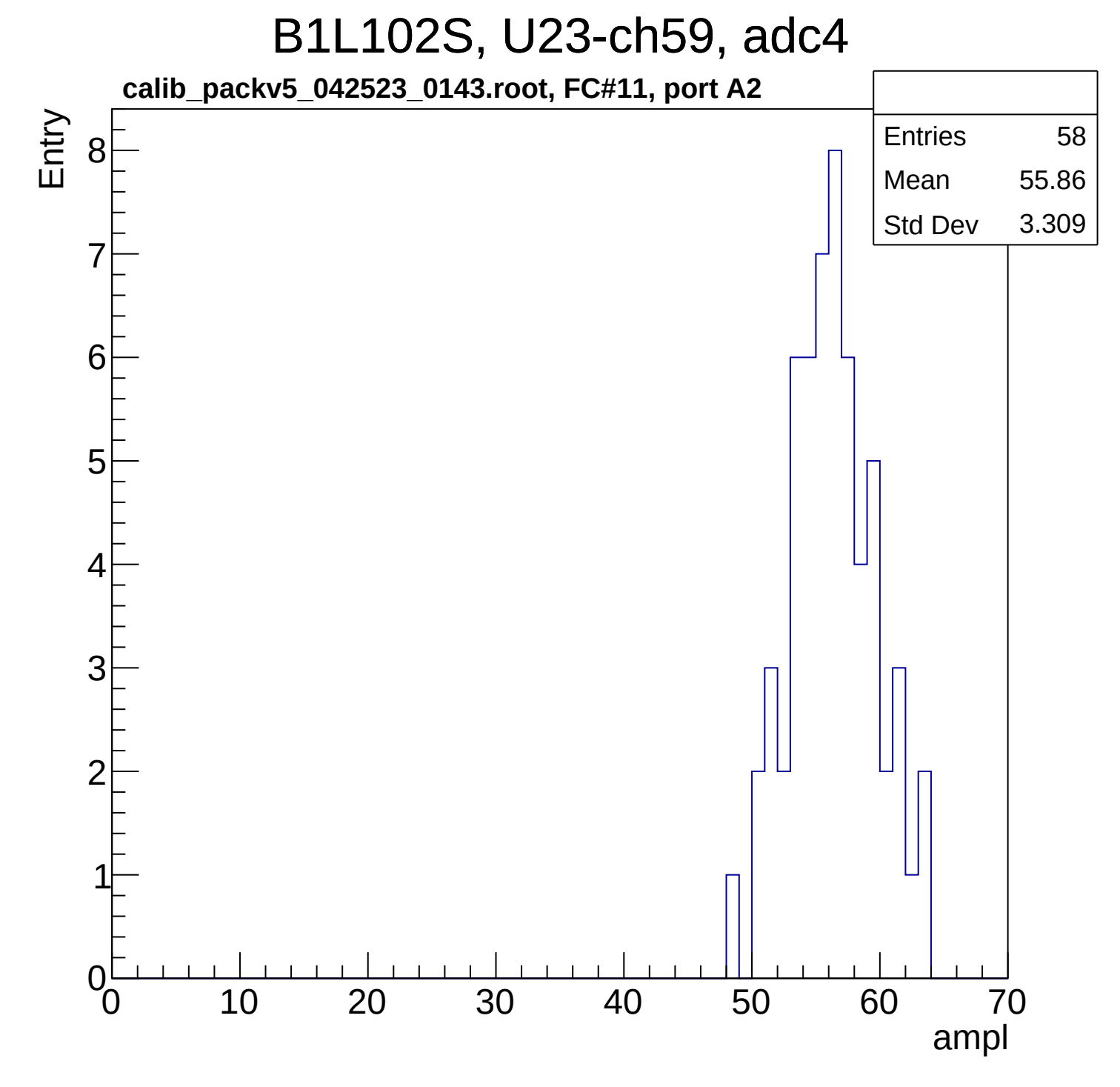

B1L102S, U23-ch58, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

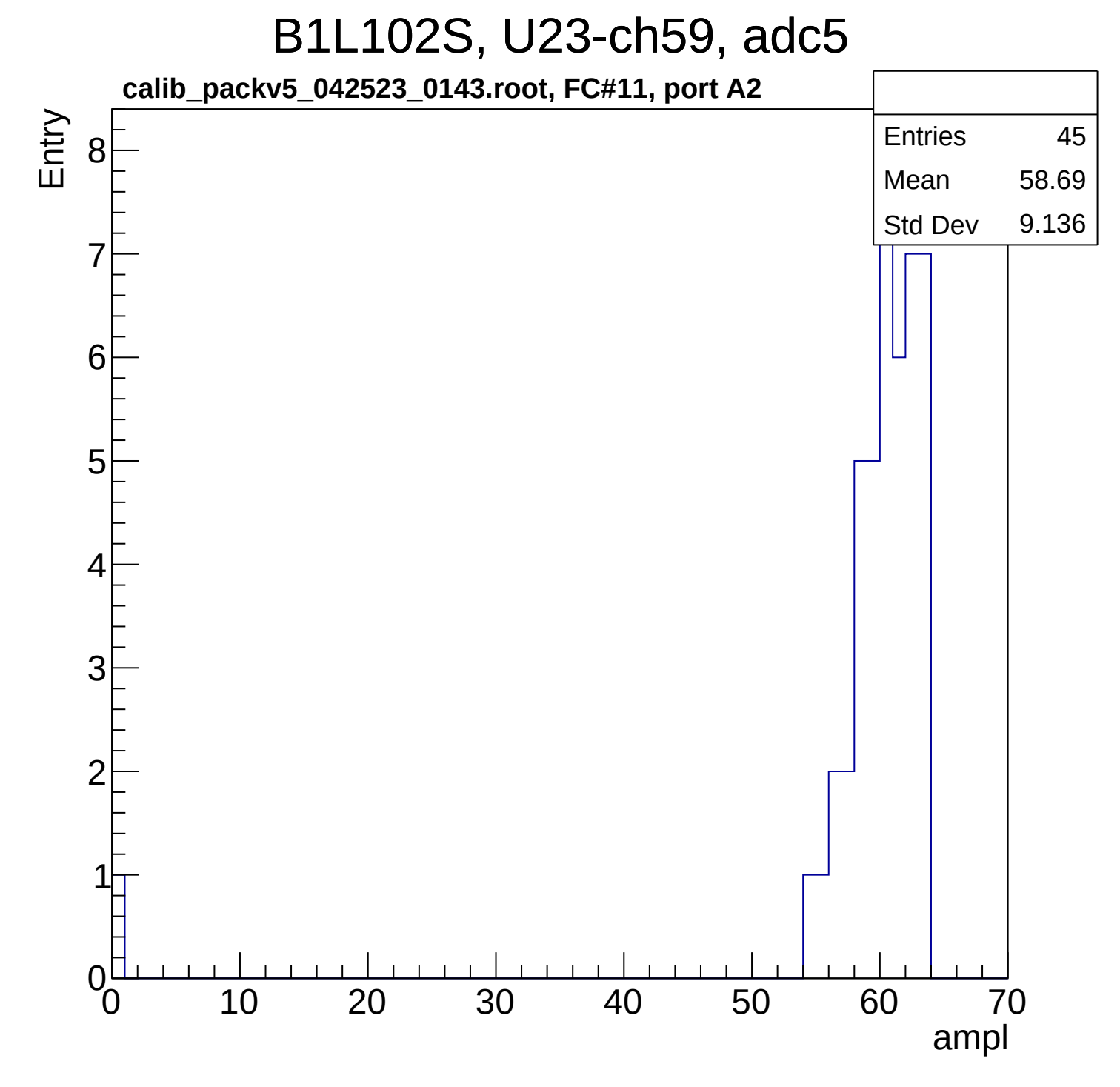

B1L102S, U23-ch59, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 30.53
Std Dev 3.423
Gaus mean : 30.1855
Gaus Width: 4.2202
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

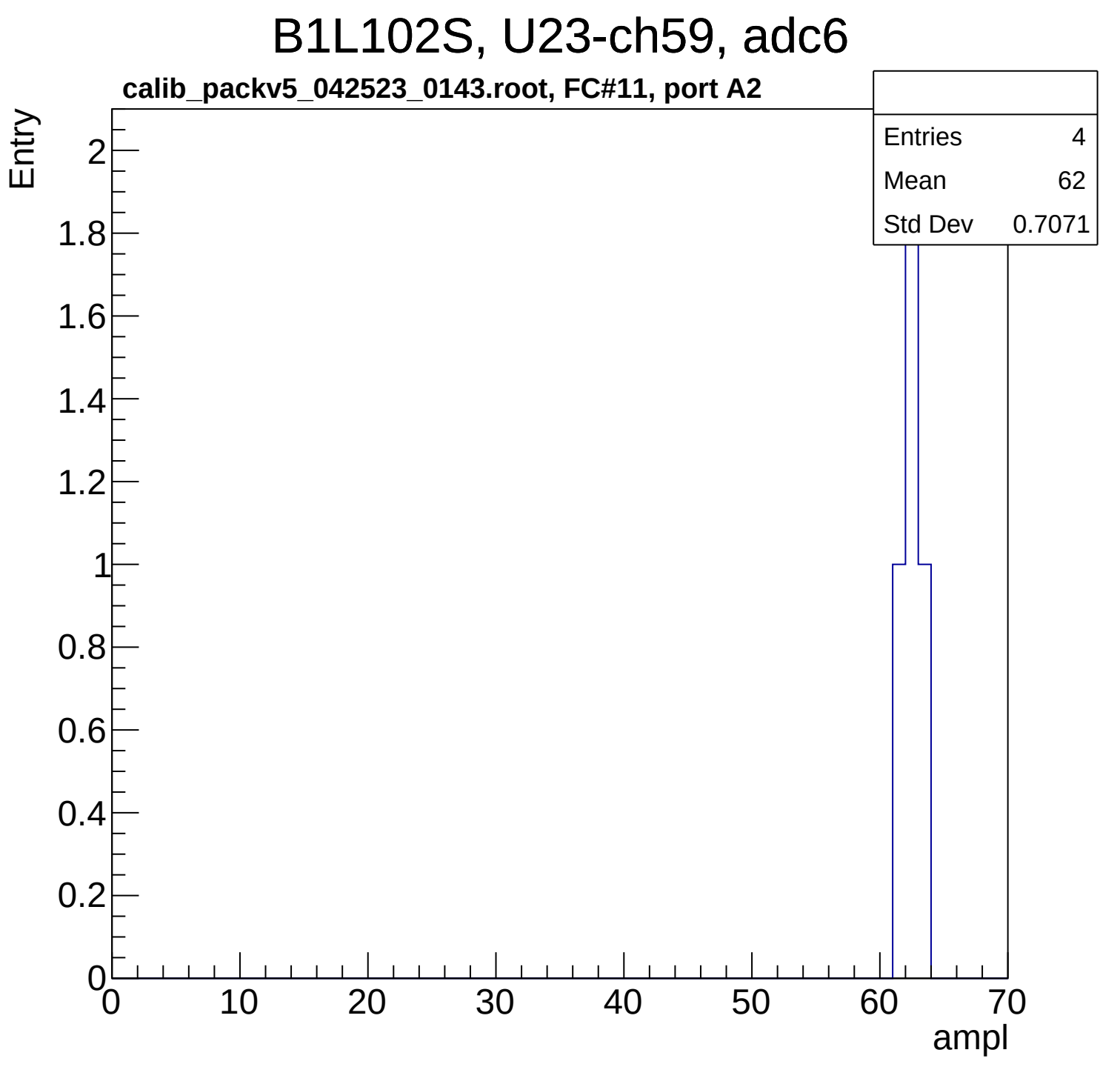

B1L102S, U23-ch59, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 36.78
Std Dev 5.695
Gaus mean : 37.1919
Gaus Width: 3.4380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

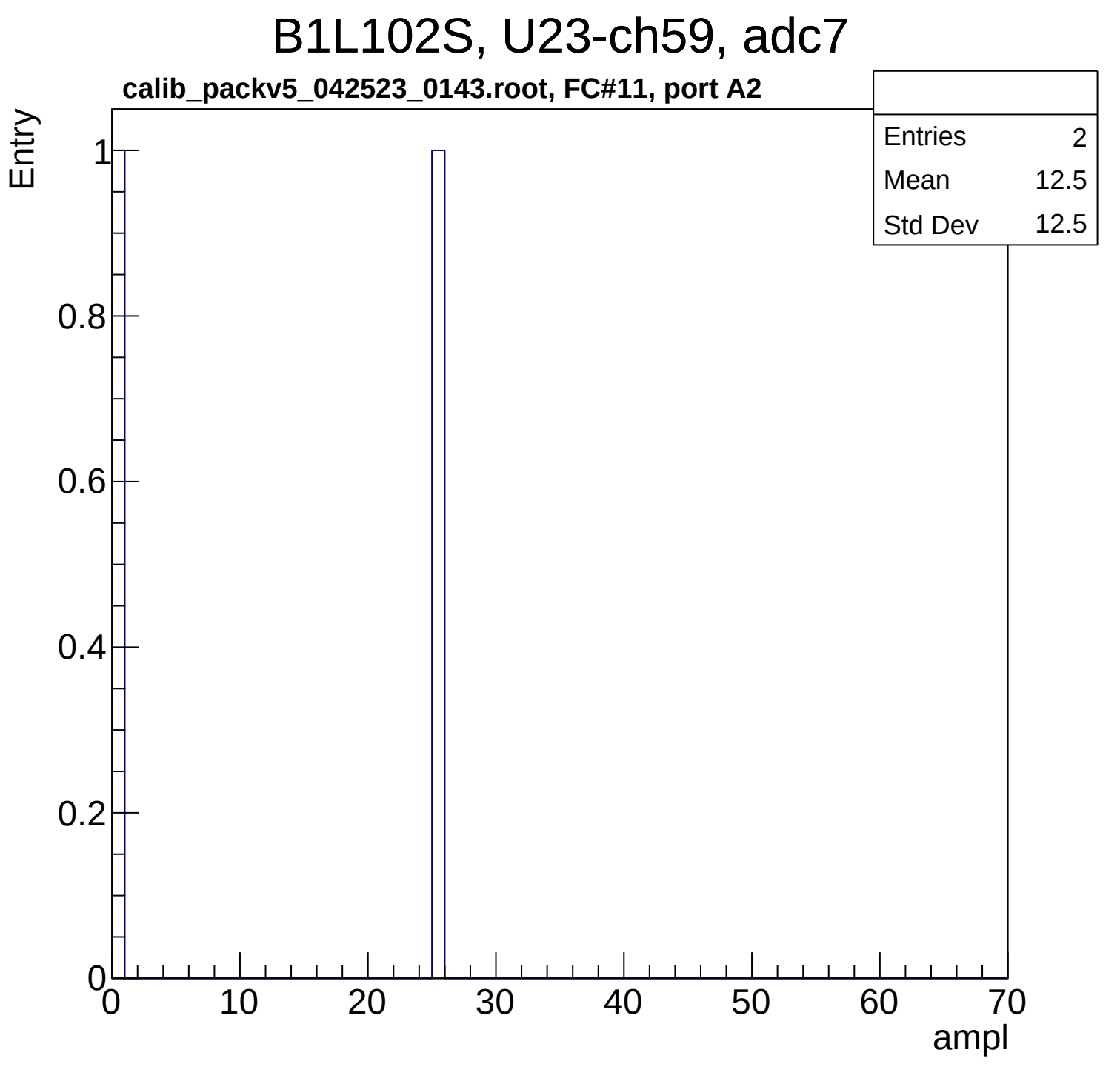

B1L102S, U23-ch59, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 43.12
Std Dev 3.407
Gaus mean : 43.5199
Gaus Width: 3.6536
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

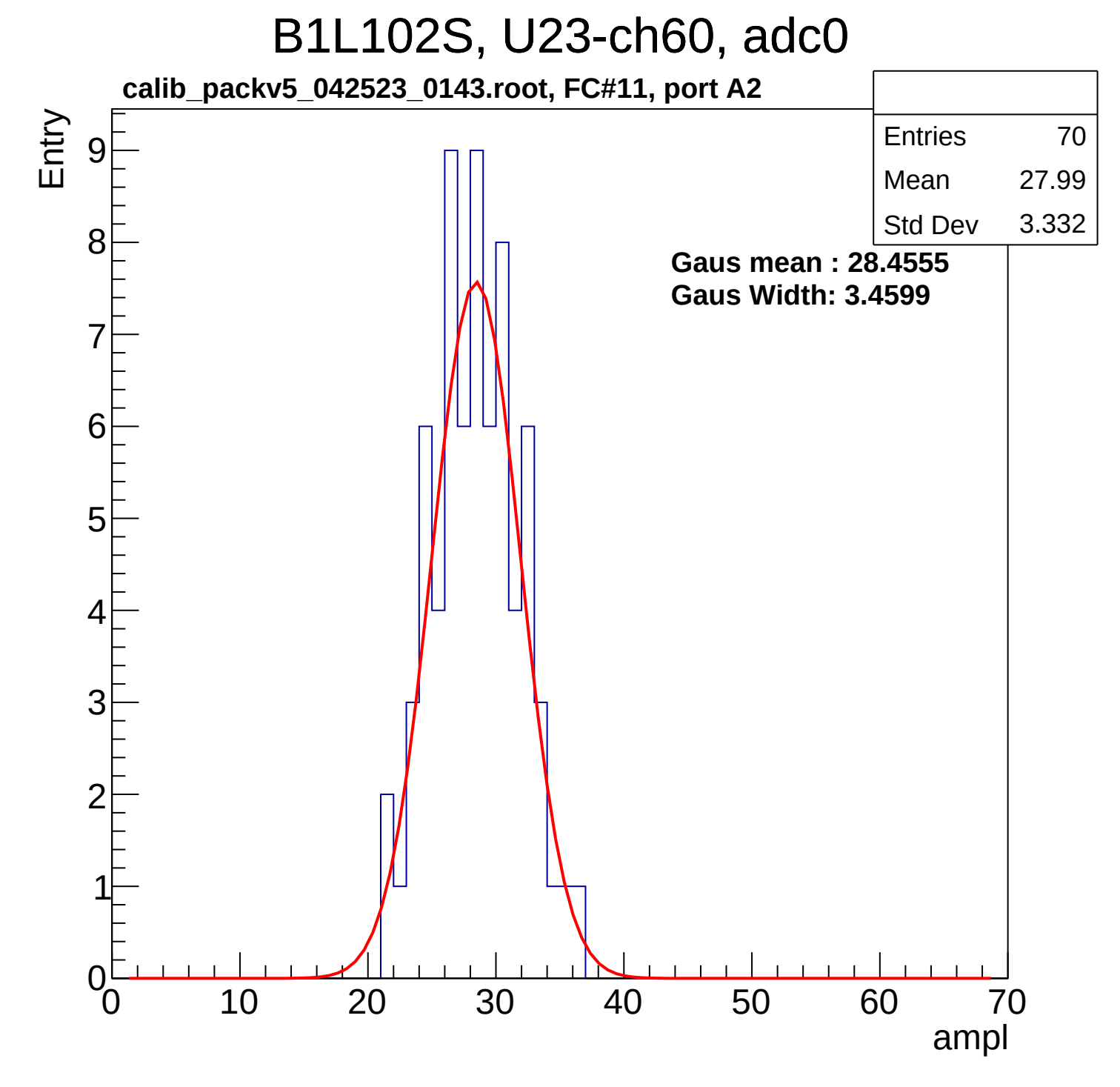

B1L102S, U23-ch59, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 49.8
Std Dev 3.222
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

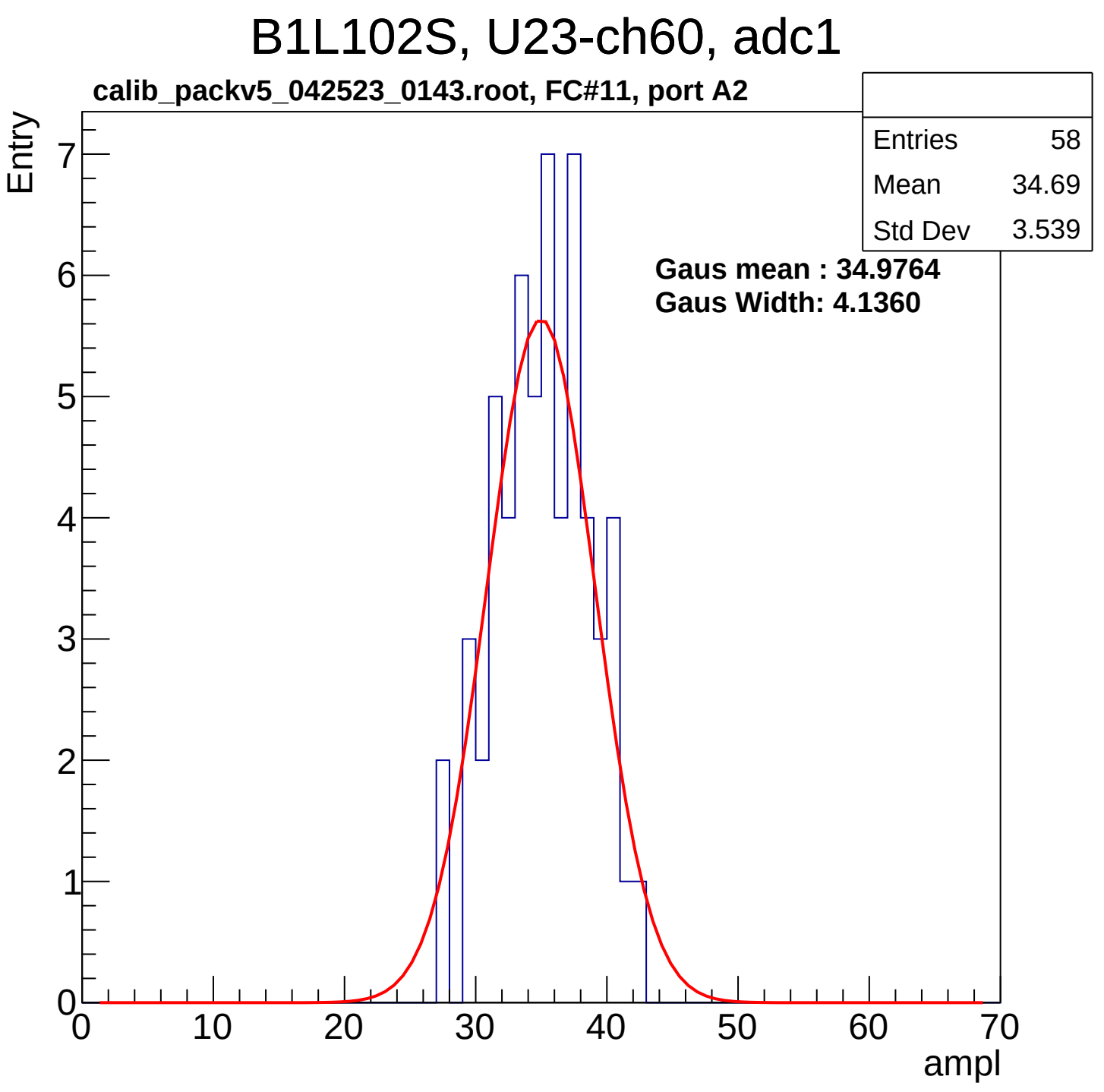

B1L102S, U23-ch59, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.86
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

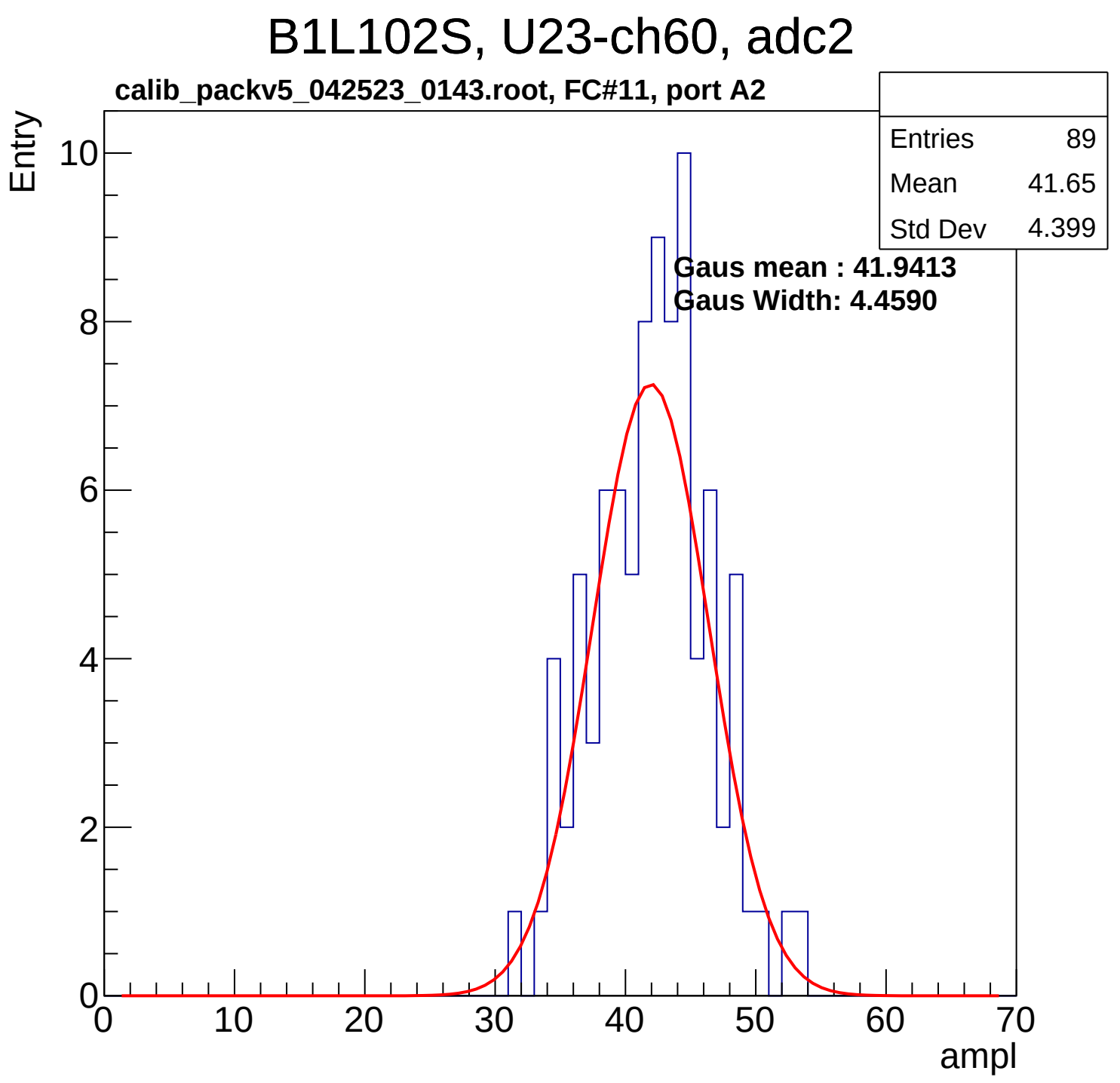

B1L102S, U23-ch59, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.69
Std Dev 9.136
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

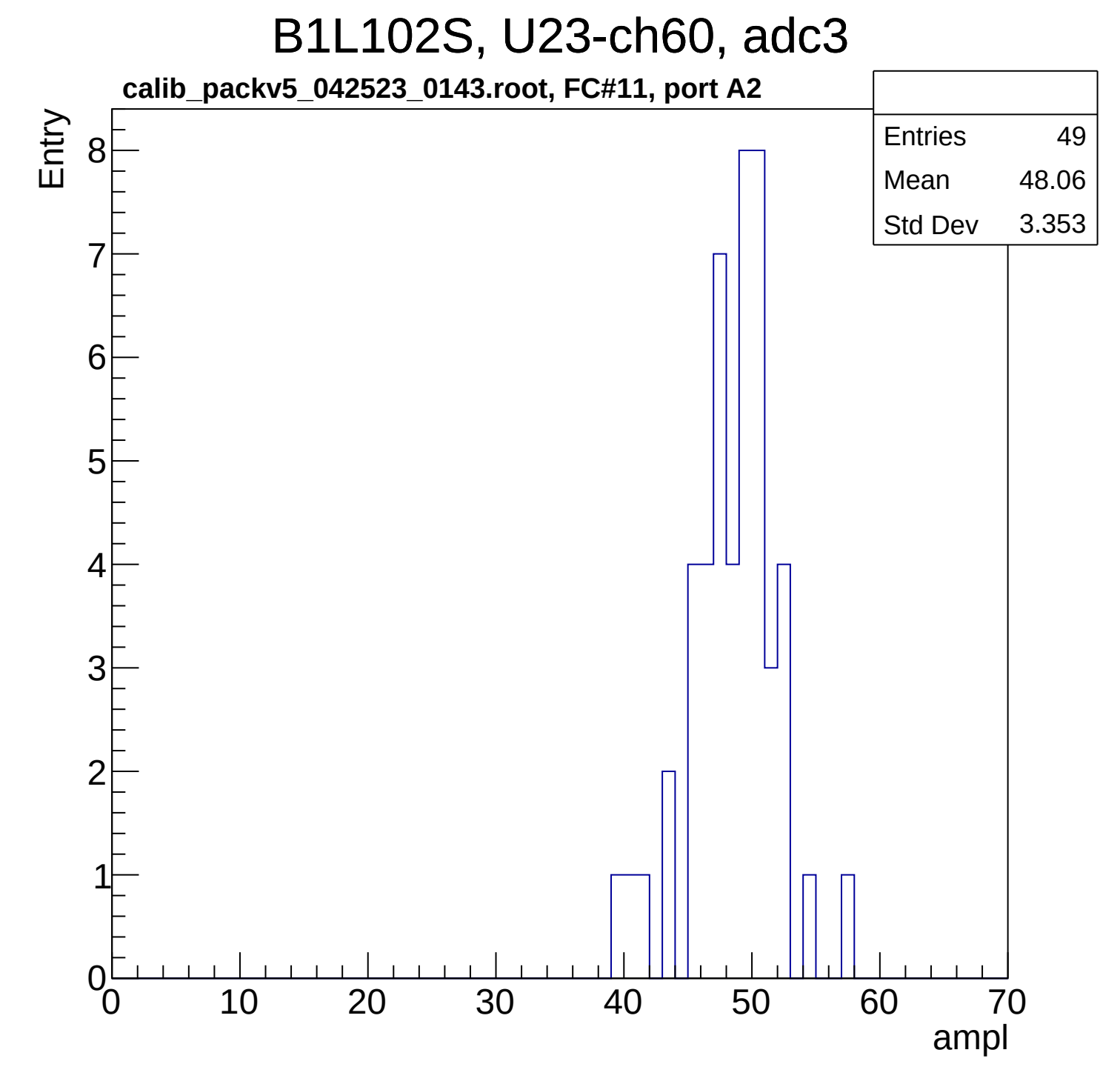

B1L102S, U23-ch59, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

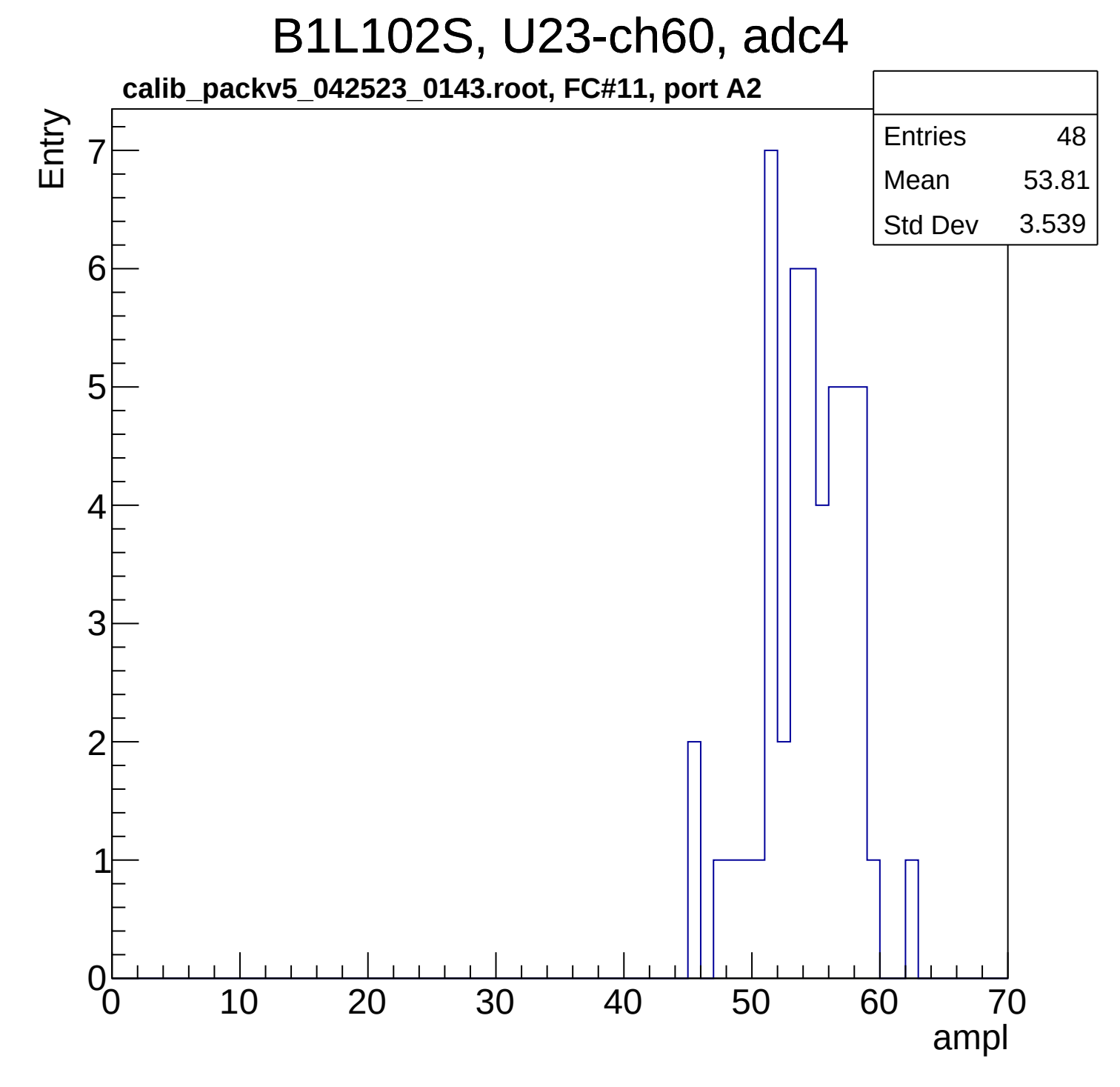

B1L102S, U23-ch59, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

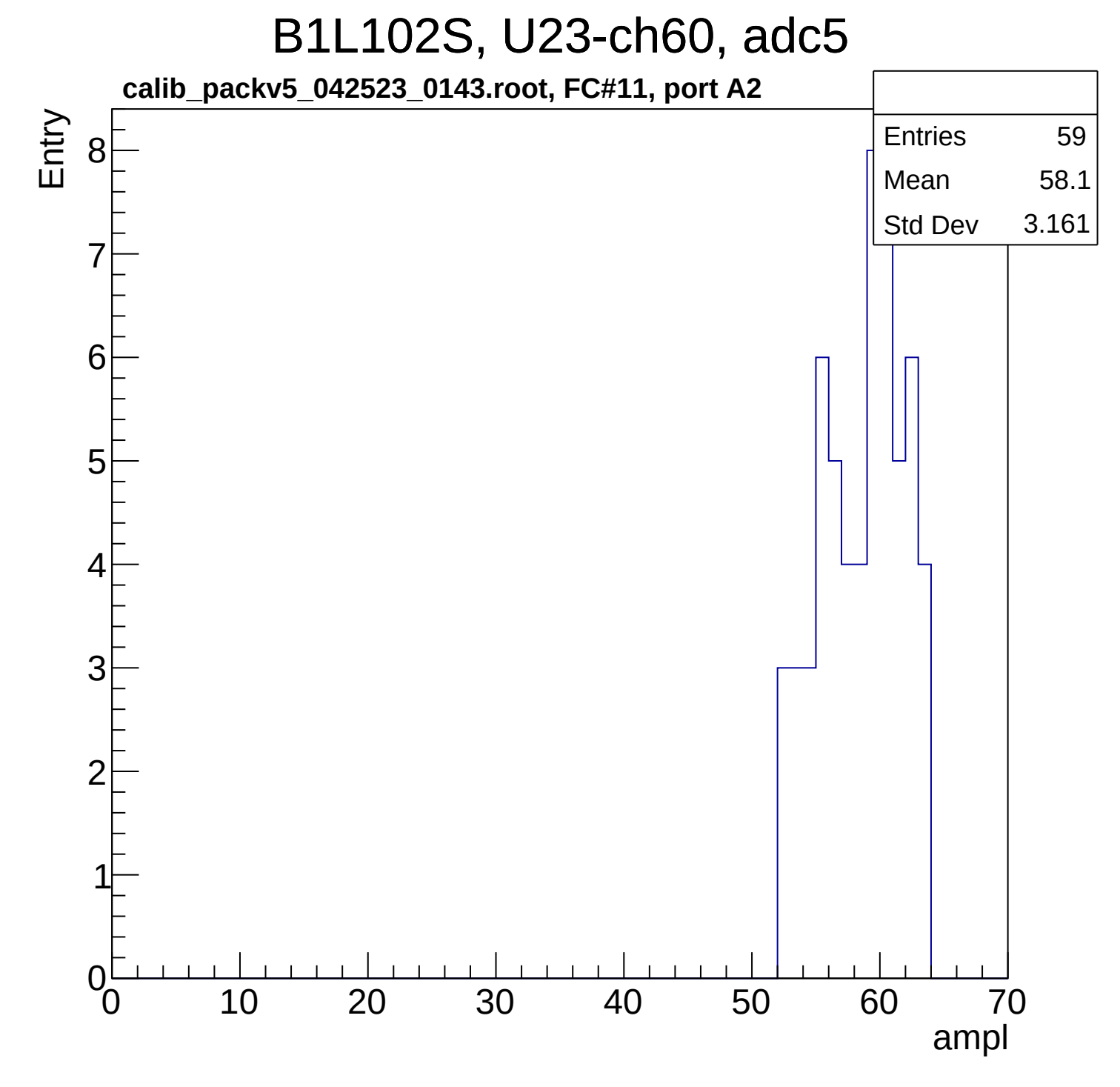

B1L102S, U23-ch60, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 27.99
Std Dev 3.332
Gaus mean : 28.4555
Gaus Width: 3.4599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

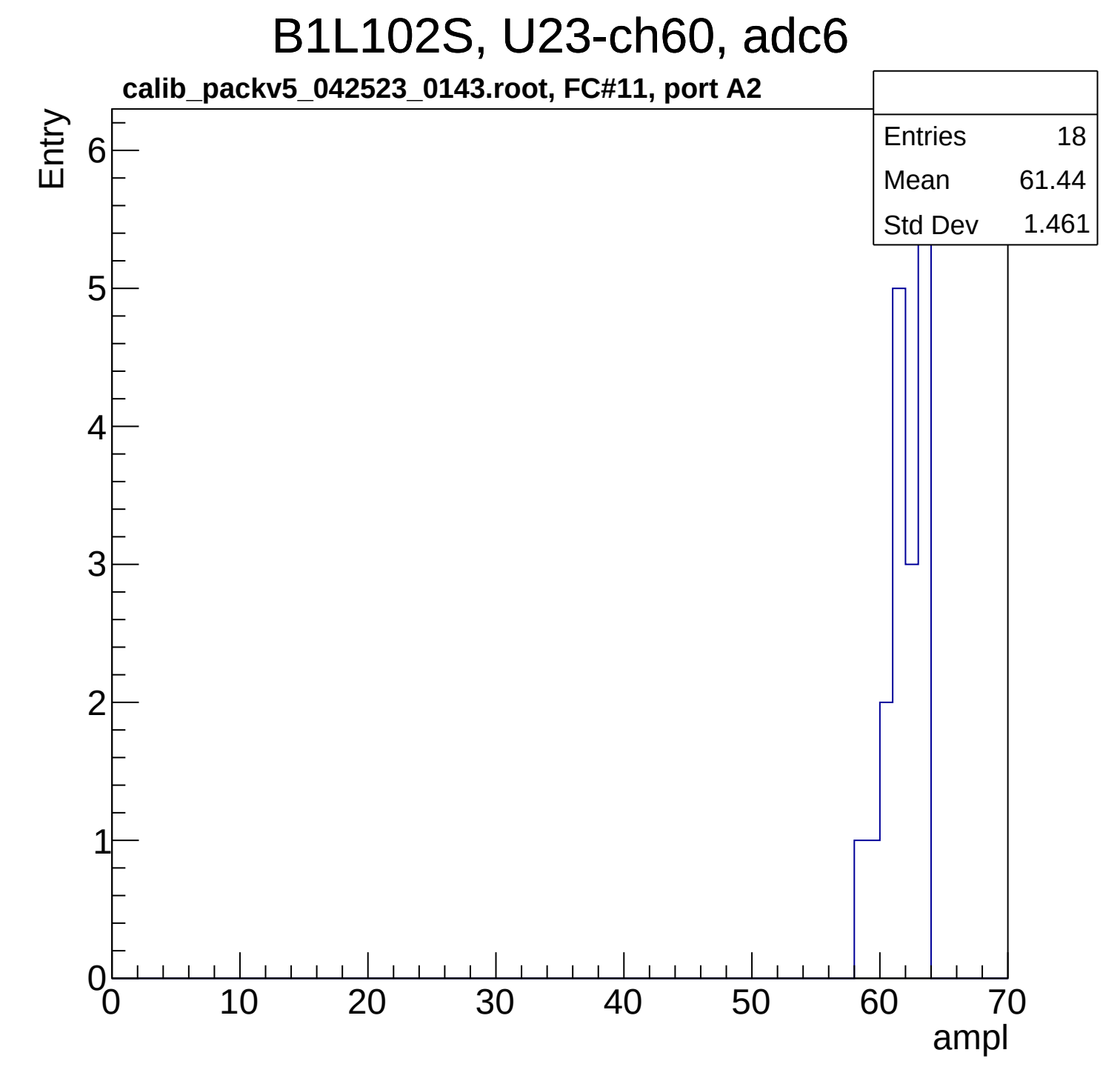

B1L102S, U23-ch60, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 34.69
Std Dev 3.539
Gaus mean : 34.9764
Gaus Width: 4.1360
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

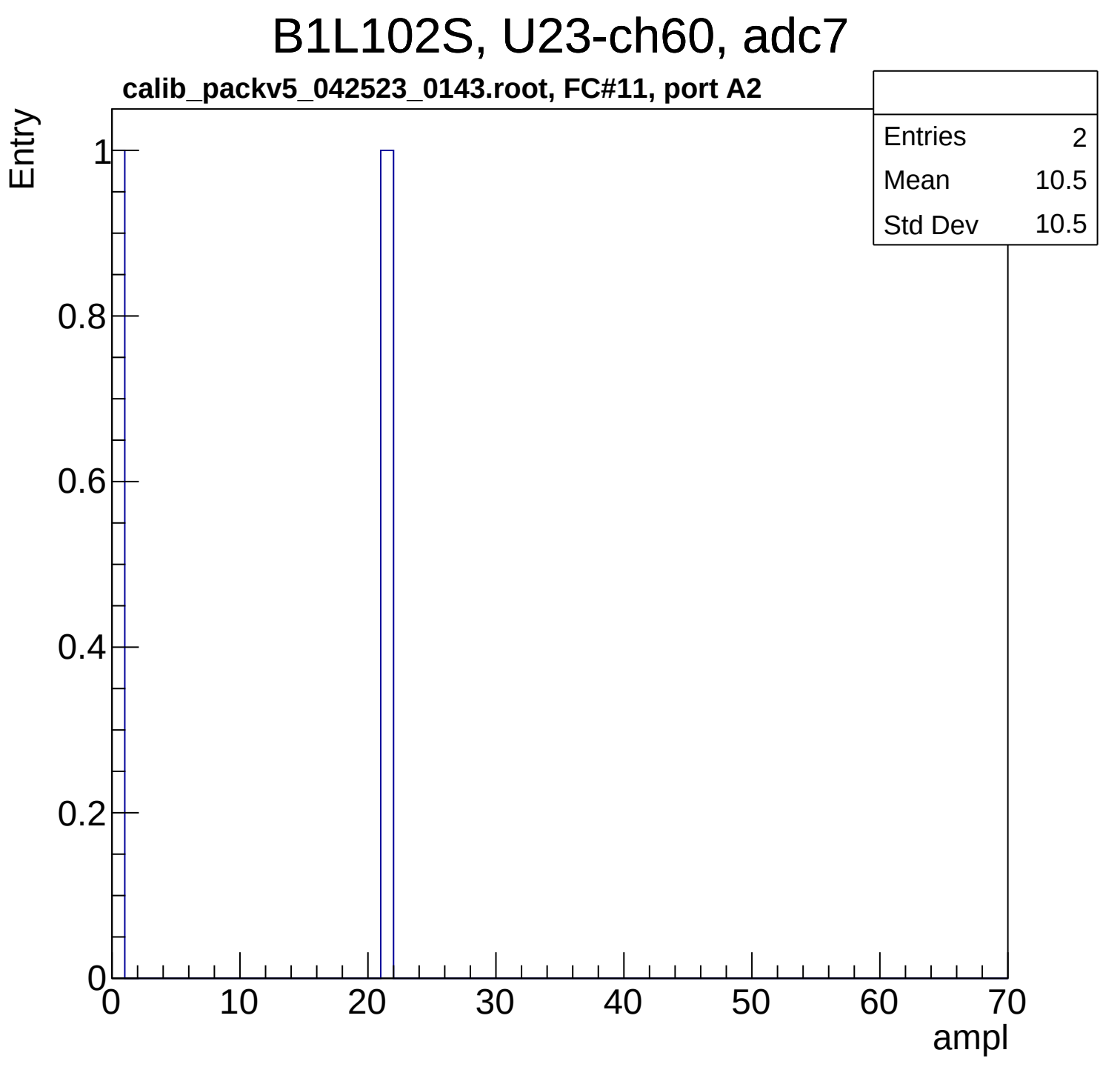

B1L102S, U23-ch60, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 41.65
Std Dev 4.399
Gaus mean : 41.9413
Gaus Width: 4.4590
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

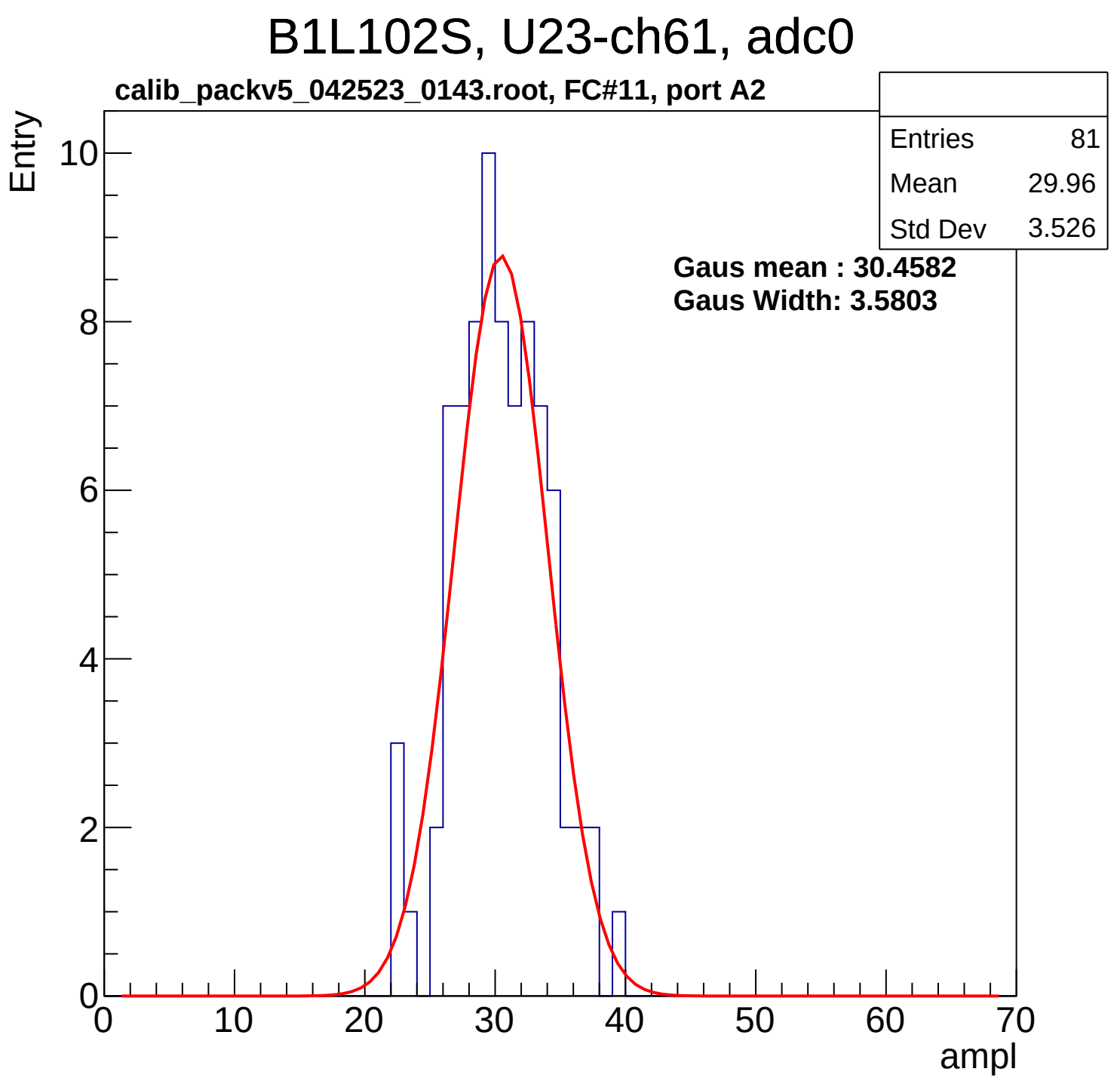

B1L102S, U23-ch60, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 48.06
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

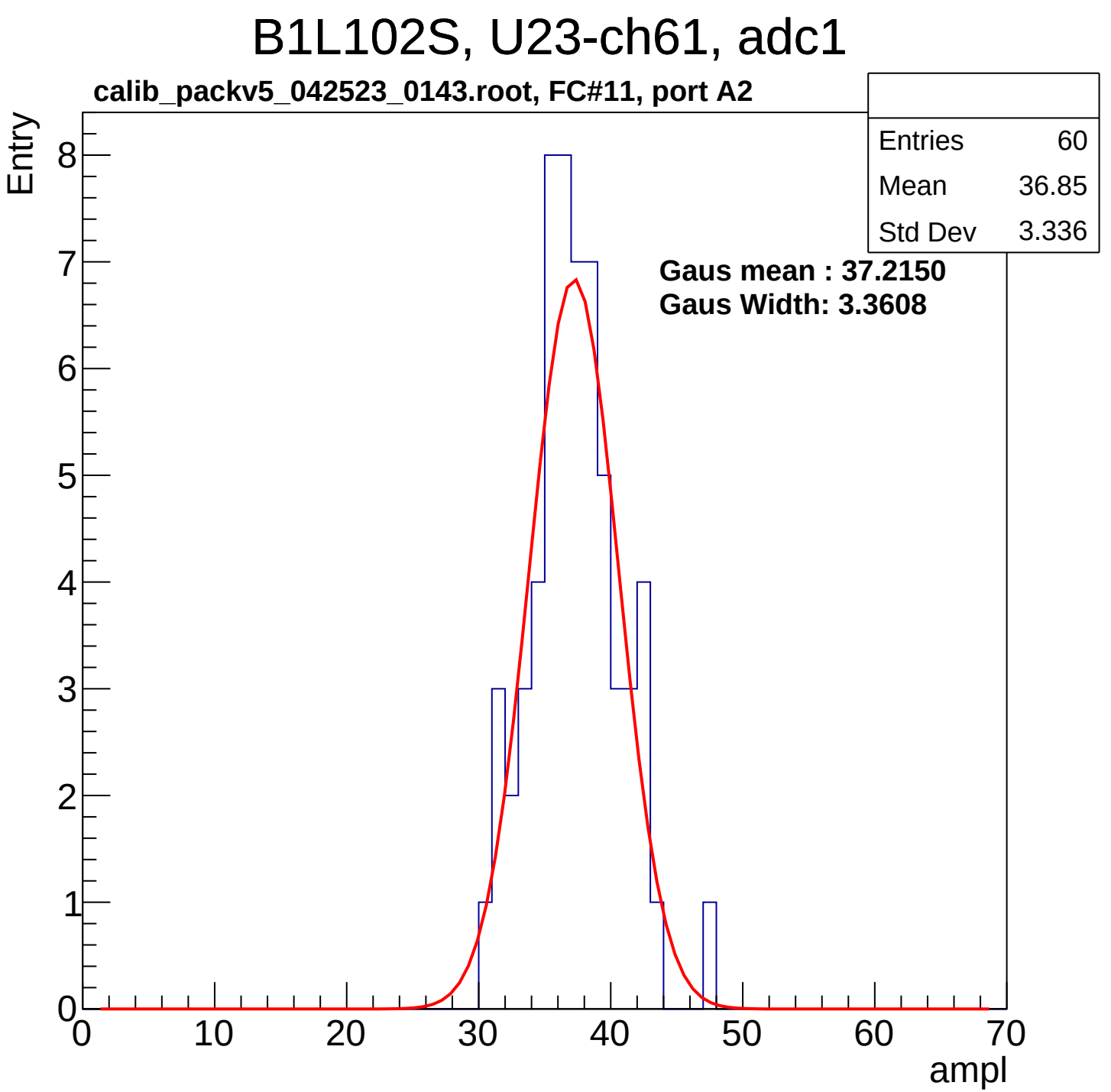

B1L102S, U23-ch60, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 53.81
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

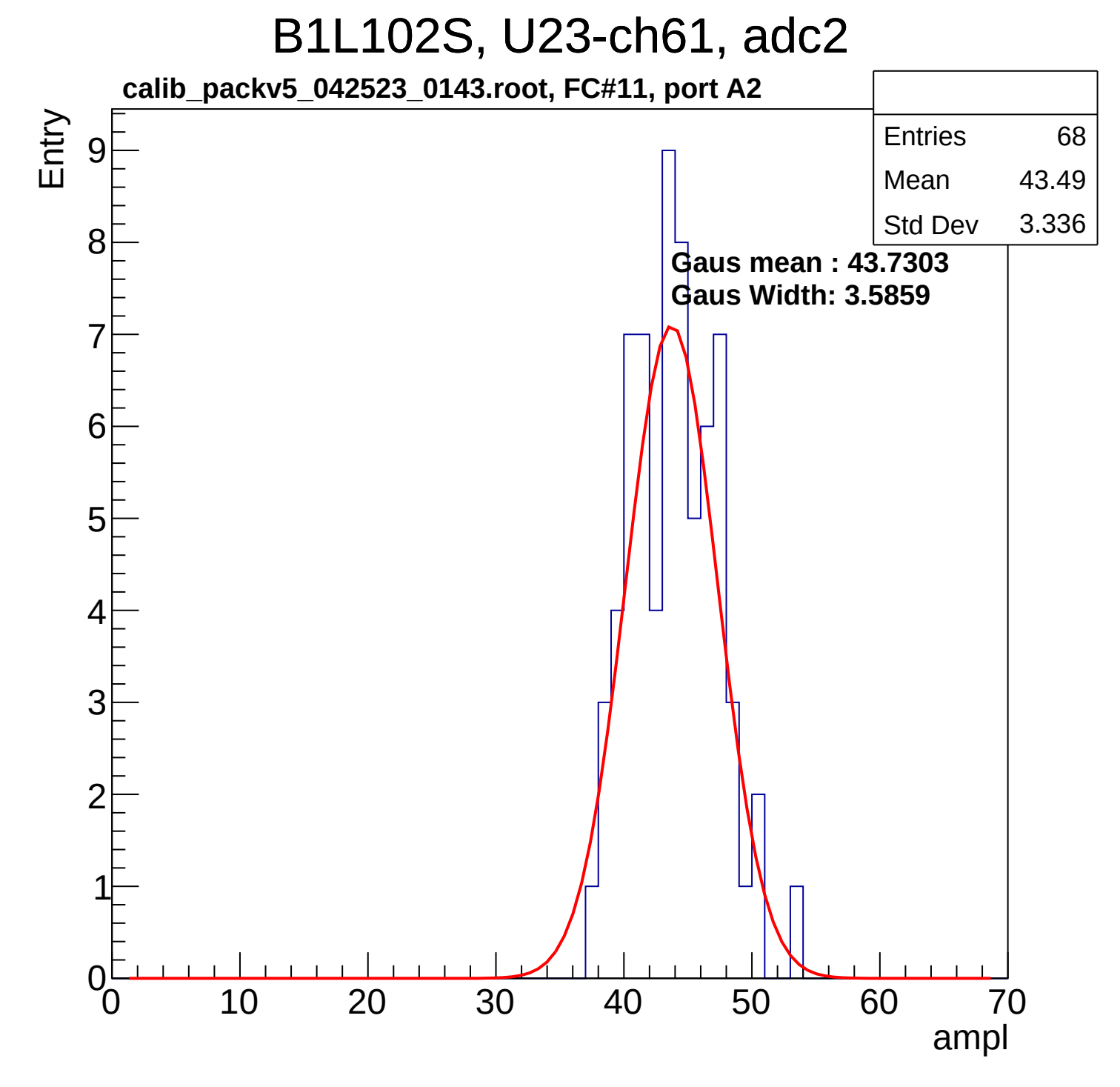

B1L102S, U23-ch60, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 58.1
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

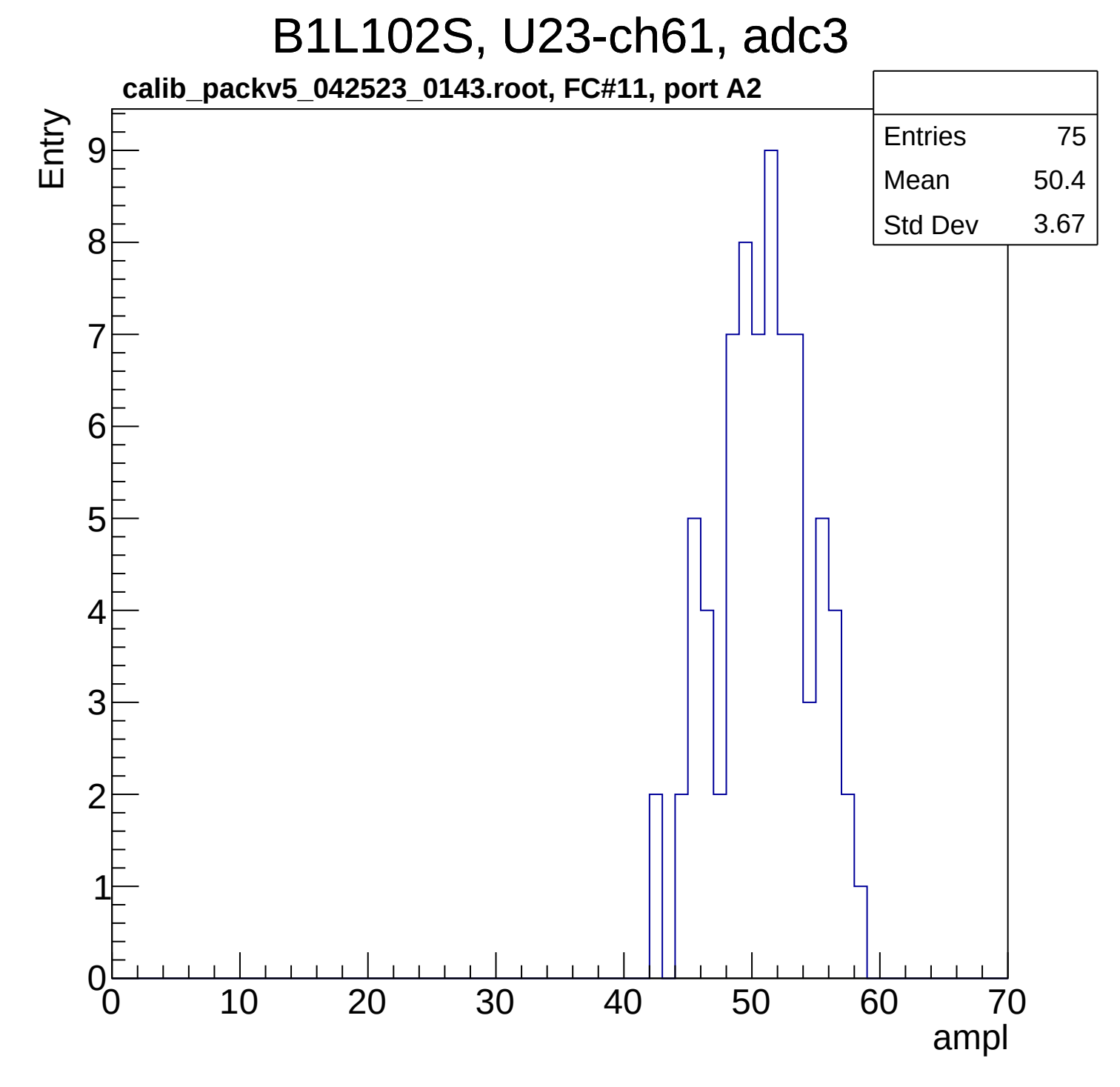

B1L102S, U23-ch60, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 61.44
Std Dev 1.461
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

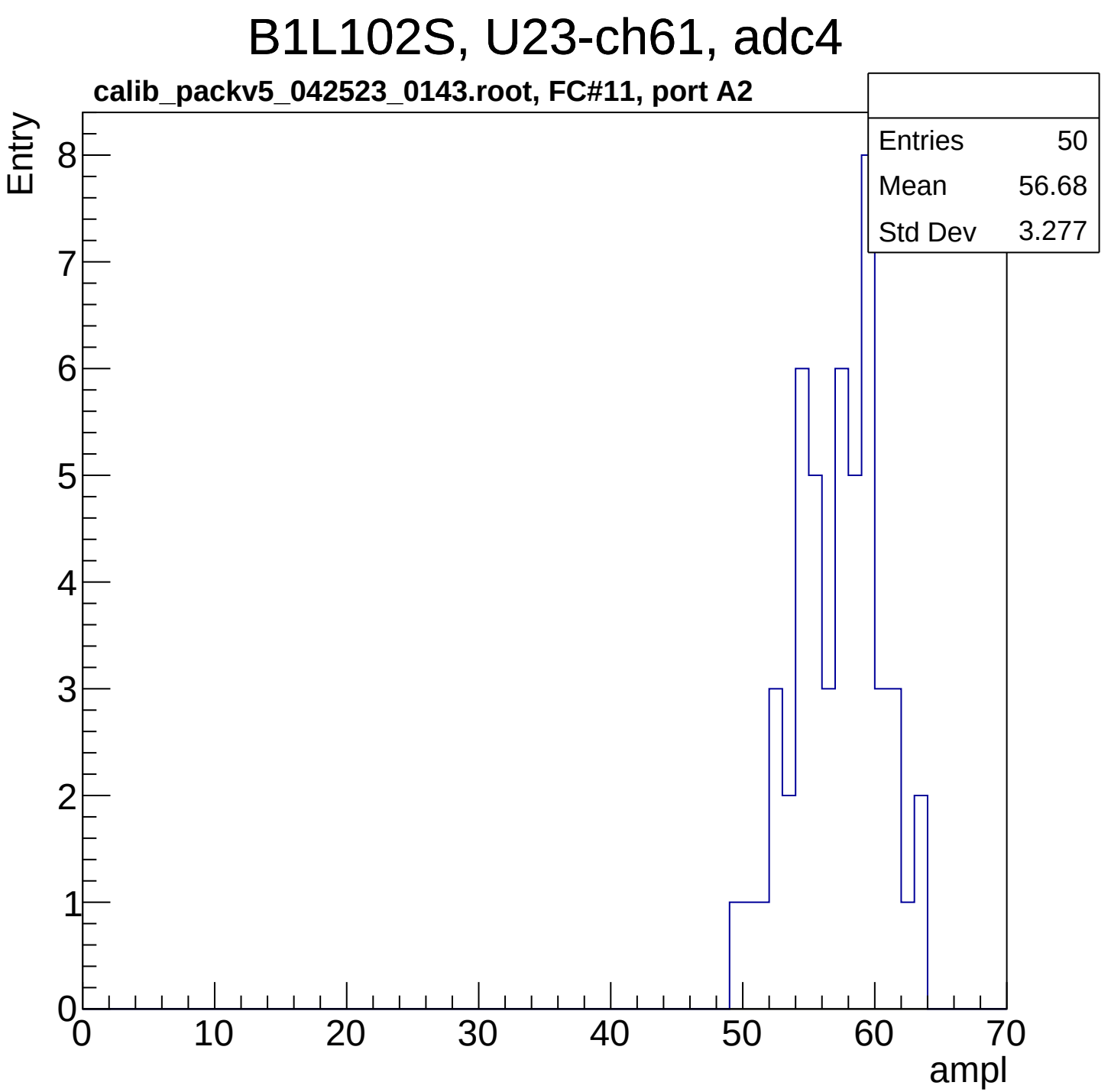

B1L102S, U23-ch60, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

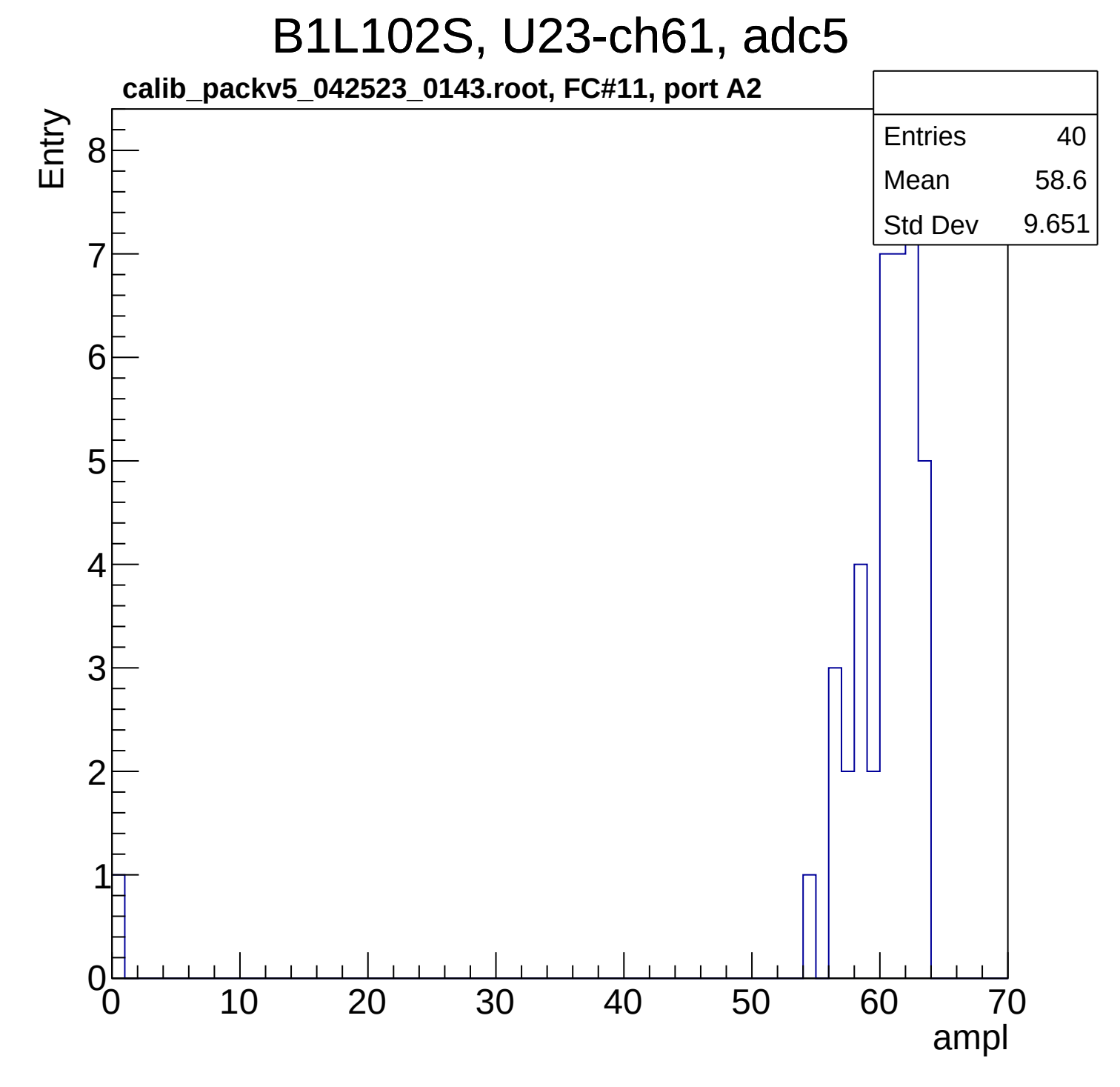

B1L102S, U23-ch61, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 29.96
Std Dev 3.526
Gaus mean : 30.4582
Gaus Width: 3.5803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

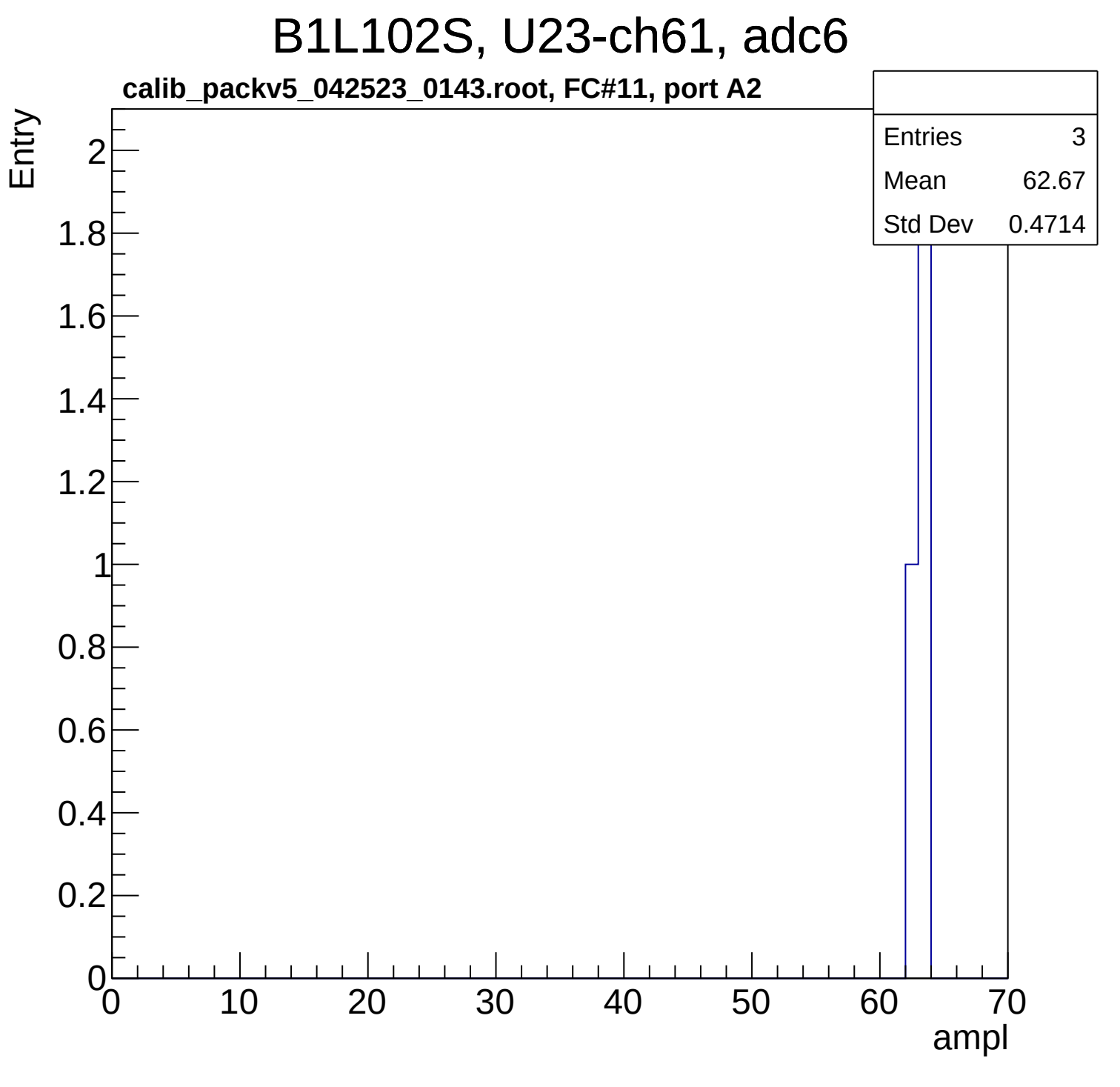

B1L102S, U23-ch61, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 36.85
Std Dev 3.336
Gaus mean : 37.2150
Gaus Width: 3.3608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

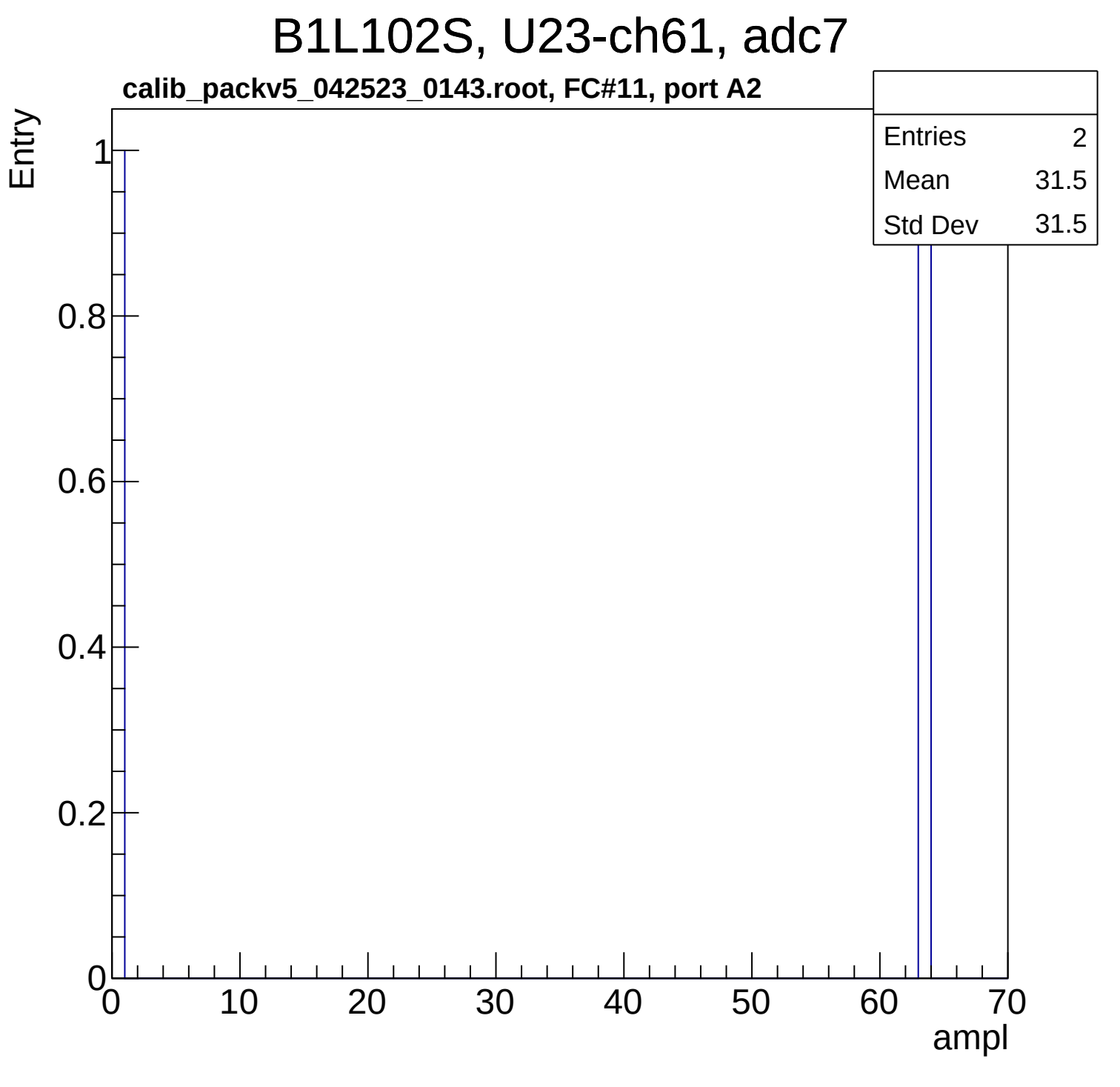

B1L102S, U23-ch61, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 43.49
Std Dev 3.336
Gaus mean : 43.7303
Gaus Width: 3.5859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

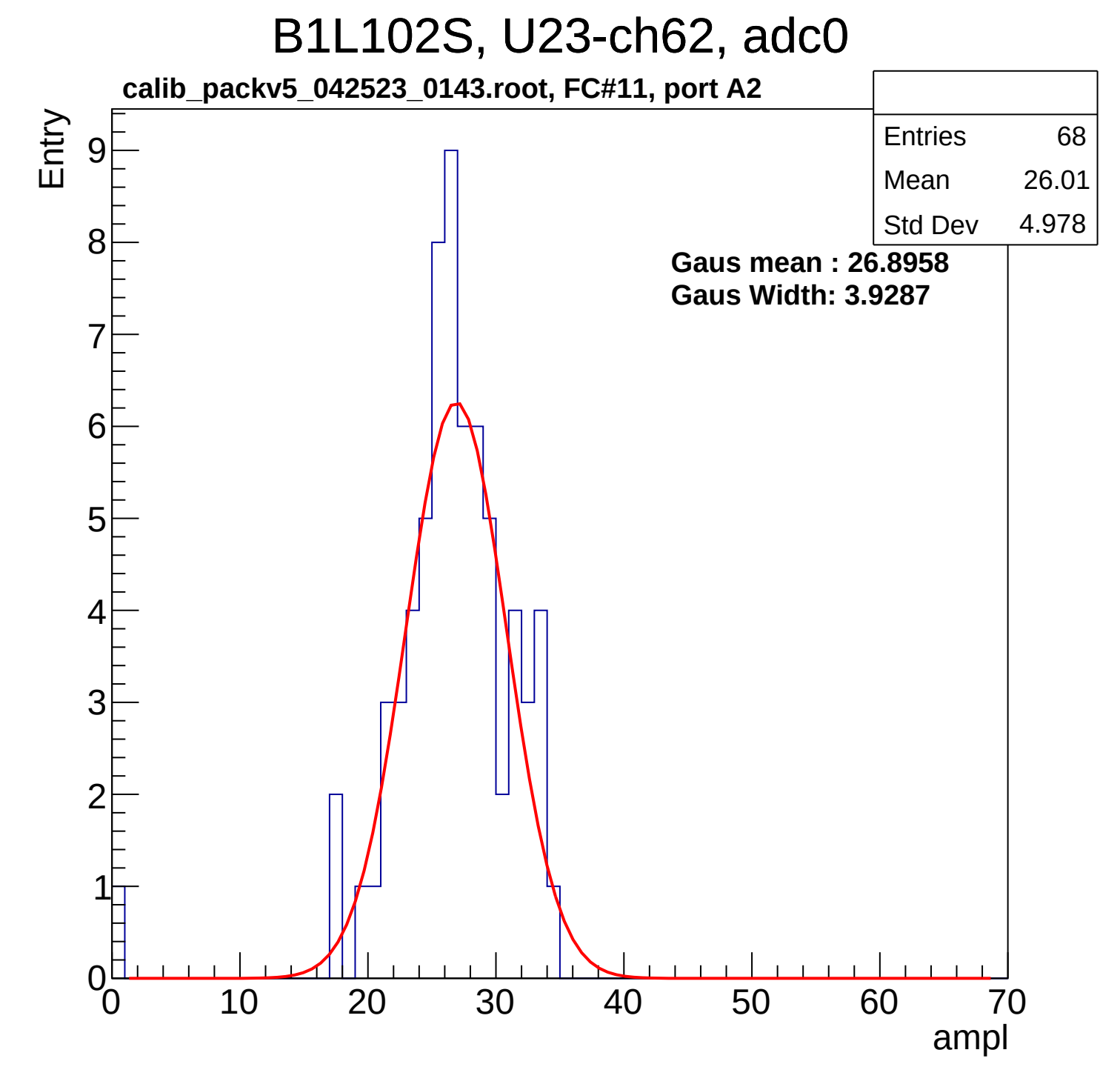

B1L102S, U23-ch61, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 50.4
Std Dev 3.67
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

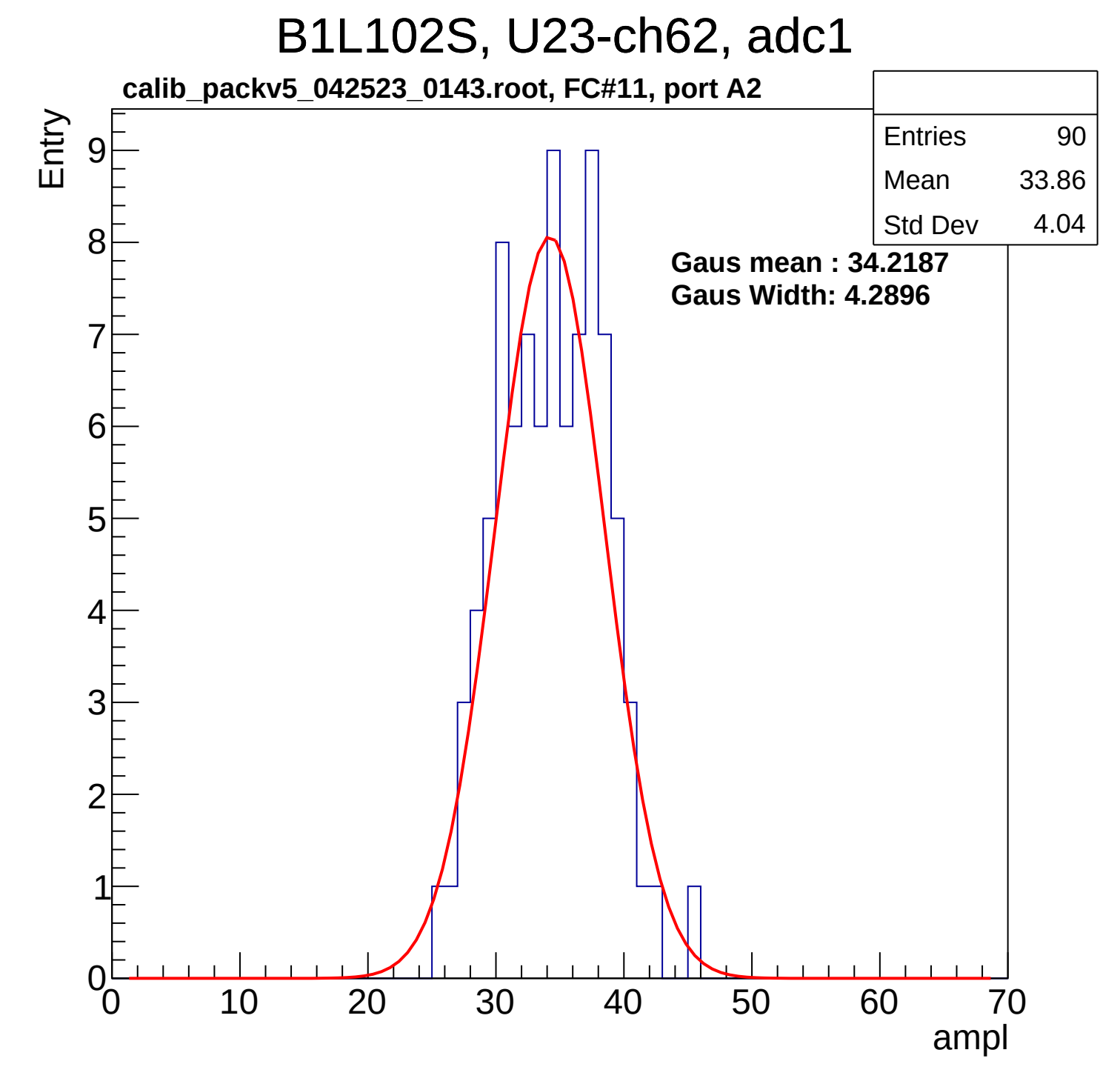

B1L102S, U23-ch61, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 56.68
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

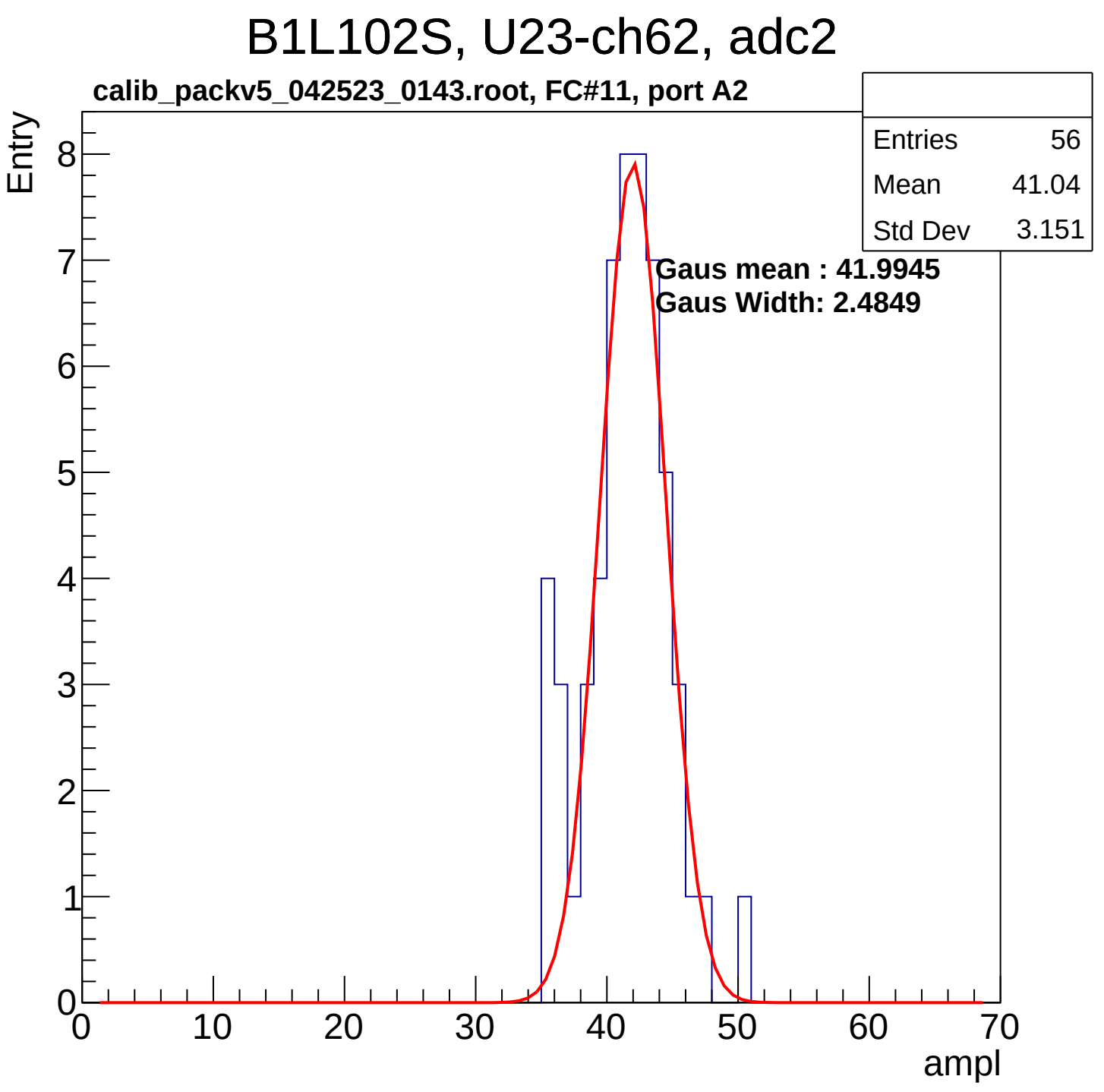

B1L102S, U23-ch61, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.6
Std Dev 9.651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

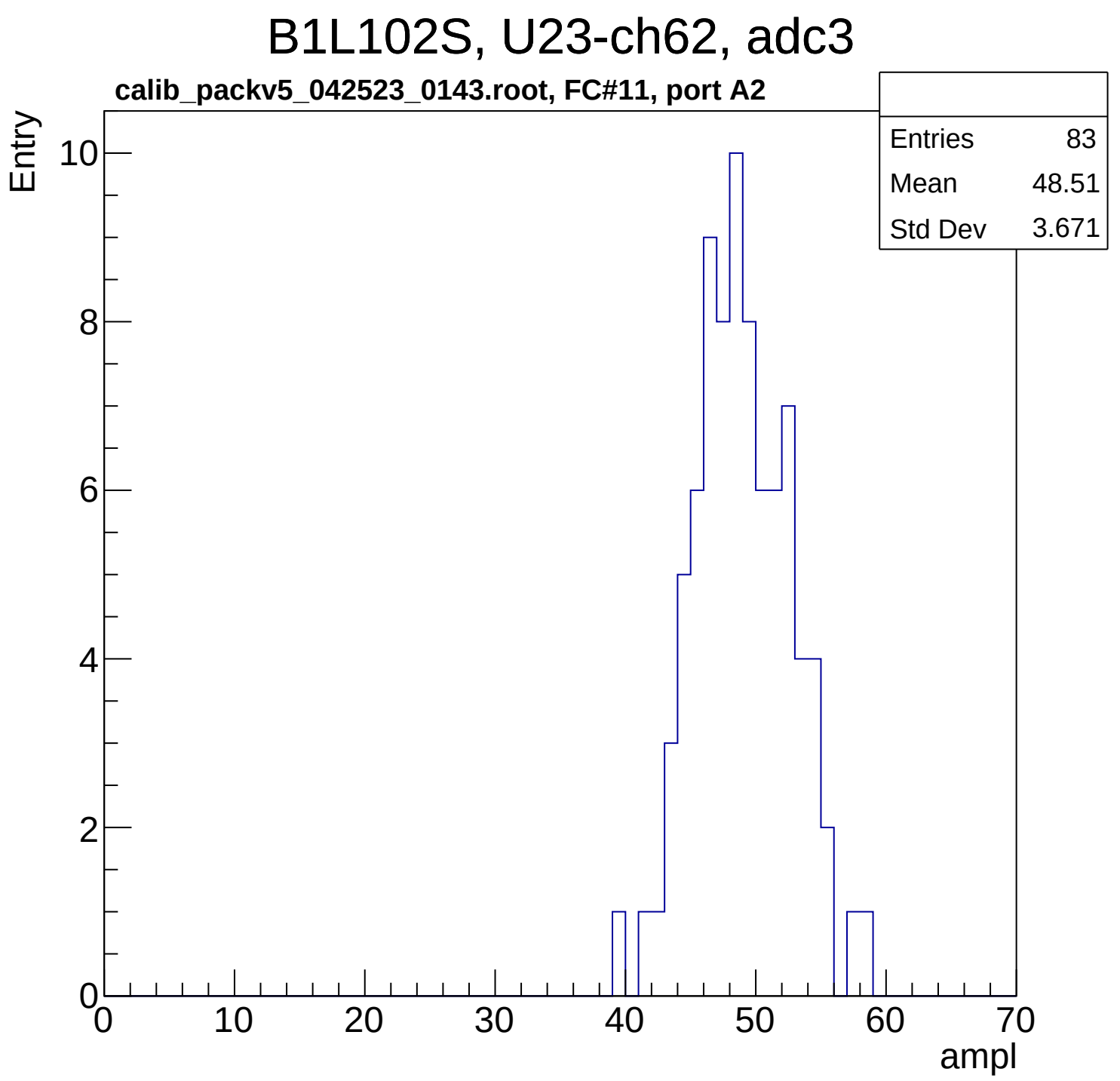

B1L102S, U23-ch61, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

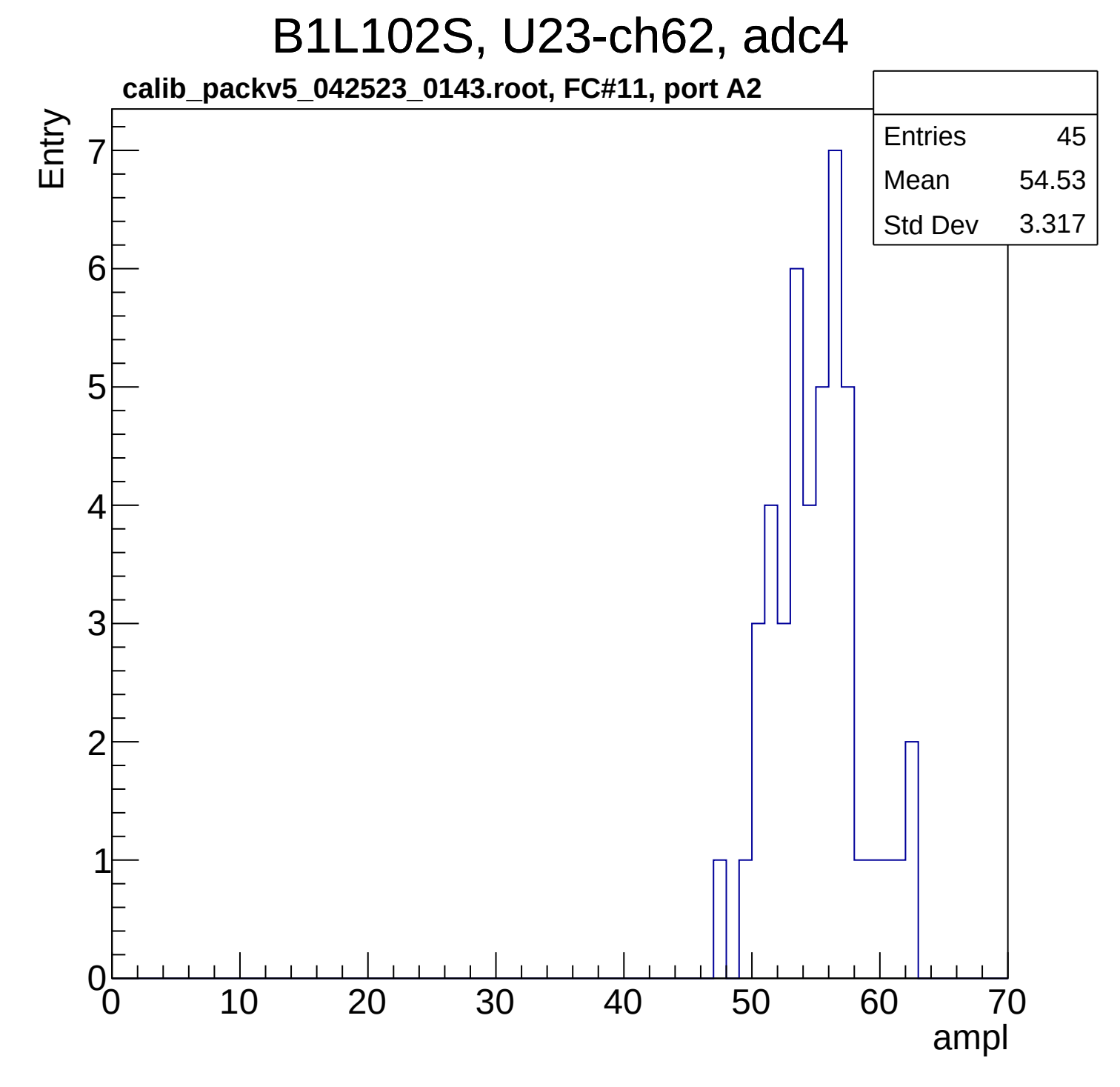

B1L102S, U23-ch61, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

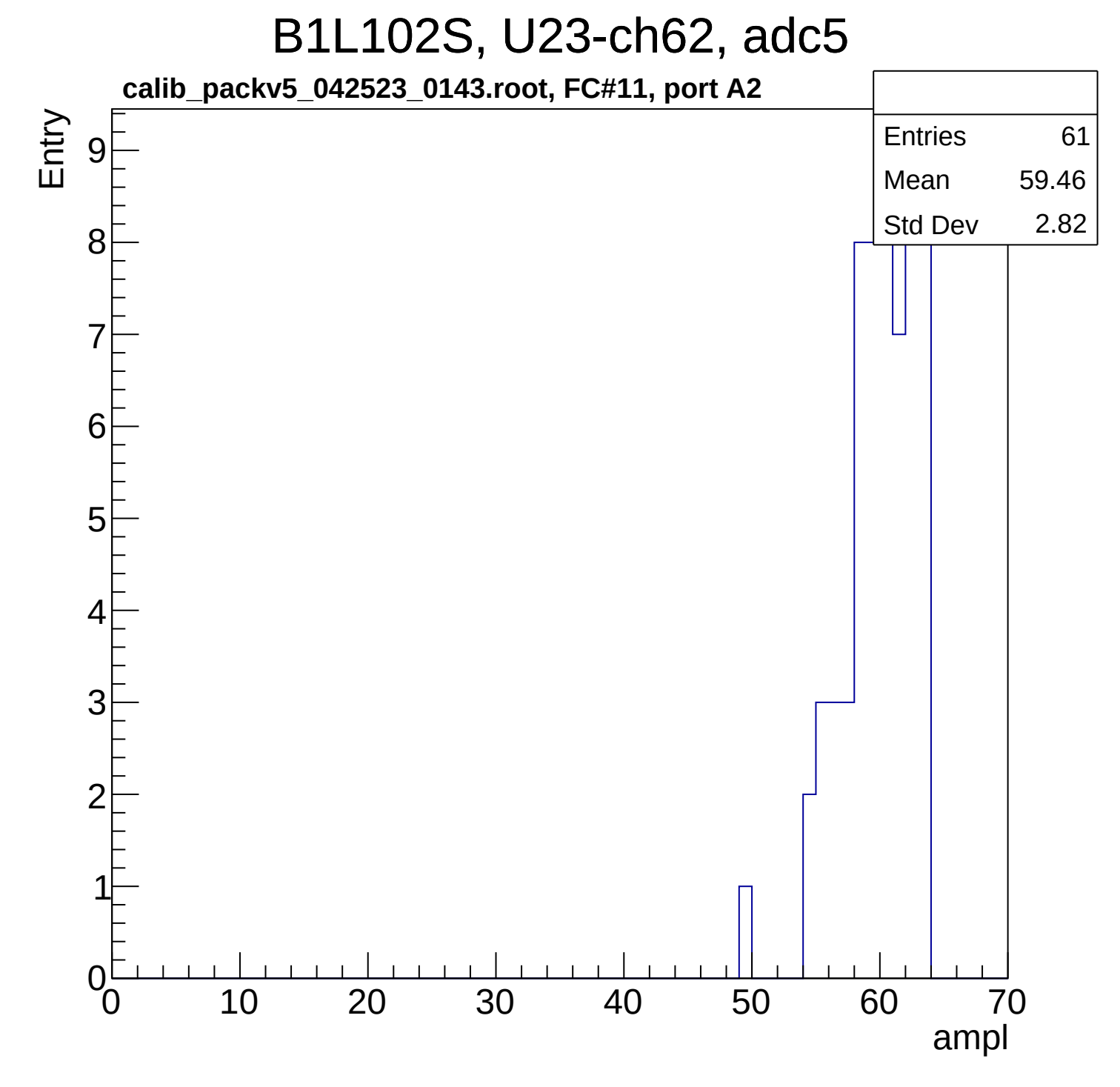

B1L102S, U23-ch62, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 26.01
Std Dev 4.978
Gaus mean : 26.8958
Gaus Width: 3.9287
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

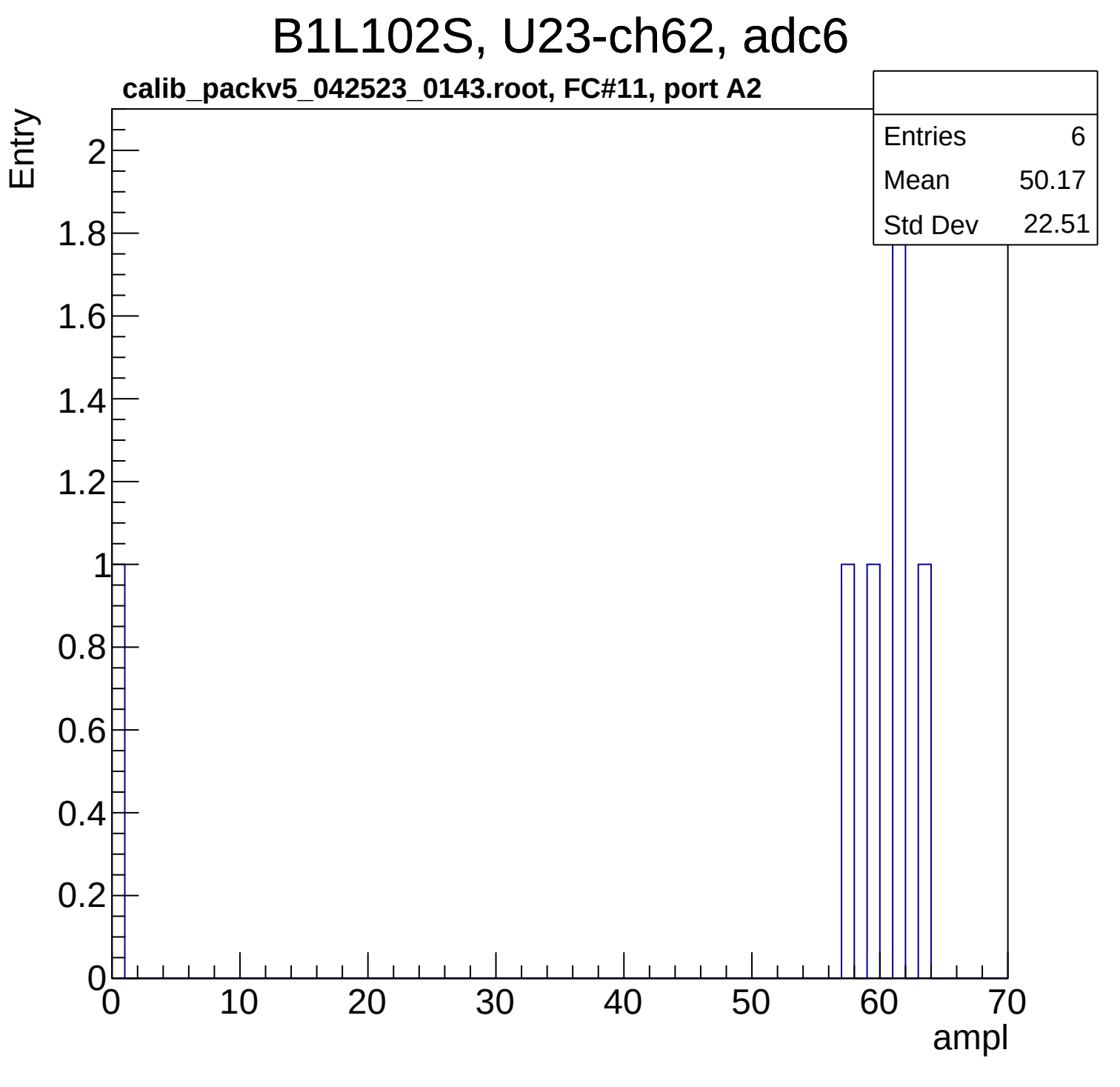

B1L102S, U23-ch62, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 90
Mean 33.86
Std Dev 4.04
Gaus mean : 34.2187
Gaus Width: 4.2896
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

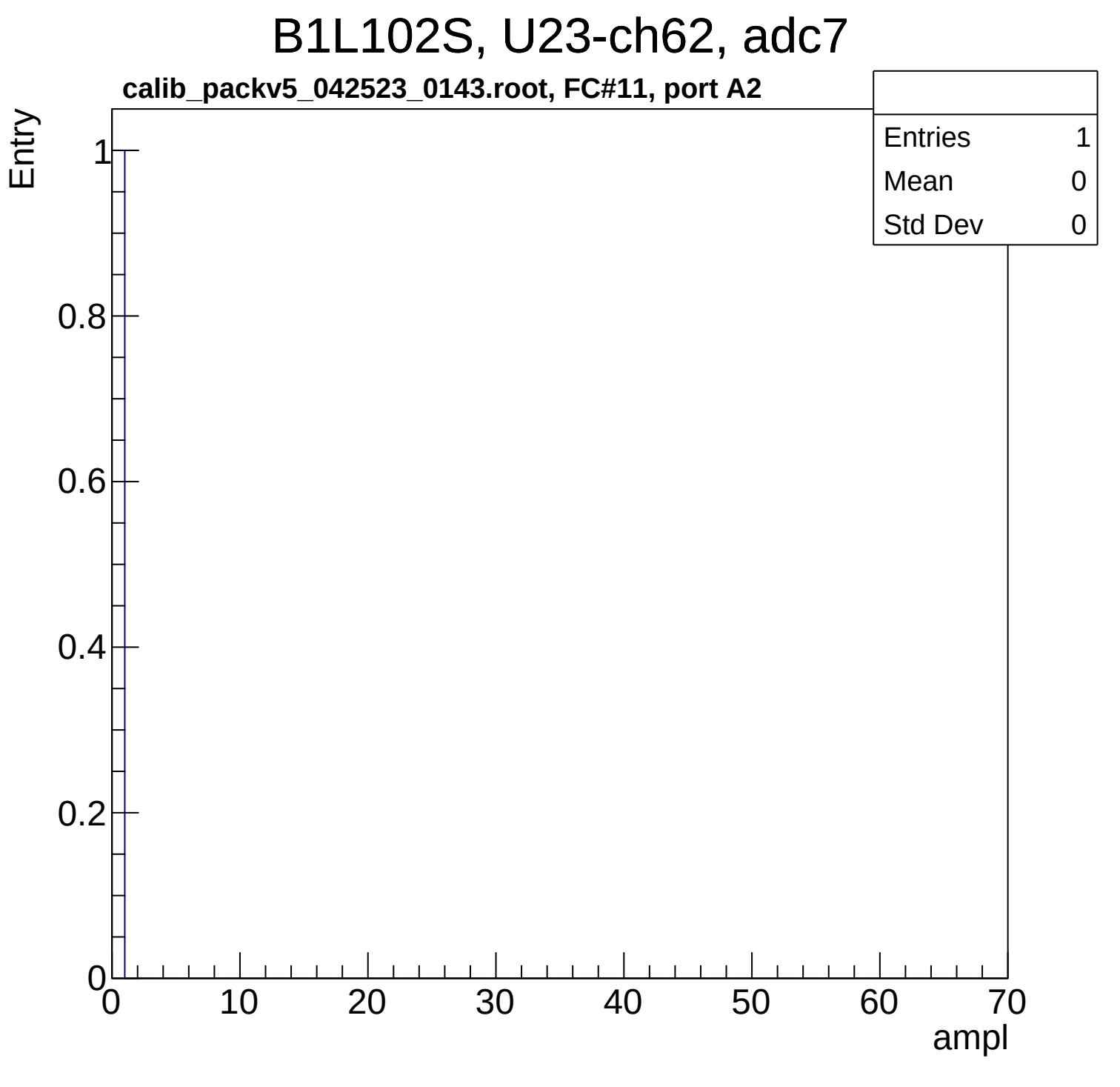

B1L102S, U23-ch62, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 41.04
Std Dev 3.151
Gaus mean : 41.9945
Gaus Width: 2.4849
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

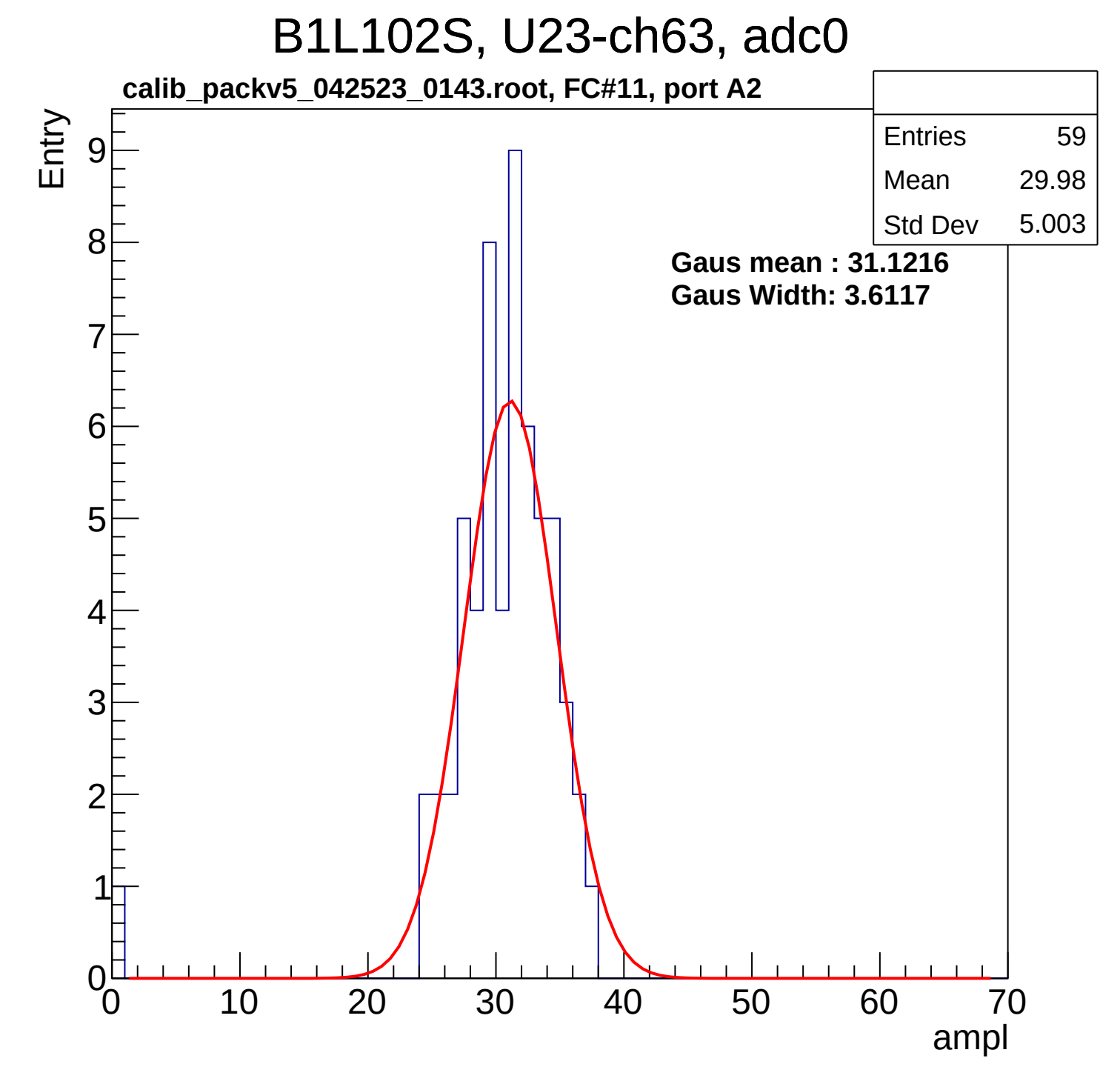

B1L102S, U23-ch62, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 48.51
Std Dev 3.671
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

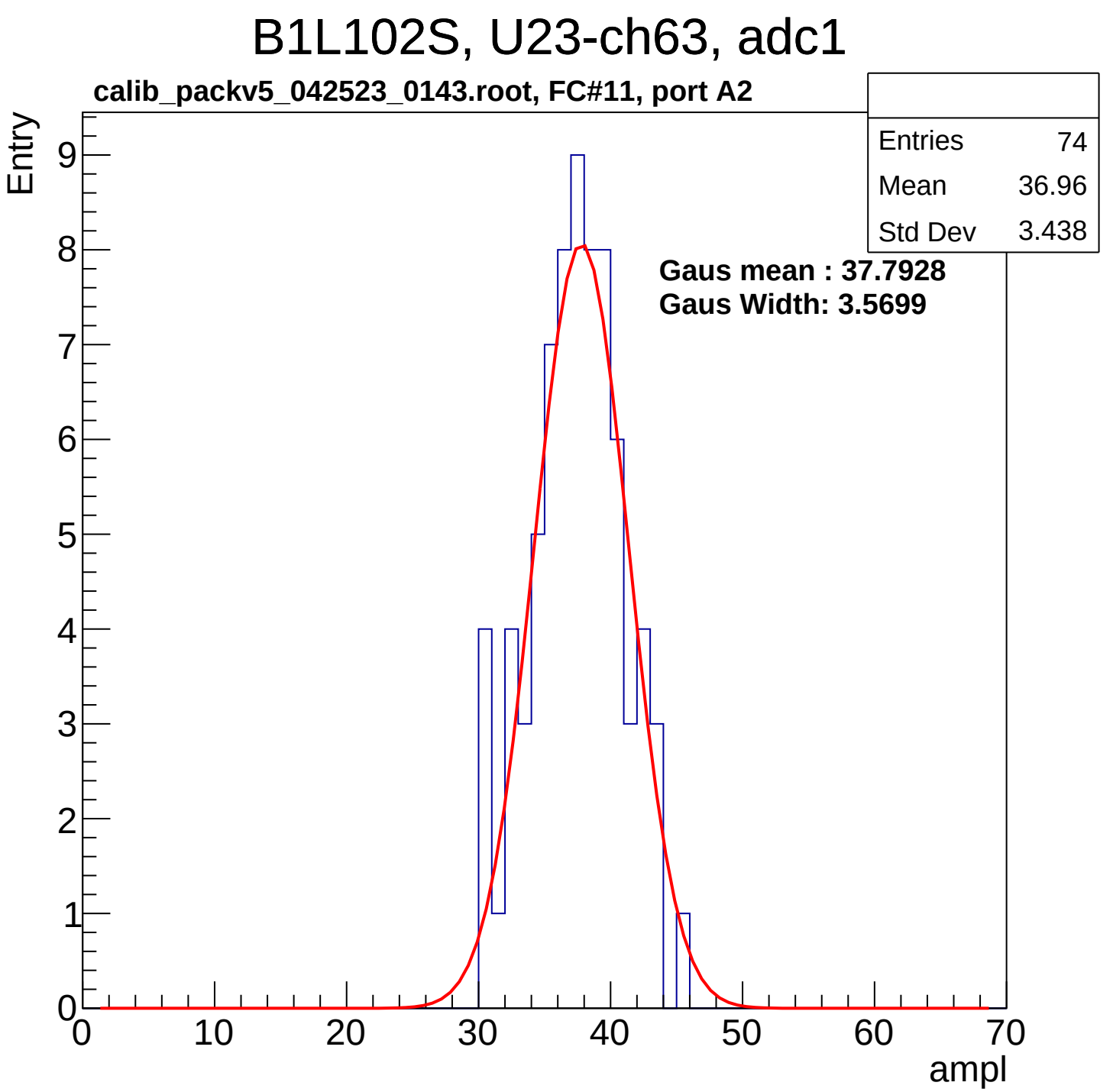

B1L102S, U23-ch62, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 54.53
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

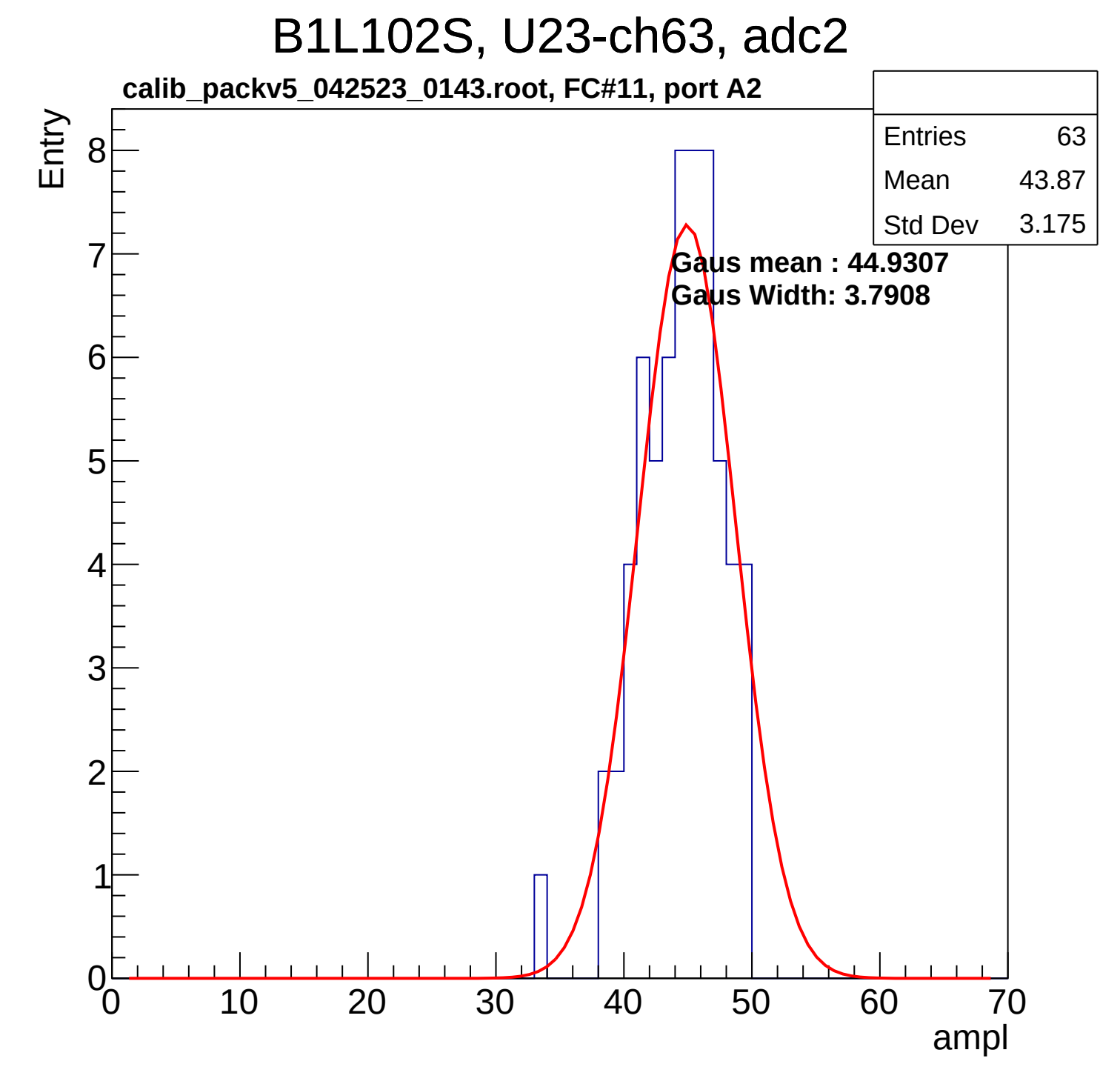

B1L102S, U23-ch62, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 59.46
Std Dev 2.82
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

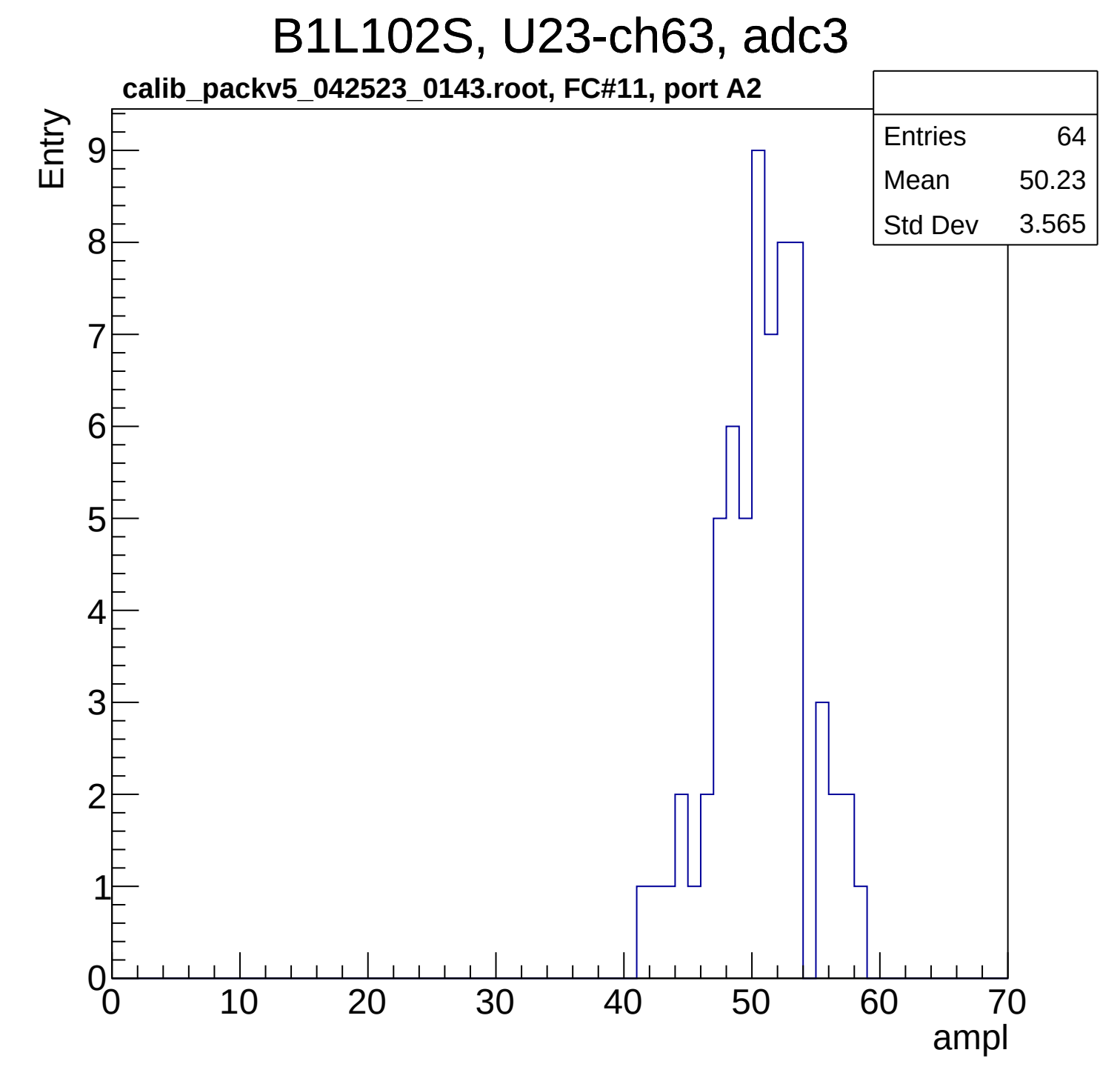

B1L102S, U23-ch62, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 50.17
Std Dev 22.51
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

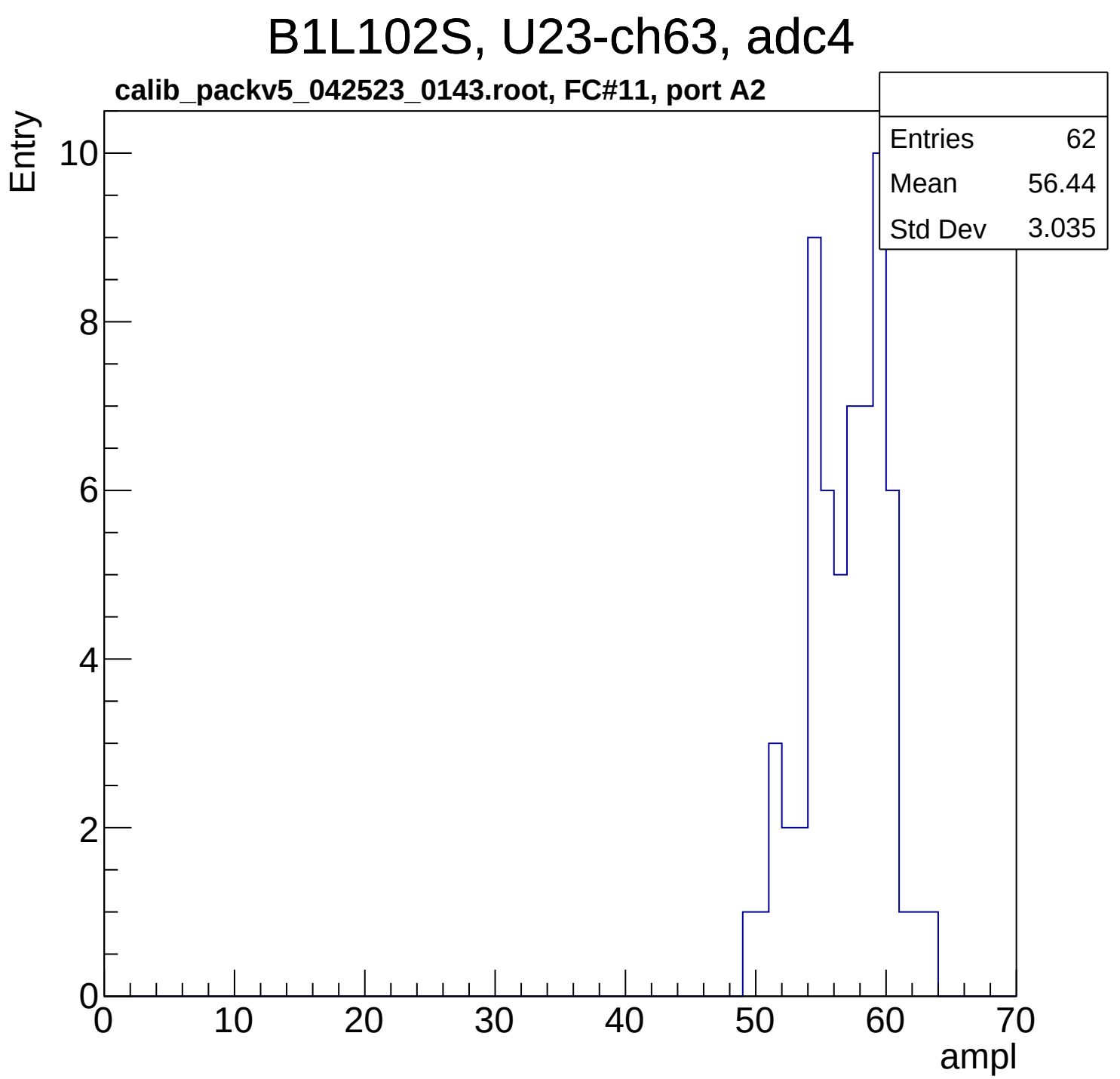

B1L102S, U23-ch62, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

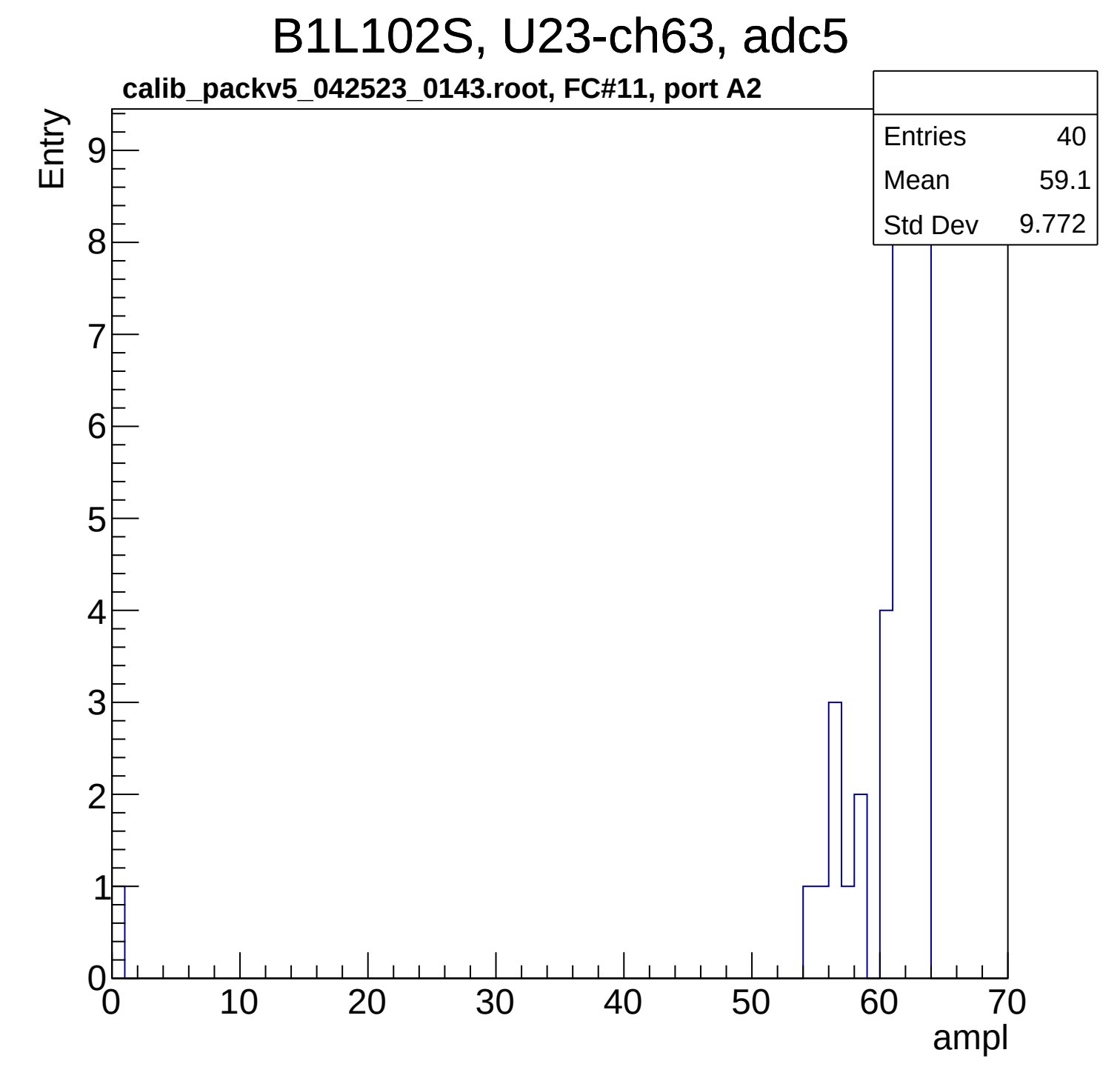

B1L102S, U23-ch63, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 29.98
Std Dev 5.003
Gaus mean : 31.1216
Gaus Width: 3.6117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

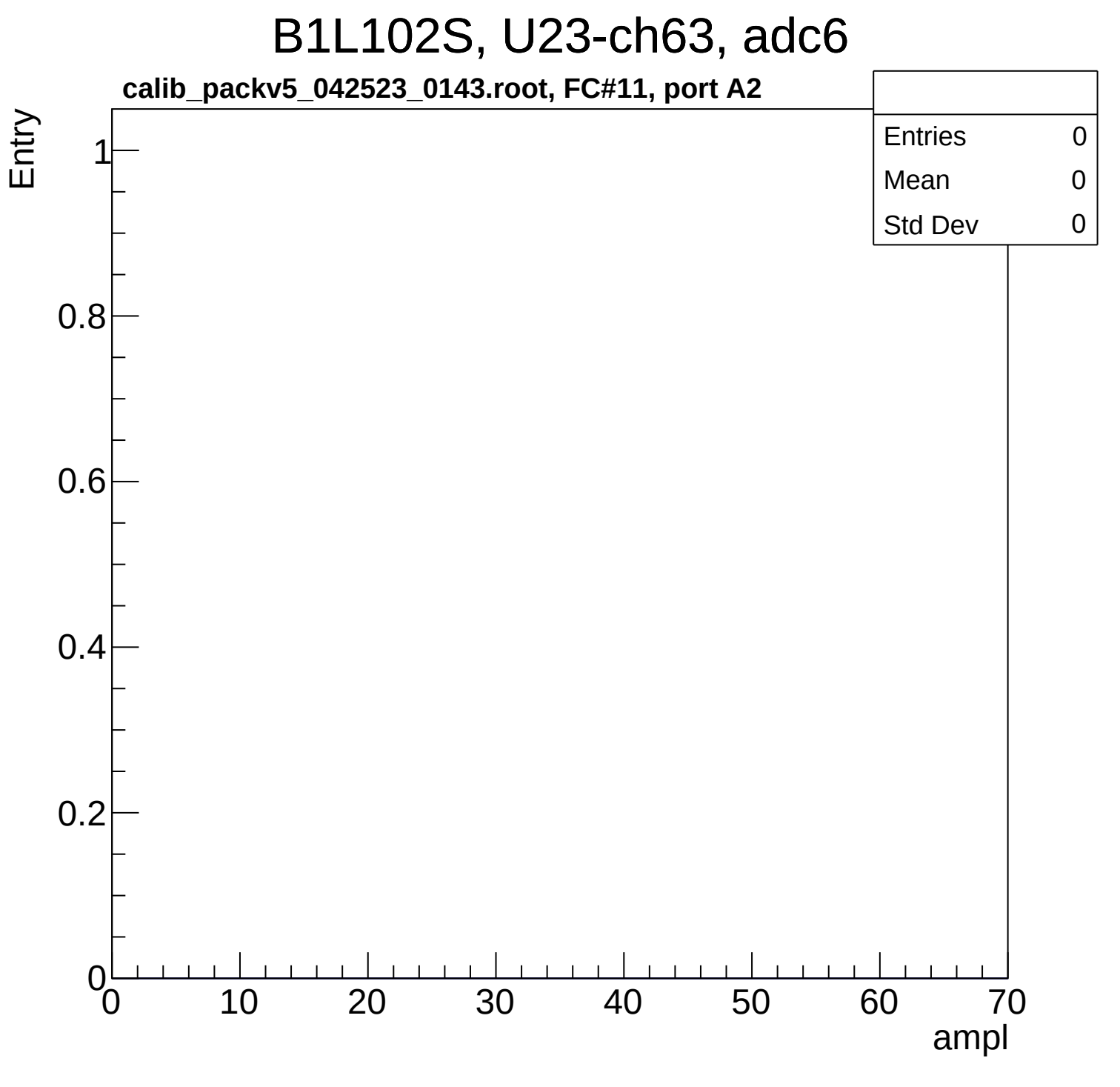

B1L102S, U23-ch63, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 36.96
Std Dev 3.438
Gaus mean : 37.7928
Gaus Width: 3.5699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

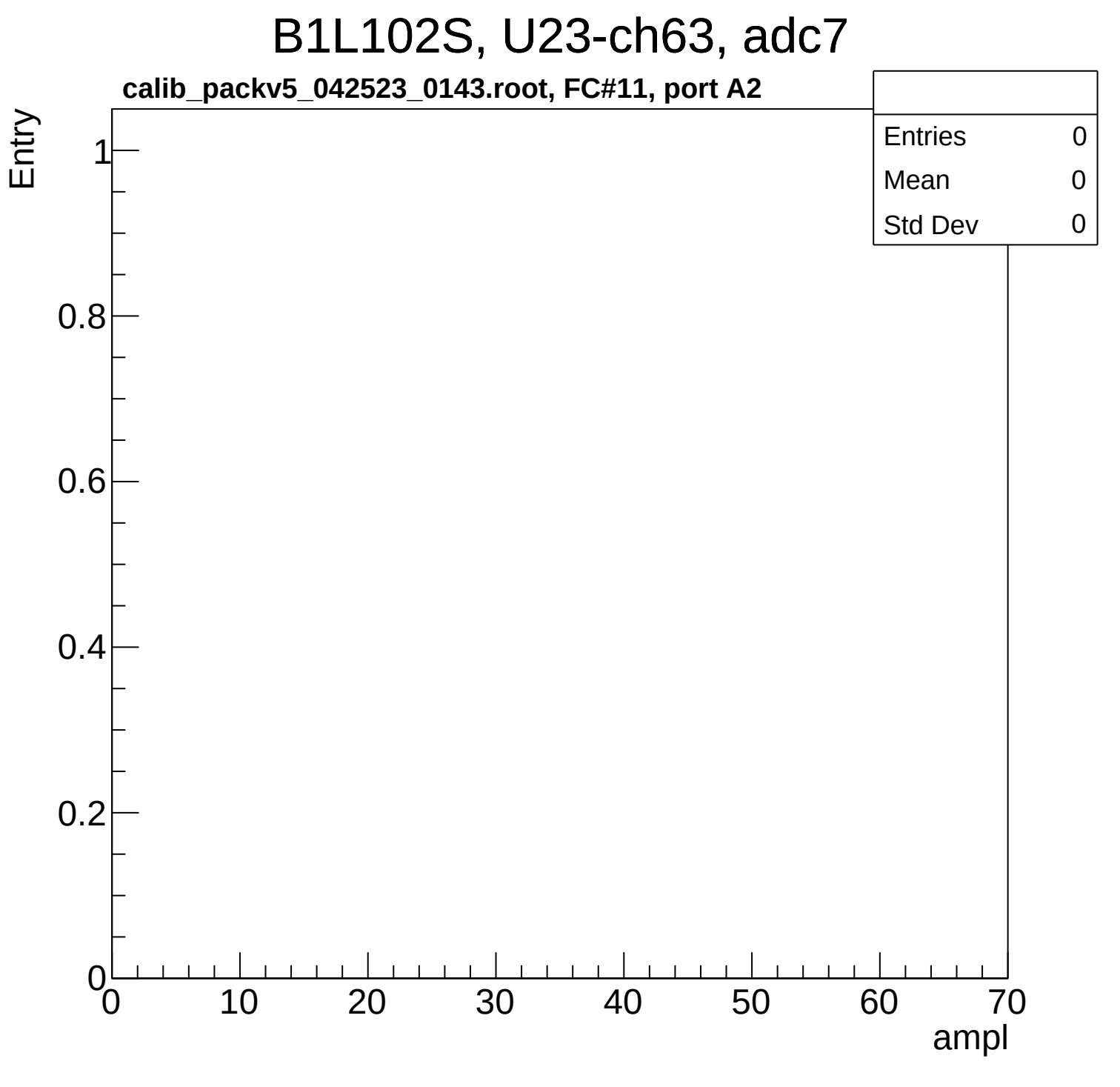

B1L102S, U23-ch63, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 43.87
Std Dev 3.175
Gaus mean : 44.9307
Gaus Width: 3.7908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

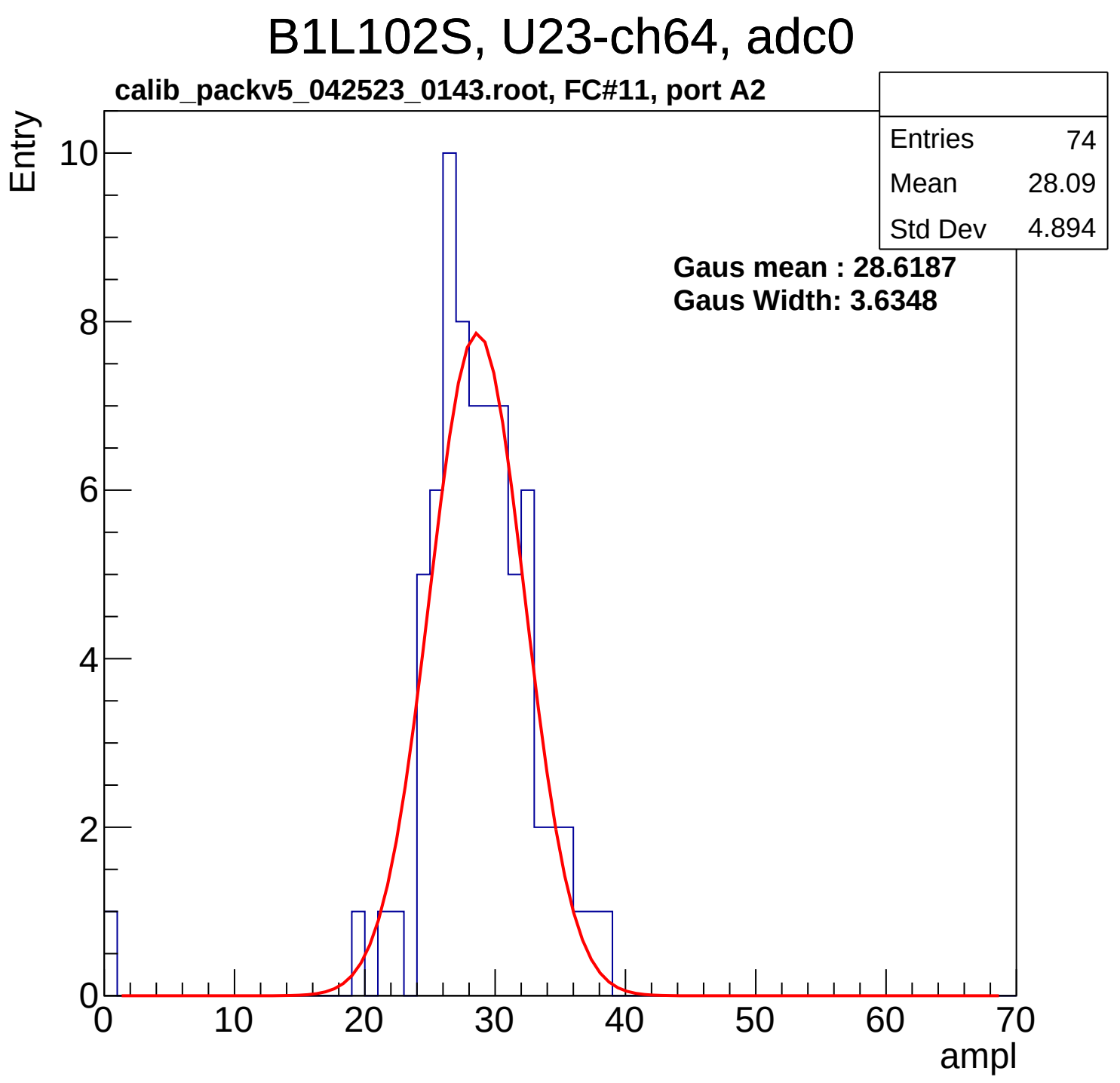

B1L102S, U23-ch63, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 50.23
Std Dev 3.565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

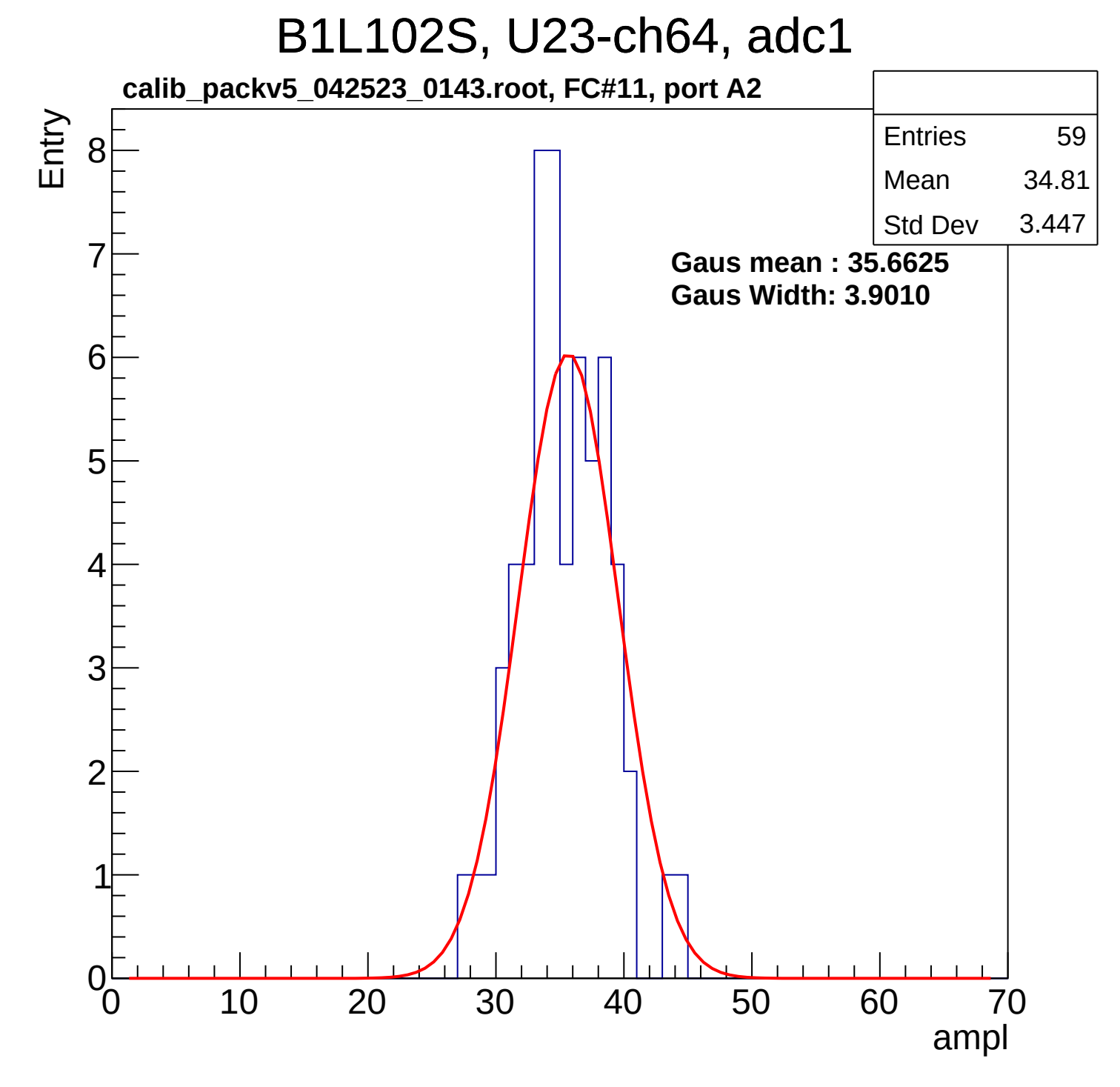

B1L102S, U23-ch63, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 56.44
Std Dev 3.035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

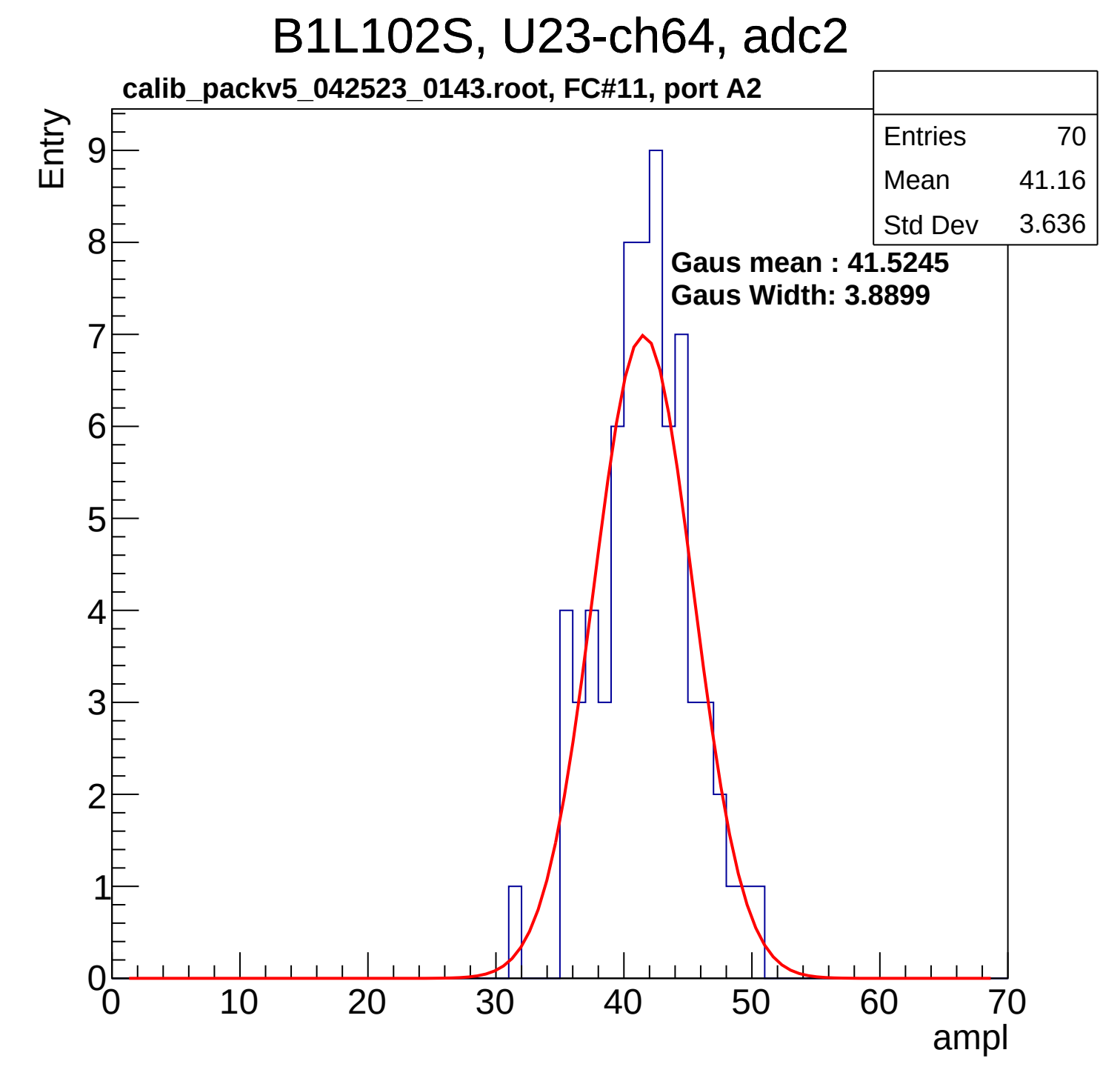

B1L102S, U23-ch63, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 59.1
Std Dev 9.772
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

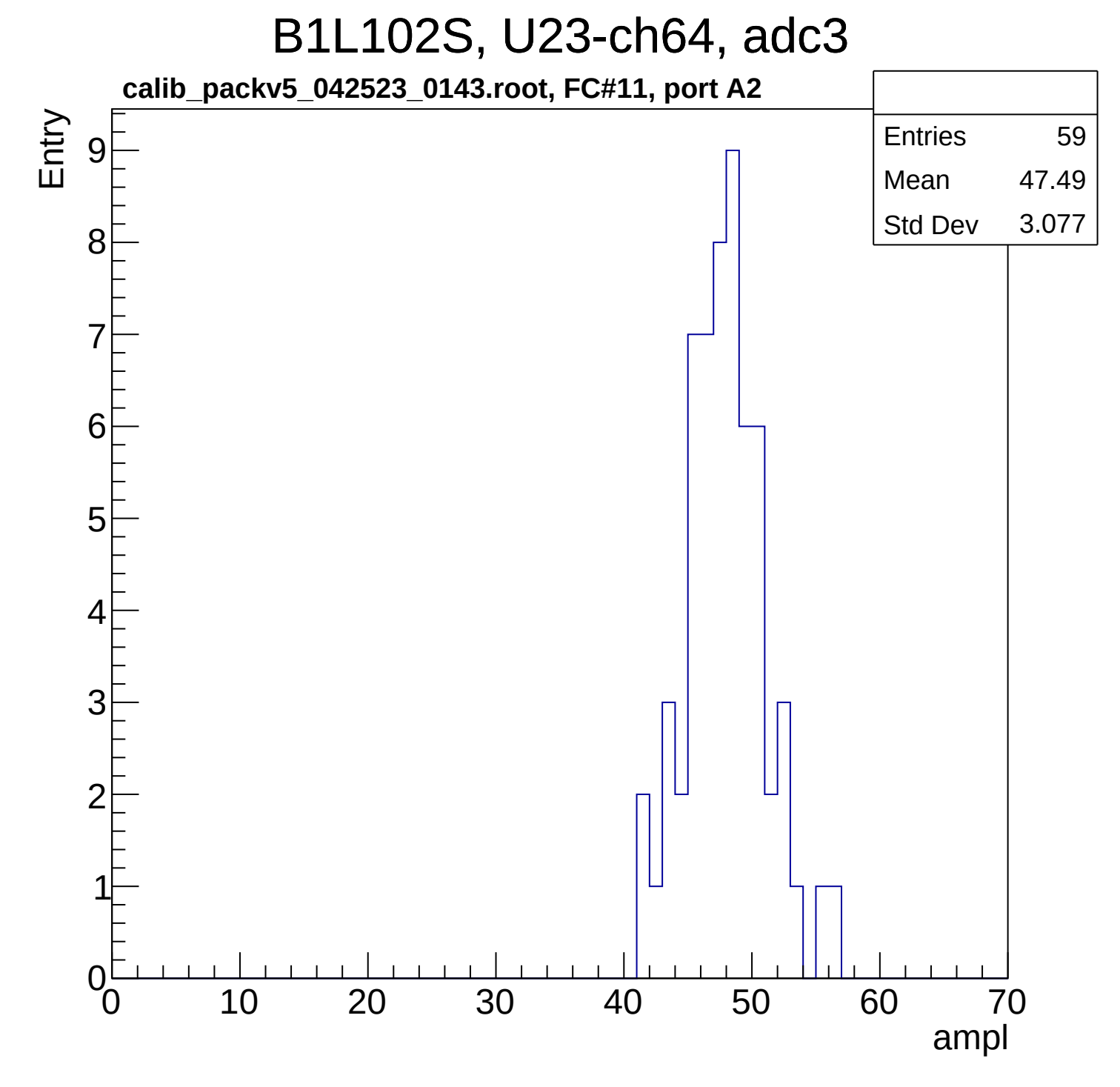

B1L102S, U23-ch63, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

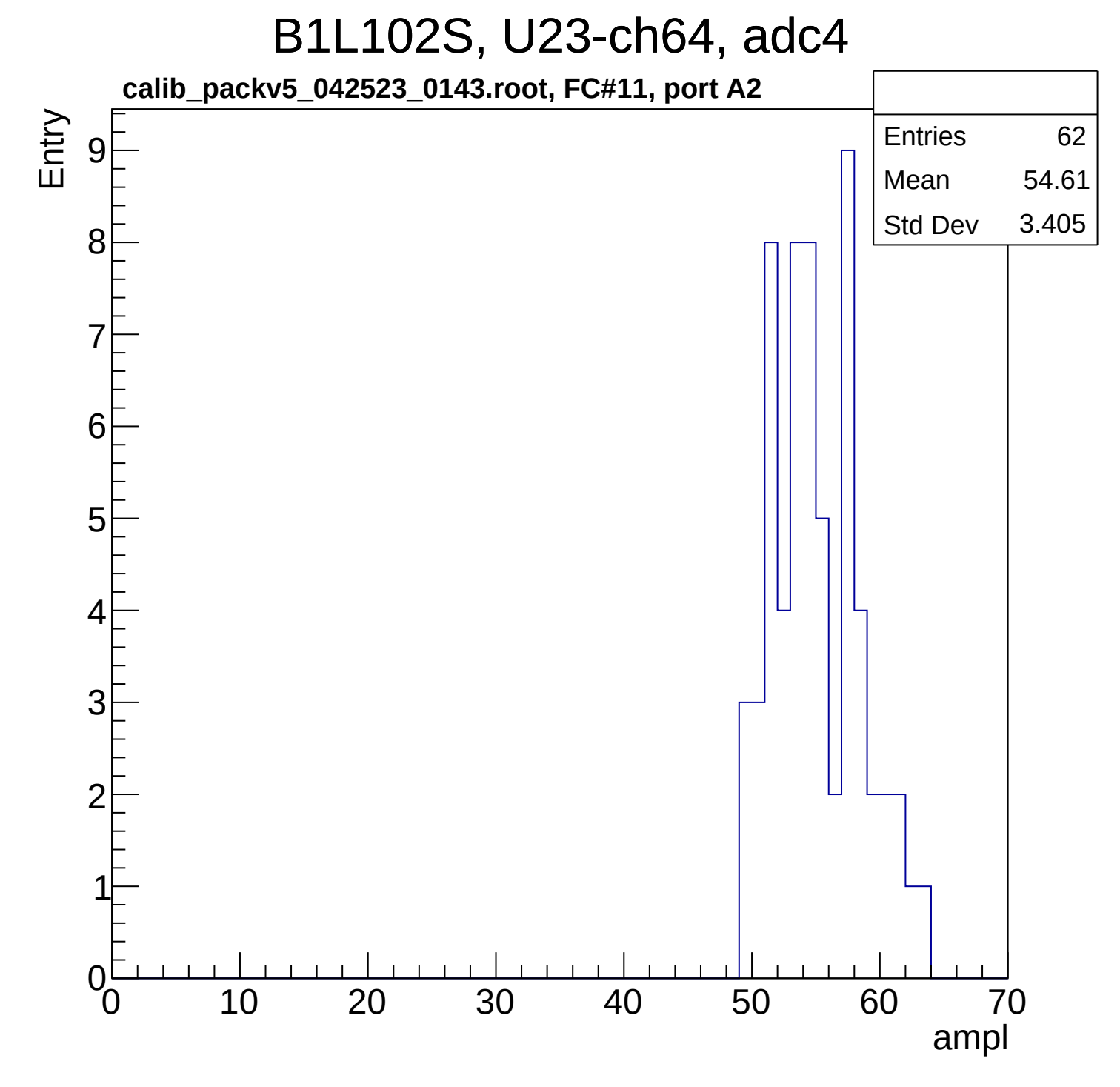

B1L102S, U23-ch63, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

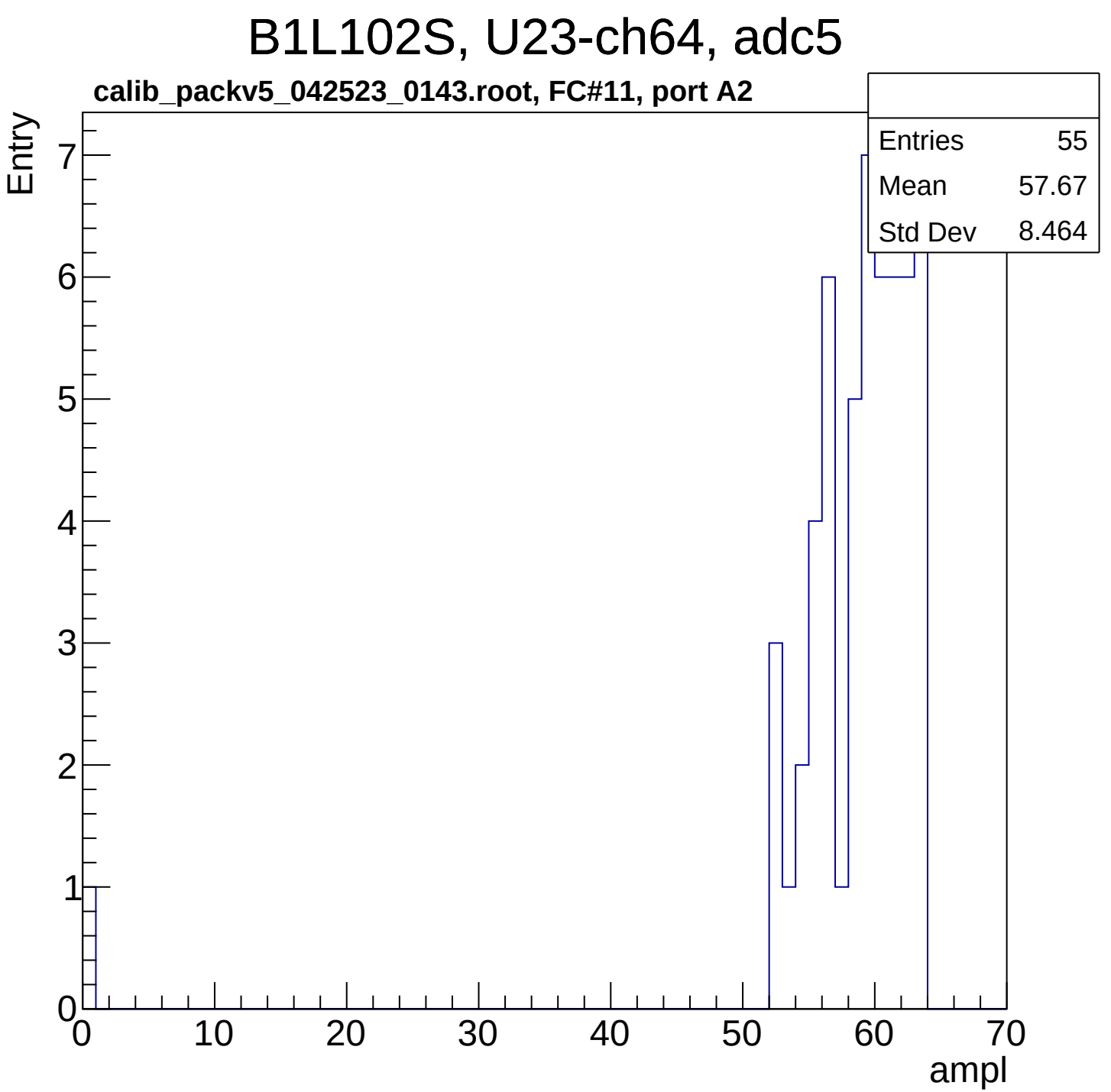

B1L102S, U23-ch64, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 28.09
Std Dev 4.894
Gaus mean : 28.6187
Gaus Width: 3.6348
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

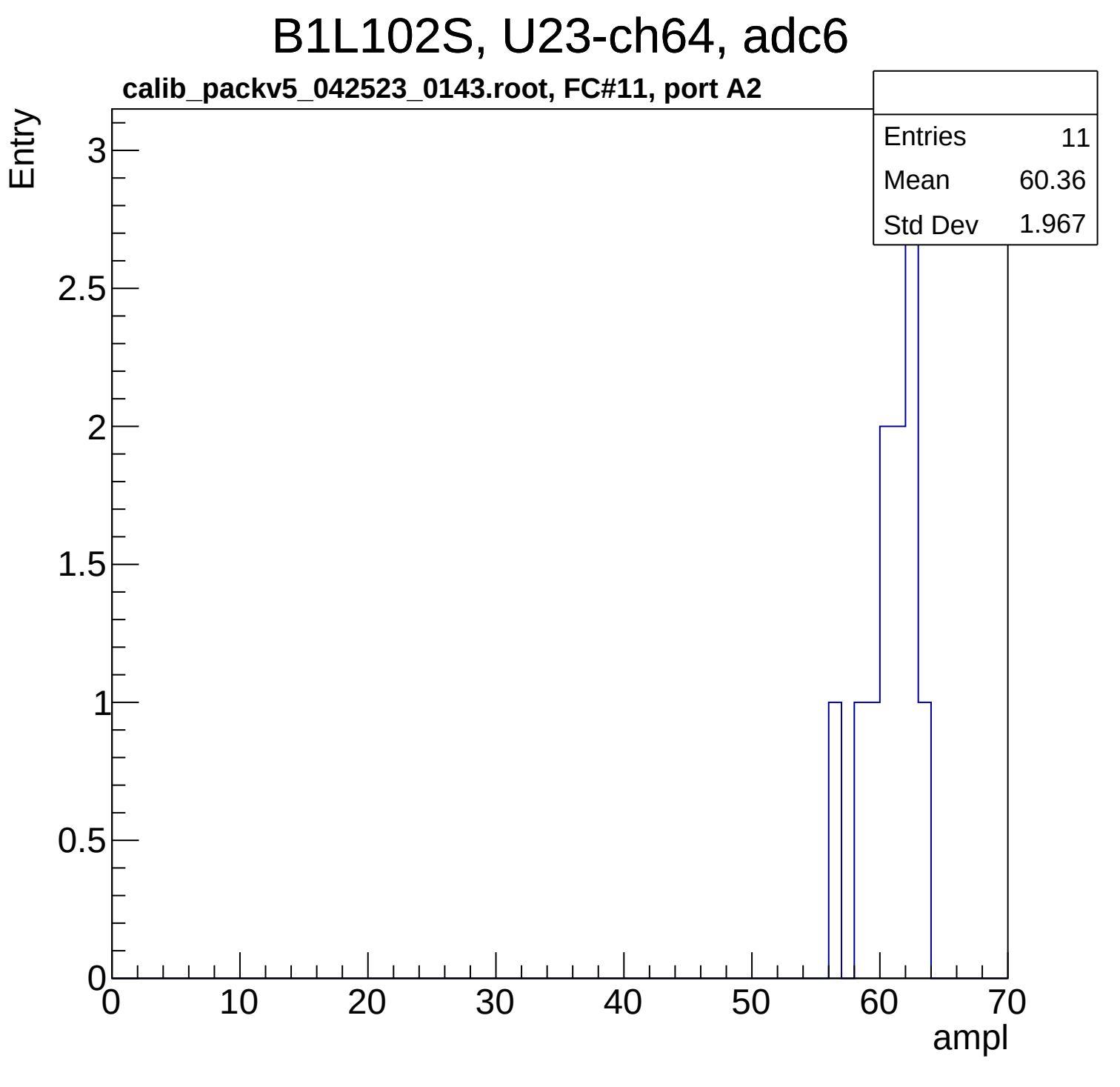

B1L102S, U23-ch64, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 34.81
Std Dev 3.447
Gaus mean : 35.6625
Gaus Width: 3.9010
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

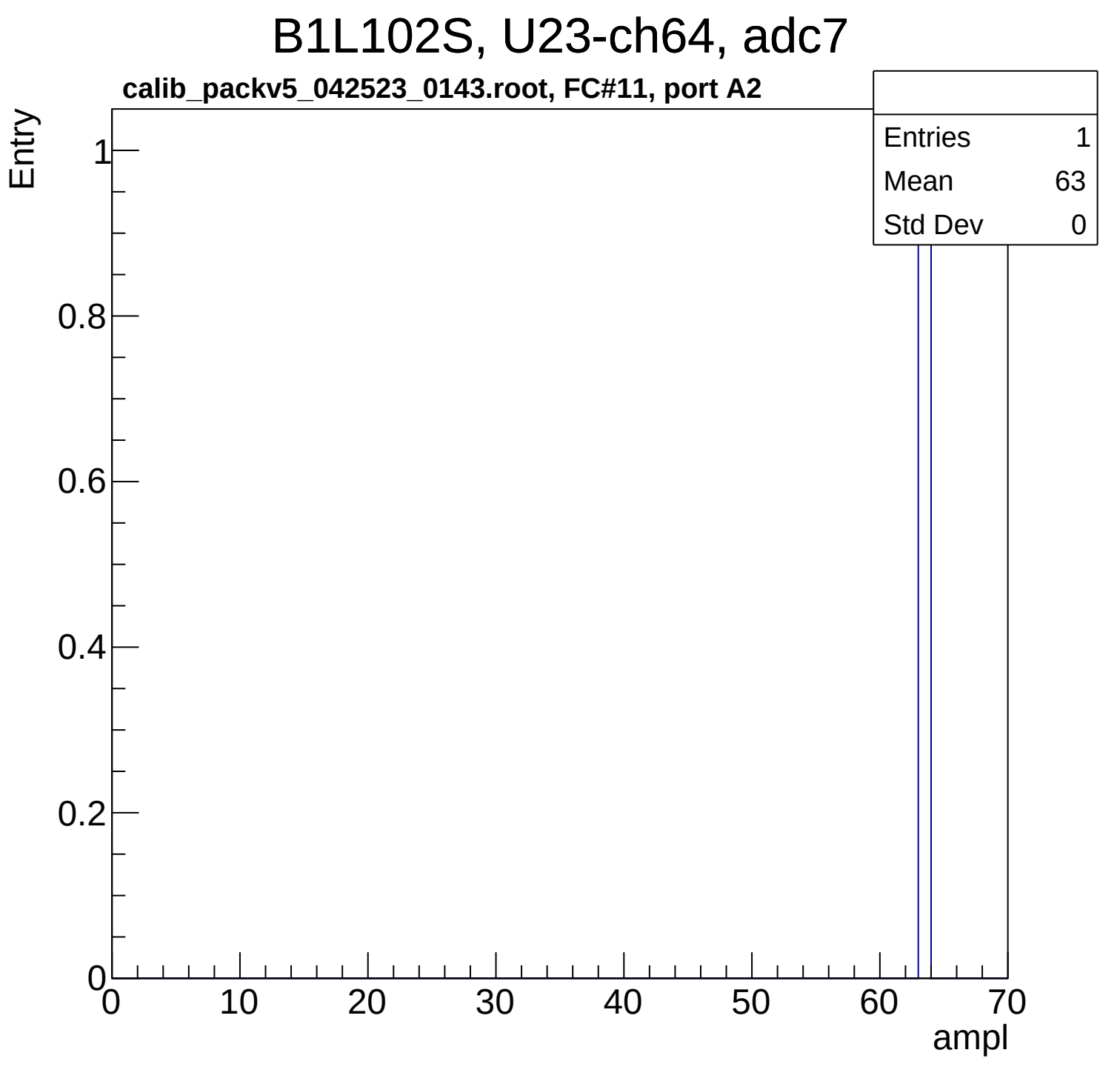

B1L102S, U23-ch64, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 41.16
Std Dev 3.636
Gaus mean : 41.5245
Gaus Width: 3.8899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

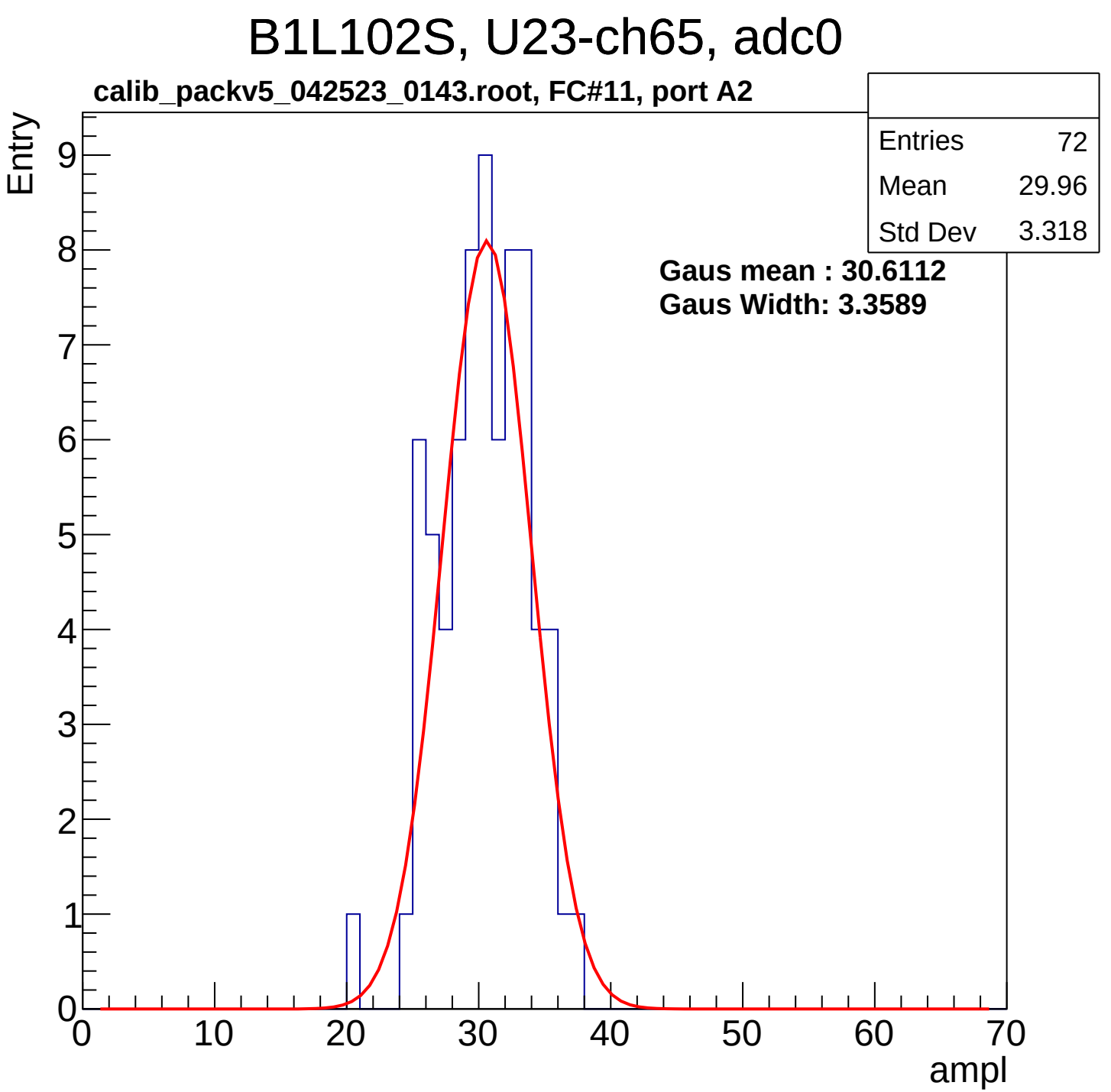

B1L102S, U23-ch64, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 47.49
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

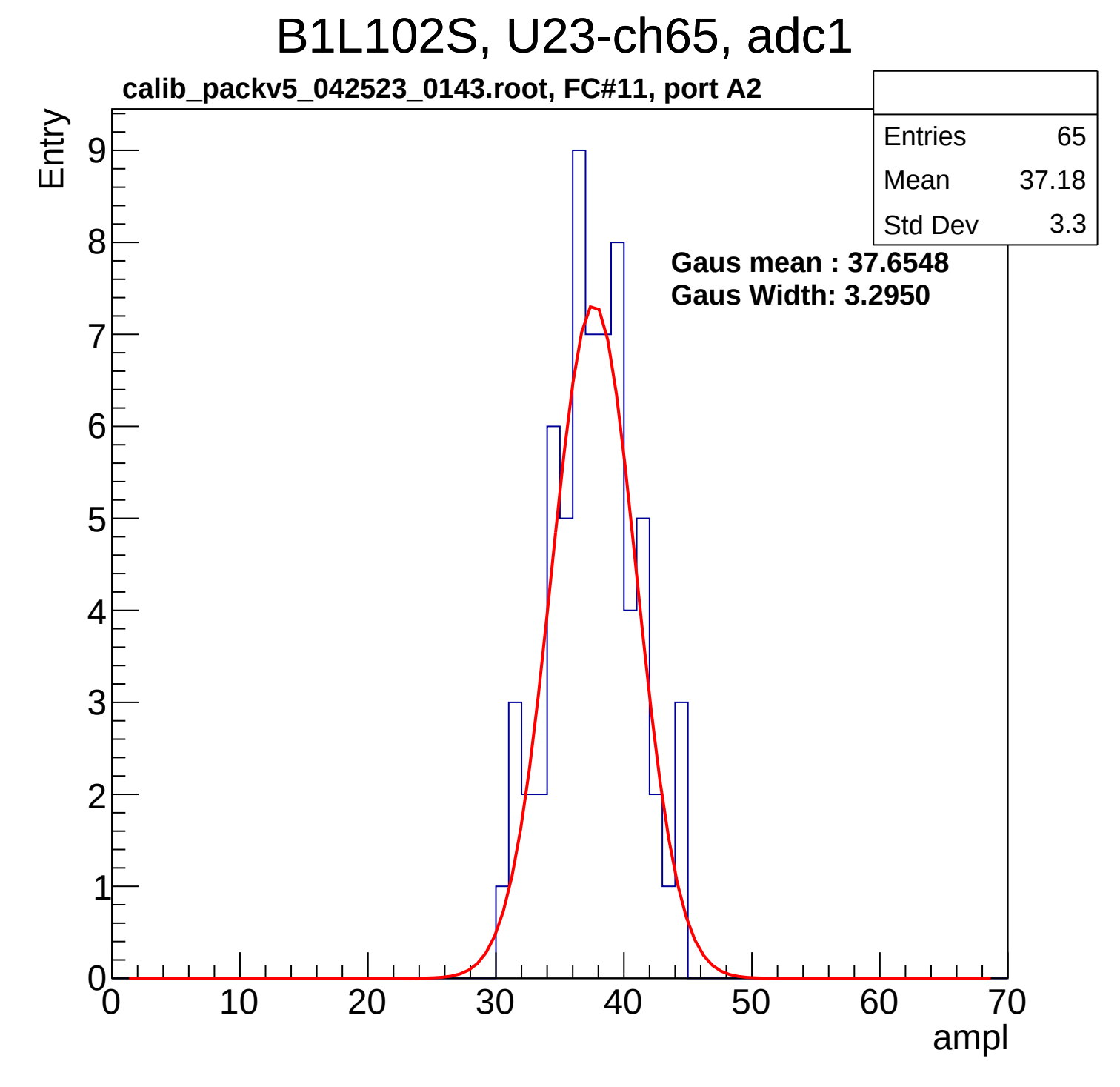

B1L102S, U23-ch64, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 54.61
Std Dev 3.405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

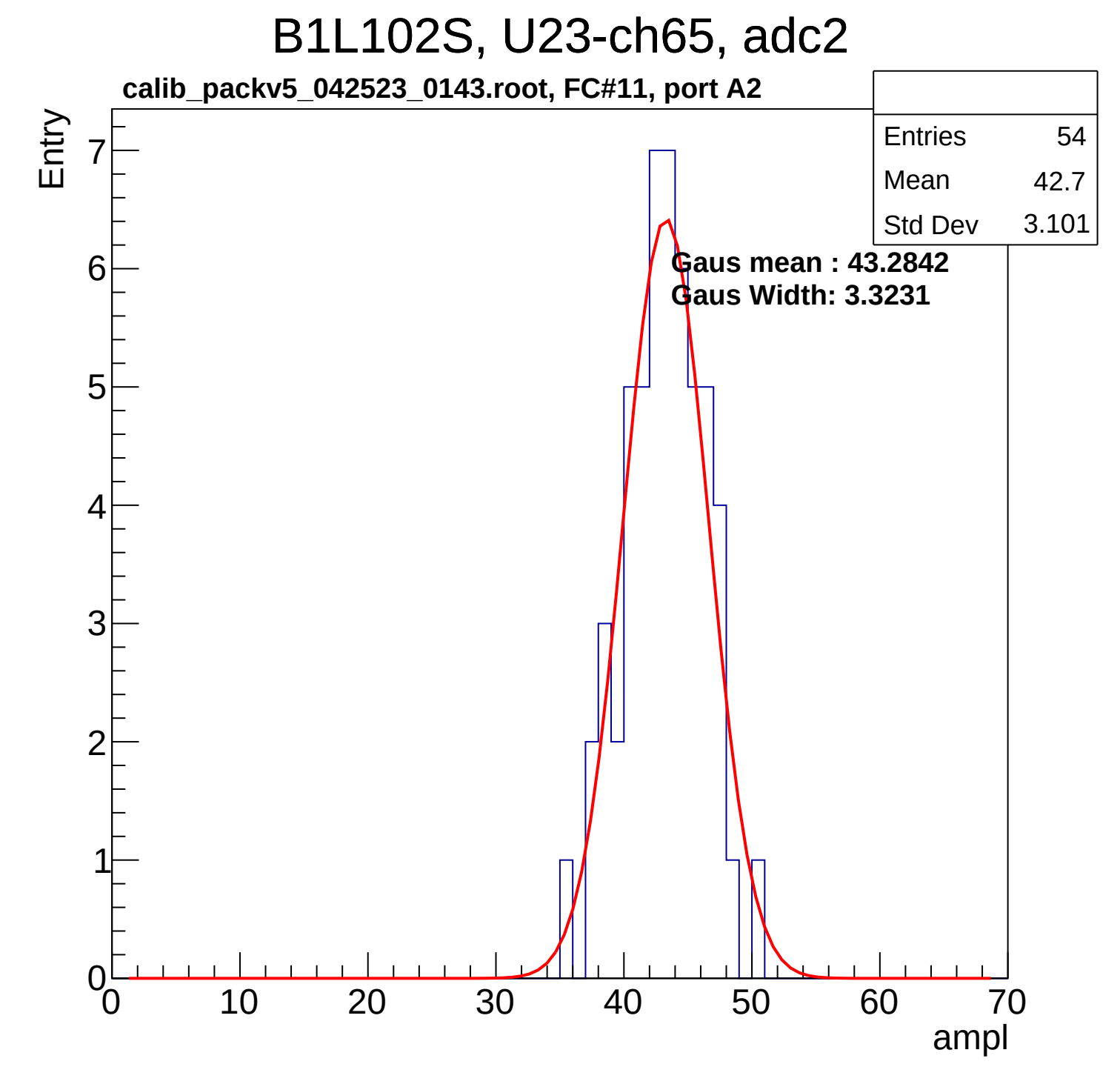

B1L102S, U23-ch64, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 57.67
Std Dev 8.464
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

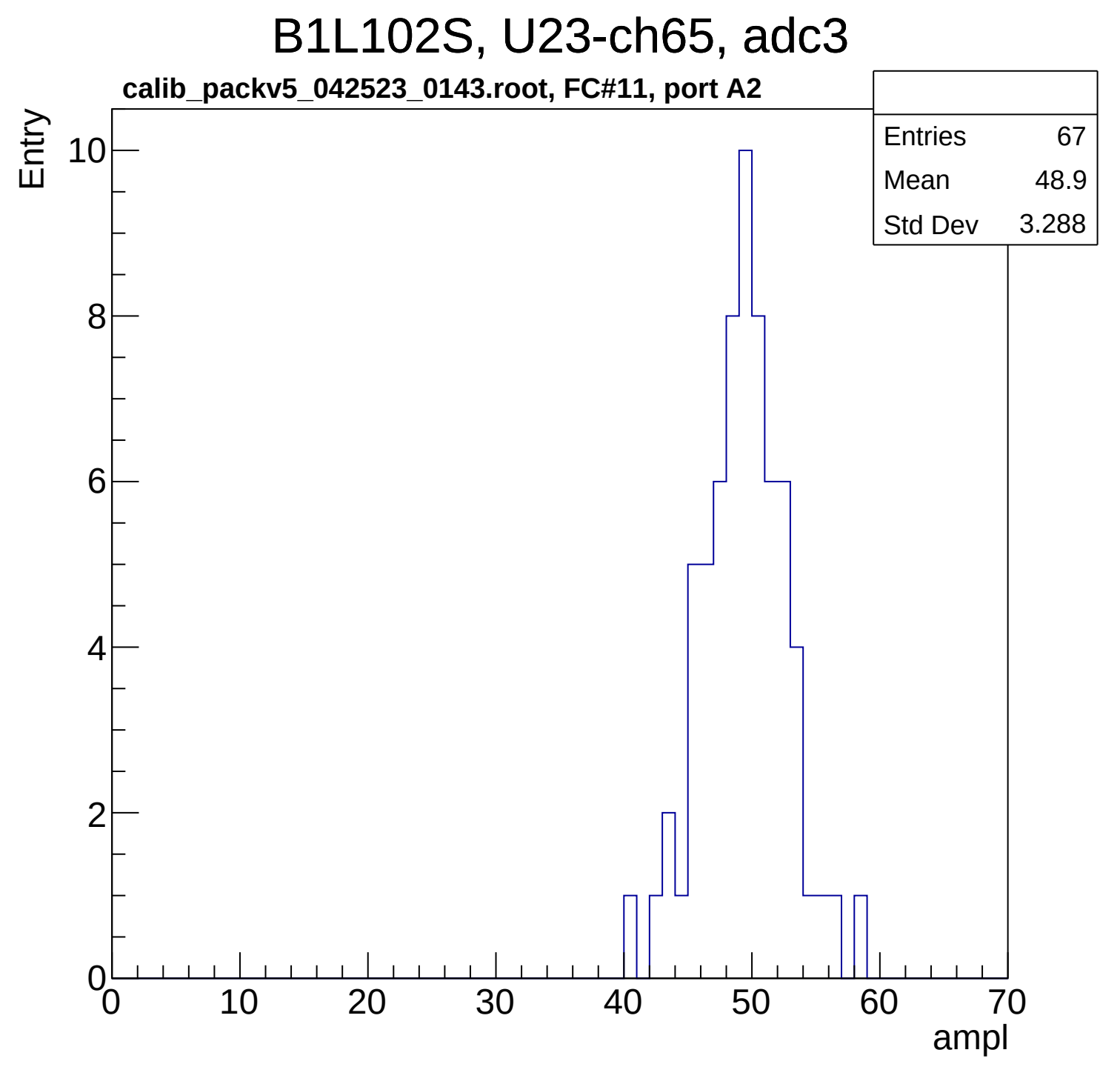

B1L102S, U23-ch64, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 60.36
Std Dev 1.967
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

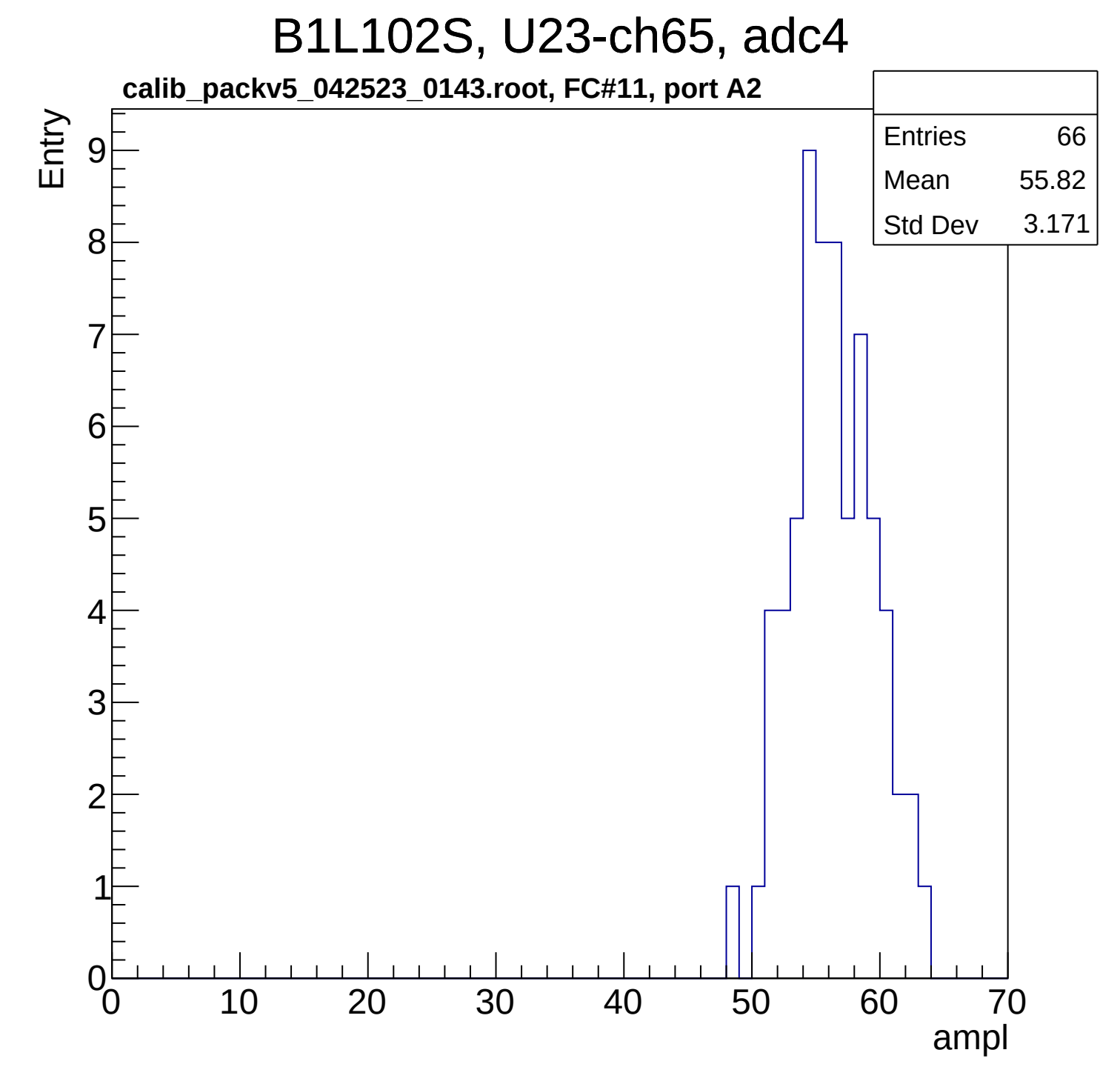

B1L102S, U23-ch64, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

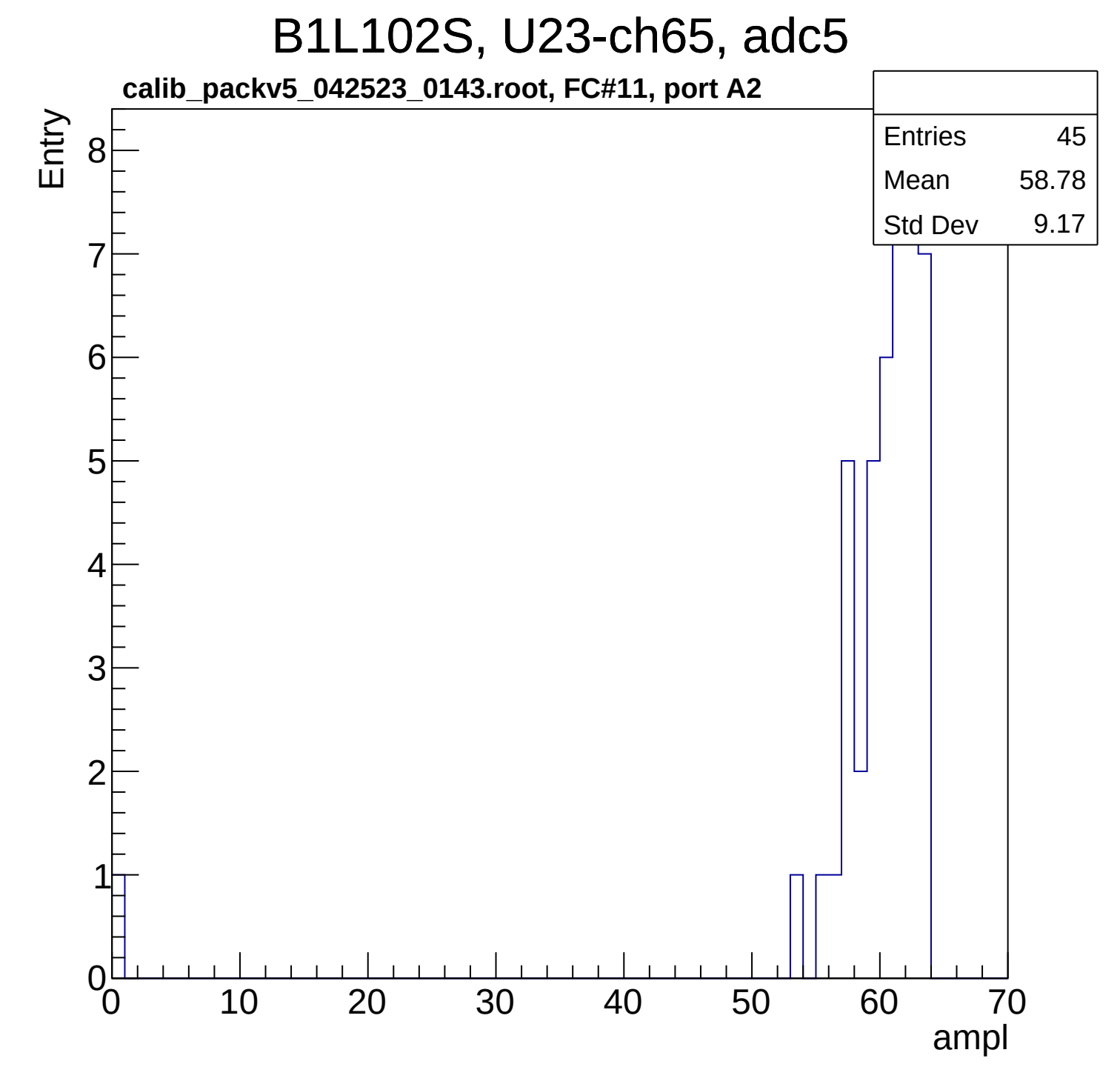

B1L102S, U23-ch65, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 29.96
Std Dev 3.318
Gaus mean : 30.6112
Gaus Width: 3.3589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

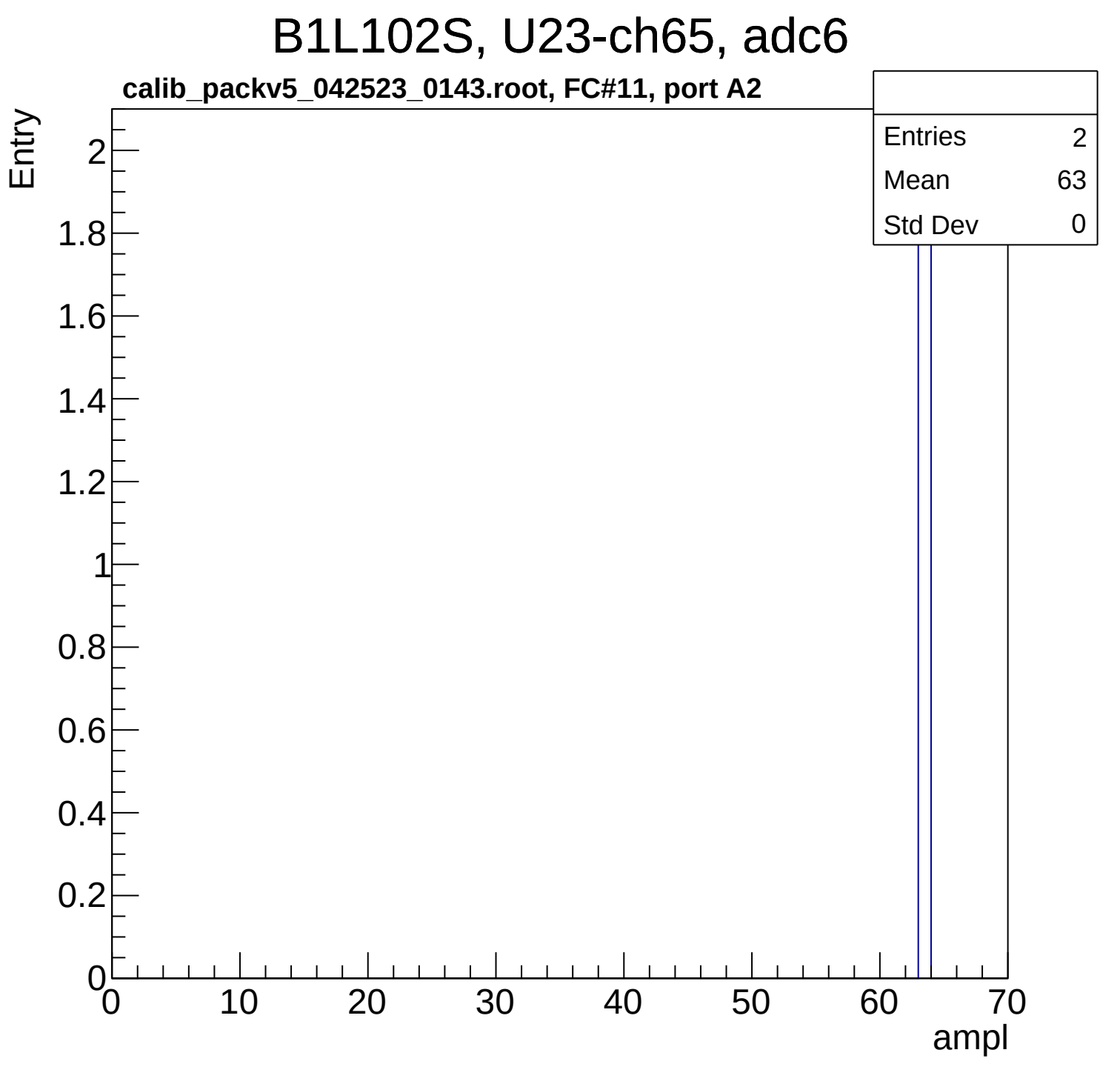

B1L102S, U23-ch65, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 37.18
Std Dev 3.3
Gaus mean : 37.6548
Gaus Width: 3.2950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

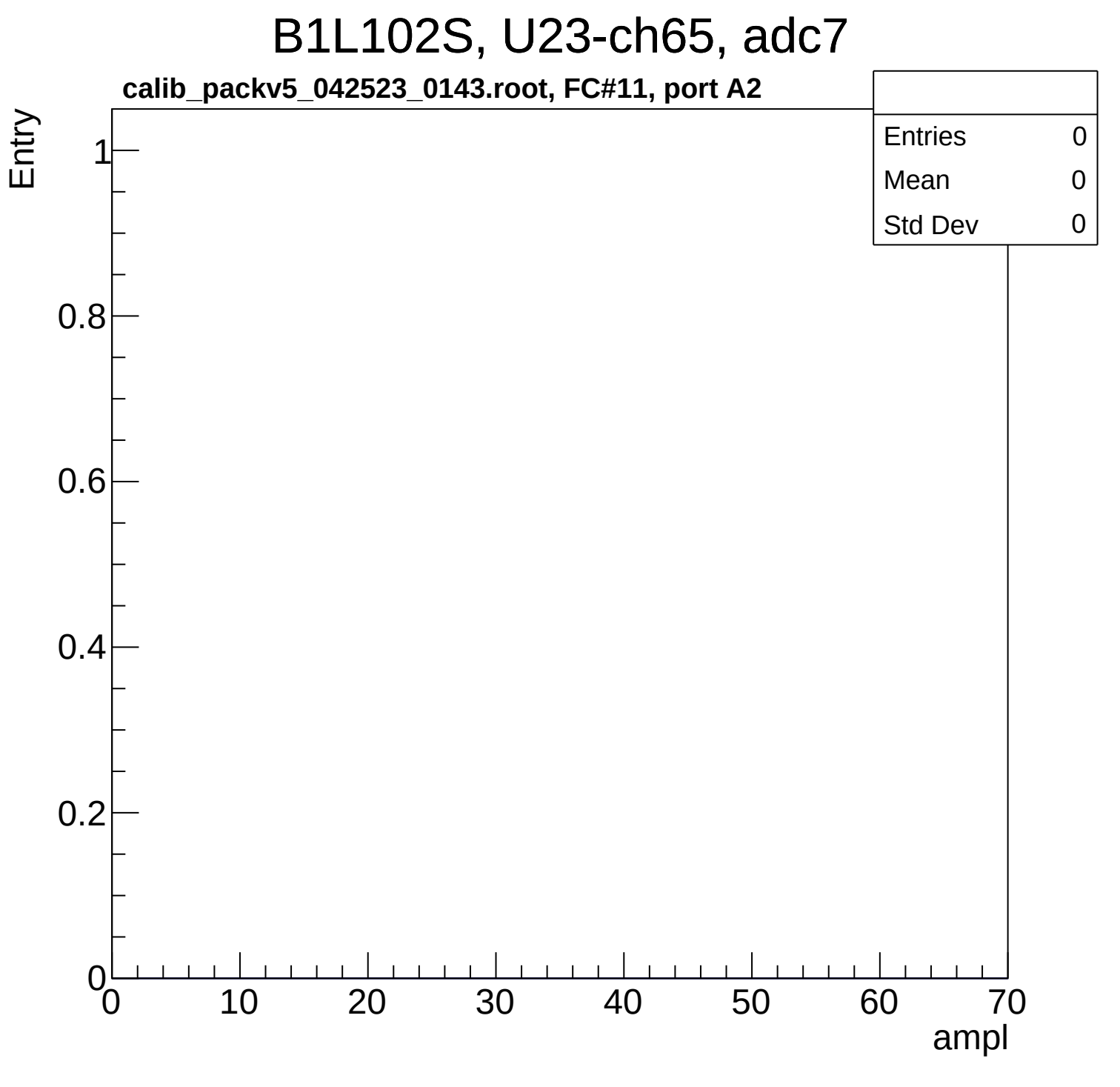

B1L102S, U23-ch65, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 42.7
Std Dev 3.101
Gaus mean : 43.2842
Gaus Width: 3.3231
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

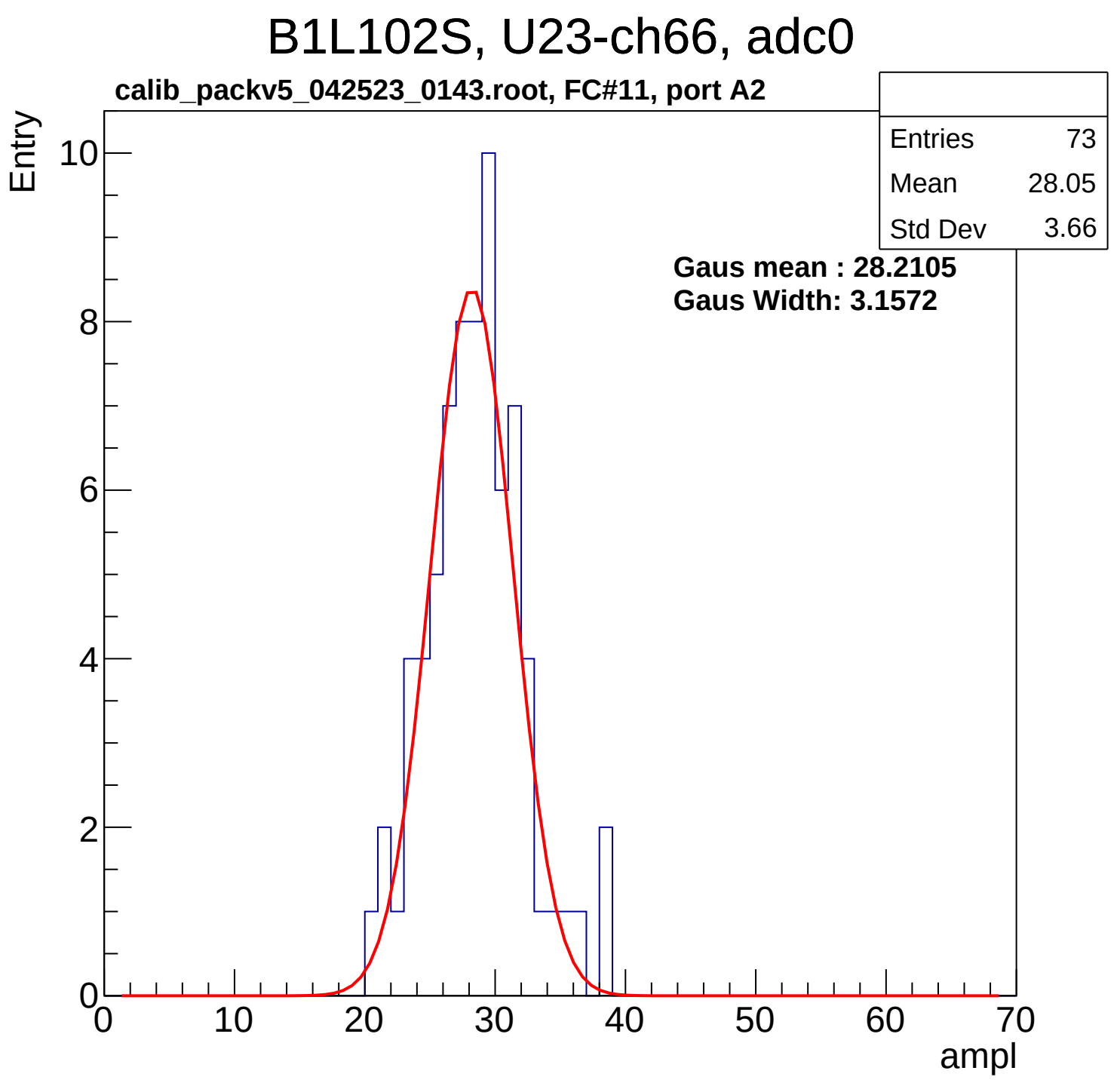

B1L102S, U23-ch65, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 48.9
Std Dev 3.288
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

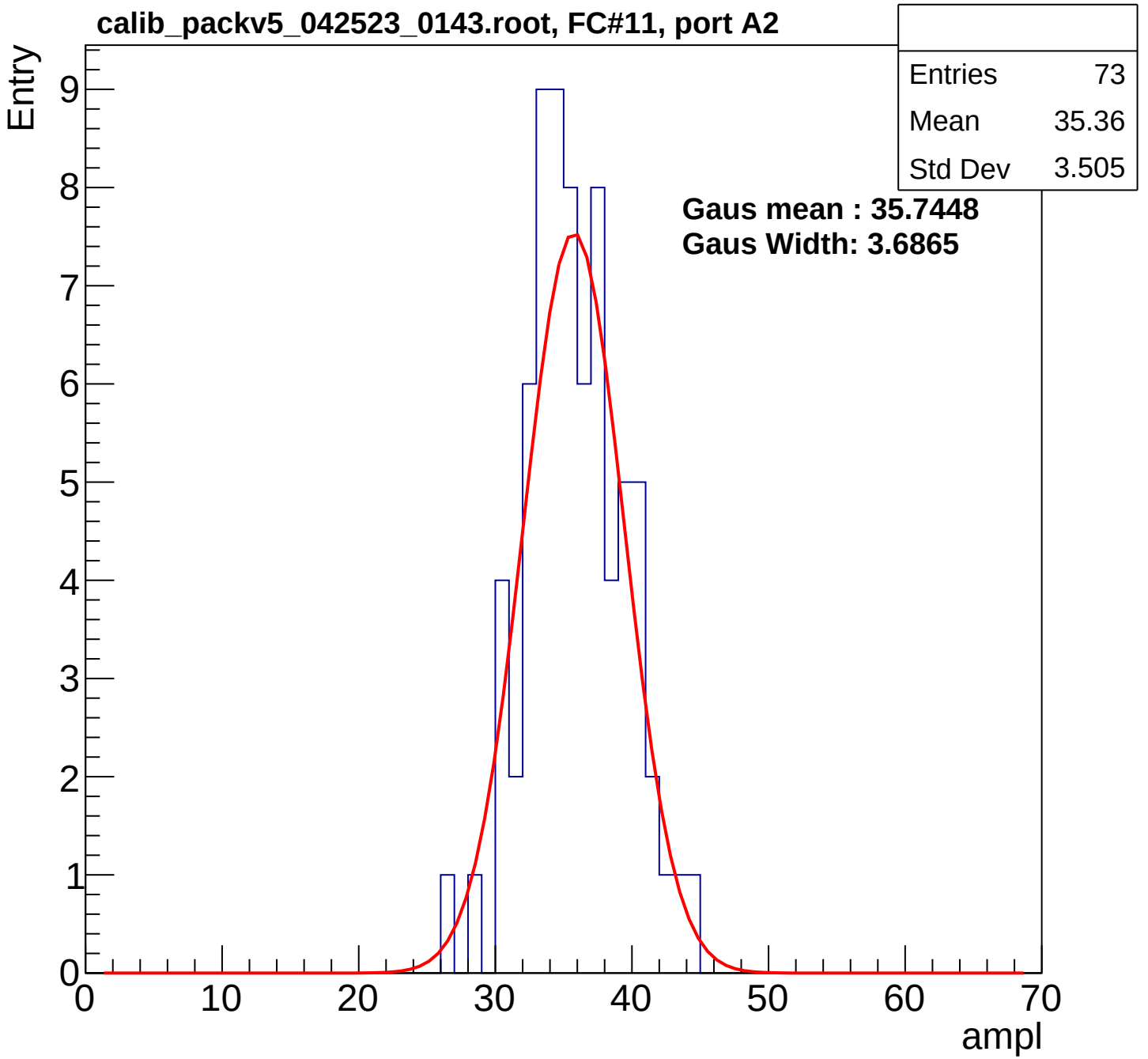

B1L102S, U23-ch65, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 55.82
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

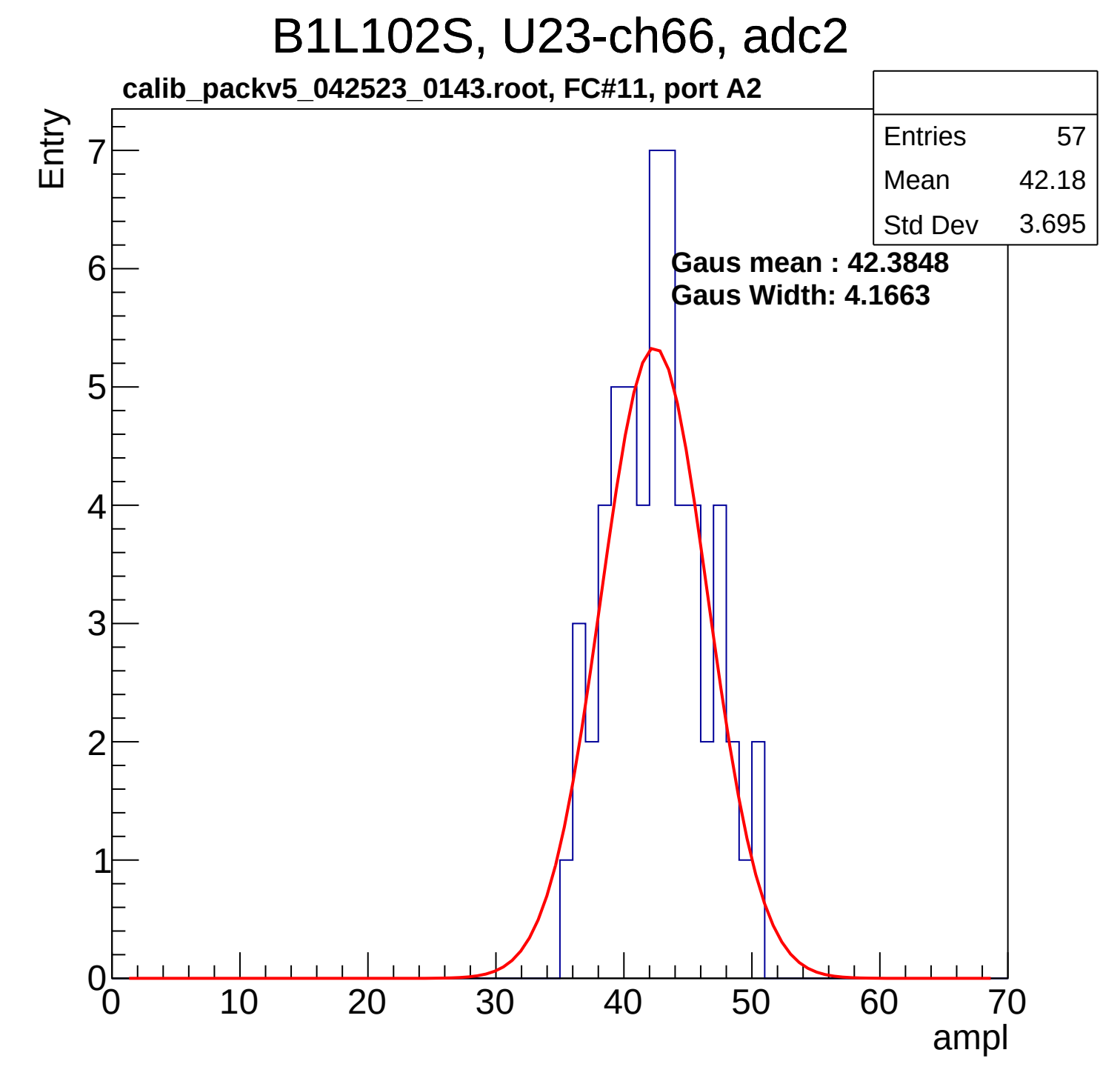

B1L102S, U23-ch65, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.78
Std Dev 9.17
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

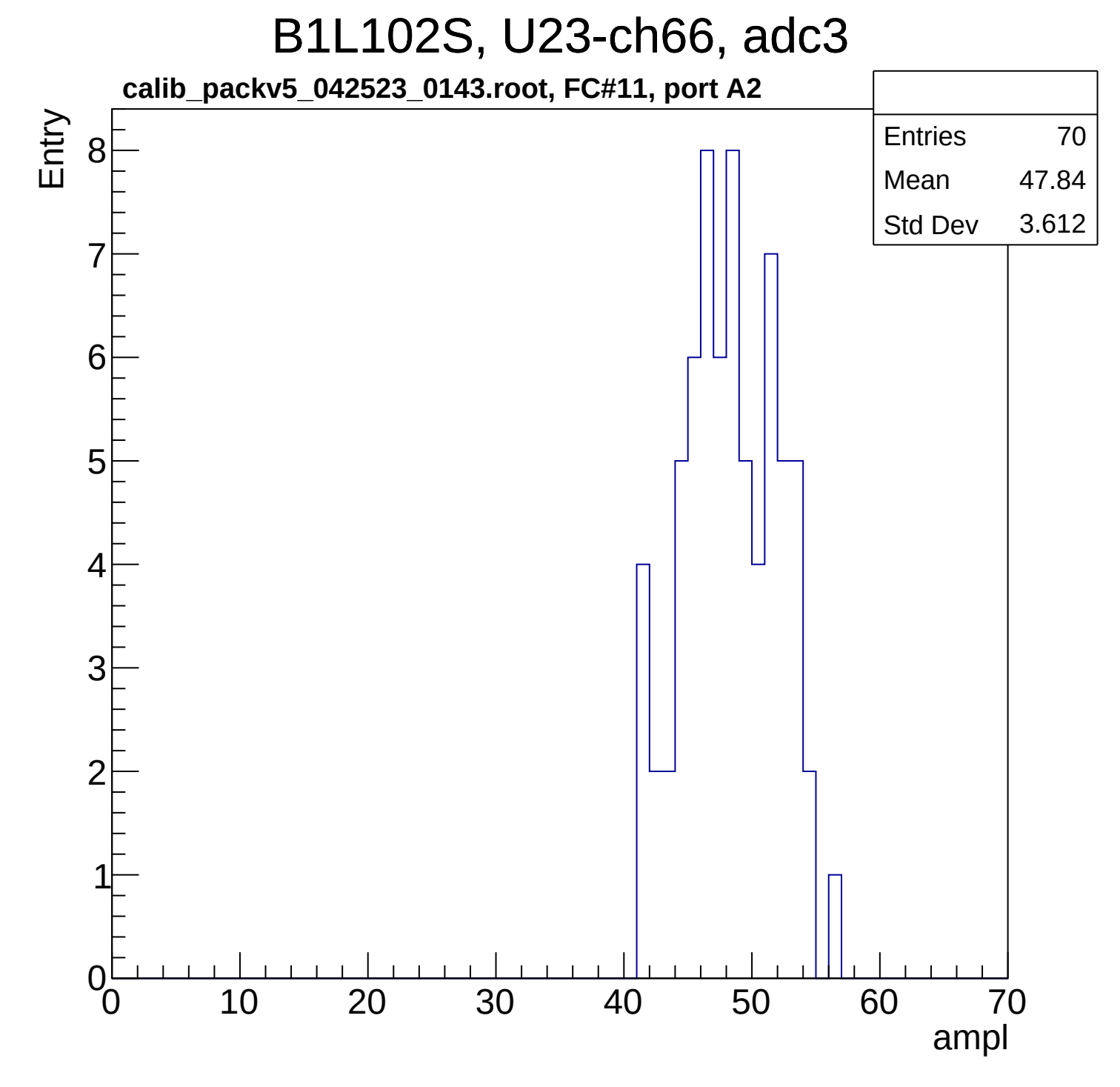

B1L102S, U23-ch65, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

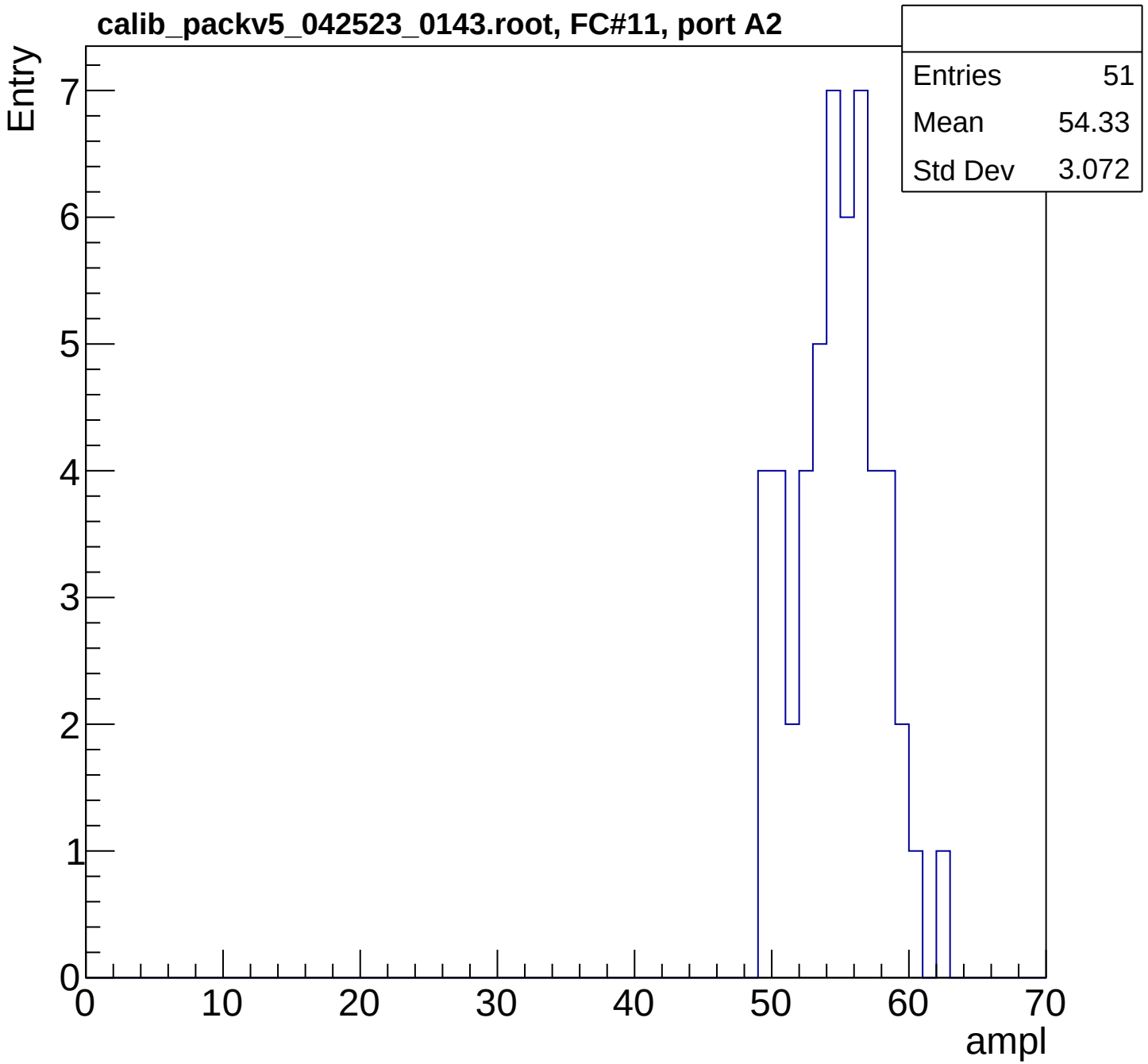

B1L102S, U23-ch65, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

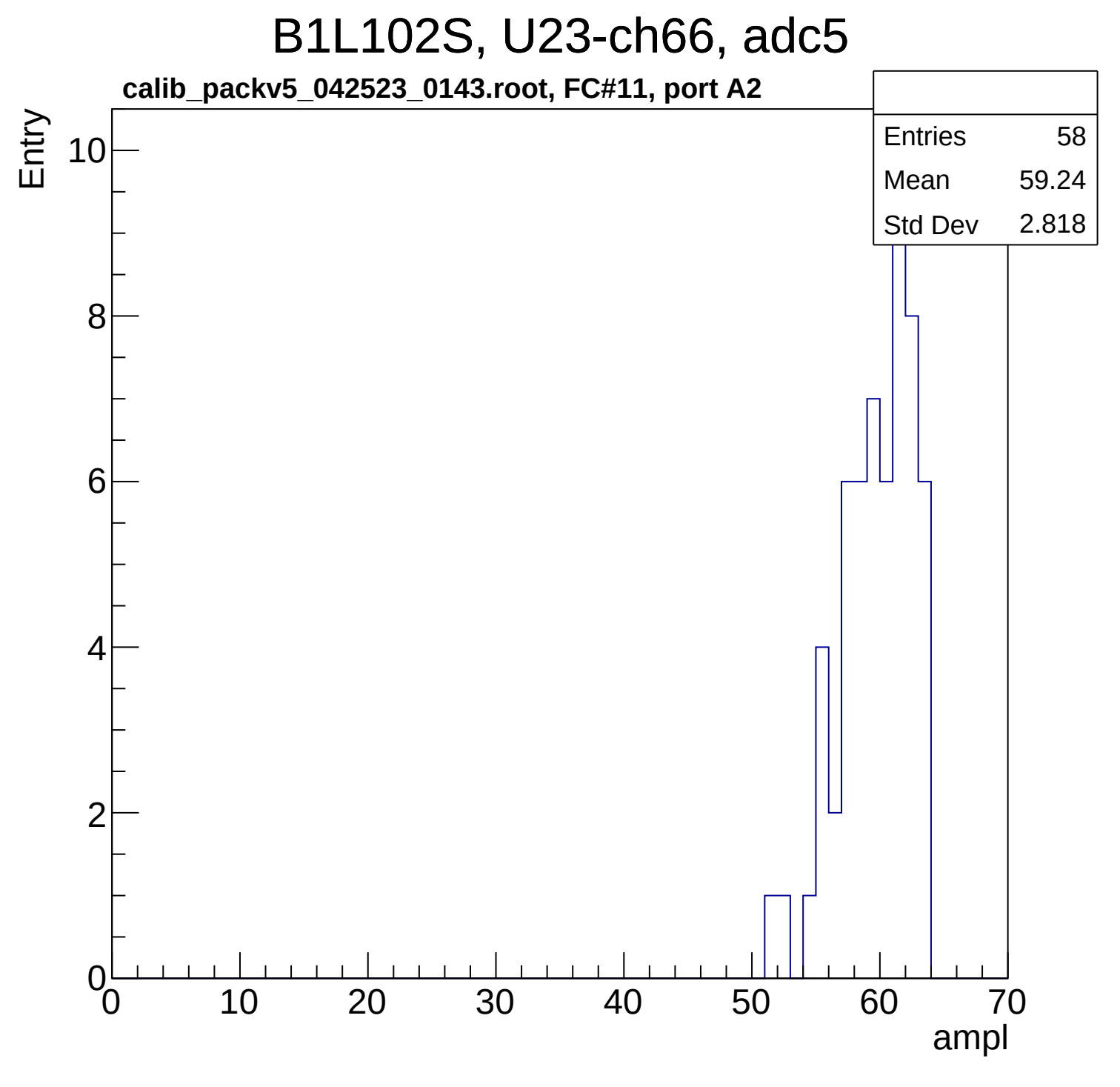

B1L102S, U23-ch66, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 28.05
Std Dev 3.66
Gaus mean : 28.2105
Gaus Width: 3.1572
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

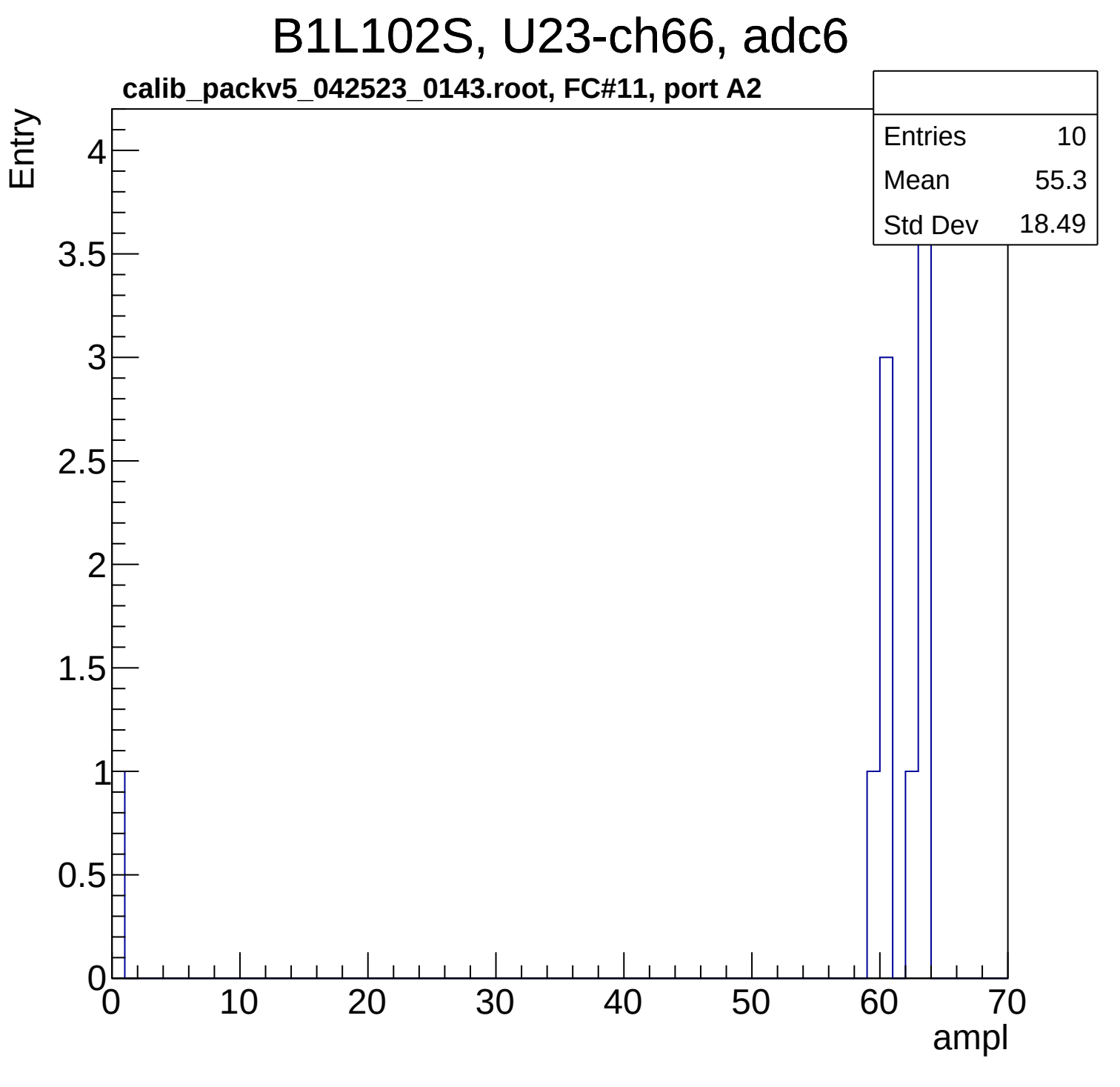

B1L102S, U23-ch66, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 35.36
Std Dev 3.505
Gaus mean : 35.7448
Gaus Width: 3.6865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

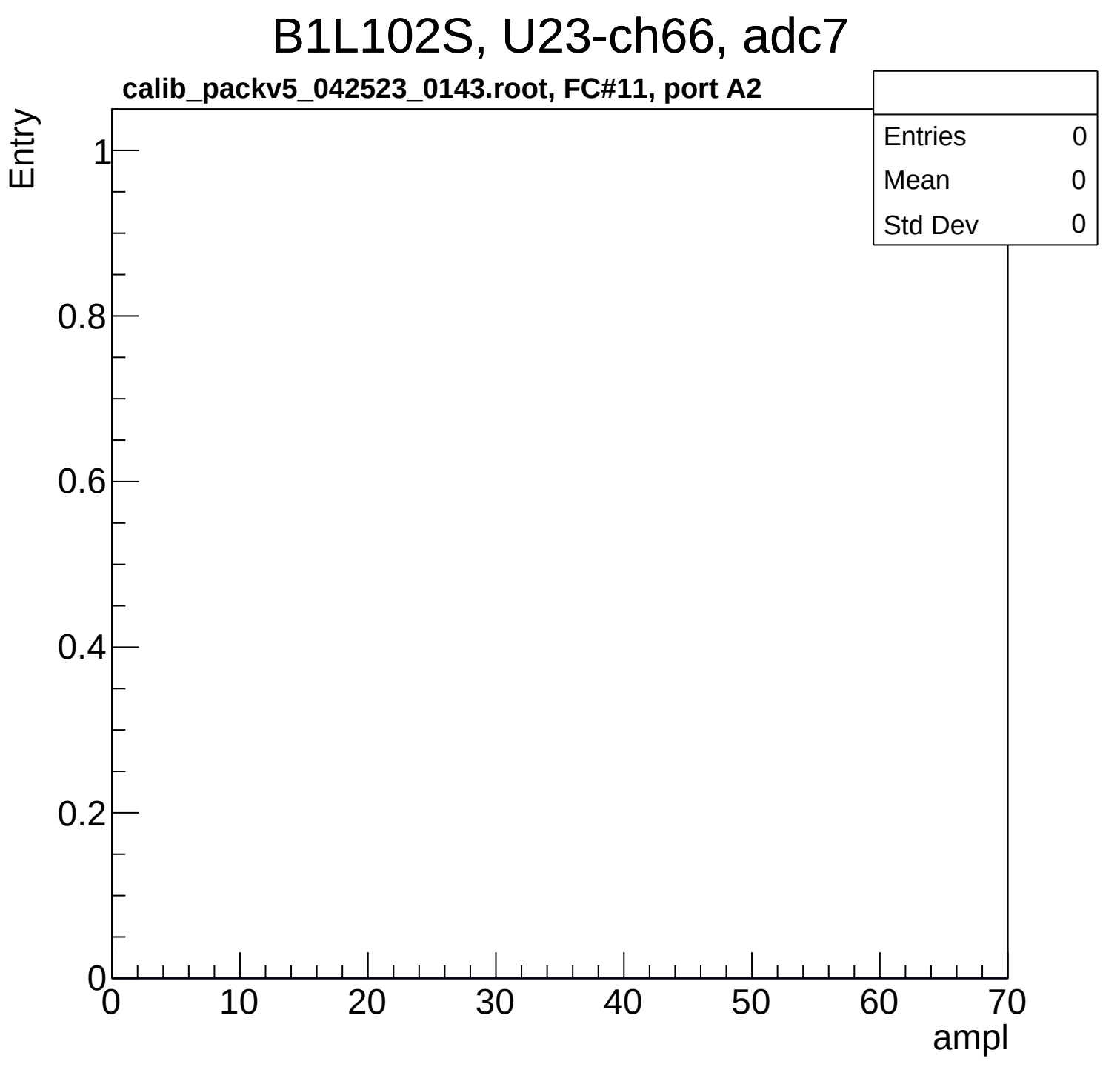

B1L102S, U23-ch66, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 42.18
Std Dev 3.695
Gaus mean : 42.3848
Gaus Width: 4.1663
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

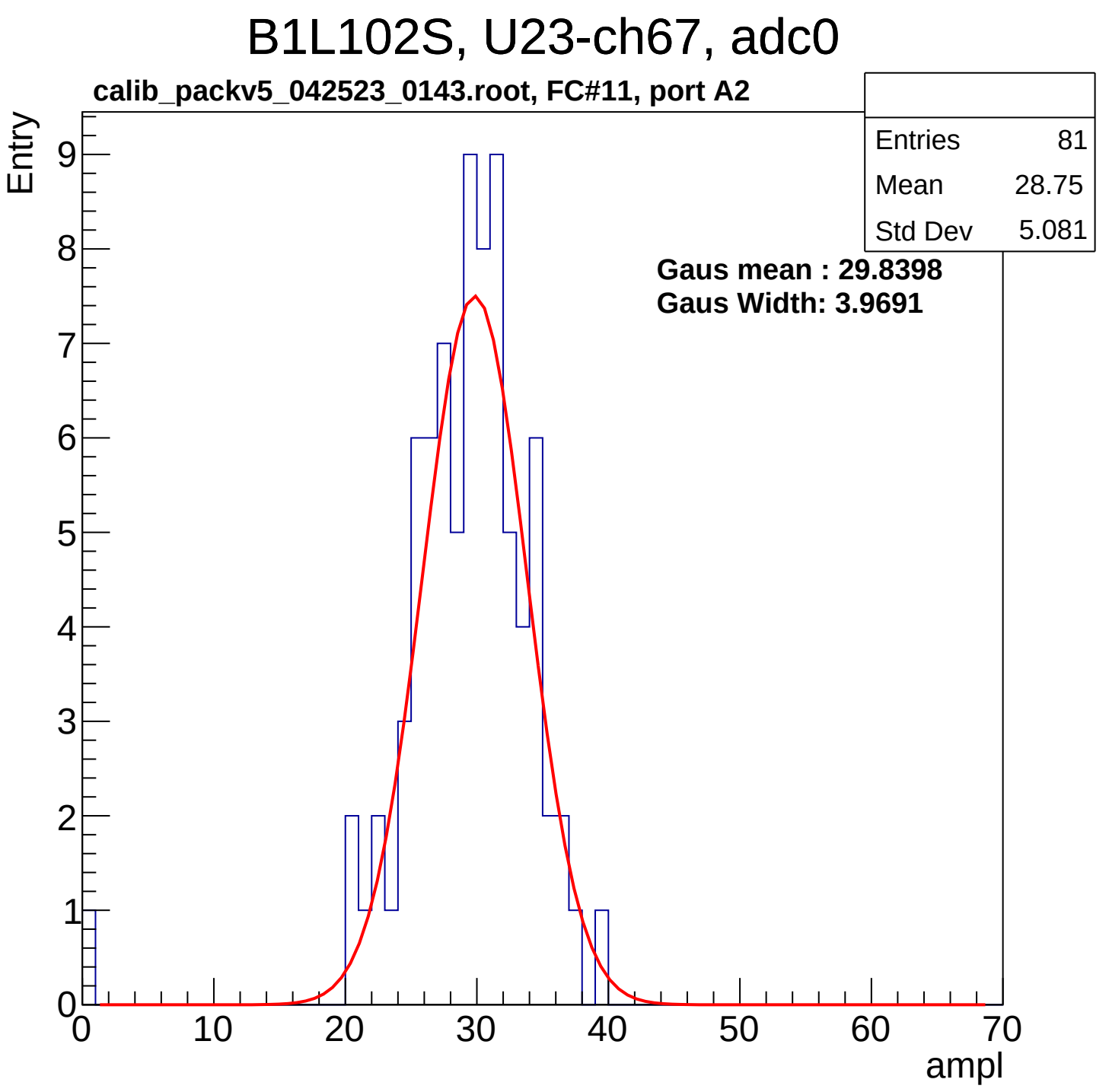

B1L102S, U23-ch66, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 47.84
Std Dev 3.612
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

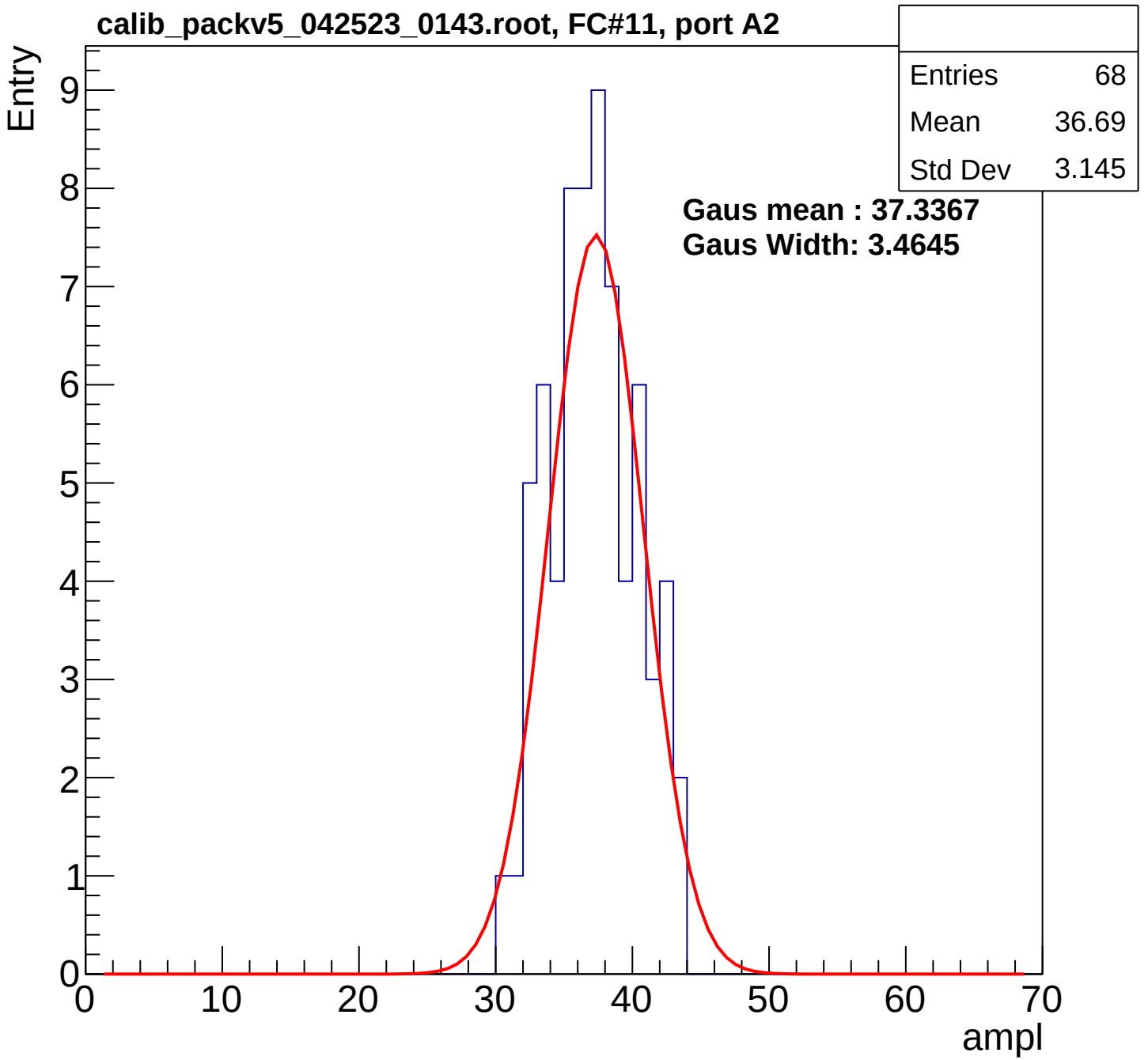

B1L102S, U23-ch66, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 54.33
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

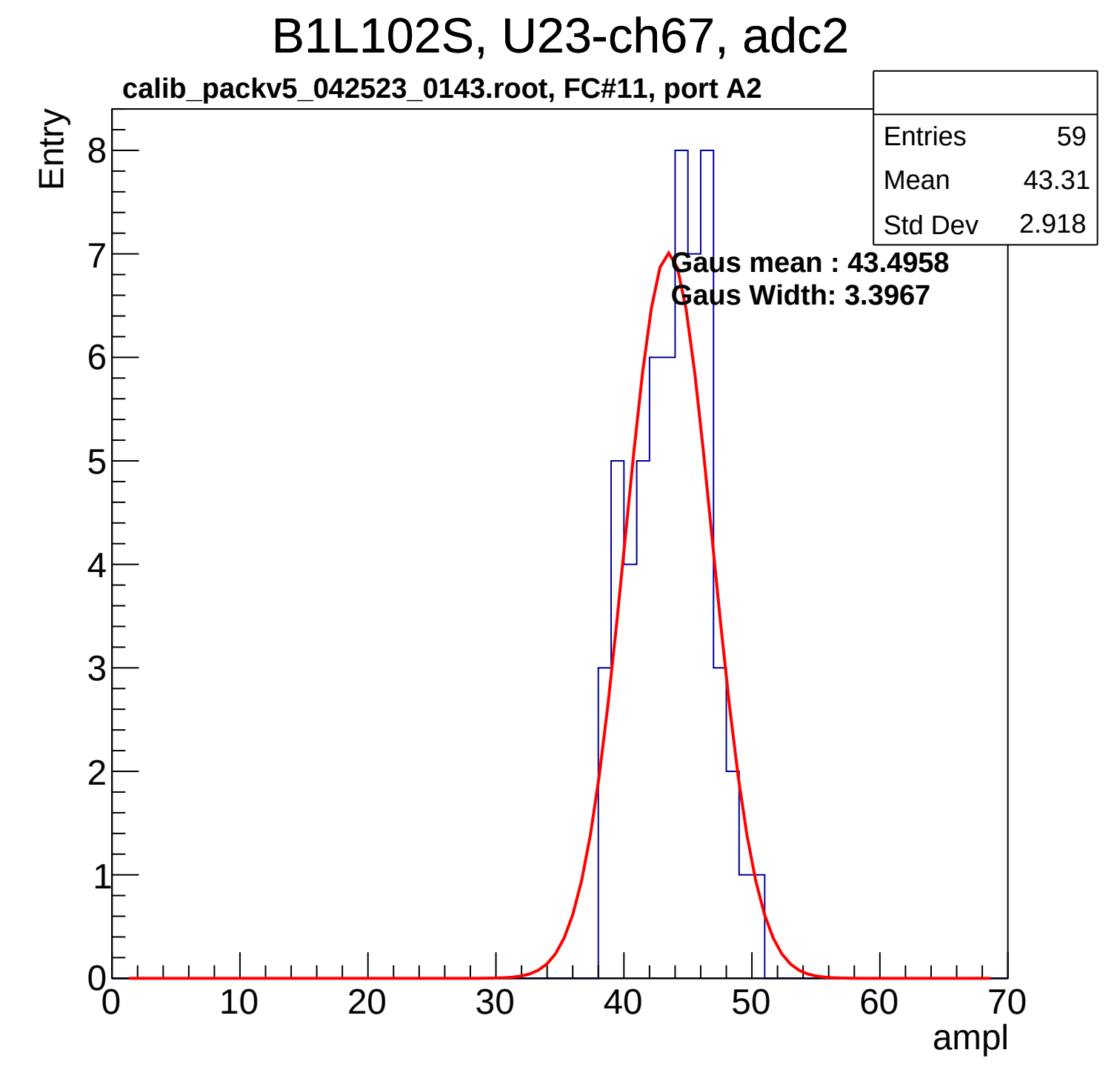

B1L102S, U23-ch66, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 59.24
Std Dev 2.818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

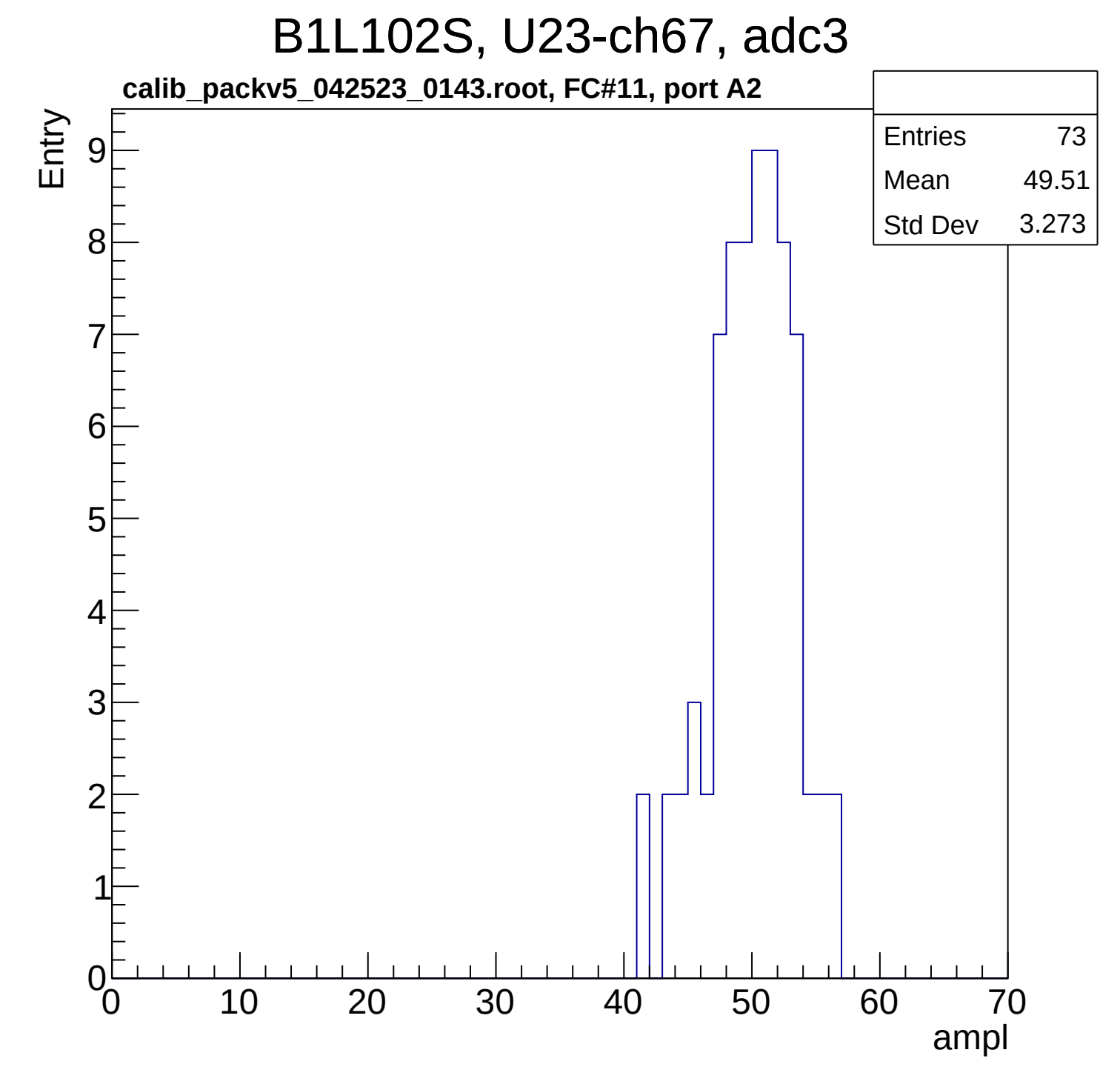

B1L102S, U23-ch66, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 55.3
Std Dev 18.49
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

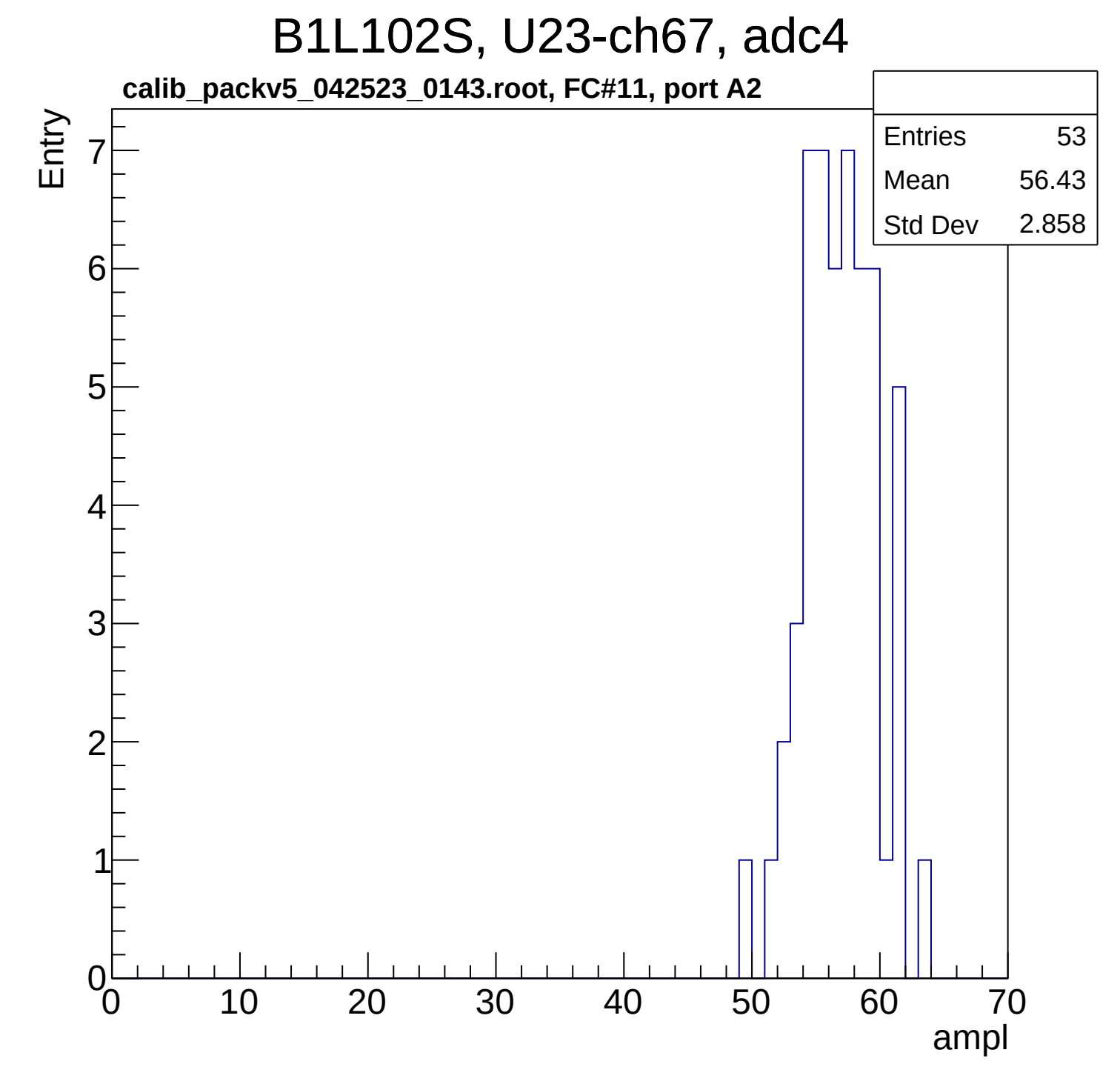

B1L102S, U23-ch66, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

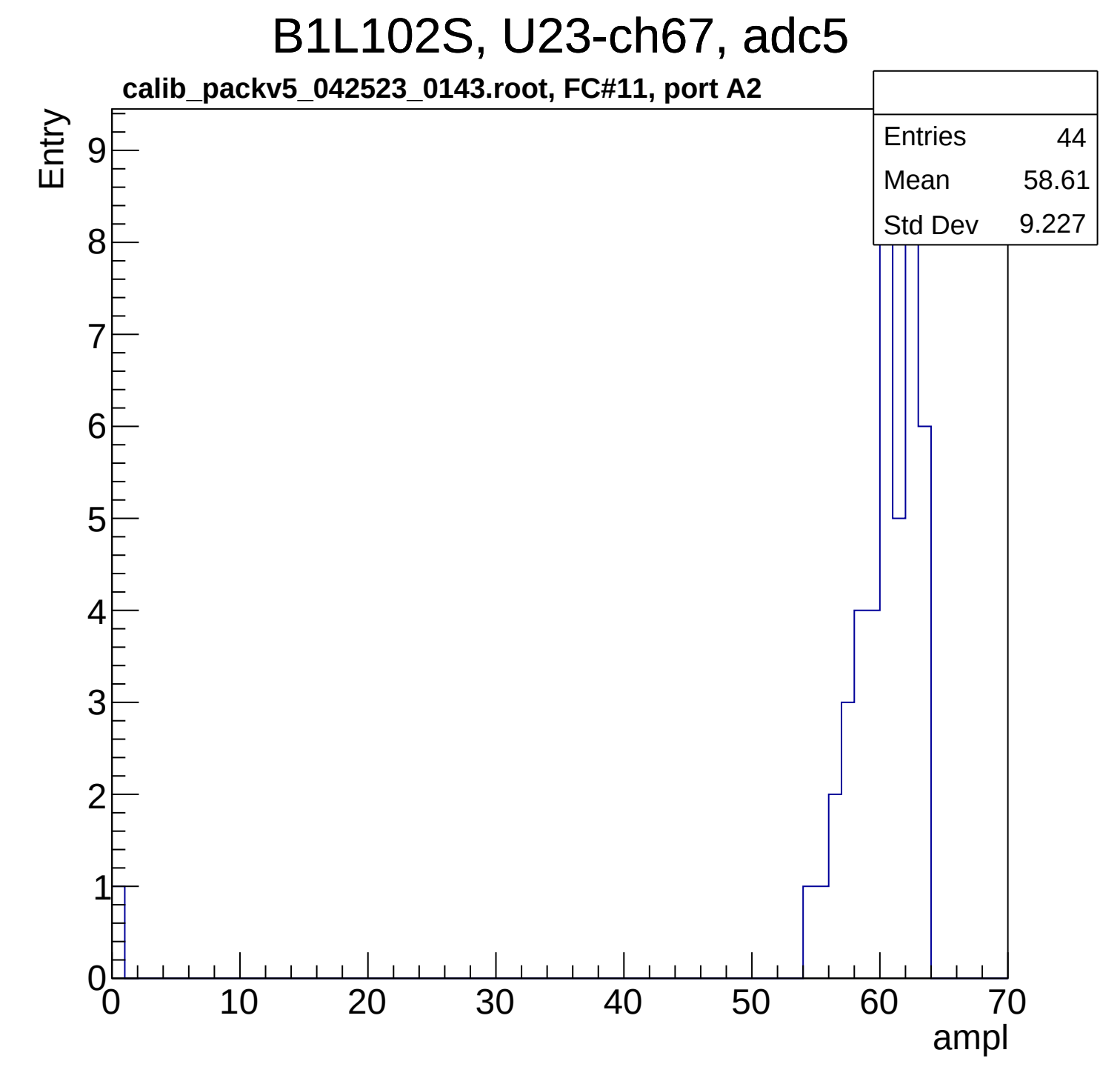

B1L102S, U23-ch67, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 28.75
Std Dev 5.081
Gaus mean : 29.8398
Gaus Width: 3.9691
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

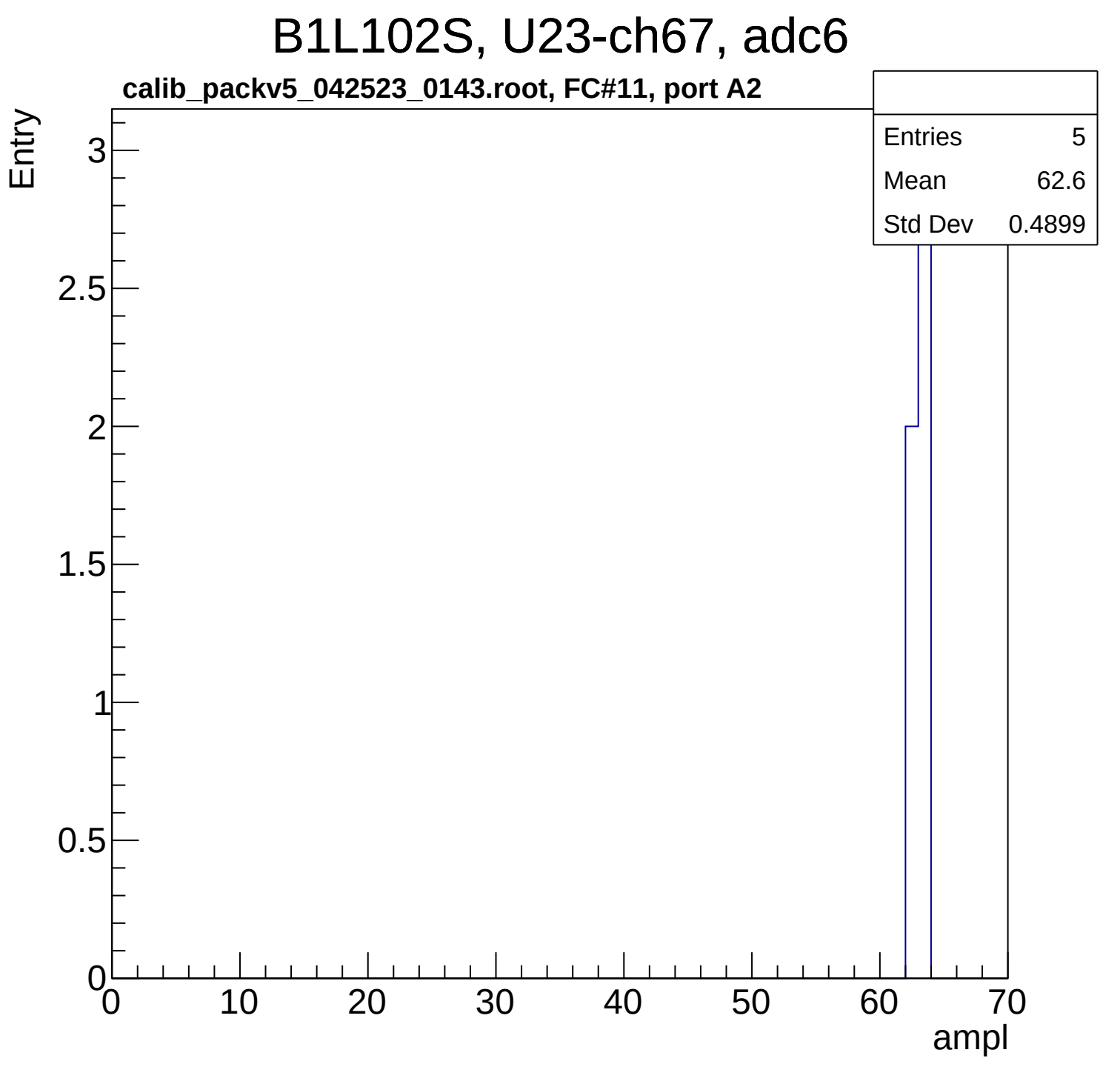

B1L102S, U23-ch67, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 36.69
Std Dev 3.145
Gaus mean : 37.3367
Gaus Width: 3.4645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

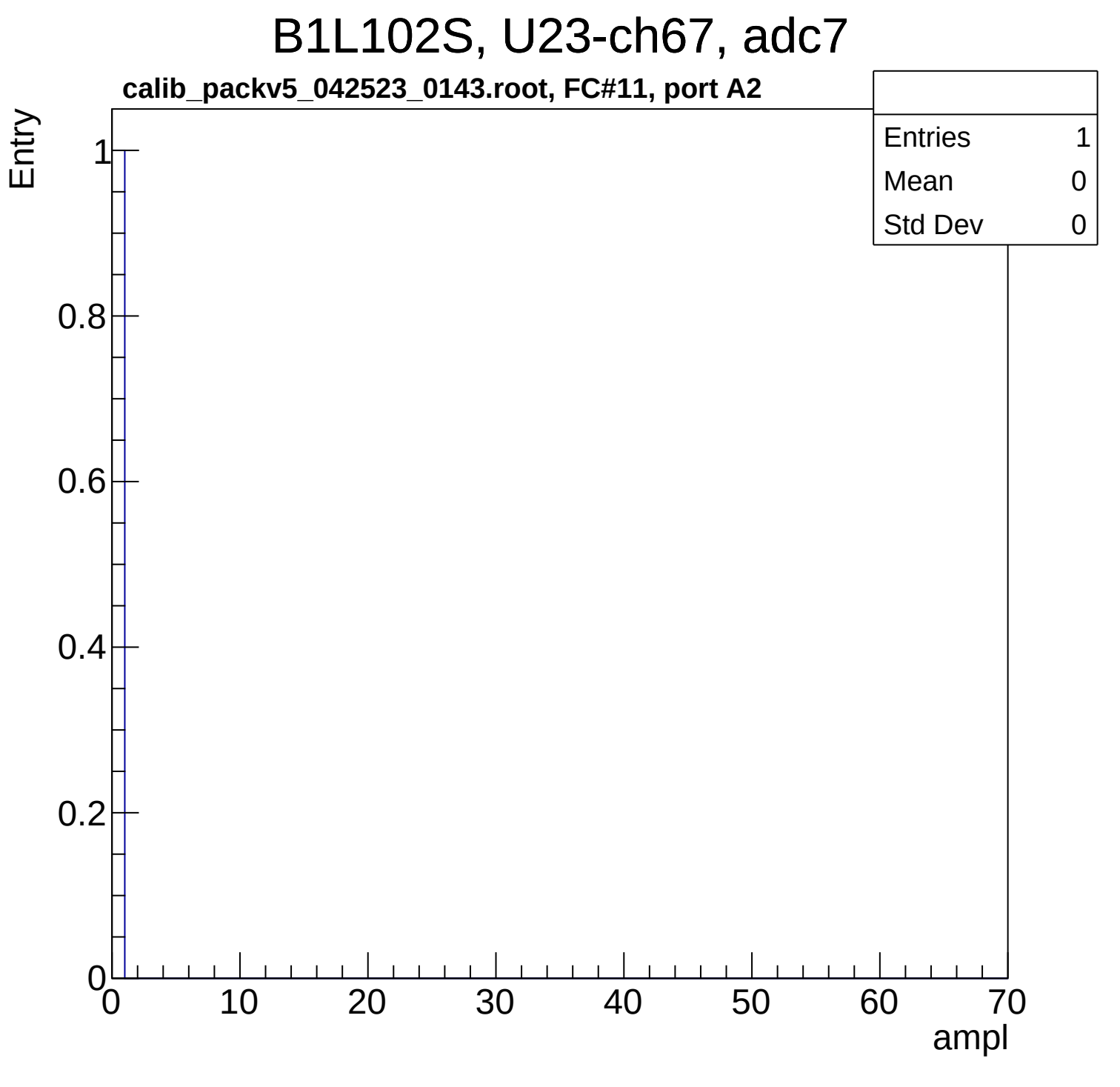

B1L102S, U23-ch67, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 43.31
Std Dev 2.918
Gaus mean : 43.4958
Gaus Width: 3.3967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

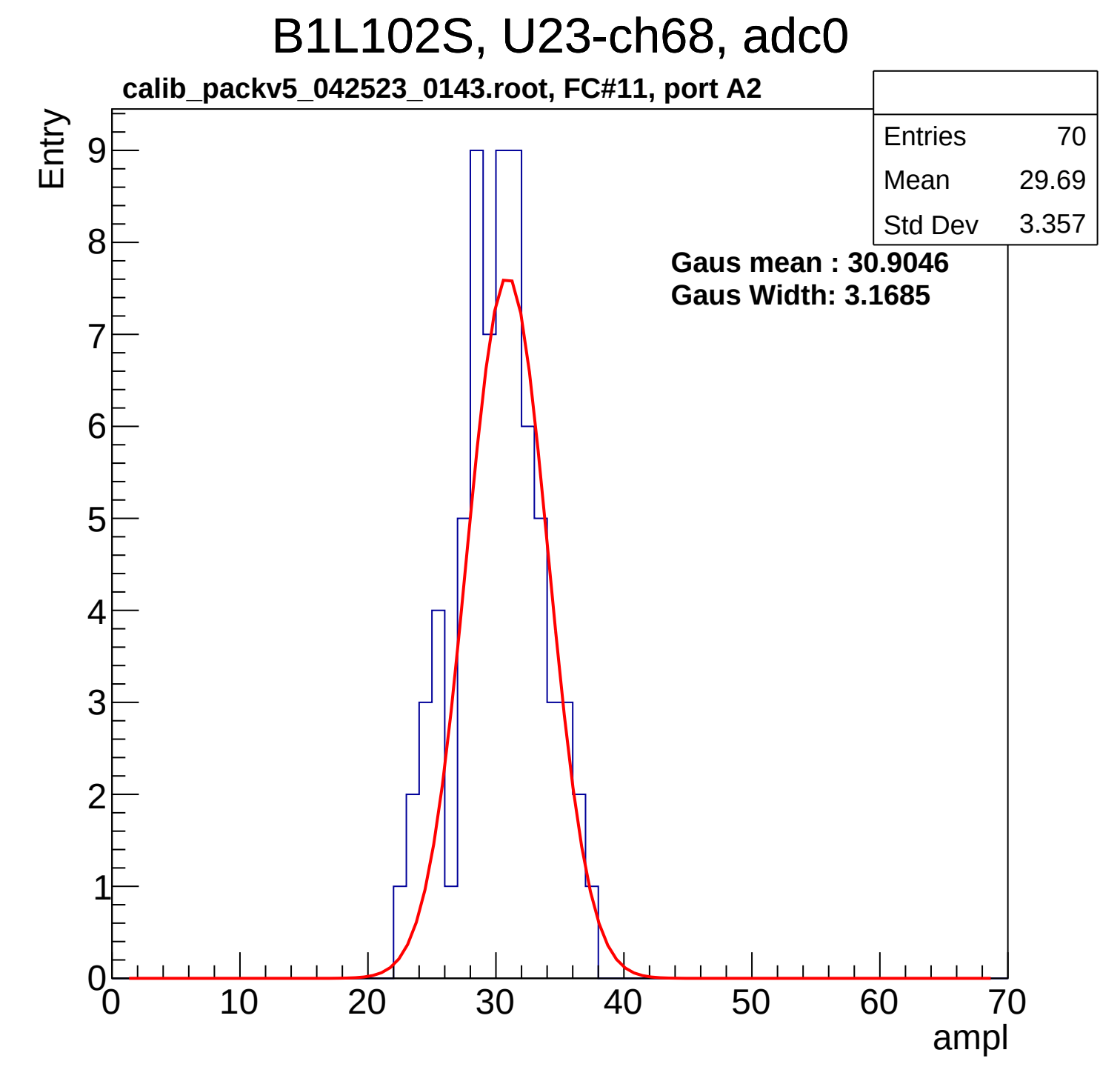

B1L102S, U23-ch67, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 49.51
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

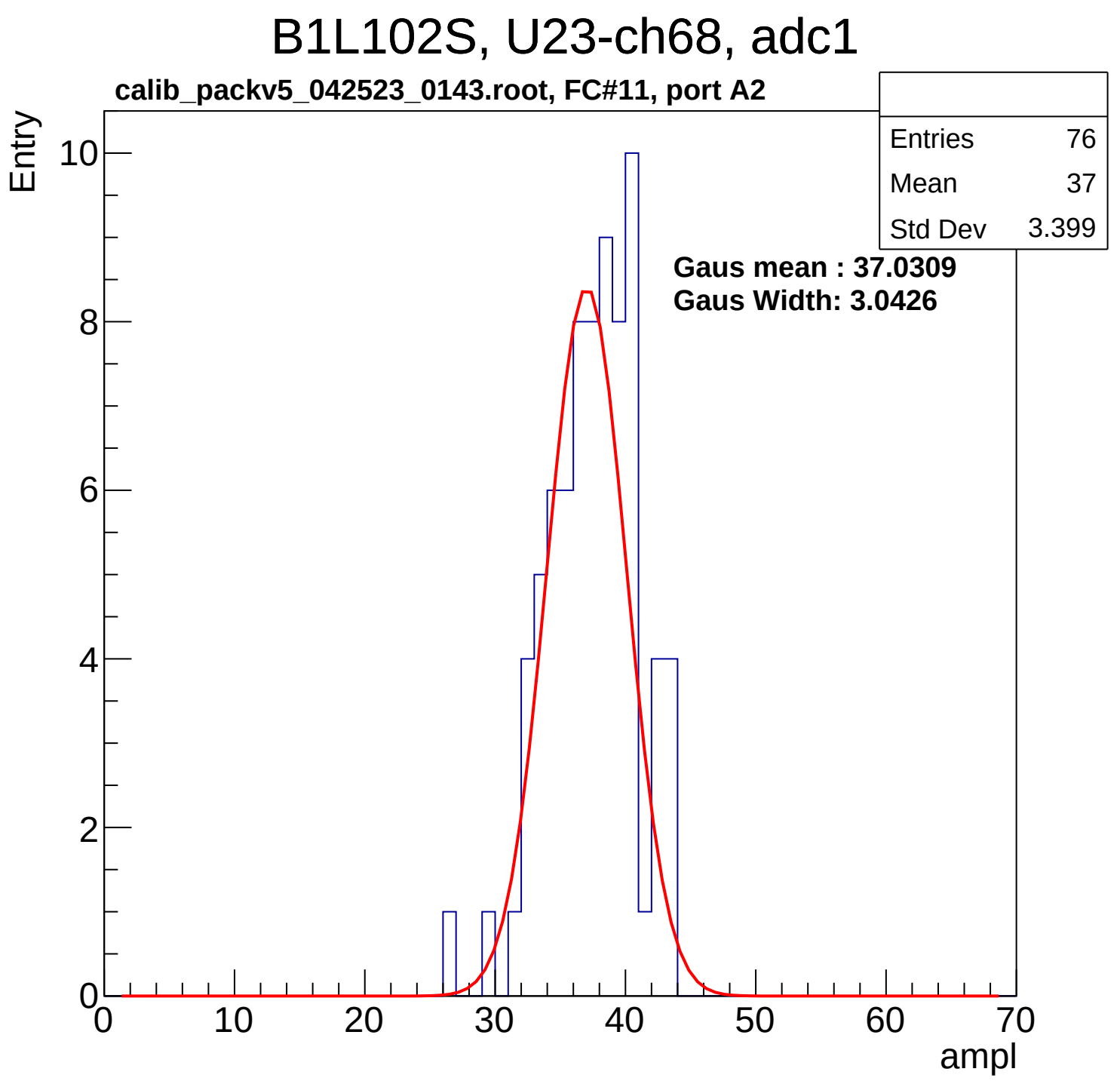

B1L102S, U23-ch67, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 56.43
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

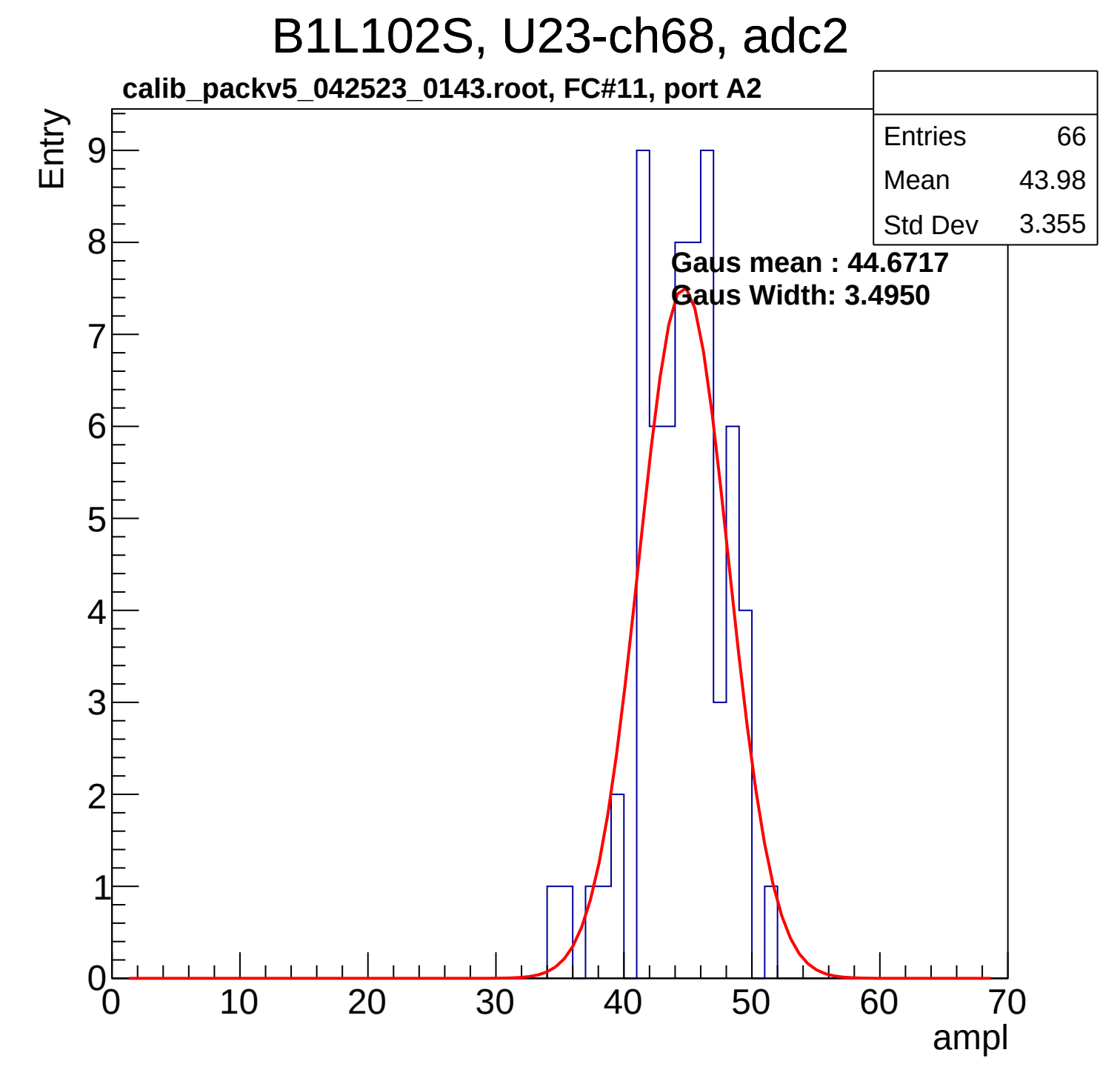

B1L102S, U23-ch67, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 58.61
Std Dev 9.227
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

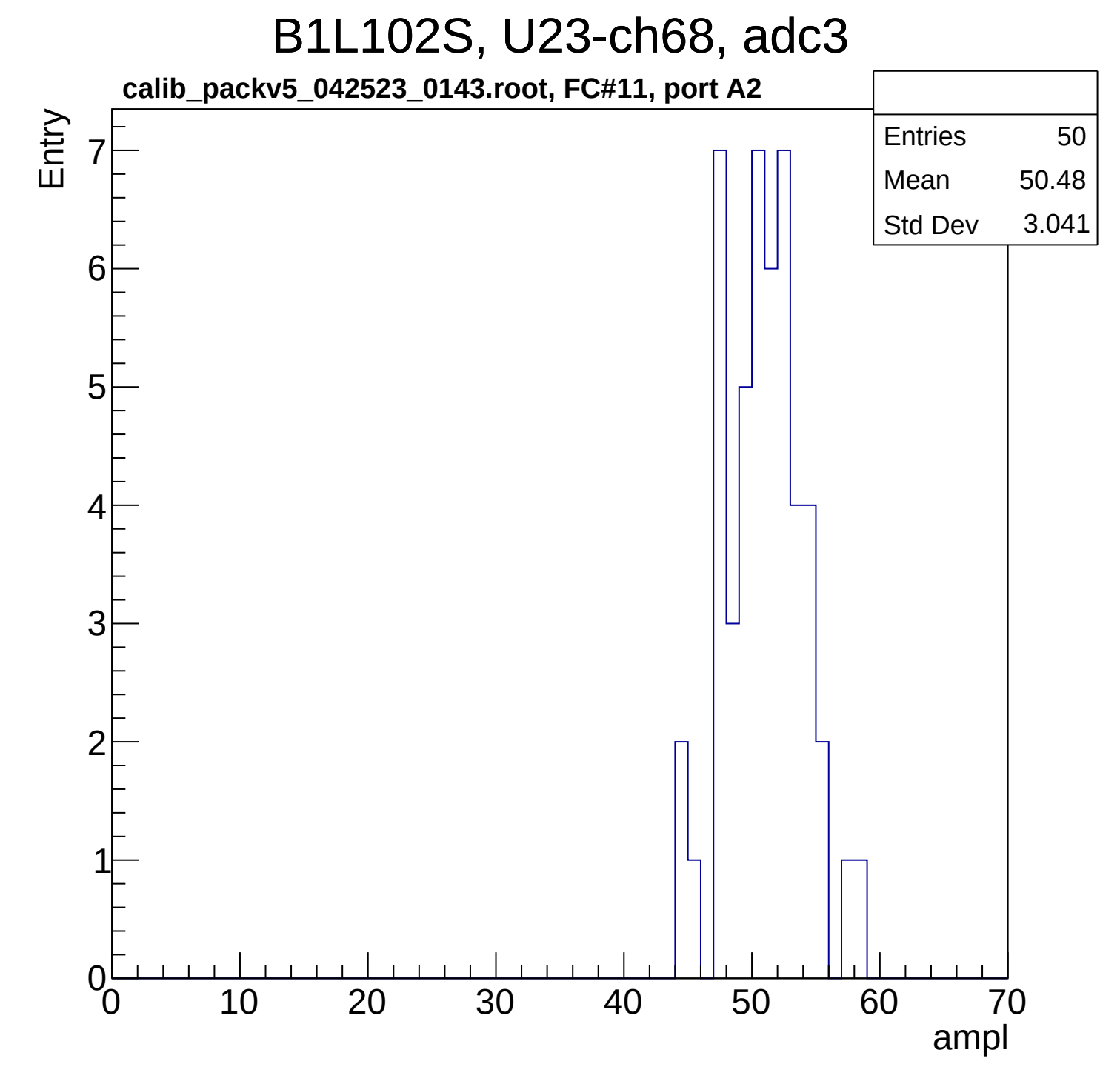

B1L102S, U23-ch67, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

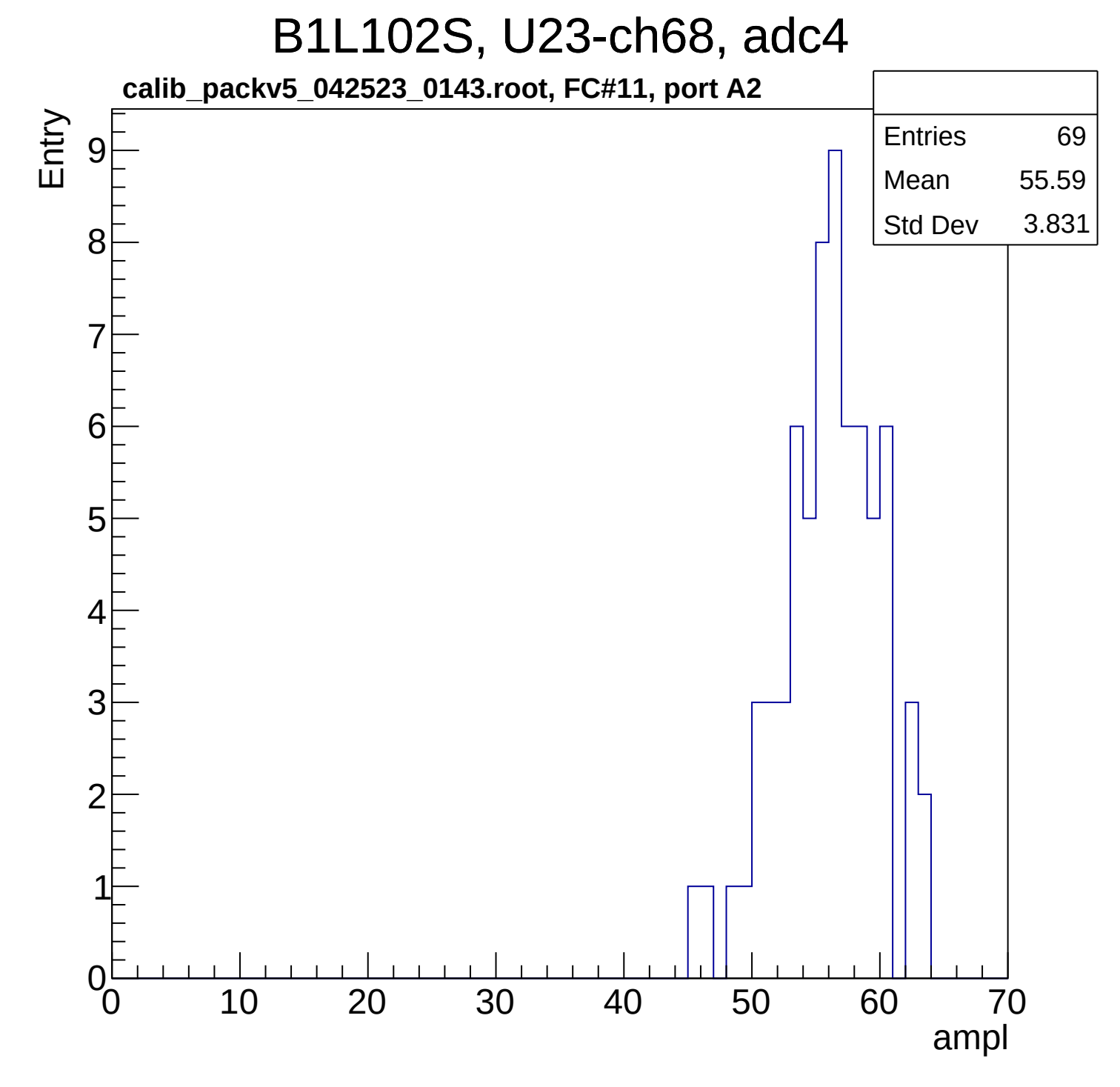

B1L102S, U23-ch67, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

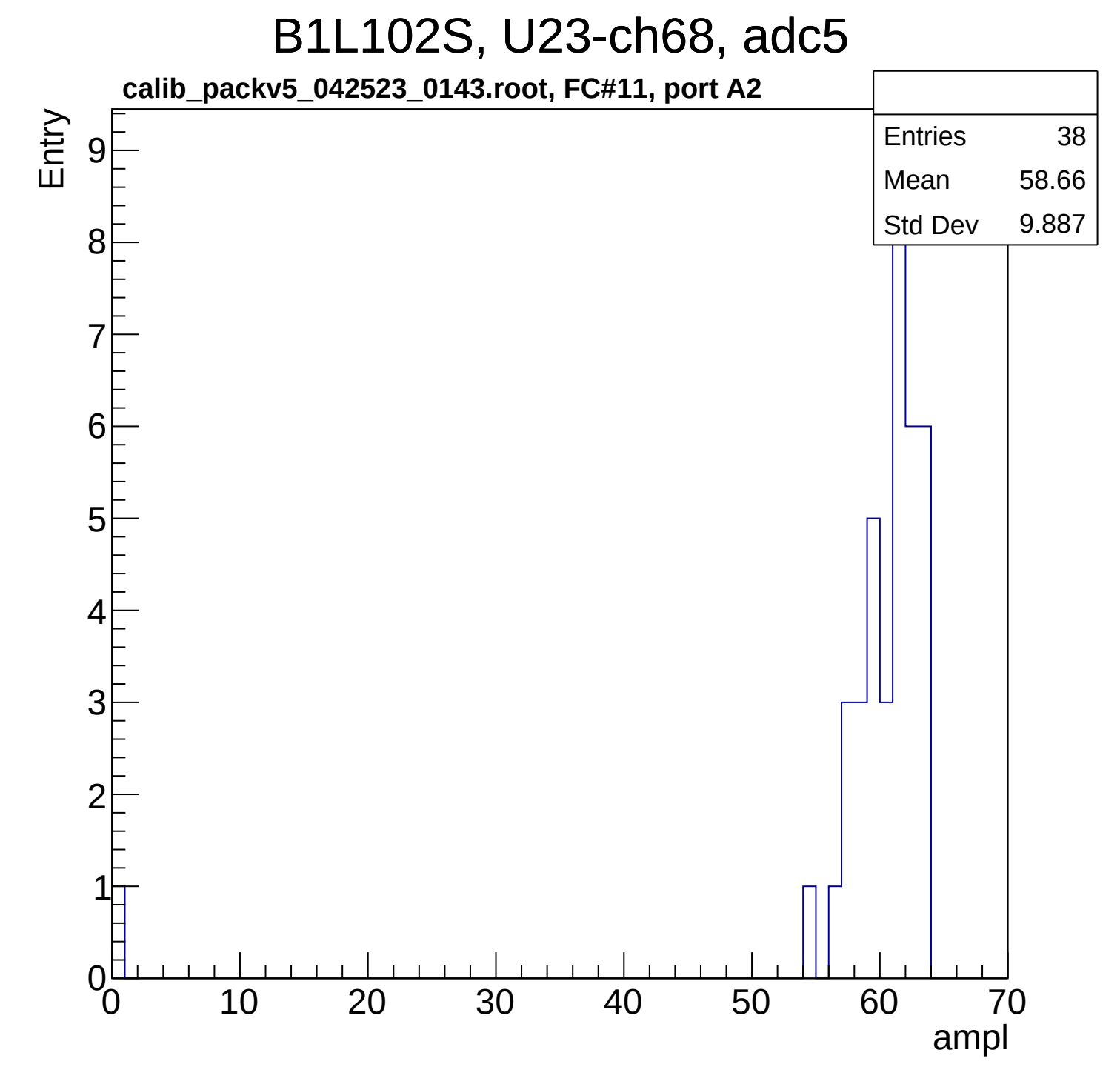

B1L102S, U23-ch68, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 29.69
Std Dev 3.357
Gaus mean : 30.9046
Gaus Width: 3.1685
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

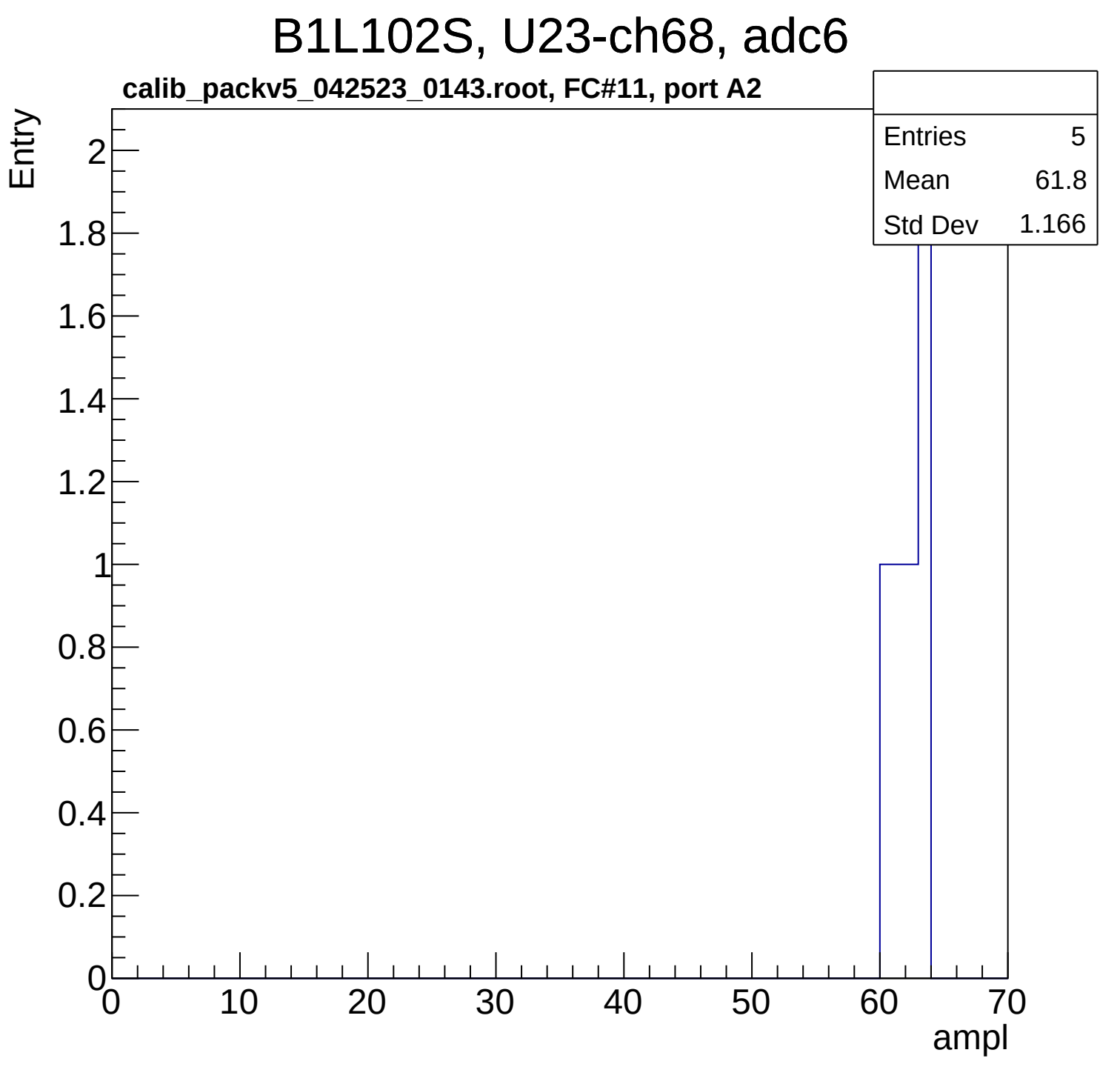

B1L102S, U23-ch68, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 37
Std Dev 3.399
Gaus mean : 37.0309
Gaus Width: 3.0426
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

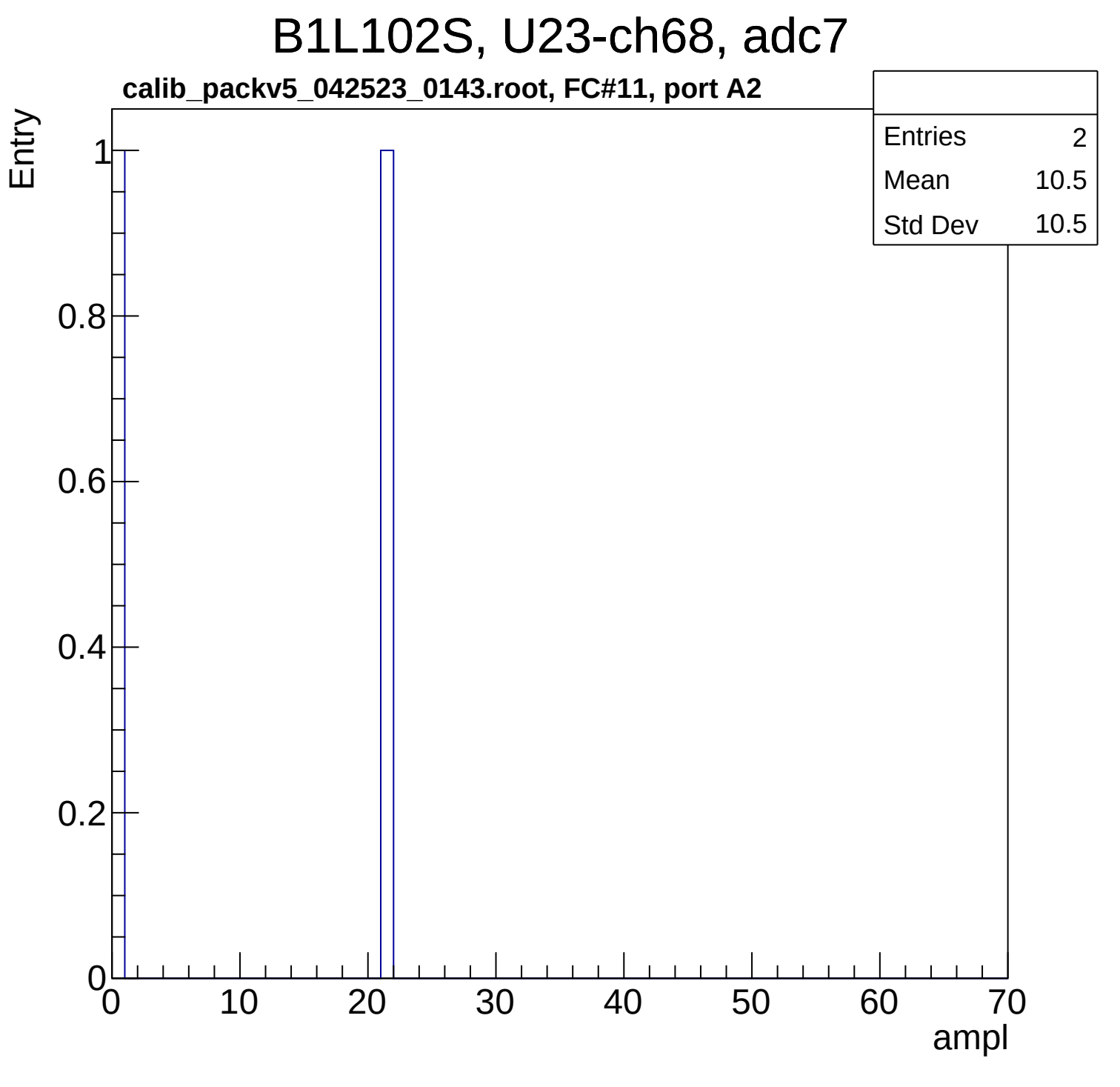

B1L102S, U23-ch68, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 43.98
Std Dev 3.355
Gaus mean : 44.6717
Gaus Width: 3.4950
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

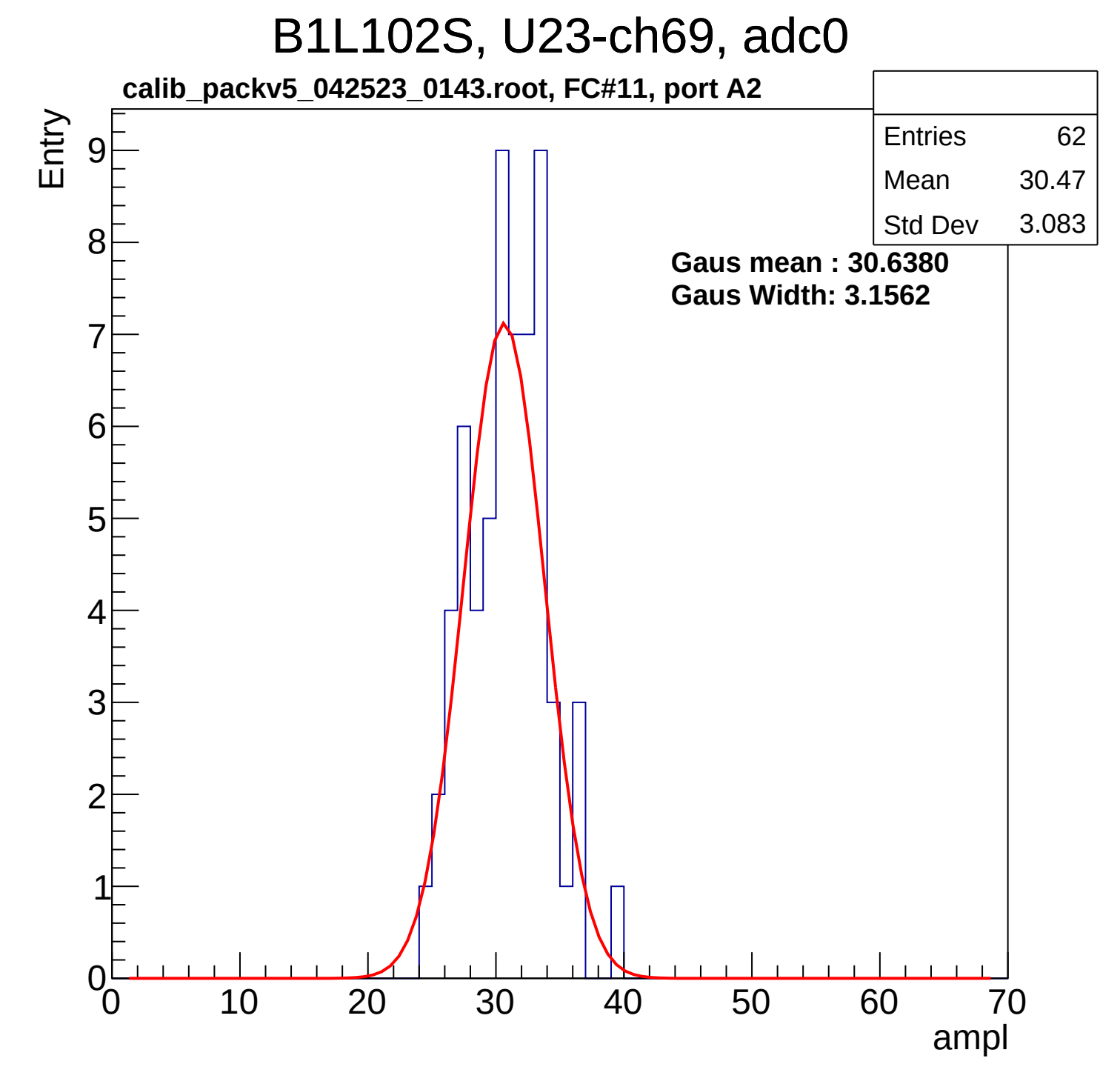

B1L102S, U23-ch68, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 50.48
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

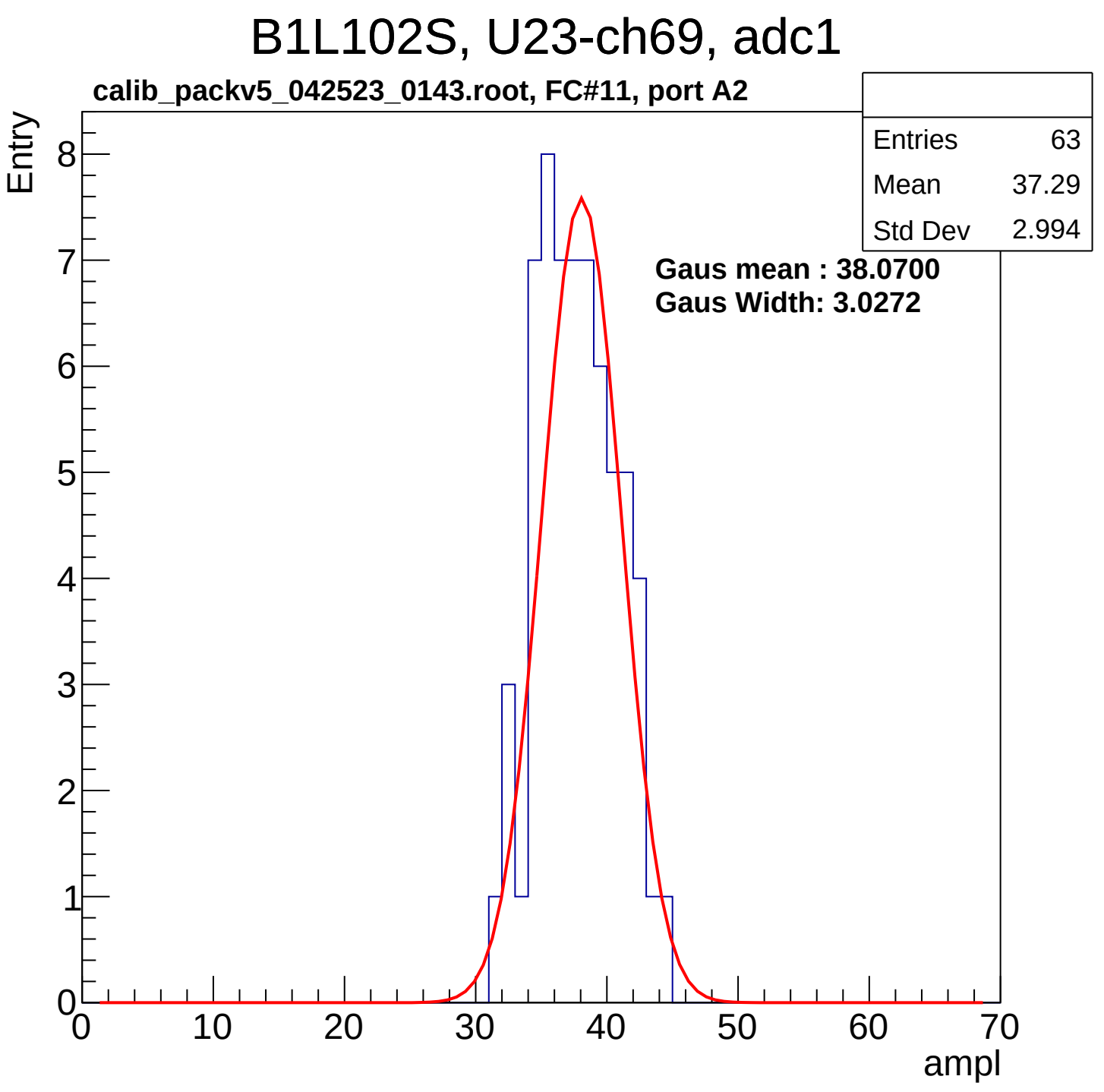

B1L102S, U23-ch68, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 55.59
Std Dev 3.831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

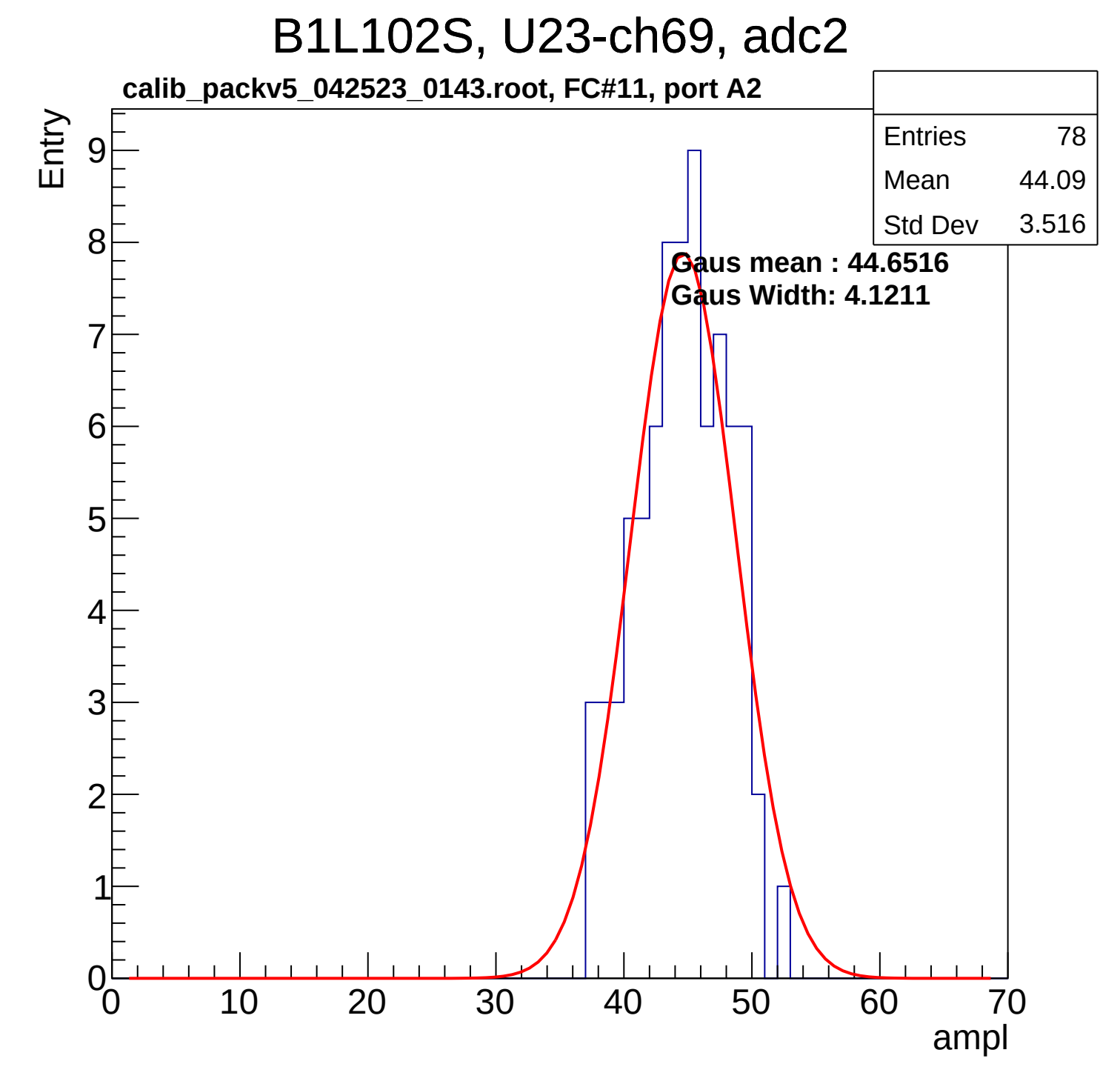

B1L102S, U23-ch68, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.66
Std Dev 9.887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

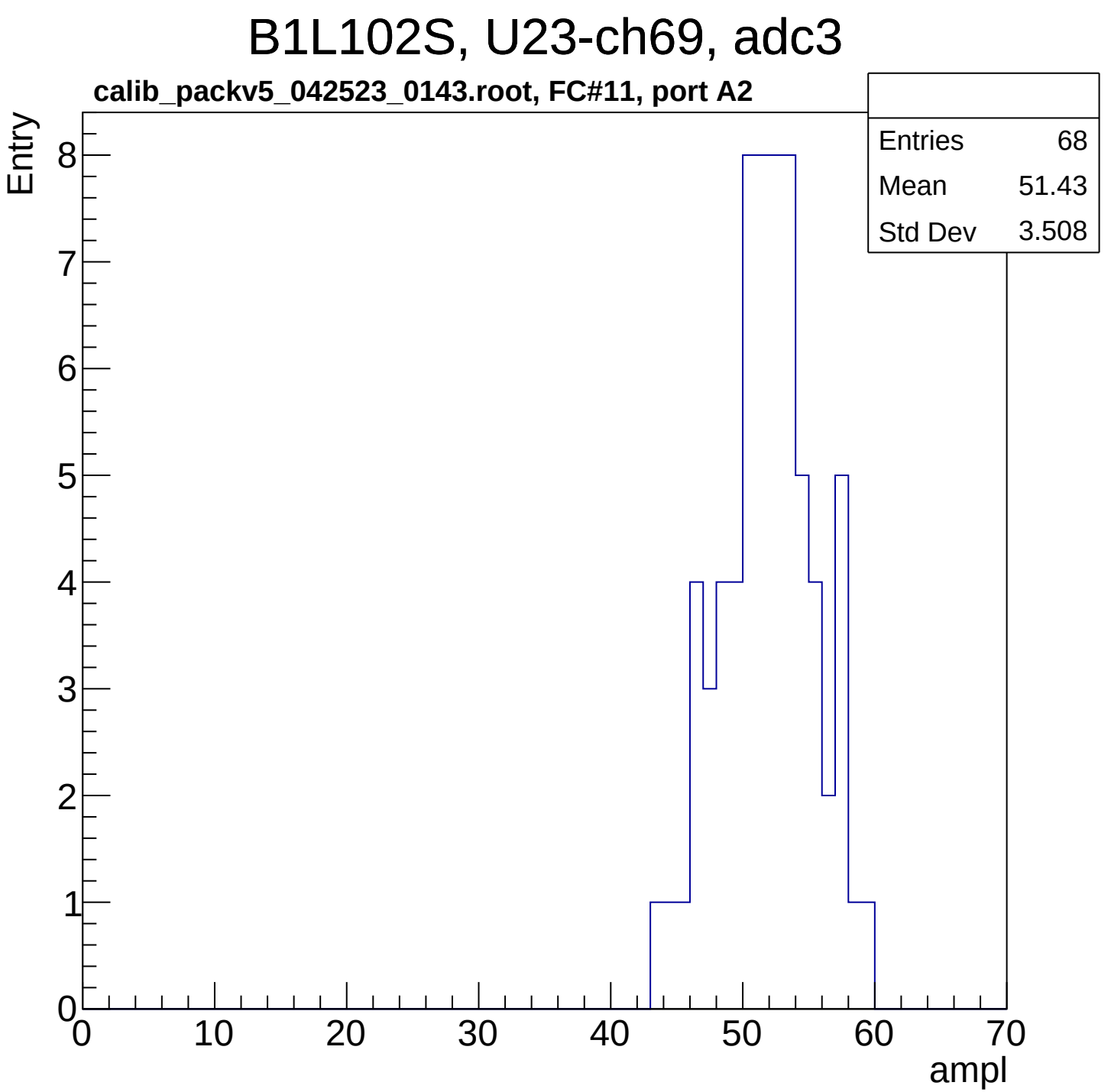

B1L102S, U23-ch68, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

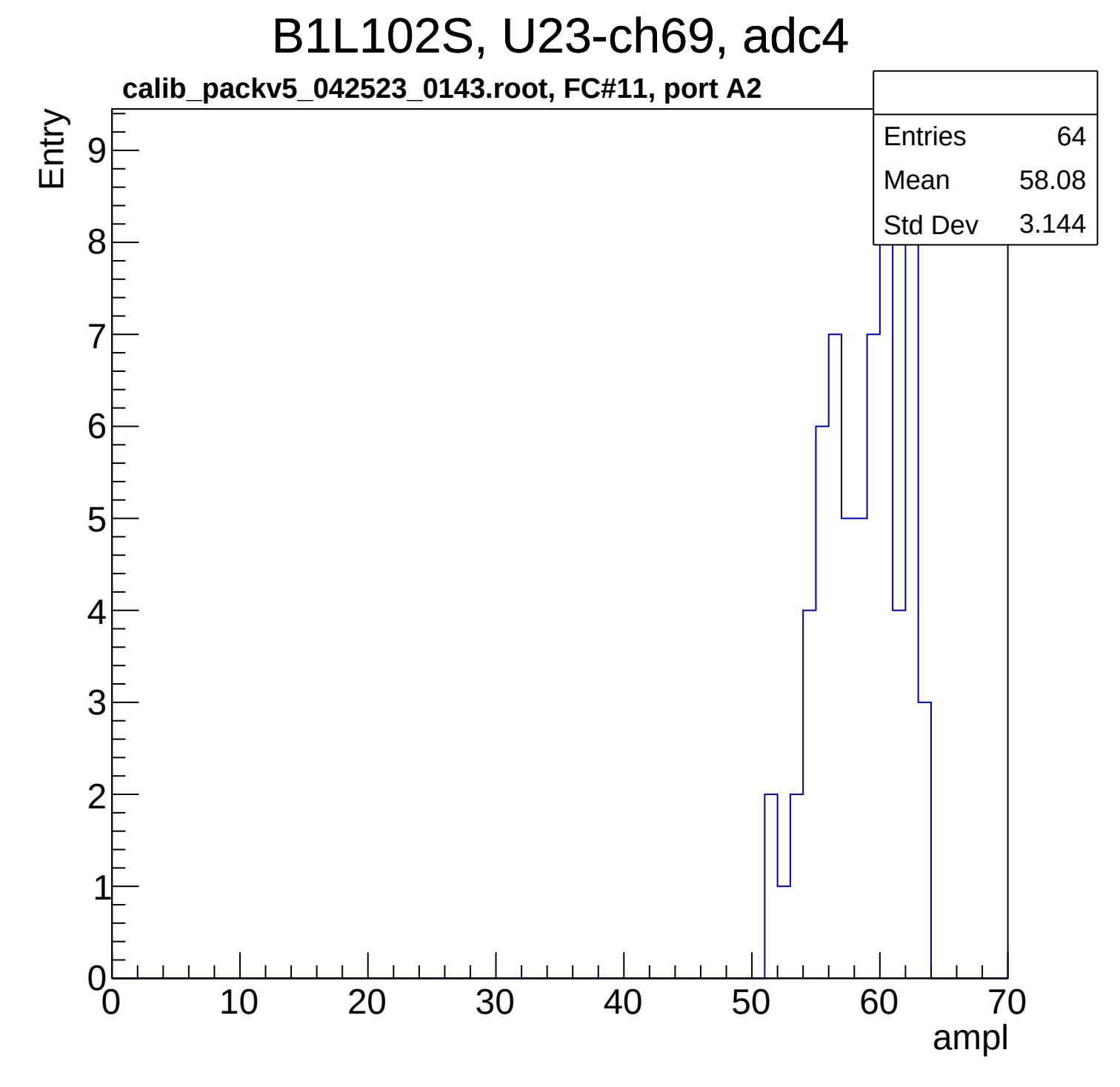

B1L102S, U23-ch68, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

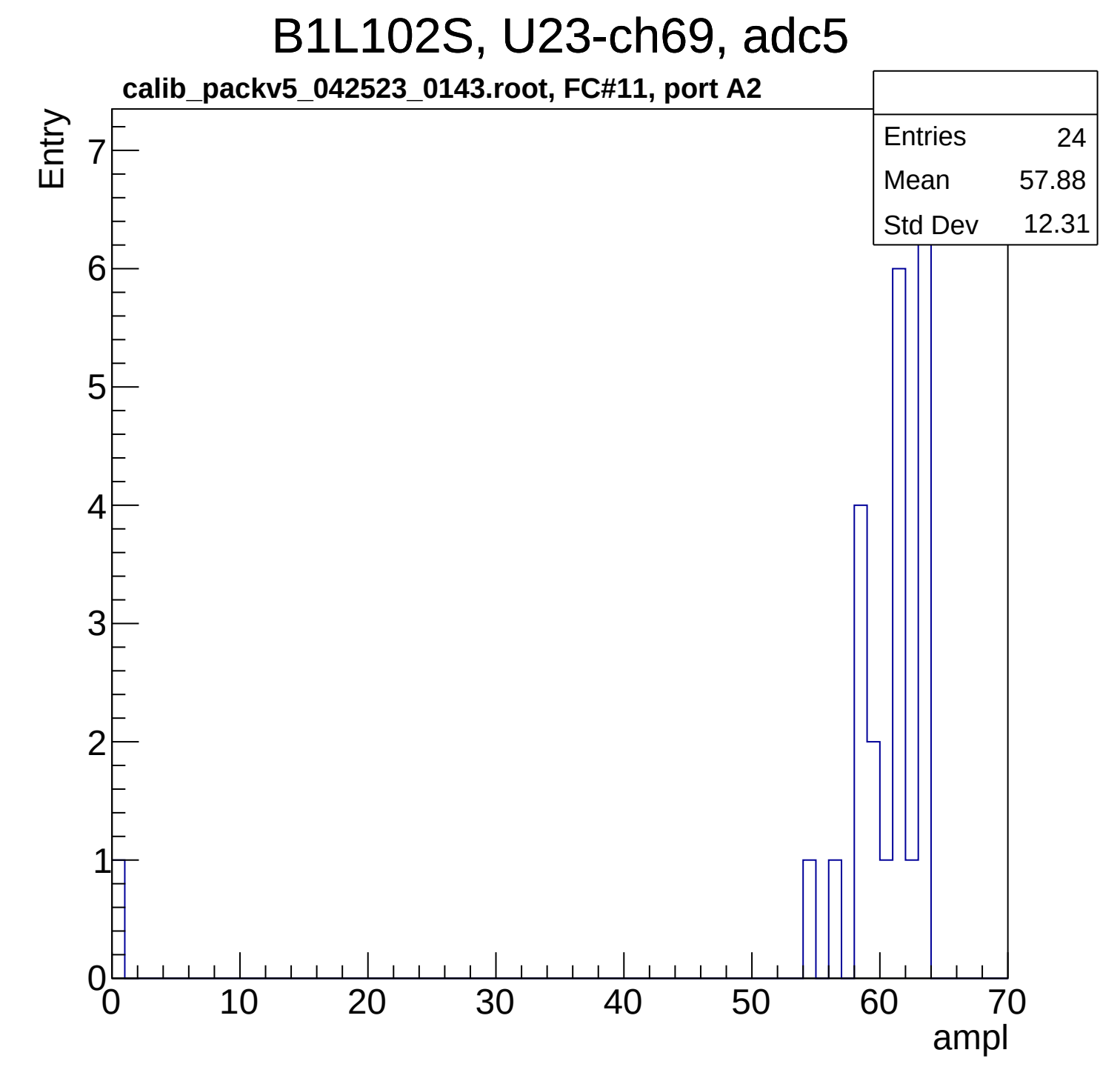

B1L102S, U23-ch69, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 30.47
Std Dev 3.083
Gaus mean : 30.6380
Gaus Width: 3.1562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

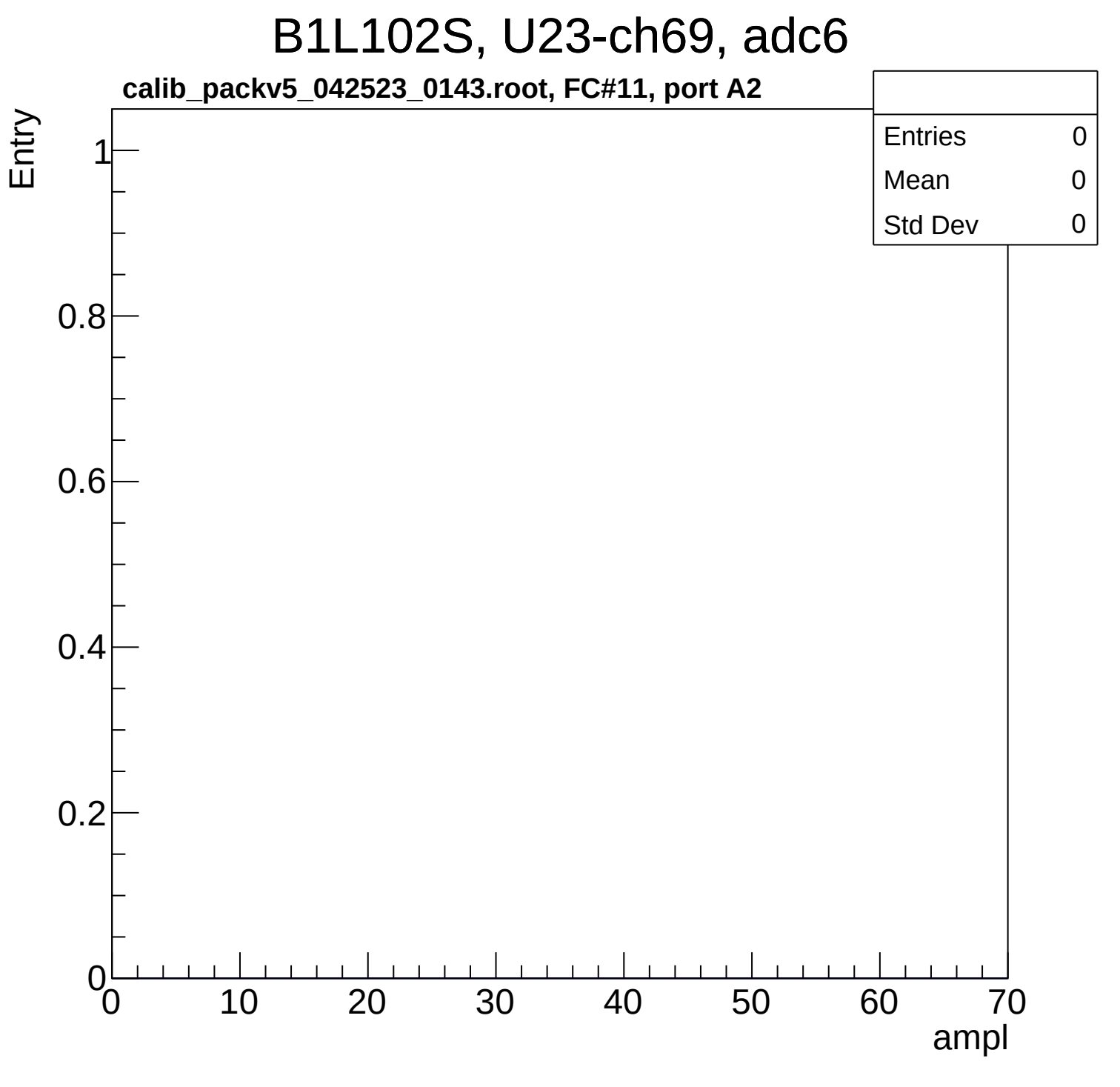

B1L102S, U23-ch69, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 37.29
Std Dev 2.994
Gaus mean : 38.0700
Gaus Width: 3.0272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

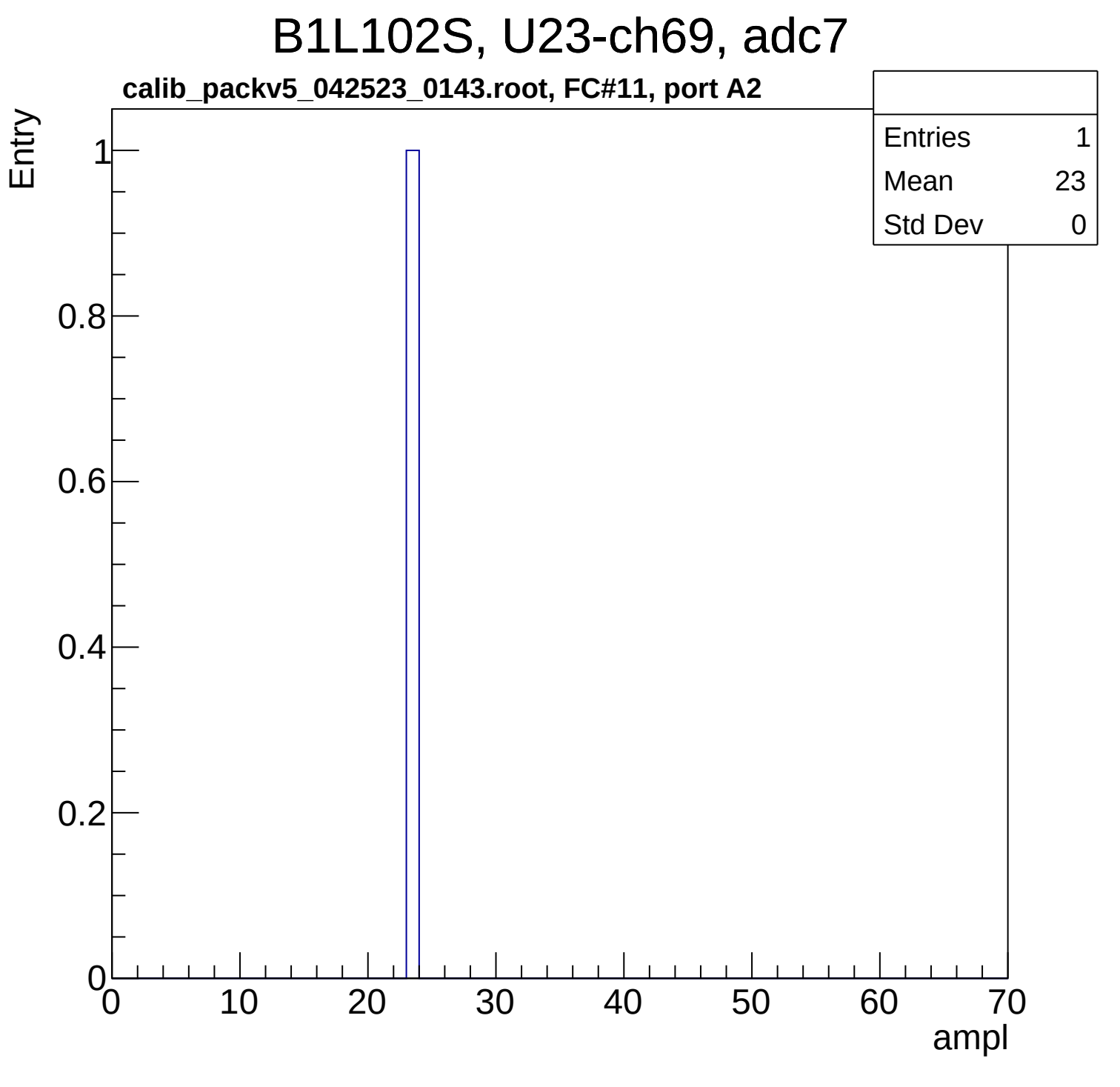

B1L102S, U23-ch69, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 44.09
Std Dev 3.516
Gaus mean : 44.6516
Gaus Width: 4.1211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

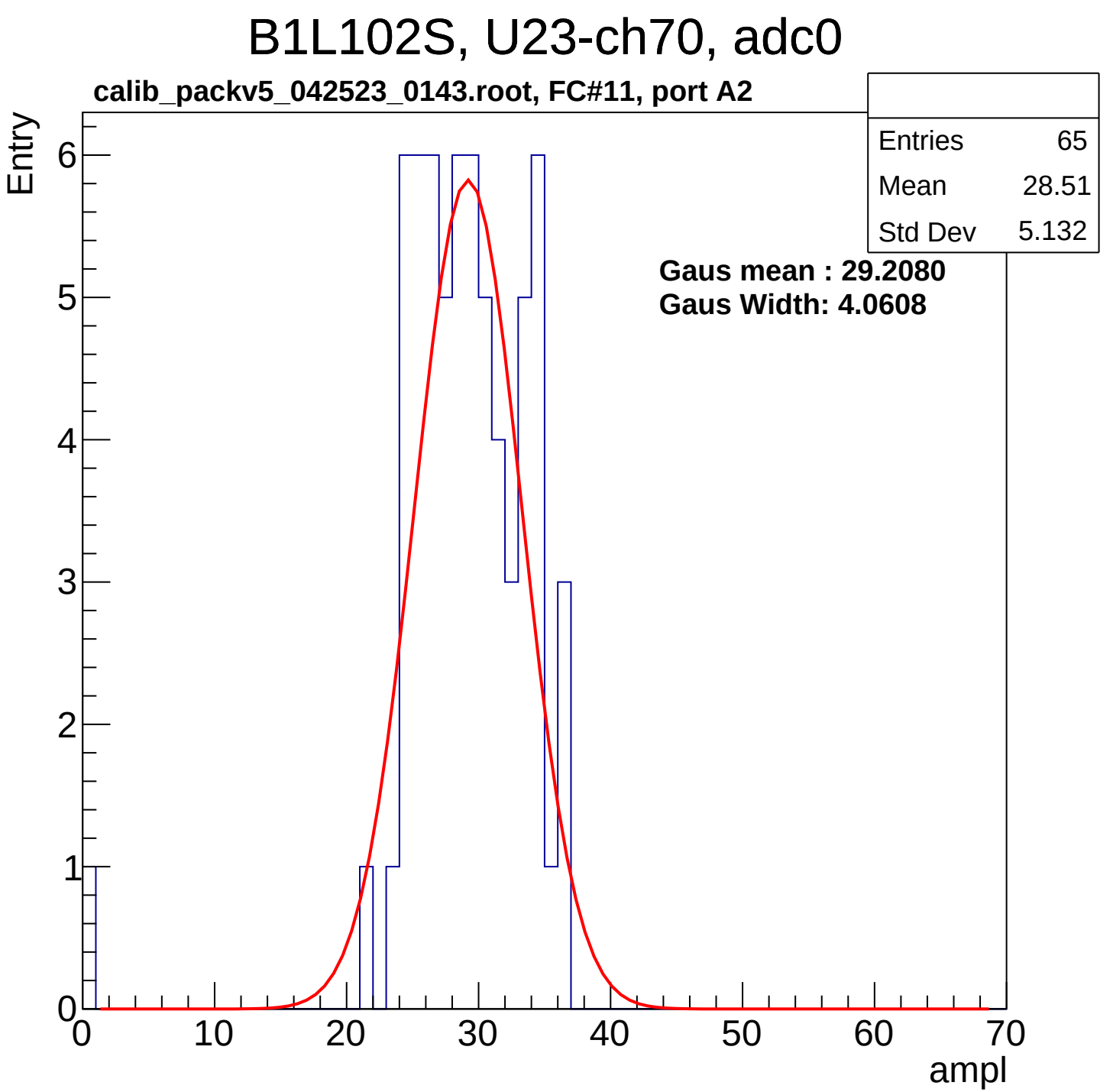

B1L102S, U23-ch69, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 51.43
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

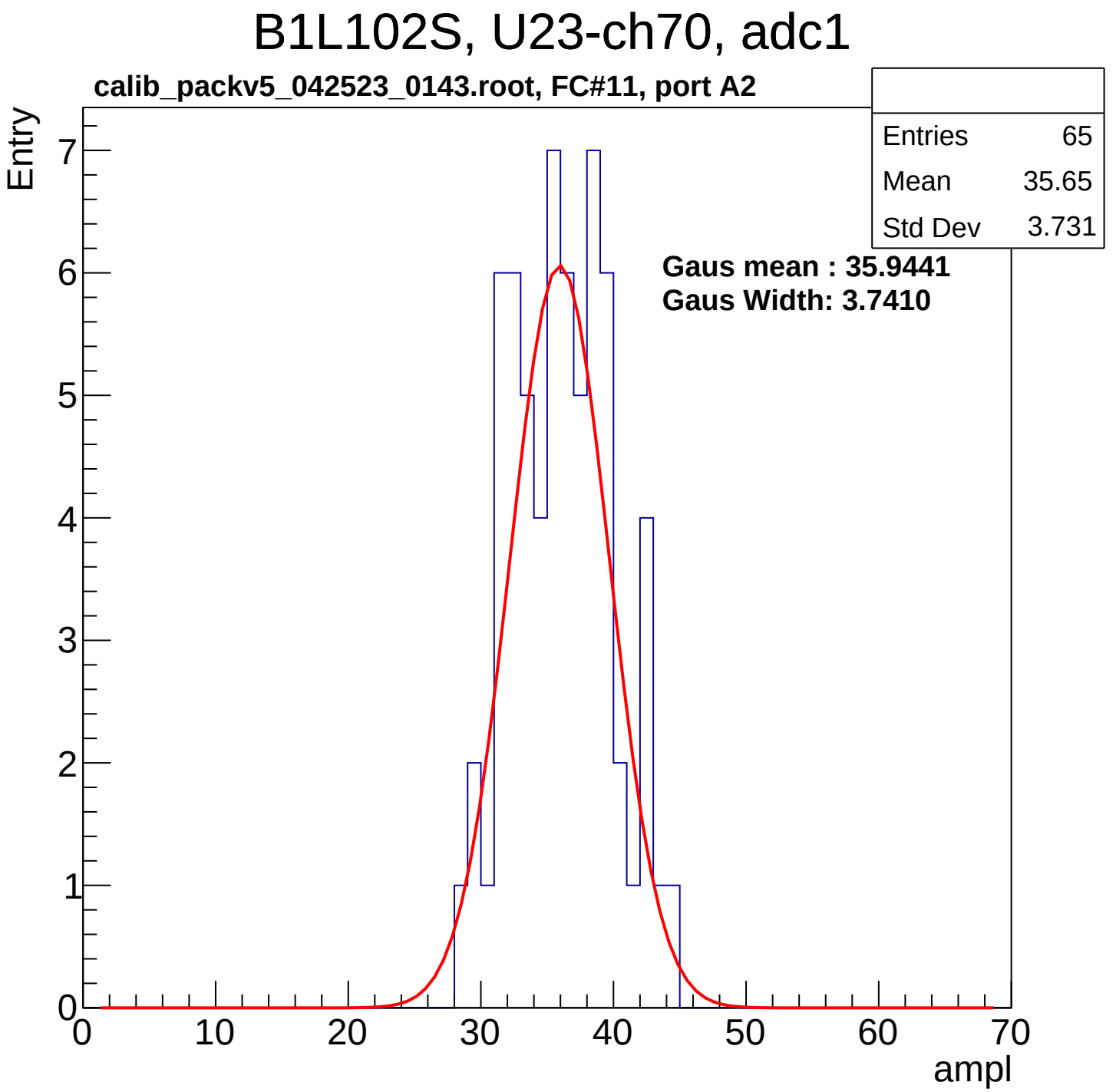

B1L102S, U23-ch69, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 58.08
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

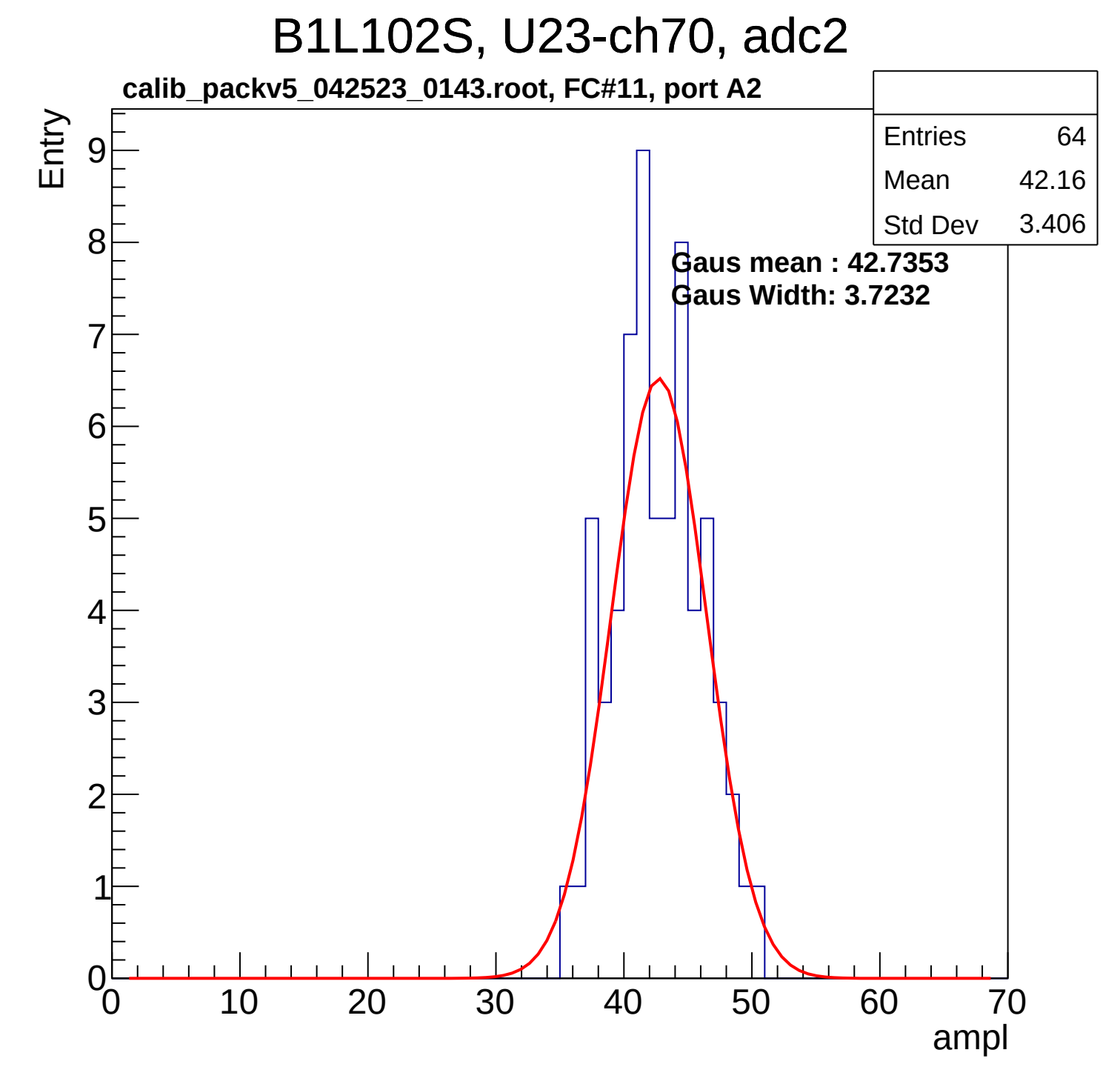

B1L102S, U23-ch69, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 24
Mean 57.88
Std Dev 12.31
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

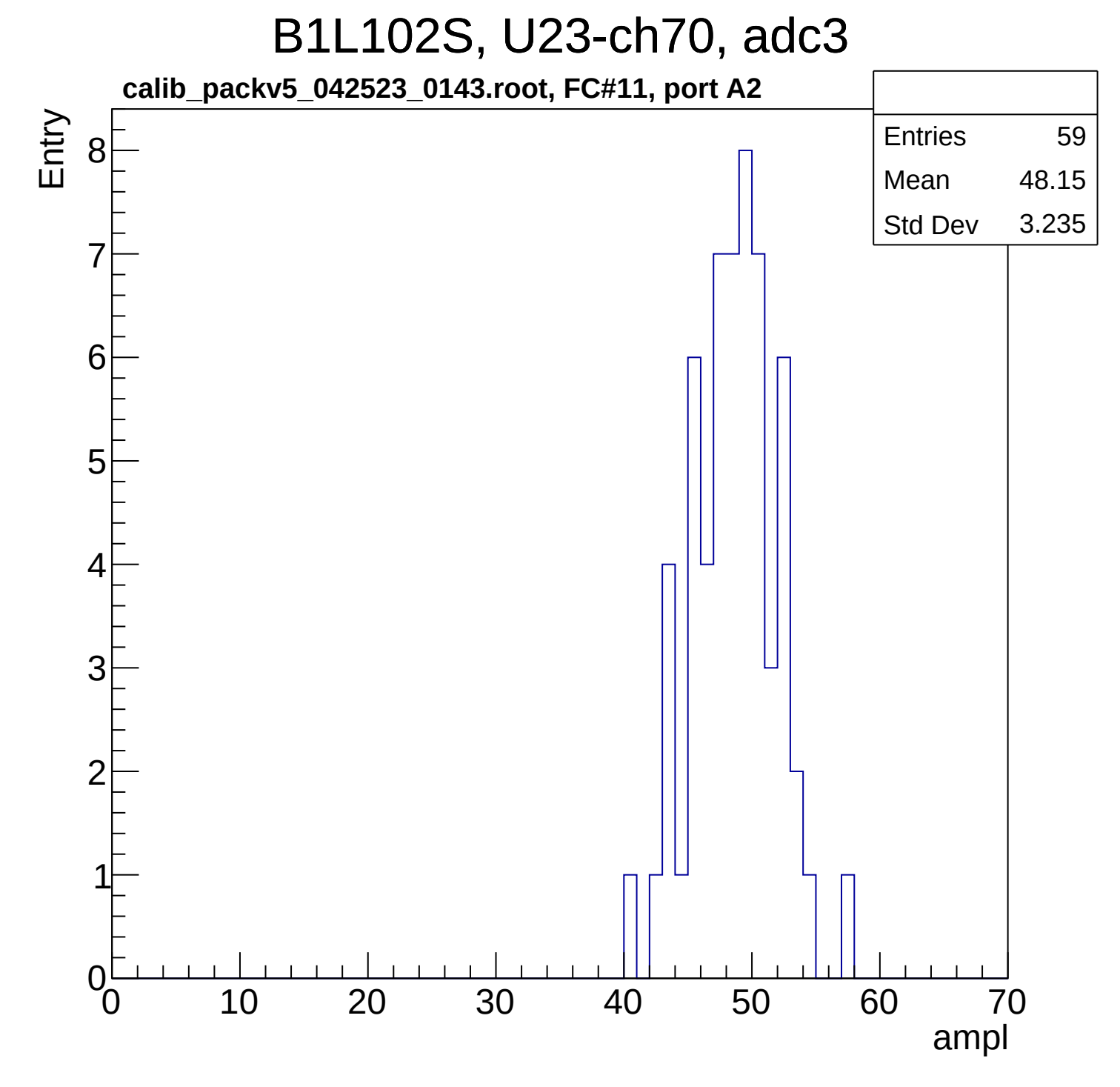

B1L102S, U23-ch69, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

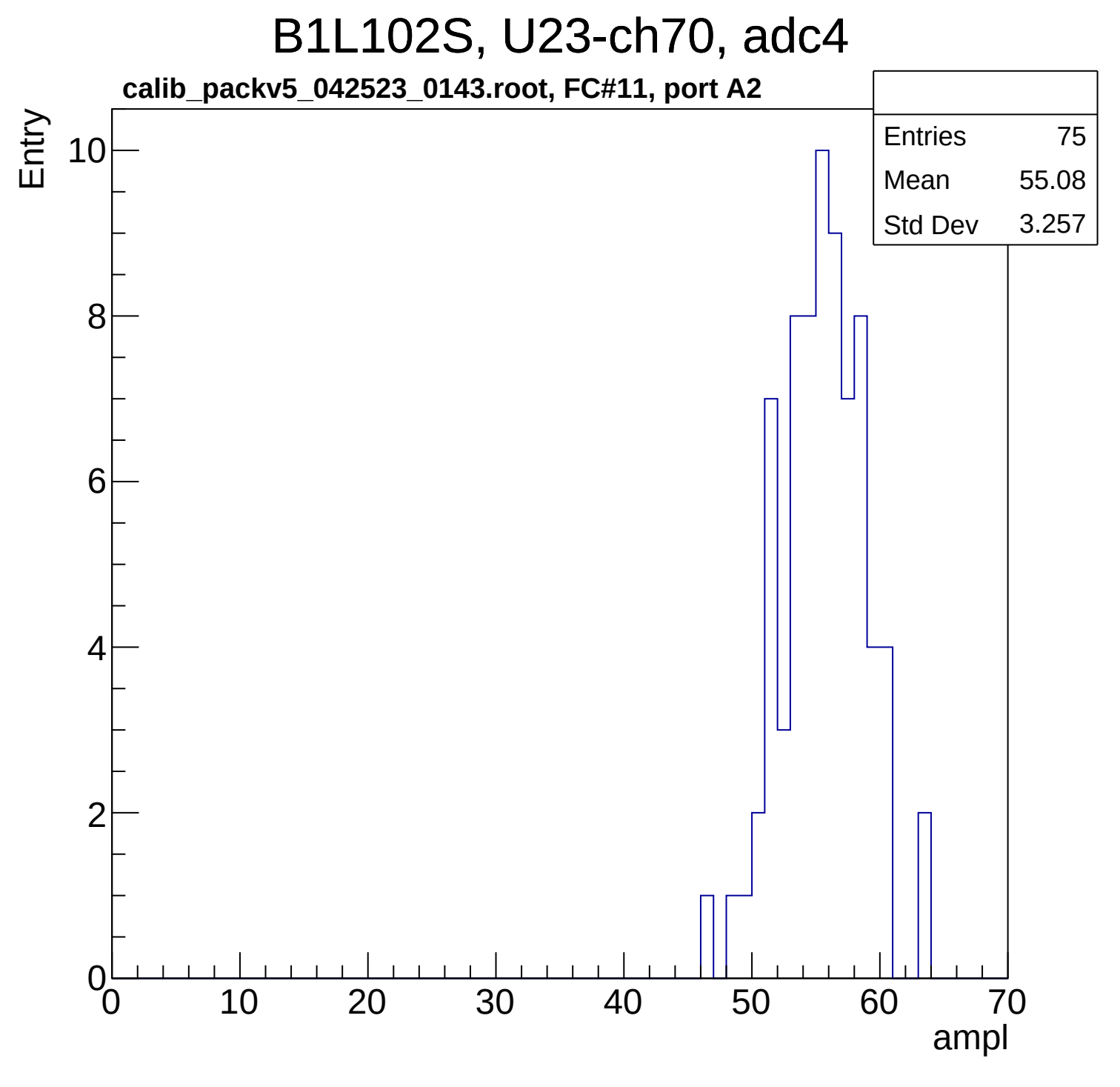

B1L102S, U23-ch69, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

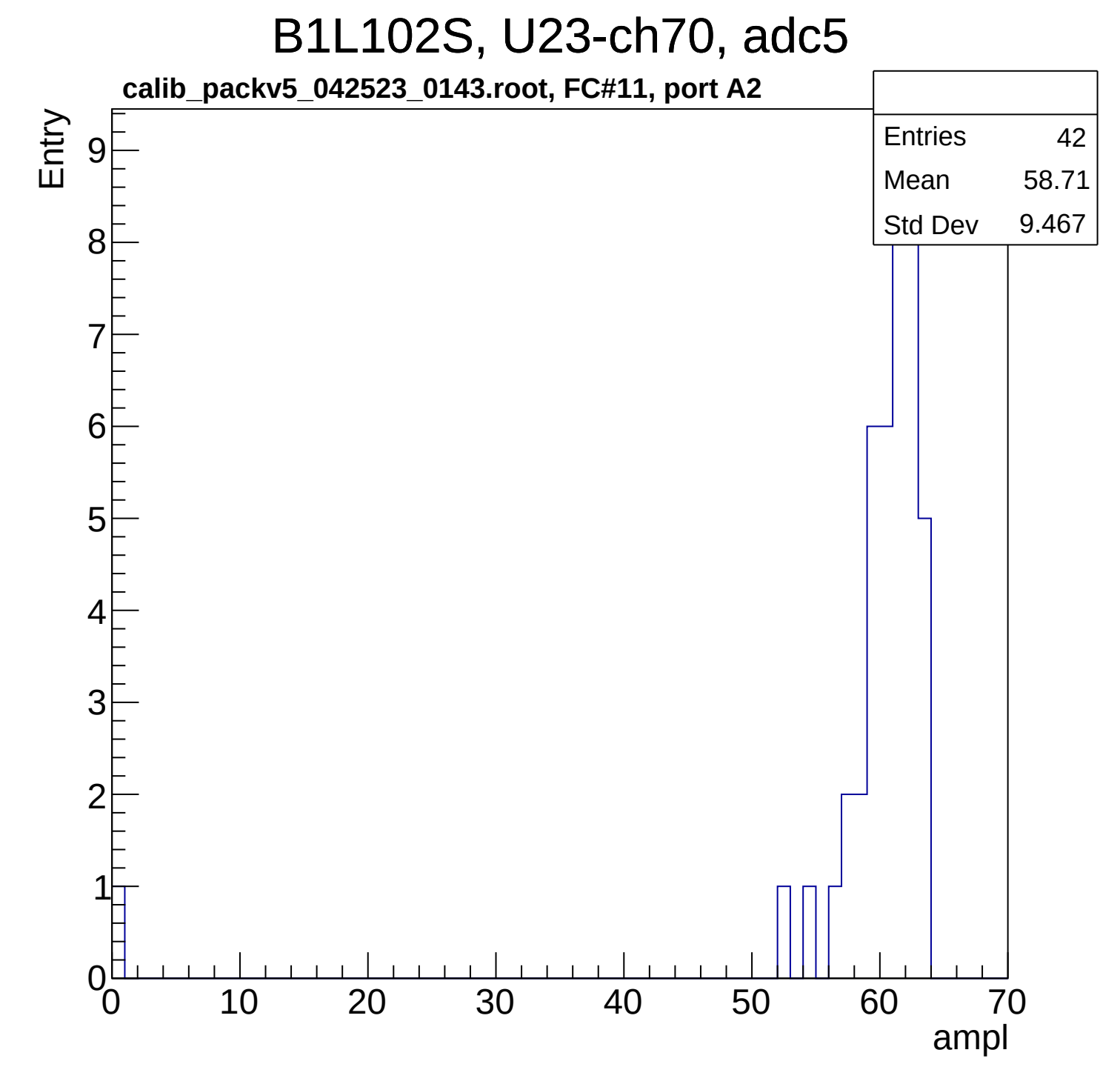

B1L102S, U23-ch70, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 28.51
Std Dev 5.132
Gaus mean : 29.2080
Gaus Width: 4.0608
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

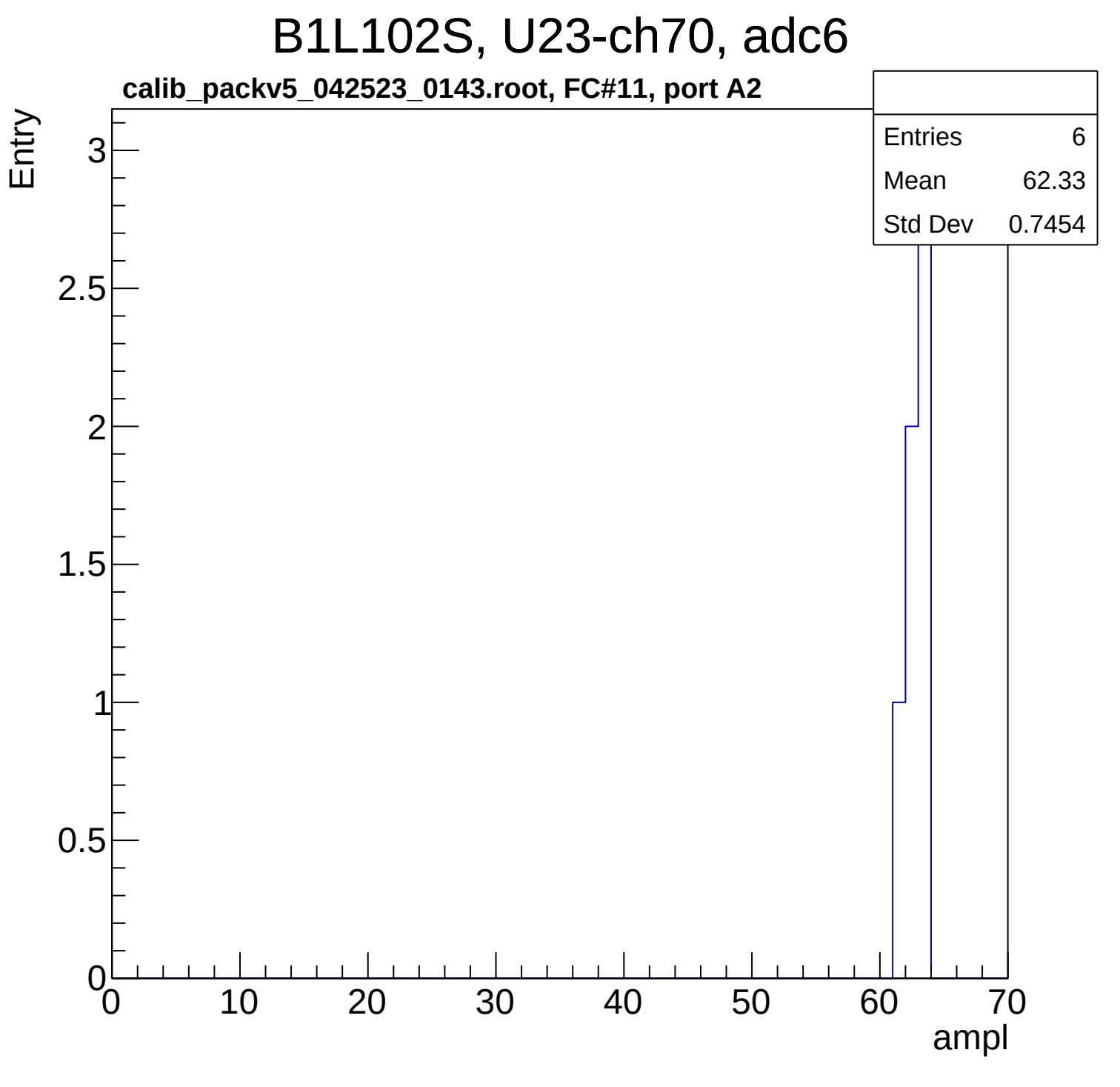

B1L102S, U23-ch70, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.65
Std Dev 3.731
Gaus mean : 35.9441
Gaus Width: 3.7410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

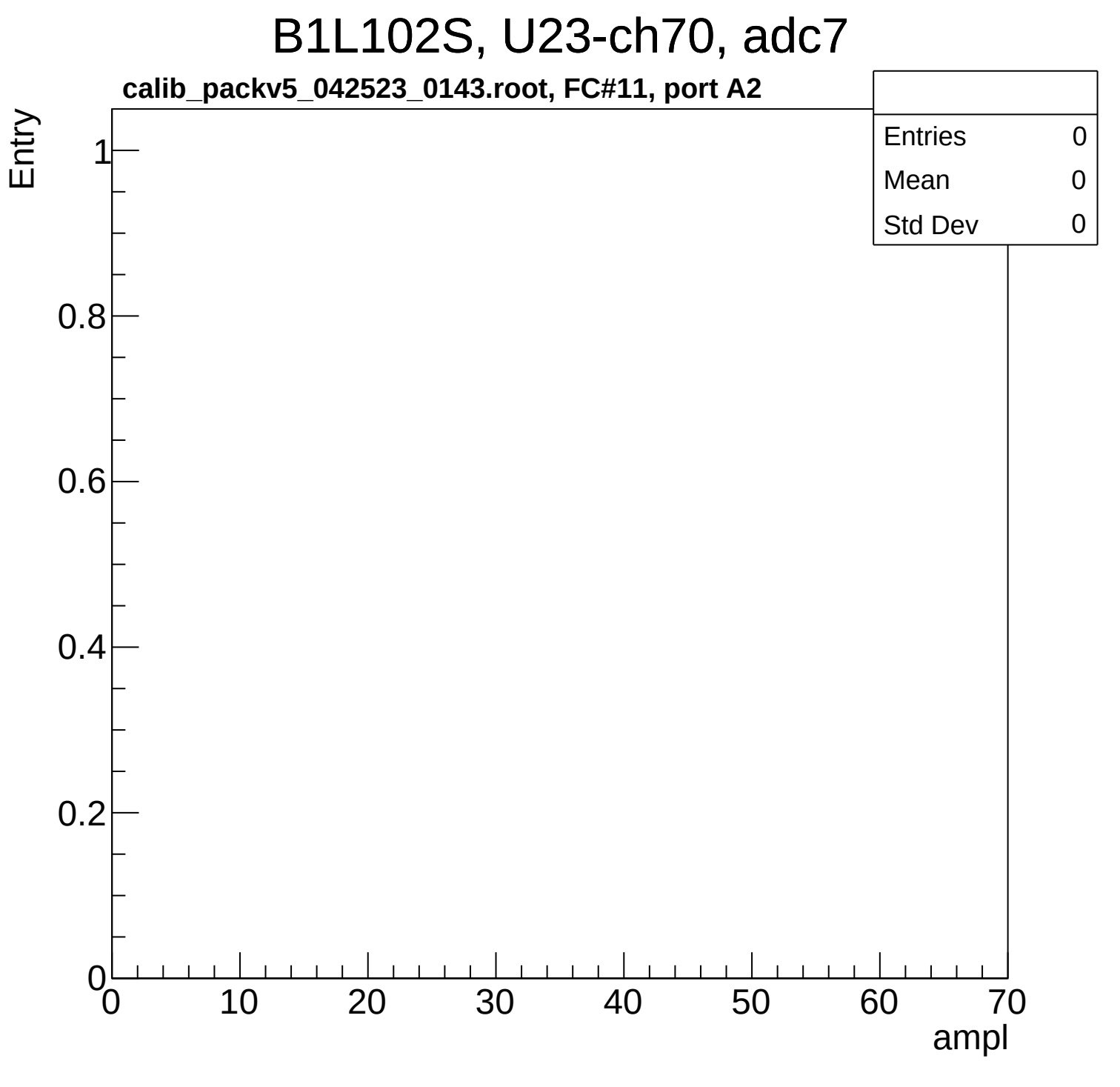

B1L102S, U23-ch70, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 42.16
Std Dev 3.406
Gaus mean : 42.7353
Gaus Width: 3.7232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

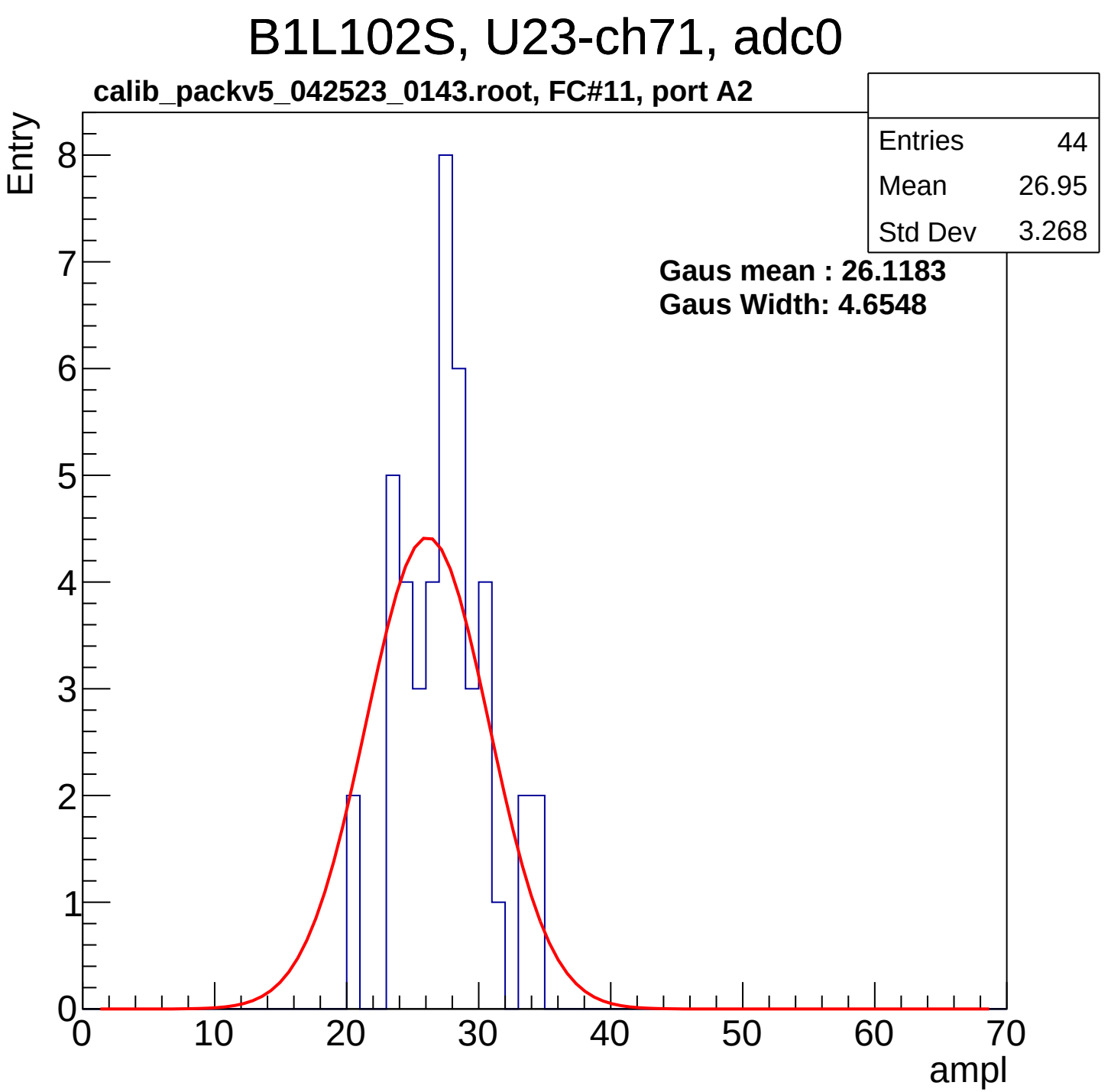

B1L102S, U23-ch70, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.15
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

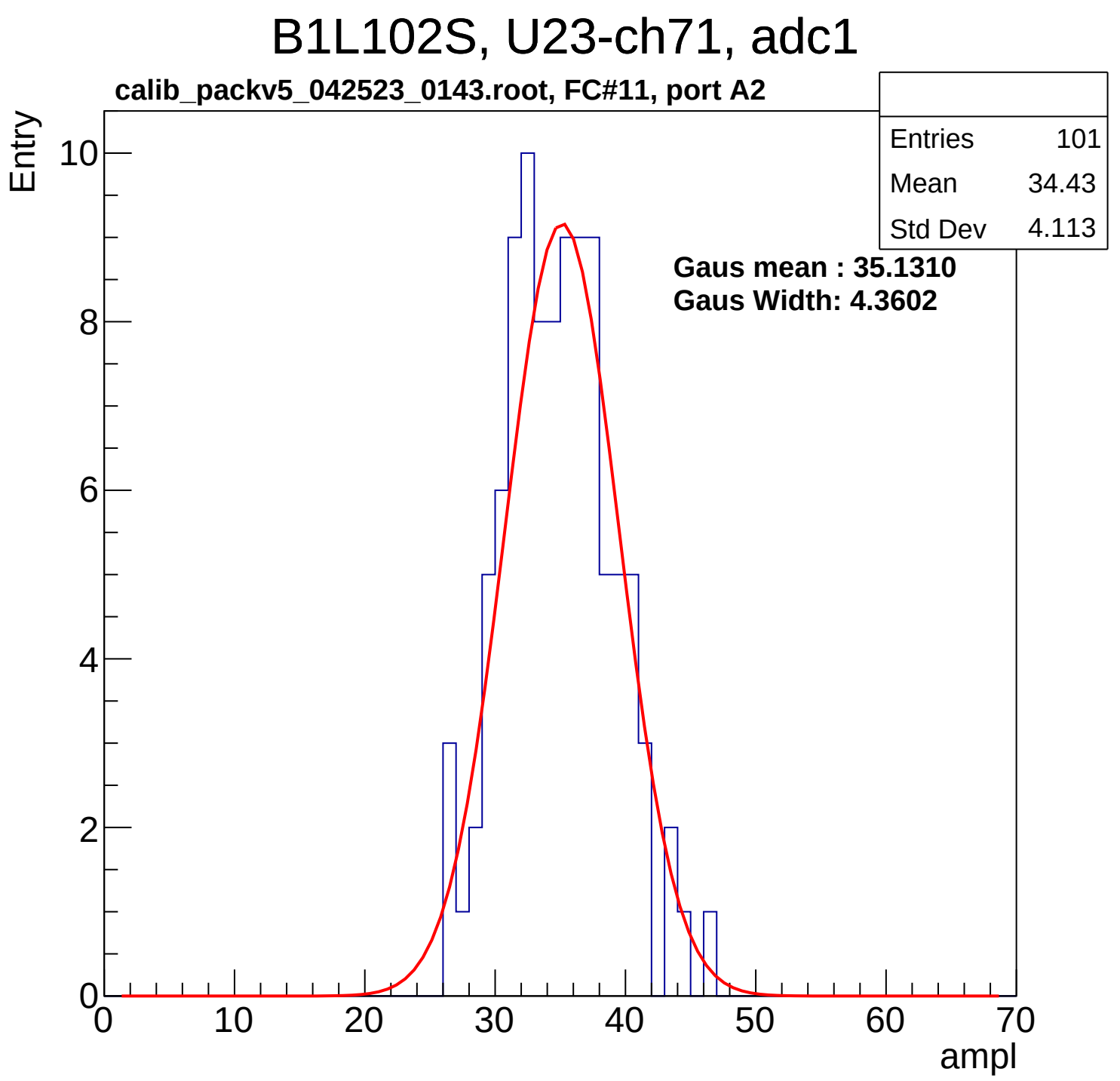

B1L102S, U23-ch70, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 55.08
Std Dev 3.257
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

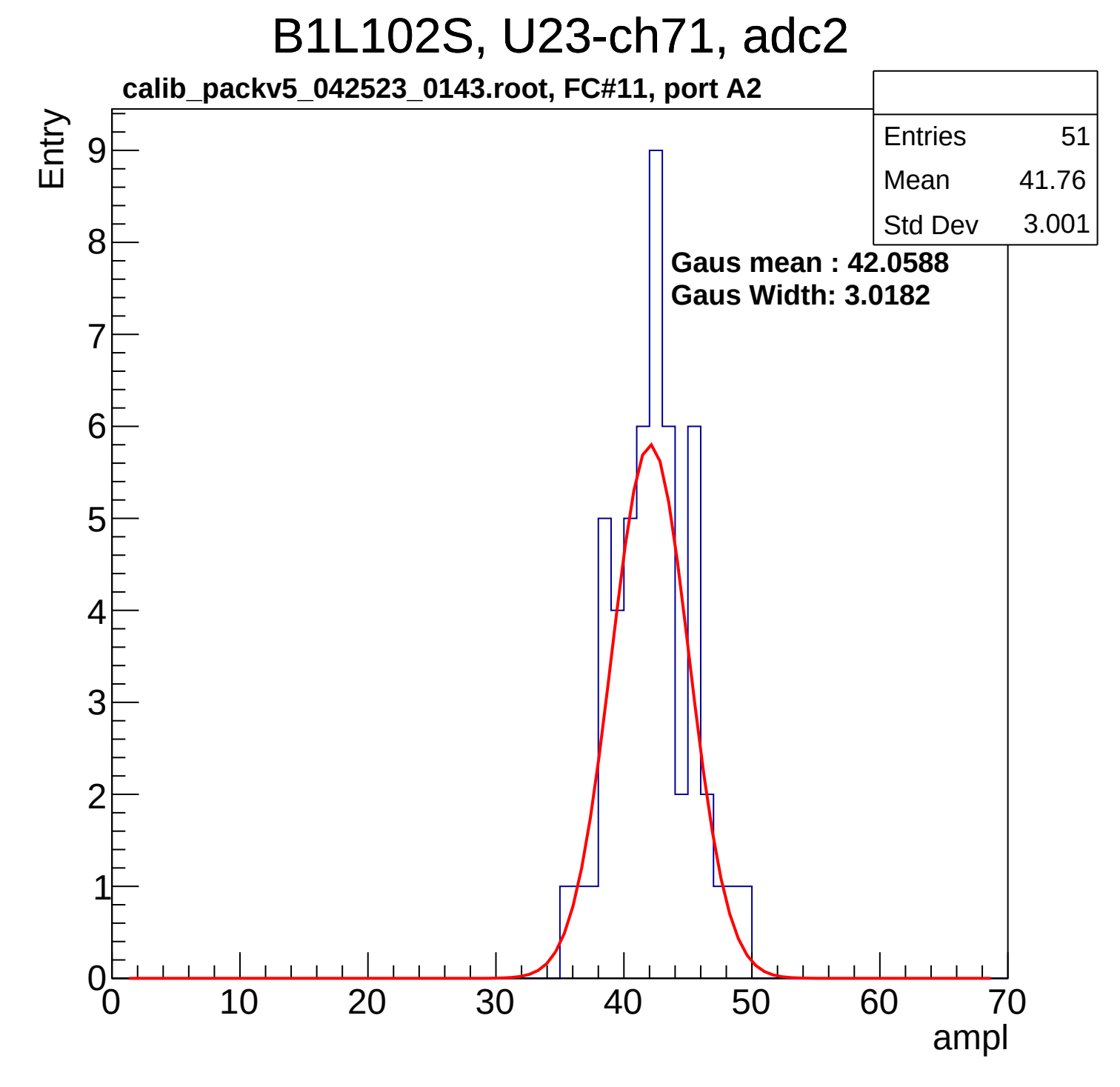

B1L102S, U23-ch70, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.71
Std Dev 9.467
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

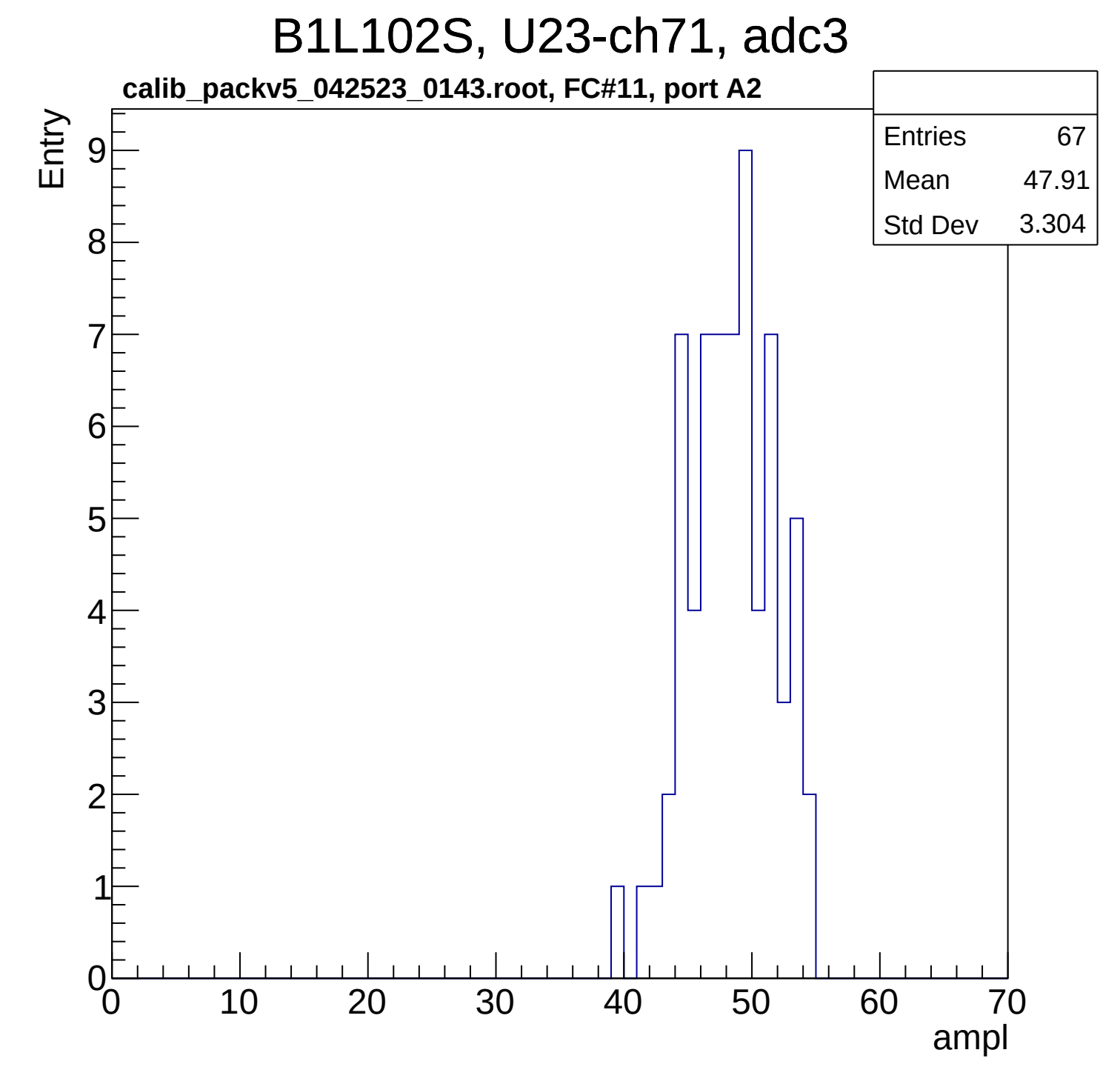

B1L102S, U23-ch70, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

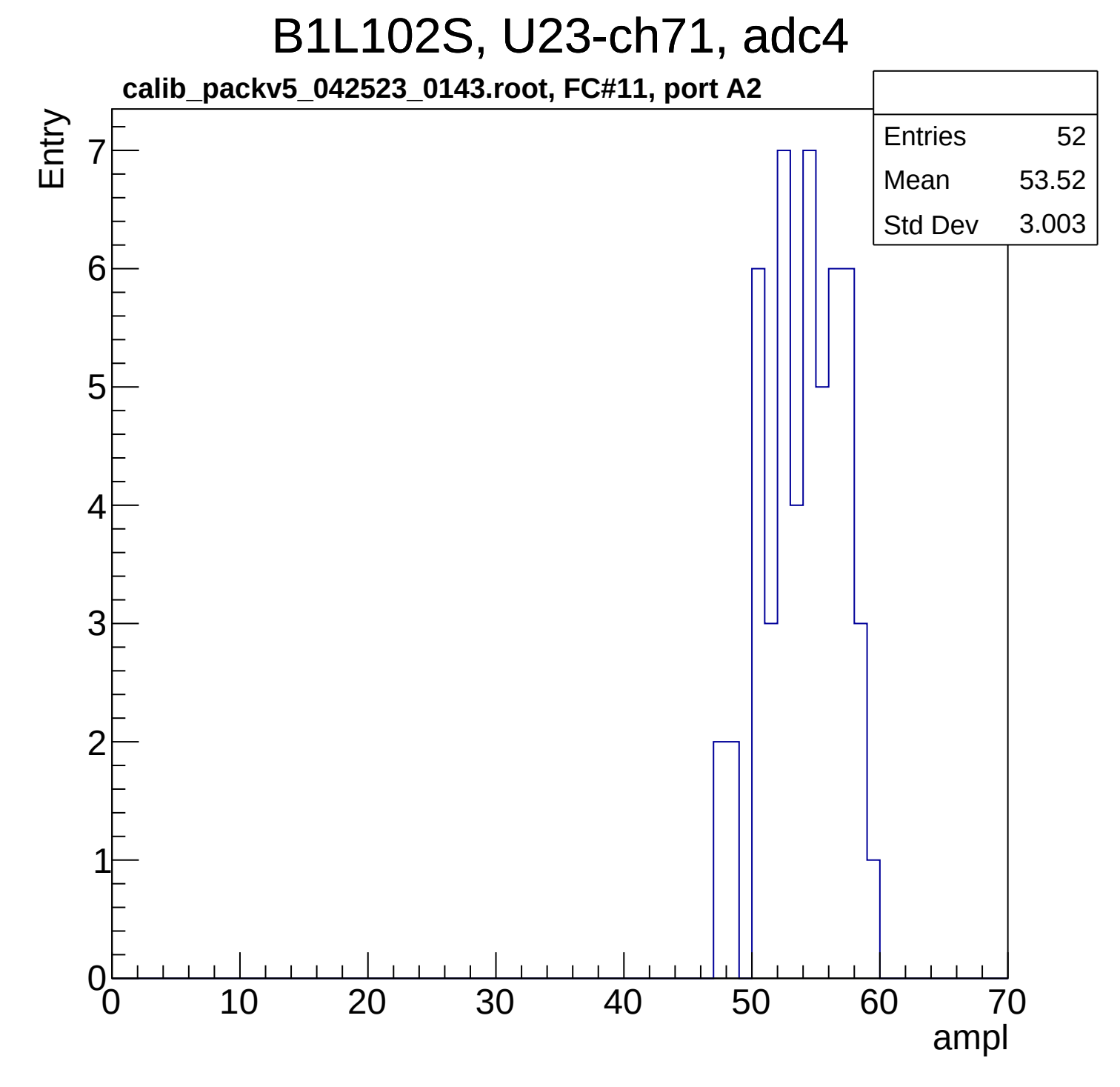

B1L102S, U23-ch70, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

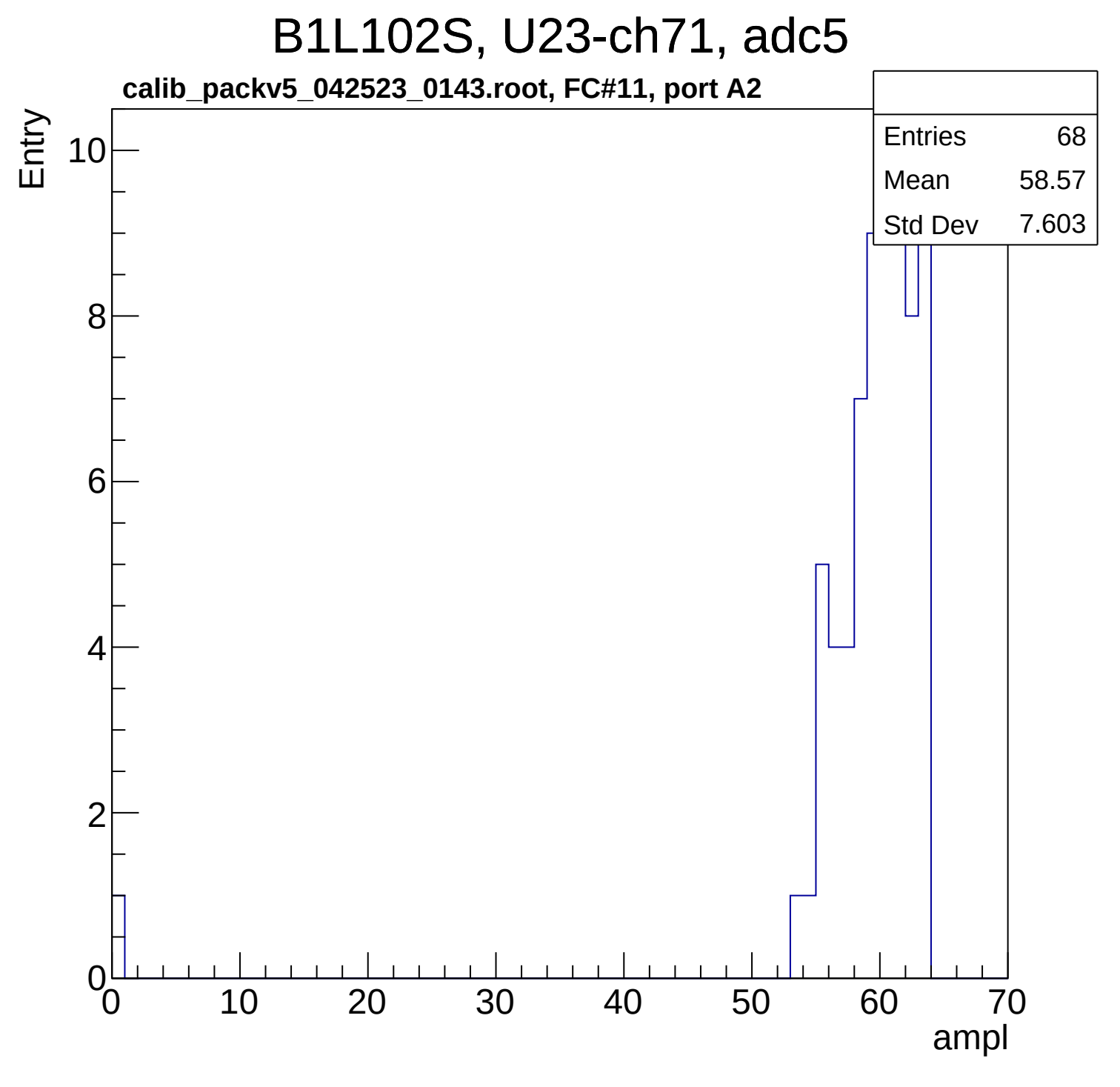

B1L102S, U23-ch71, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 26.95
Std Dev 3.268
Gaus mean : 26.1183
Gaus Width: 4.6548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

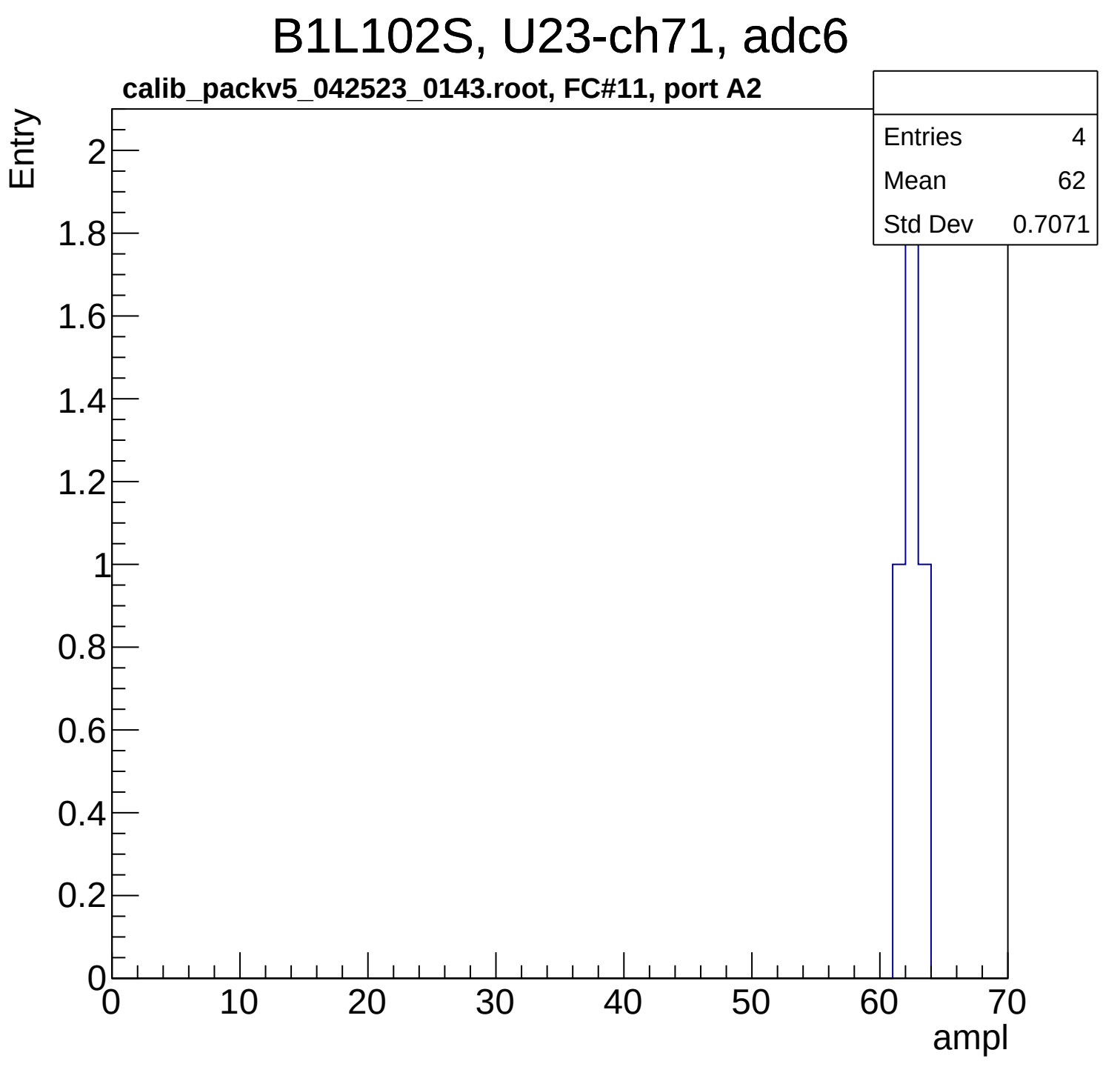

B1L102S, U23-ch71, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 101
Mean 34.43
Std Dev 4.113
Gaus mean : 35.1310
Gaus Width: 4.3602
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

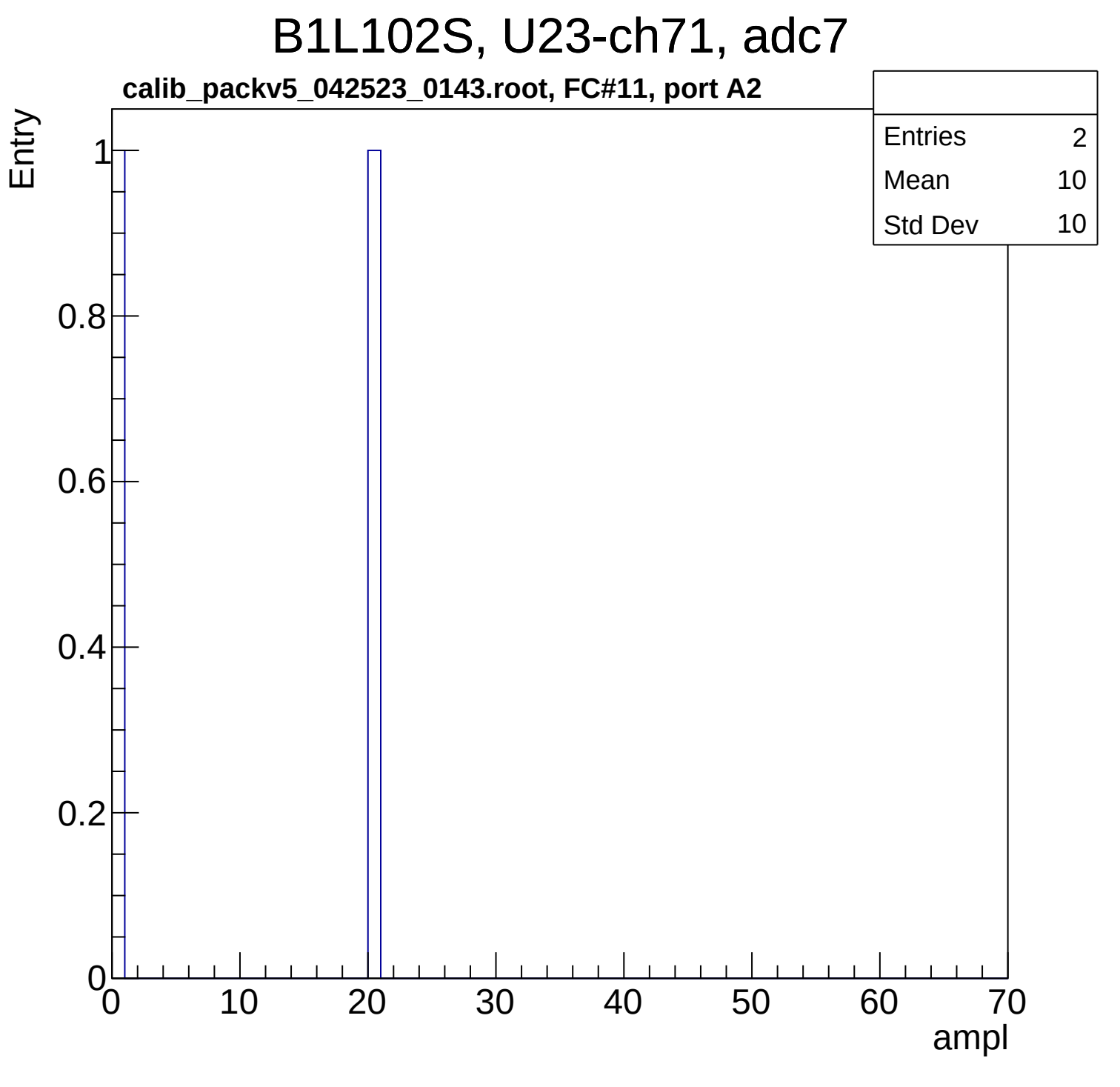

B1L102S, U23-ch71, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 41.76
Std Dev 3.001
Gaus mean : 42.0588
Gaus Width: 3.0182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

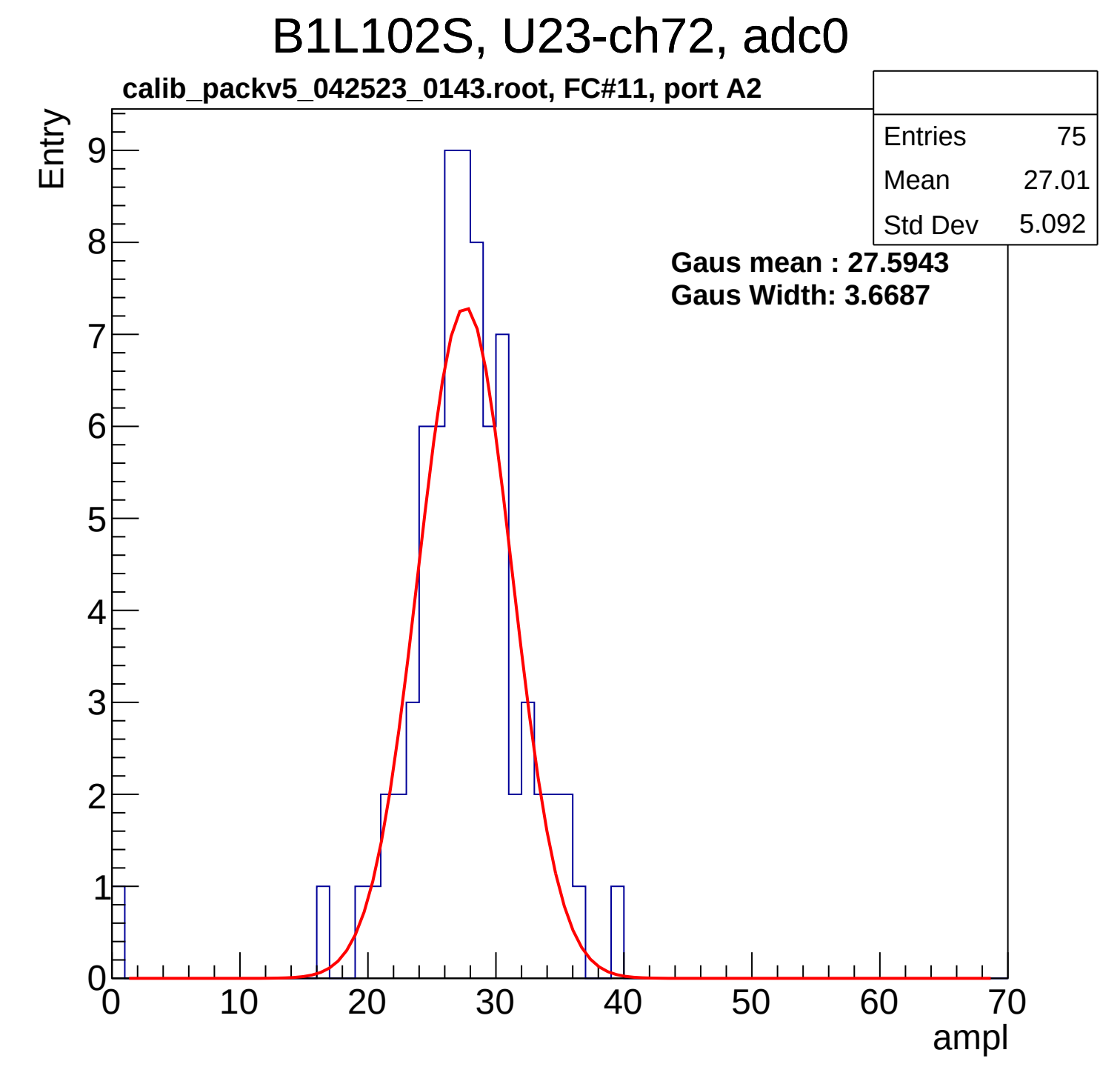

B1L102S, U23-ch71, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 47.91
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

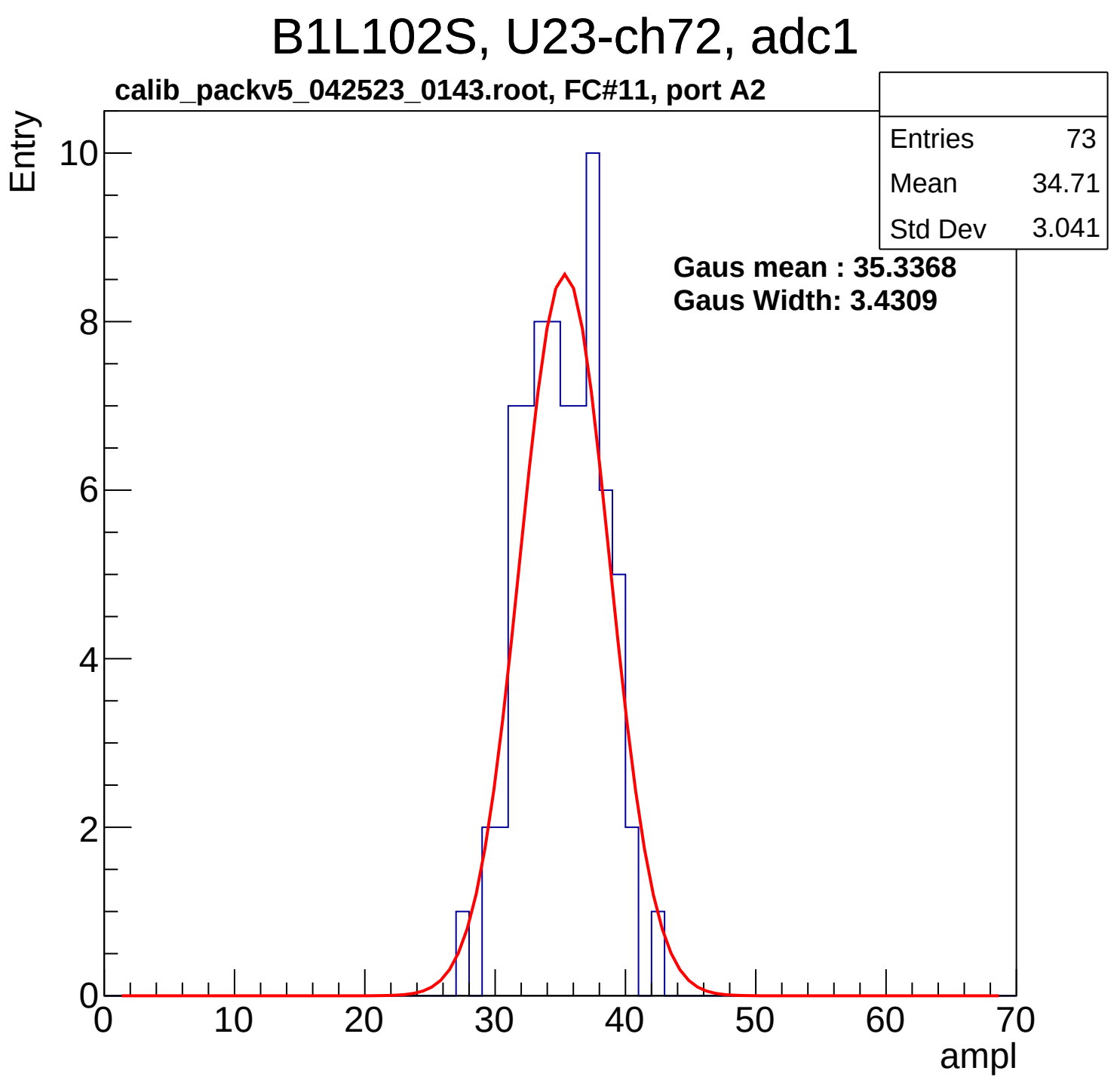

B1L102S, U23-ch71, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 53.52
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

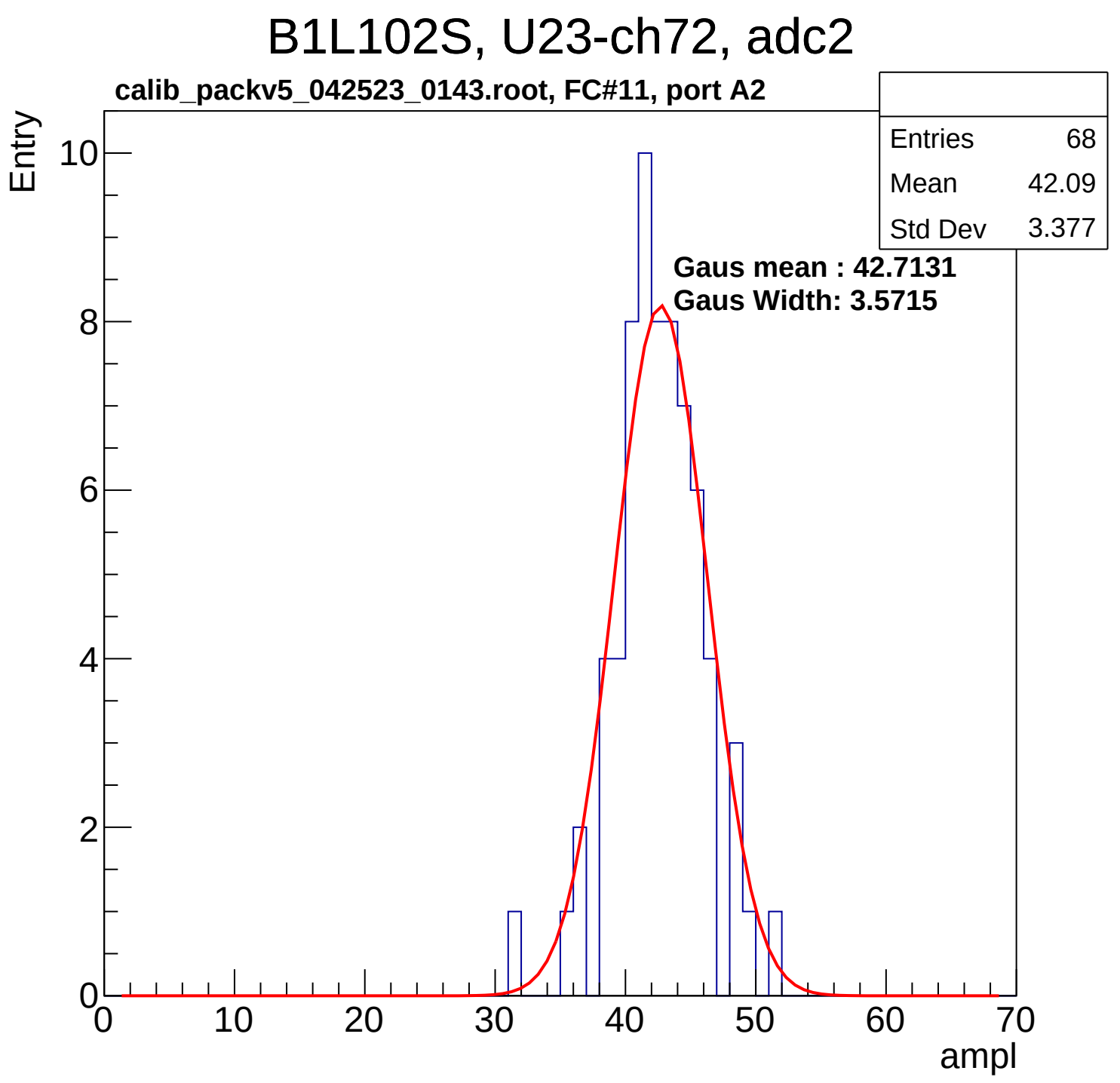

B1L102S, U23-ch71, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 58.57
Std Dev 7.603
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

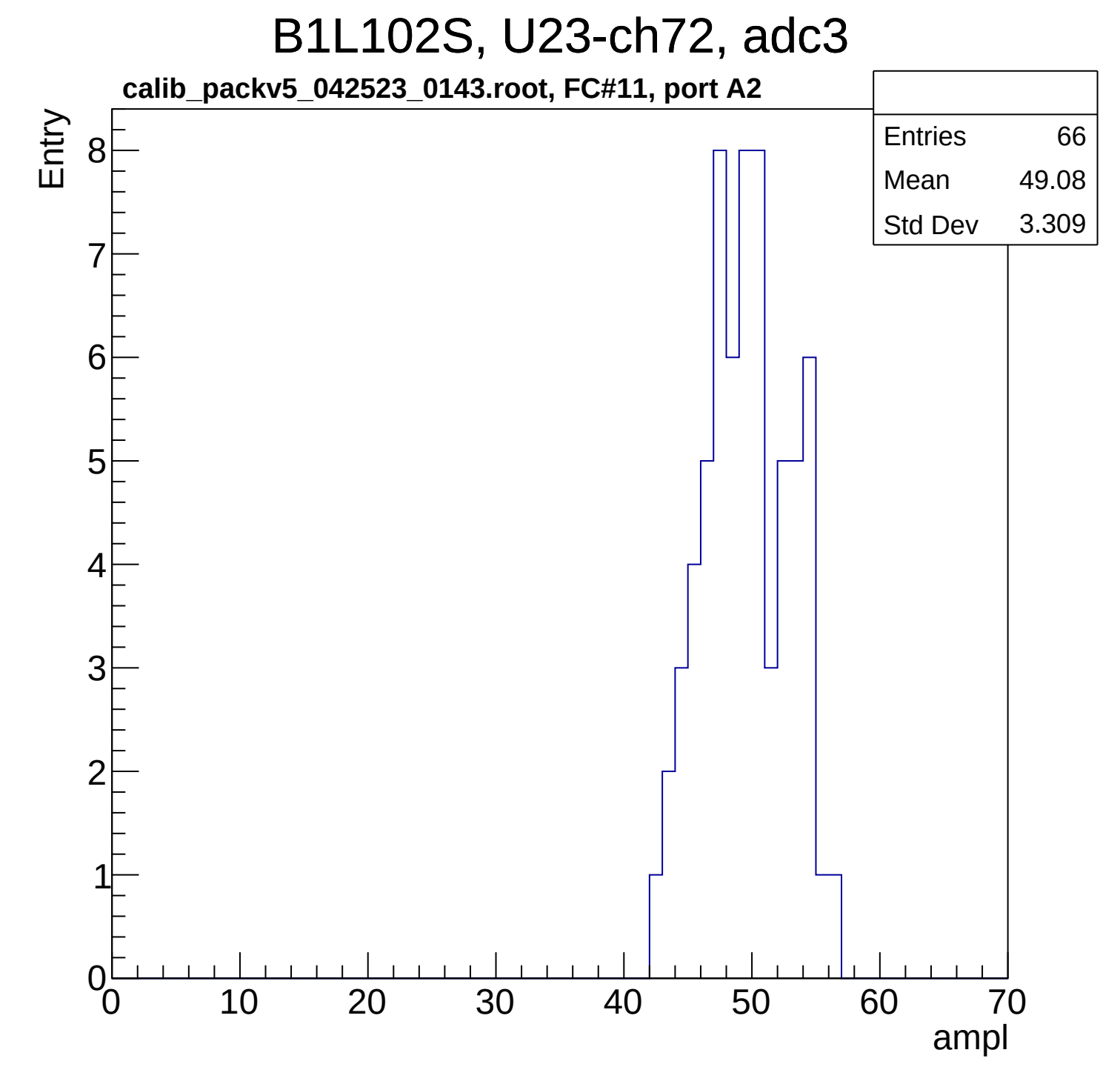

B1L102S, U23-ch71, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

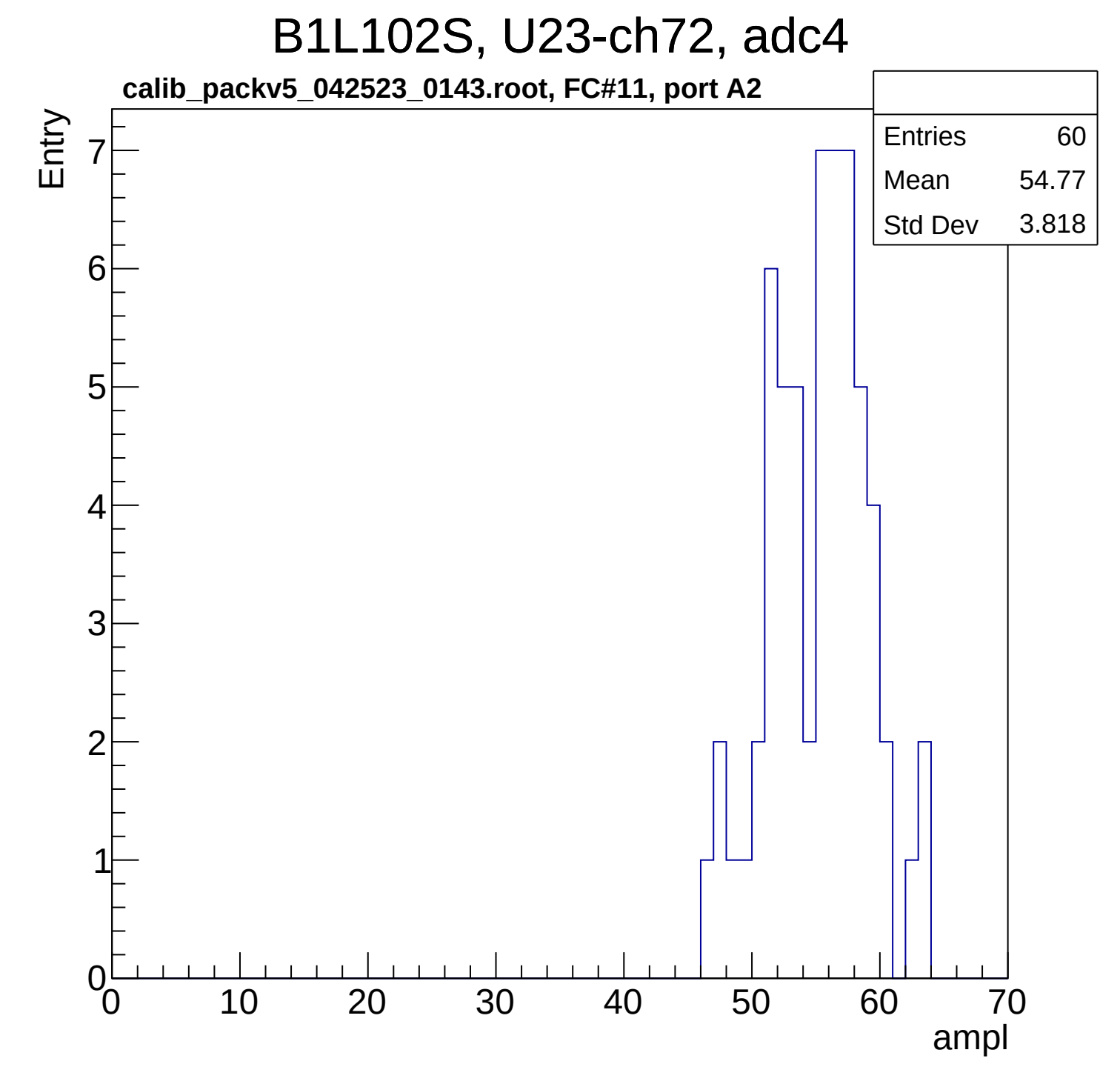

B1L102S, U23-ch71, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

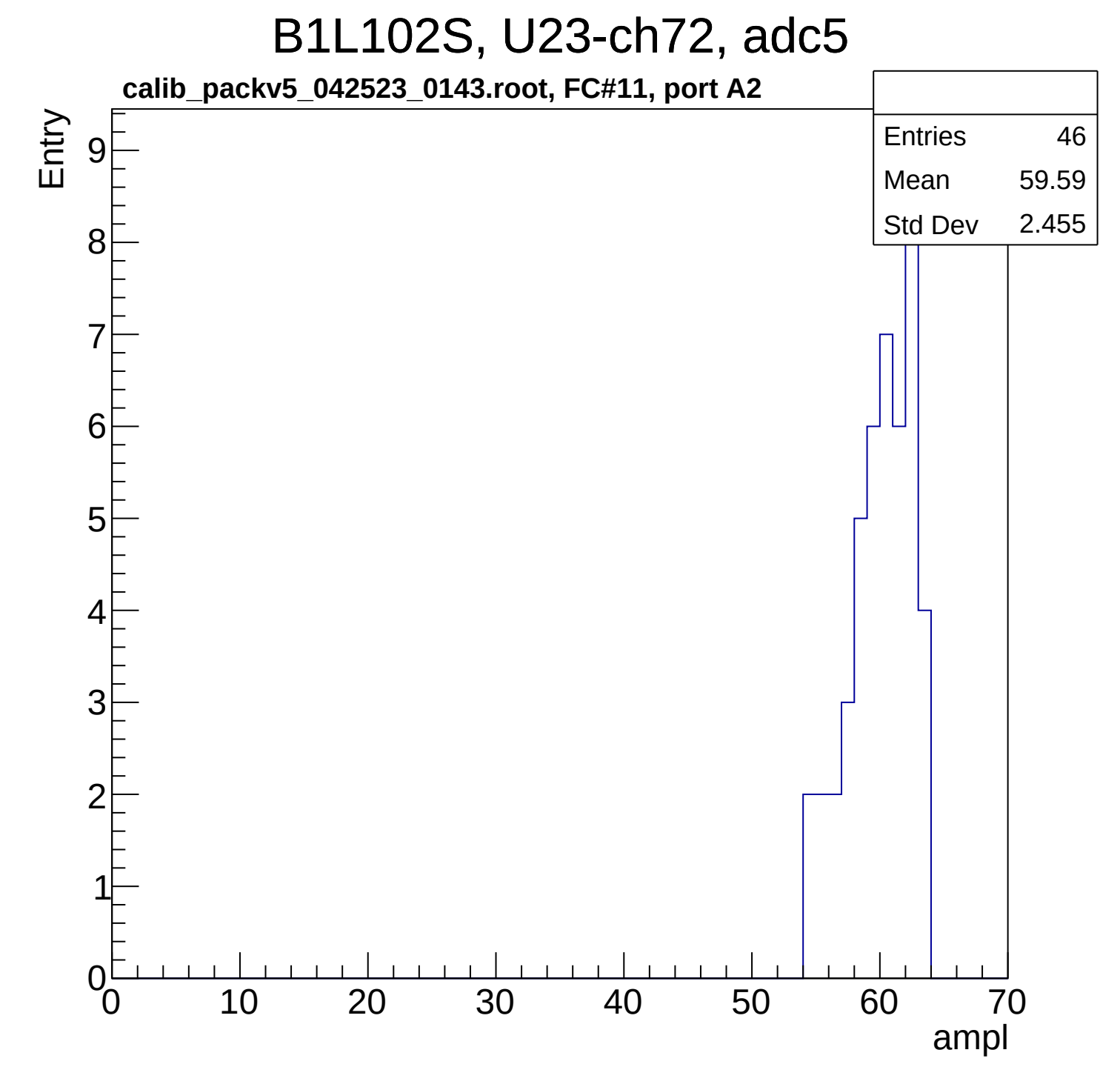

B1L102S, U23-ch72, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 27.01
Std Dev 5.092
Gaus mean : 27.5943
Gaus Width: 3.6687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

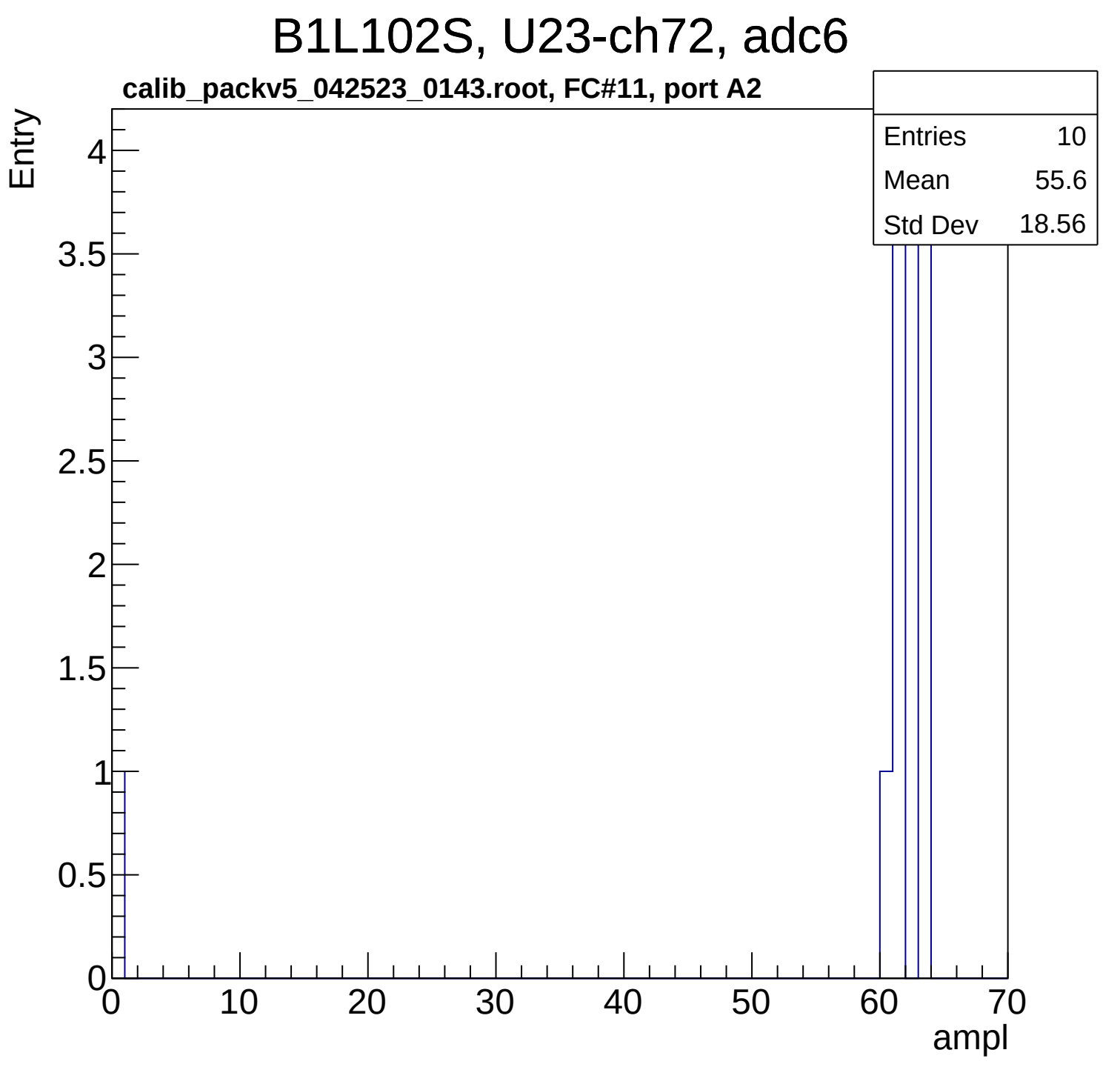

B1L102S, U23-ch72, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 34.71
Std Dev 3.041
Gaus mean : 35.3368
Gaus Width: 3.4309
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

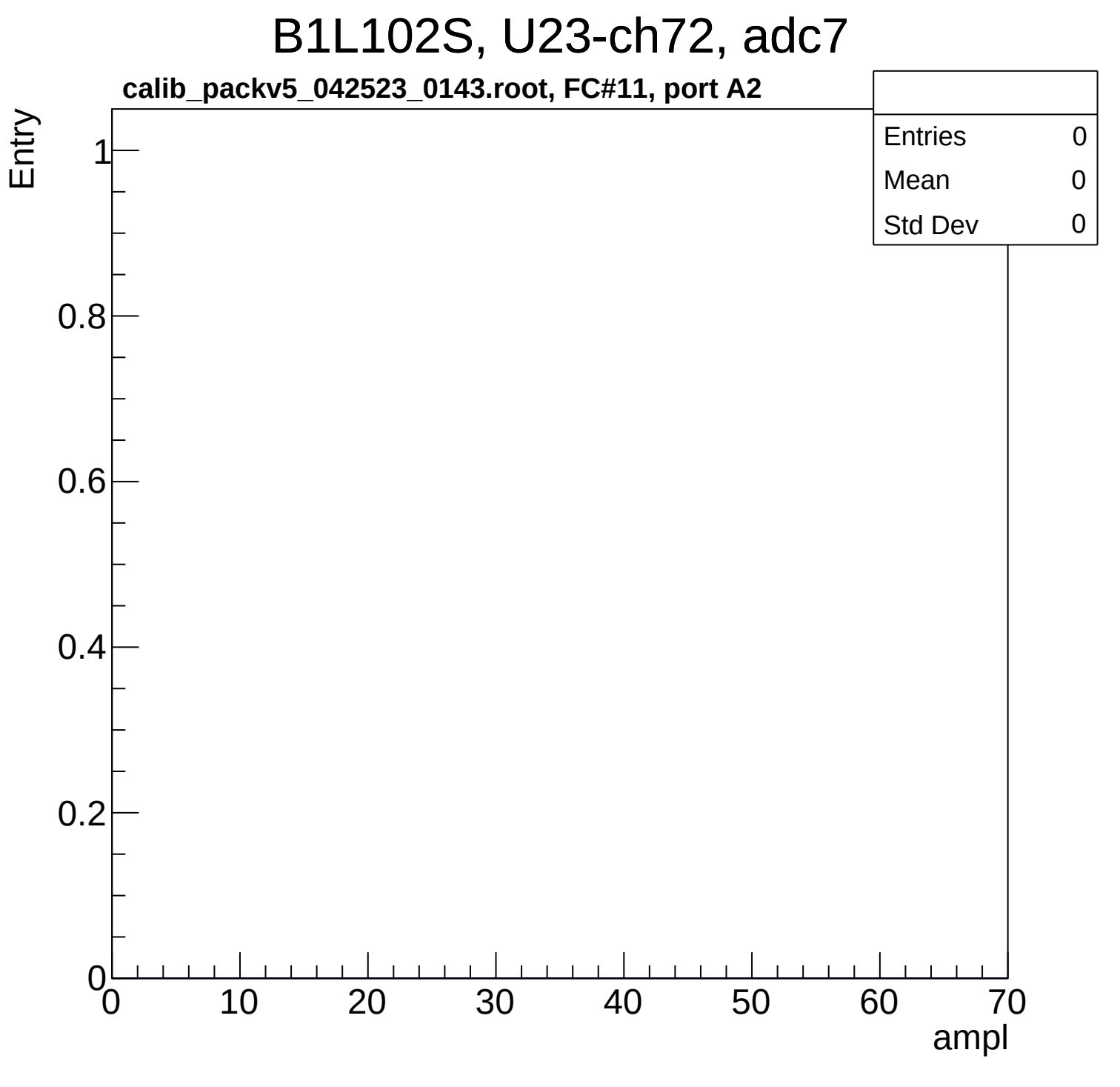

B1L102S, U23-ch72, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 42.09
Std Dev 3.377
Gaus mean : 42.7131
Gaus Width: 3.5715
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

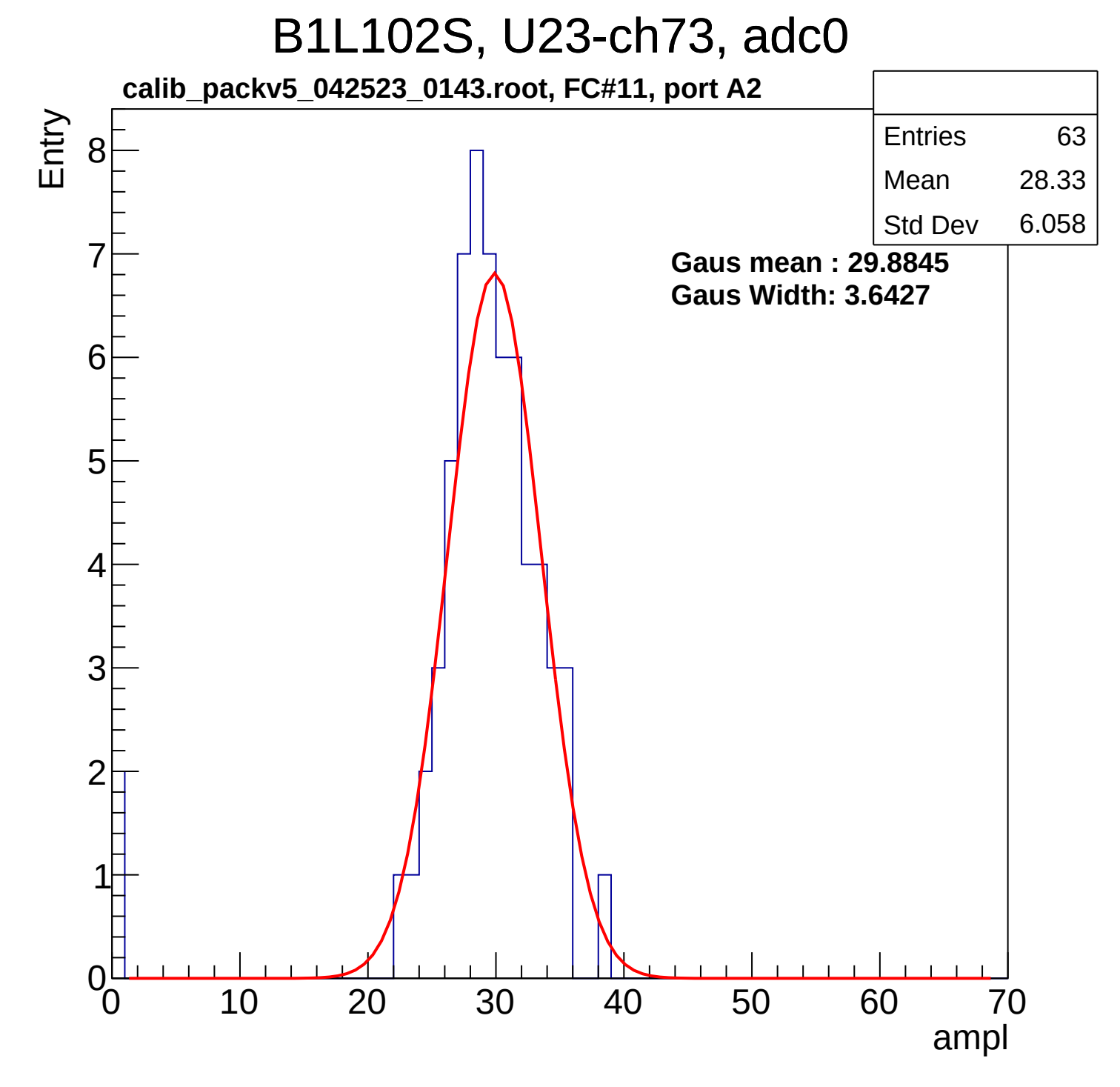

B1L102S, U23-ch72, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 49.08
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

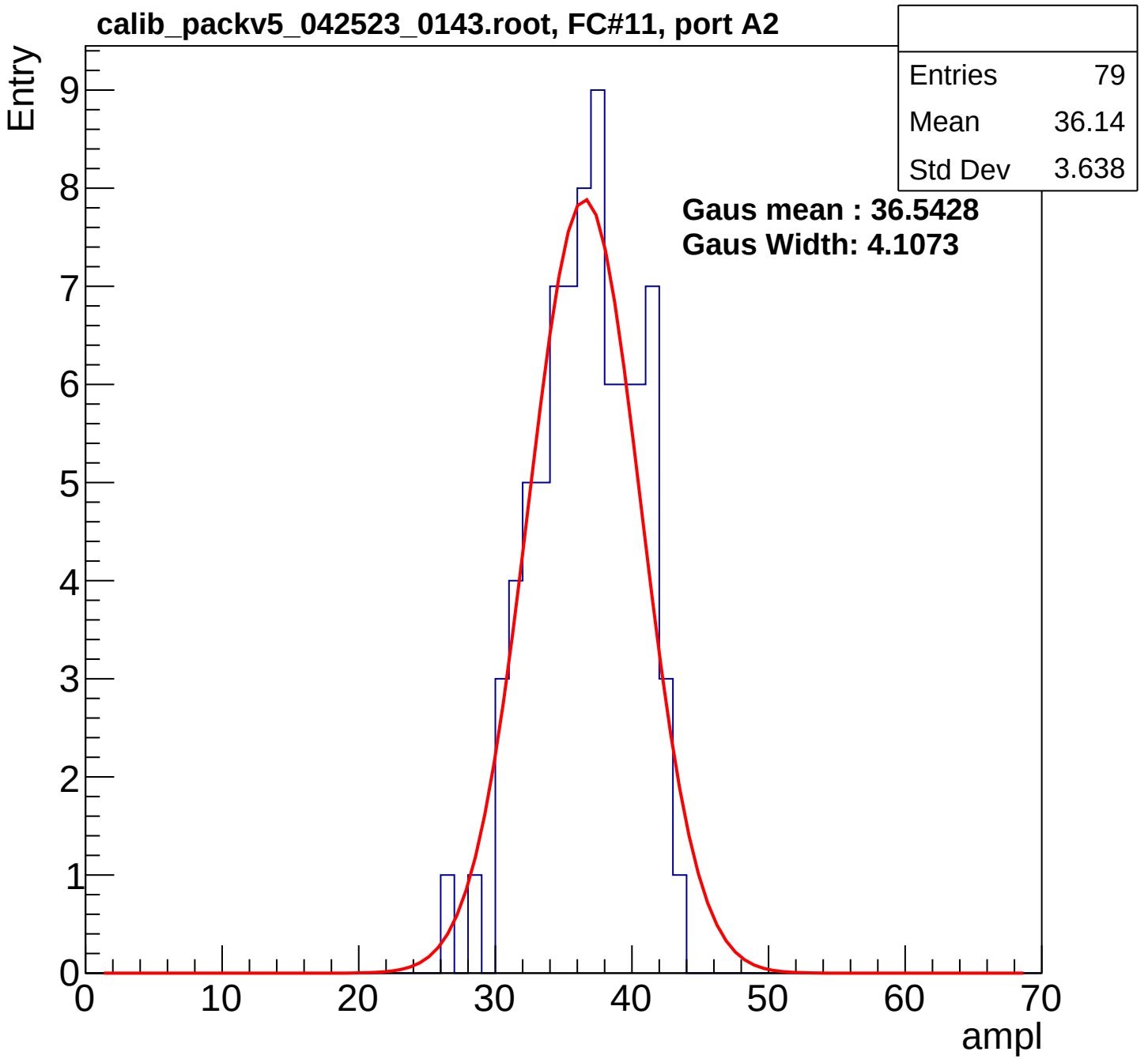

B1L102S, U23-ch72, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 54.77
Std Dev 3.818
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

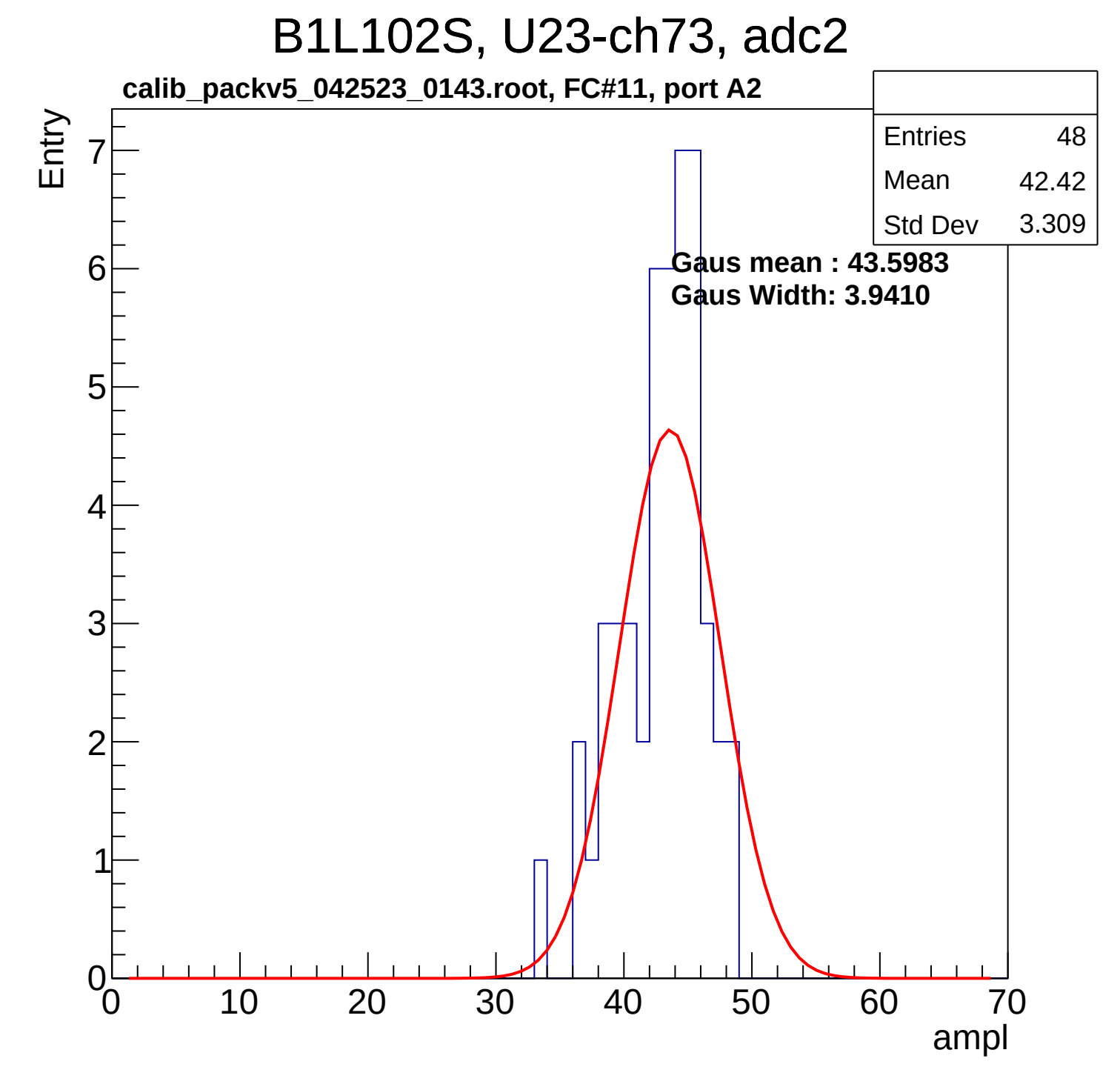

B1L102S, U23-ch72, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 59.59
Std Dev 2.455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

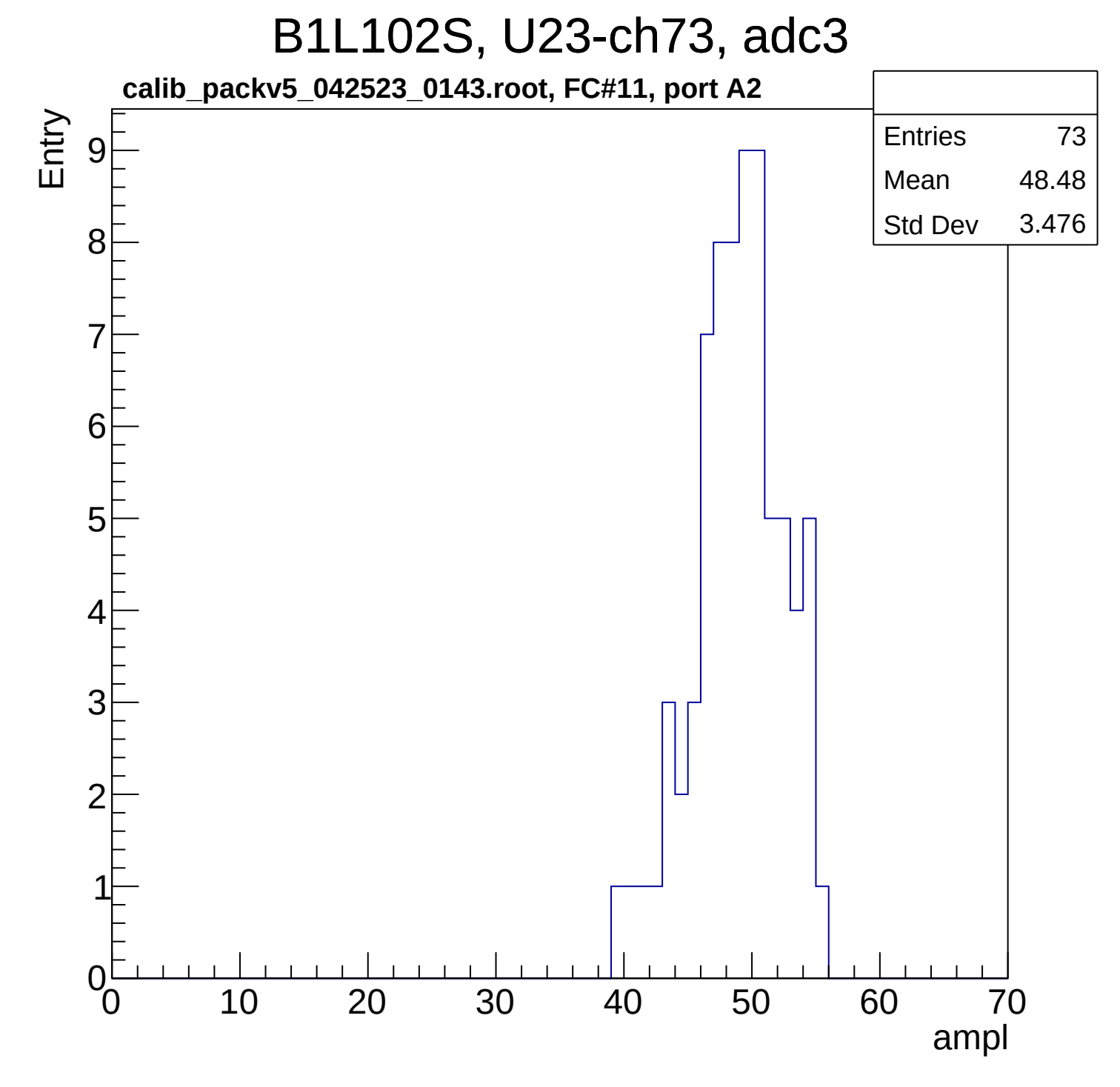

B1L102S, U23-ch72, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 55.6
Std Dev 18.56
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

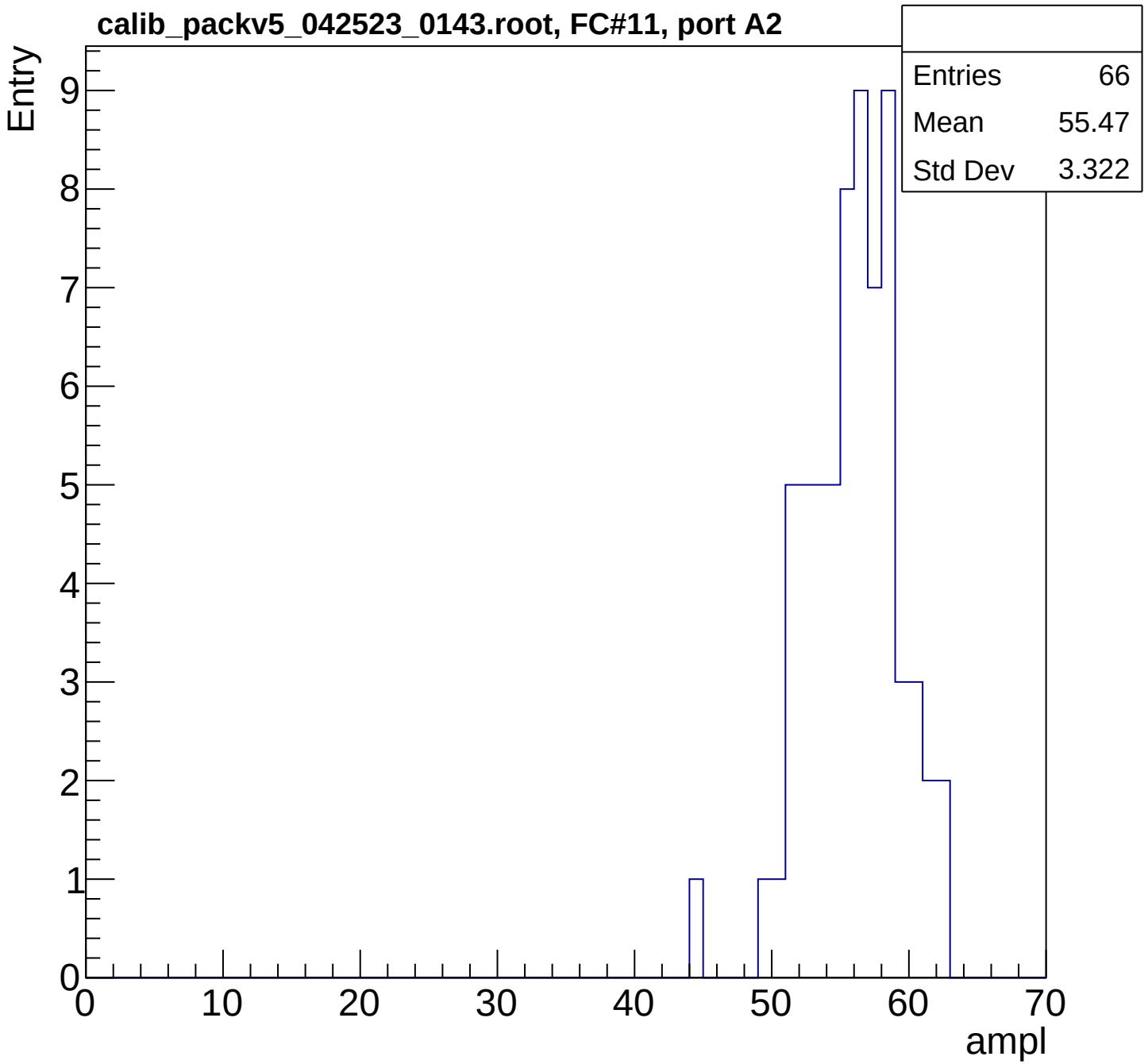

B1L102S, U23-ch72, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

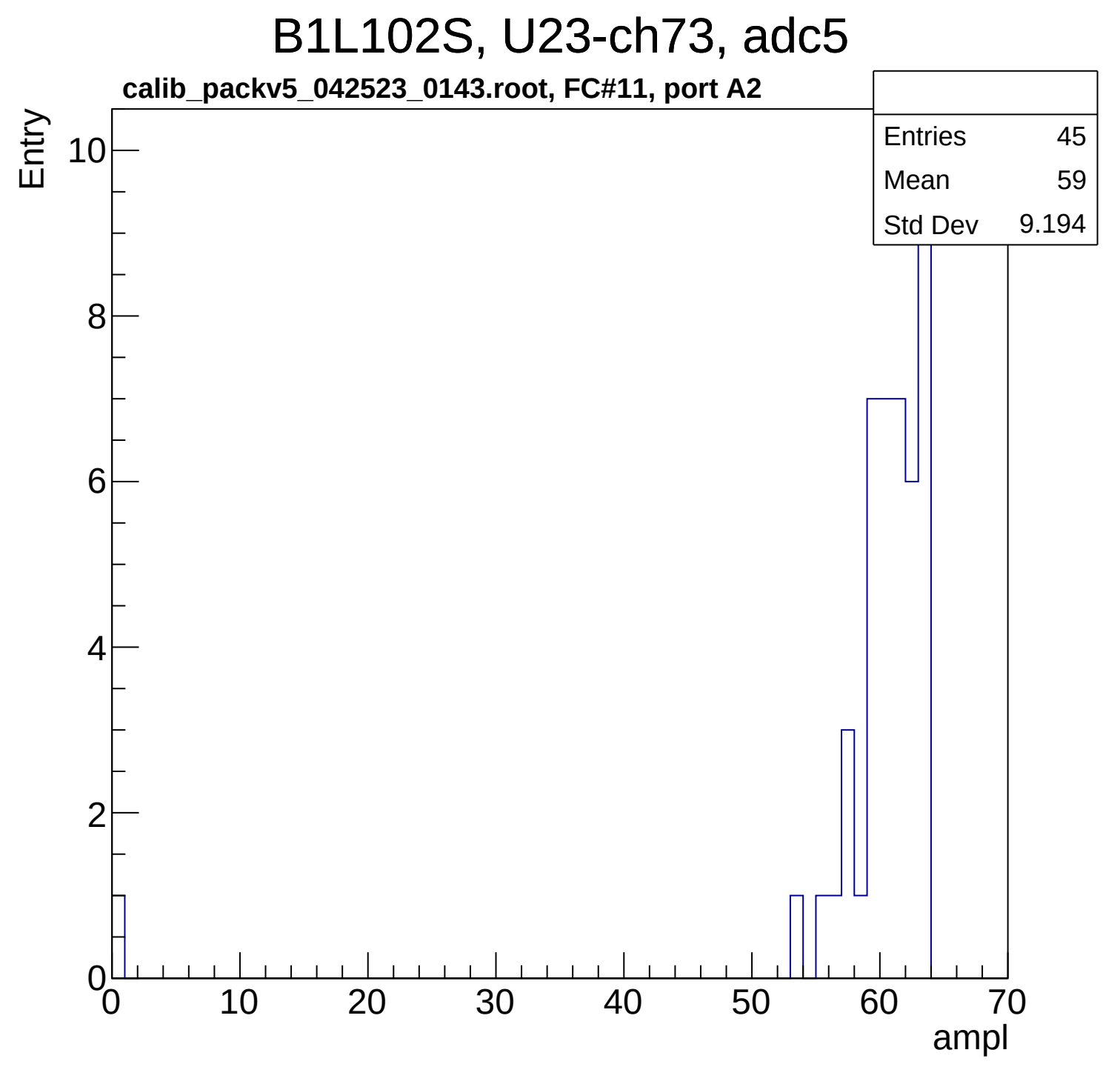

B1L102S, U23-ch73, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 28.33
Std Dev 6.058
Gaus mean : 29.8845
Gaus Width: 3.6427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

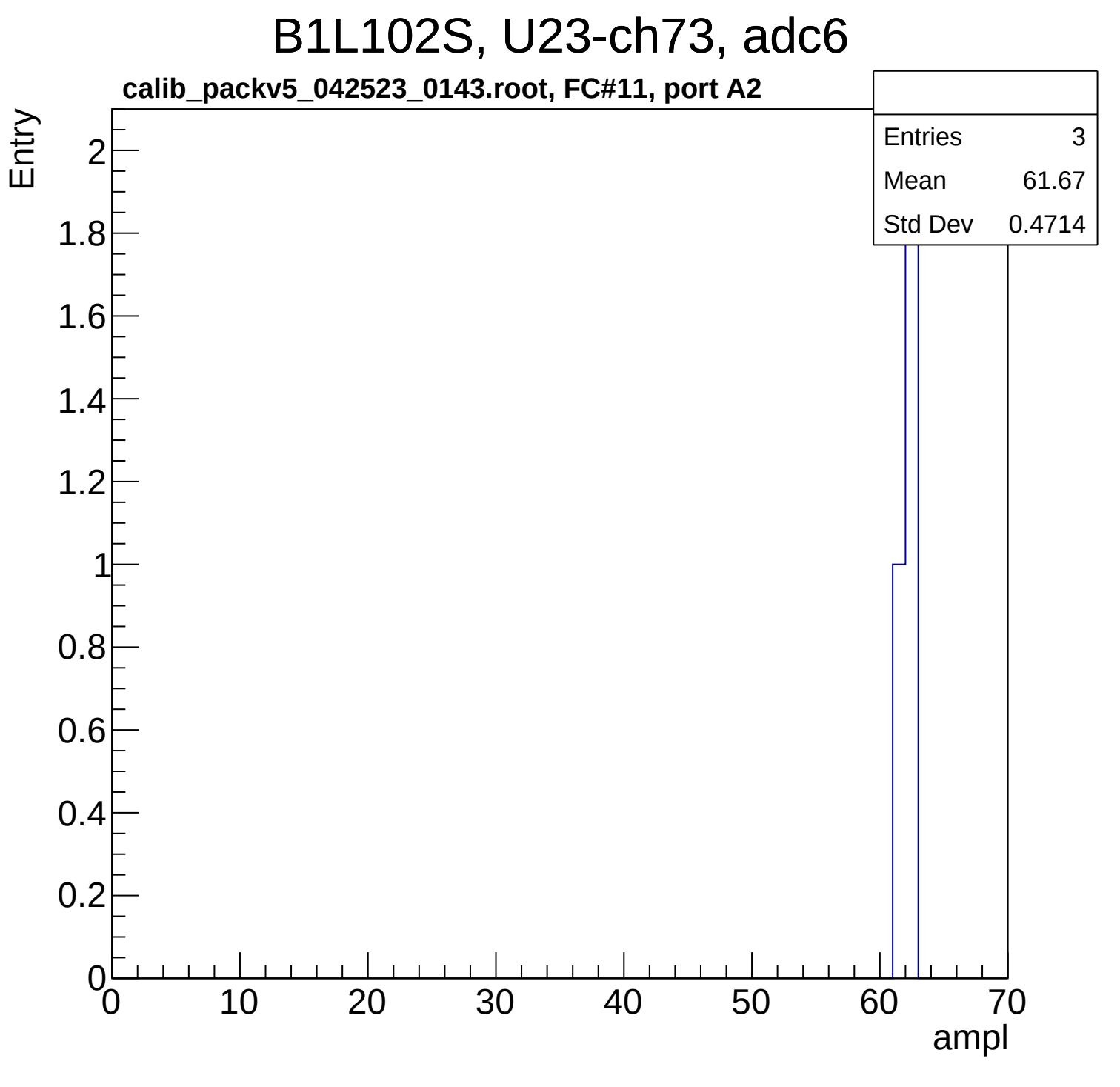

B1L102S, U23-ch73, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 36.14
Std Dev 3.638
Gaus mean : 36.5428
Gaus Width: 4.1073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

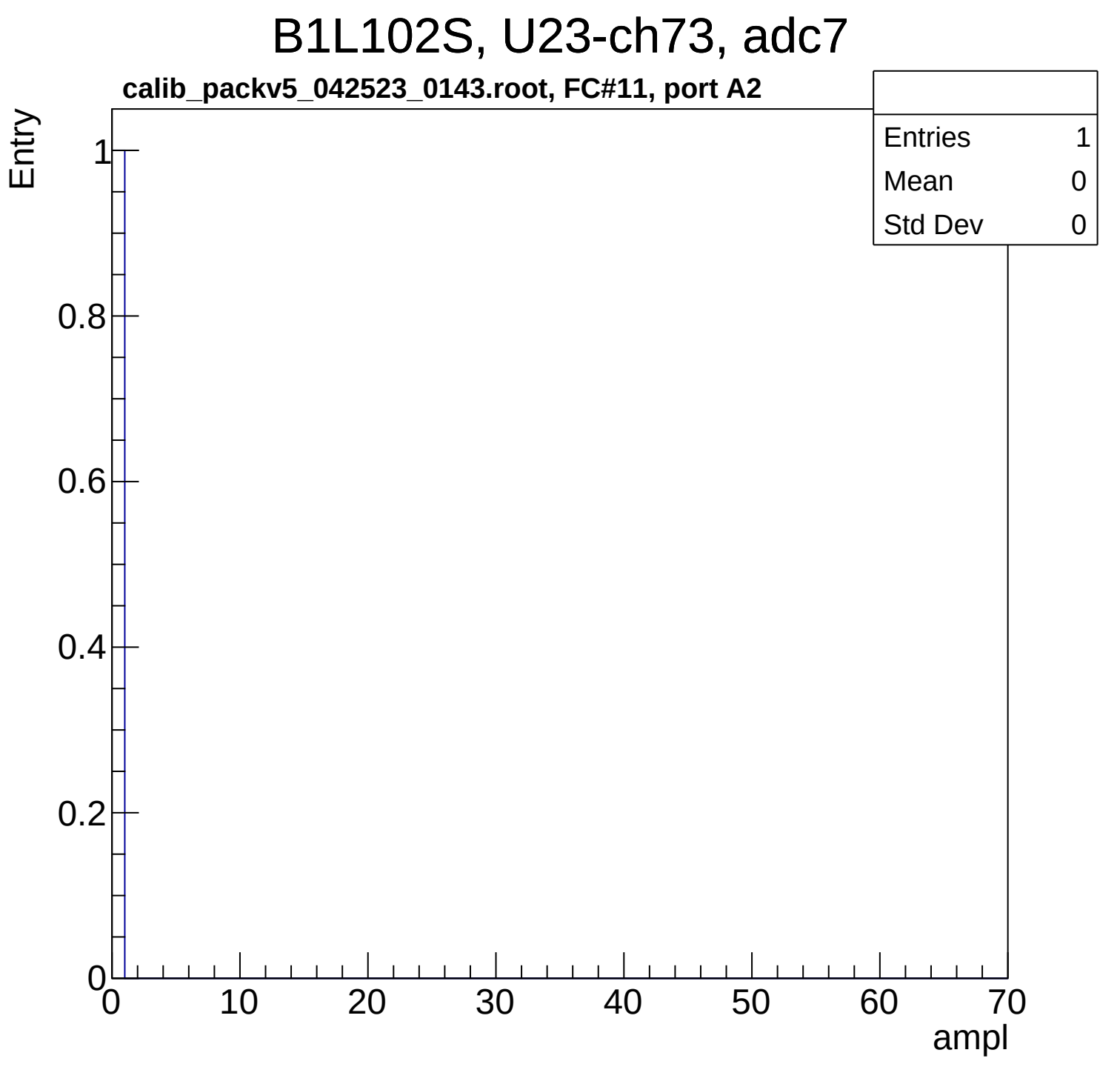

B1L102S, U23-ch73, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 42.42
Std Dev 3.309
Gaus mean : 43.5983
Gaus Width: 3.9410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

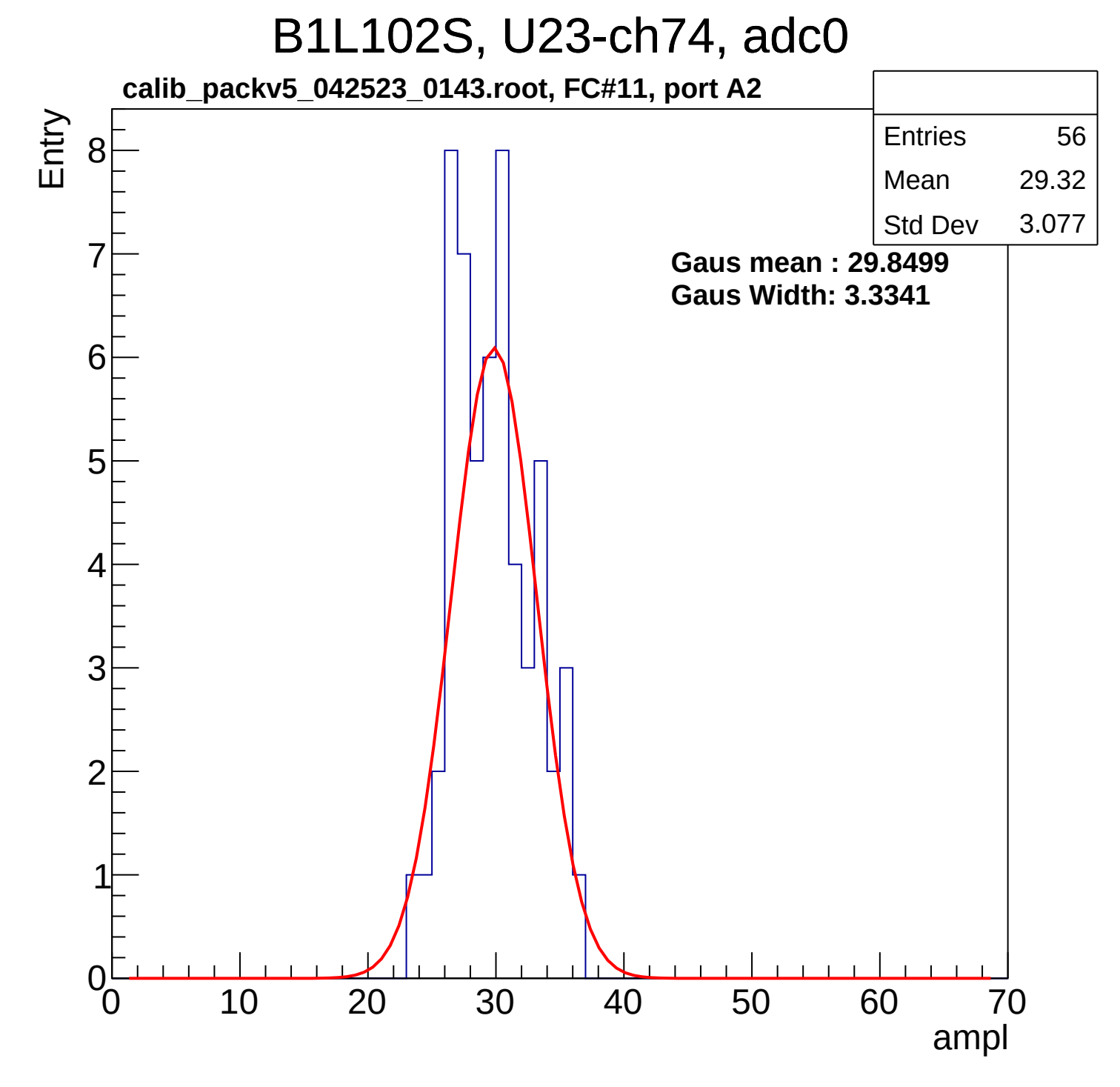

B1L102S, U23-ch73, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 48.48
Std Dev 3.476
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

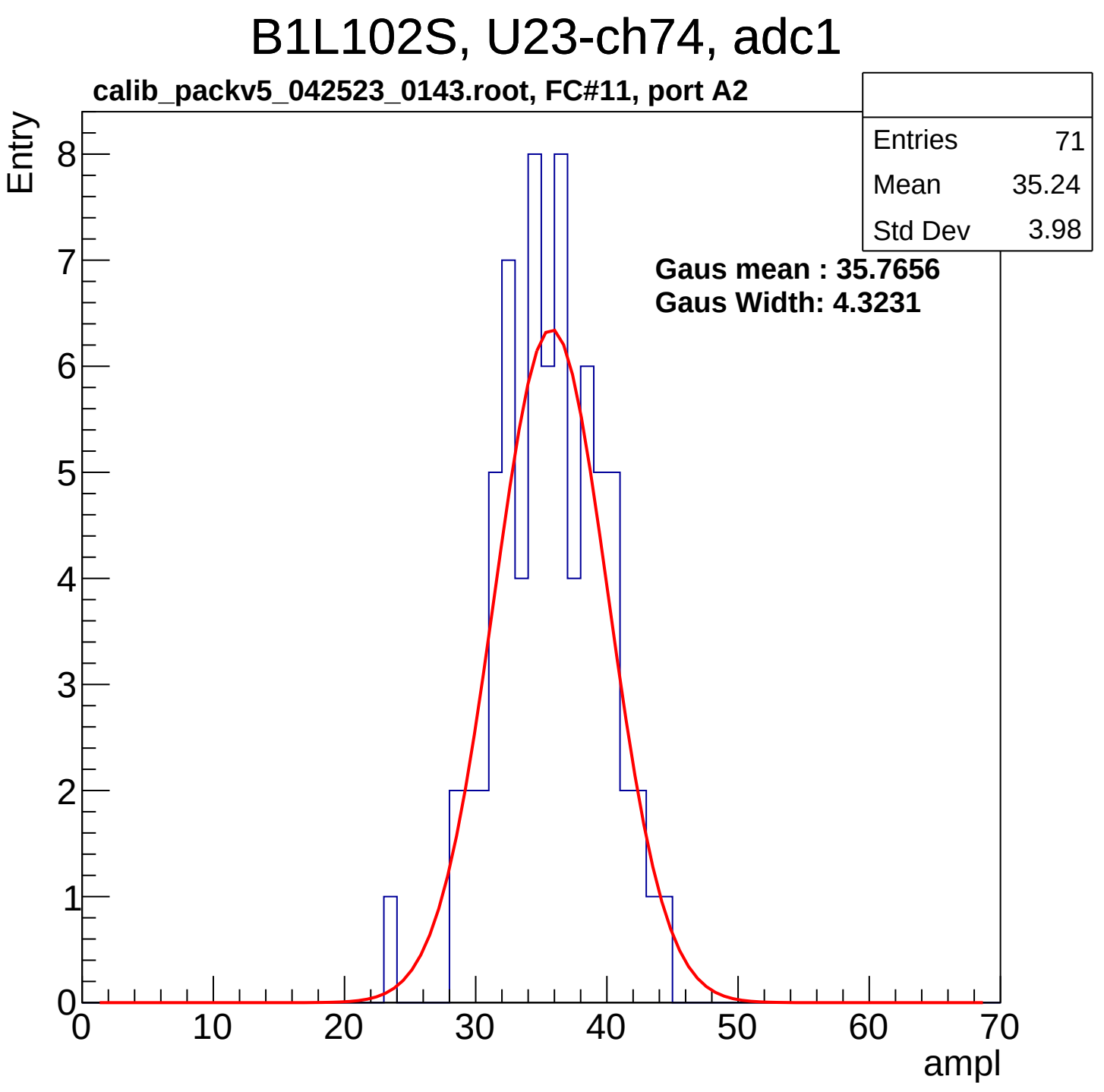

B1L102S, U23-ch73, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 55.47
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

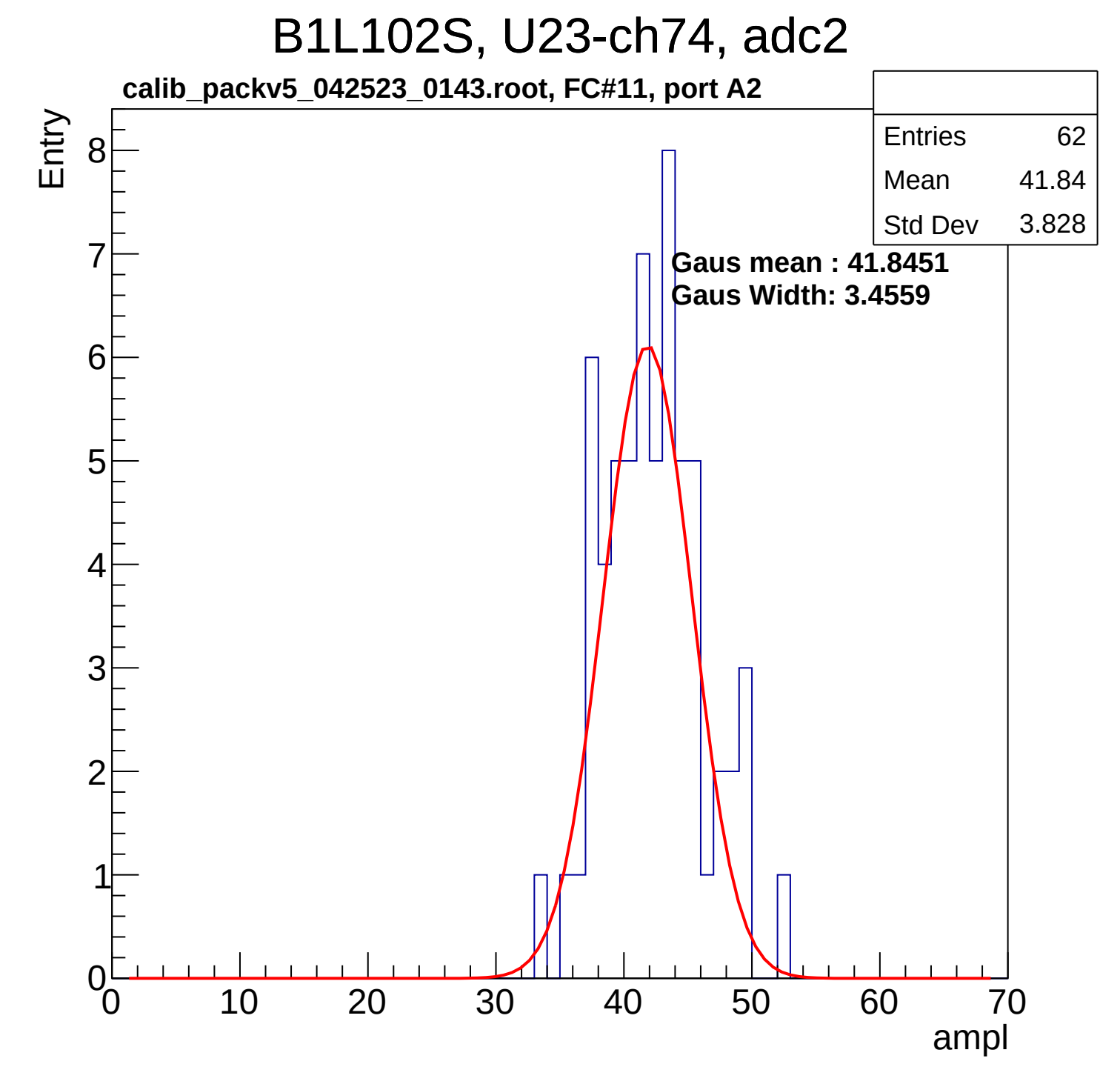

B1L102S, U23-ch73, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 59
Std Dev 9.194
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

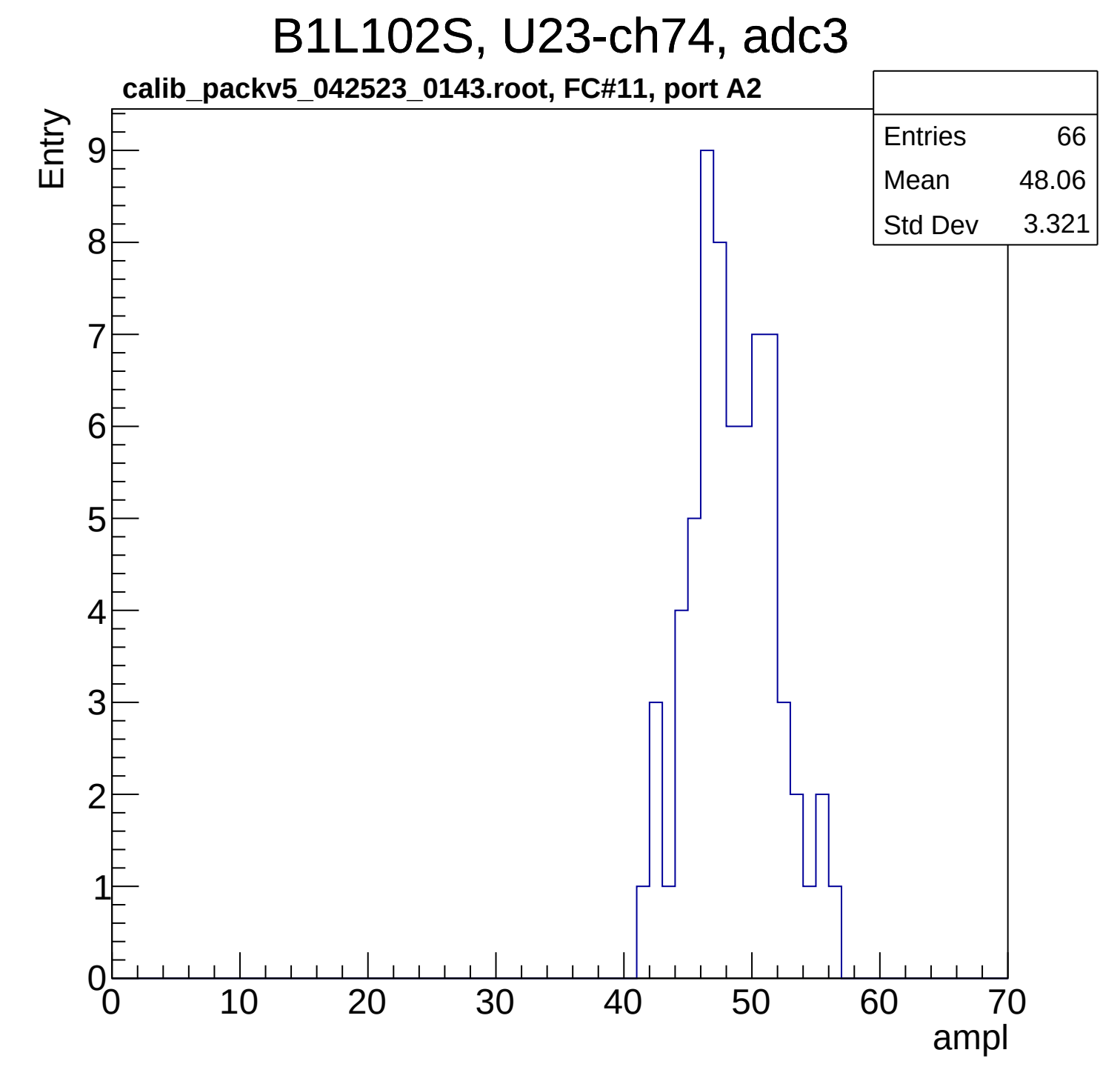

B1L102S, U23-ch73, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

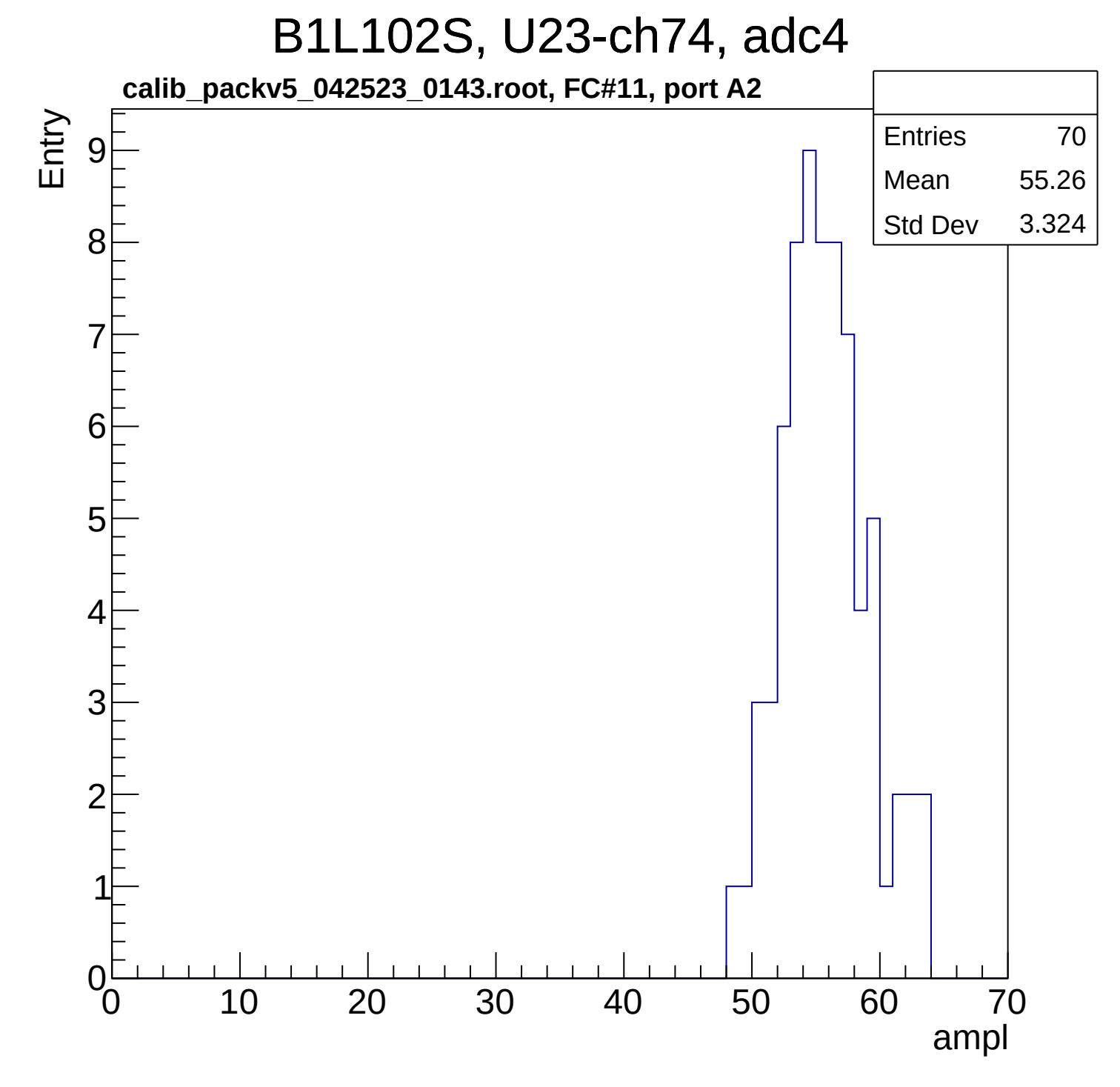

B1L102S, U23-ch73, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

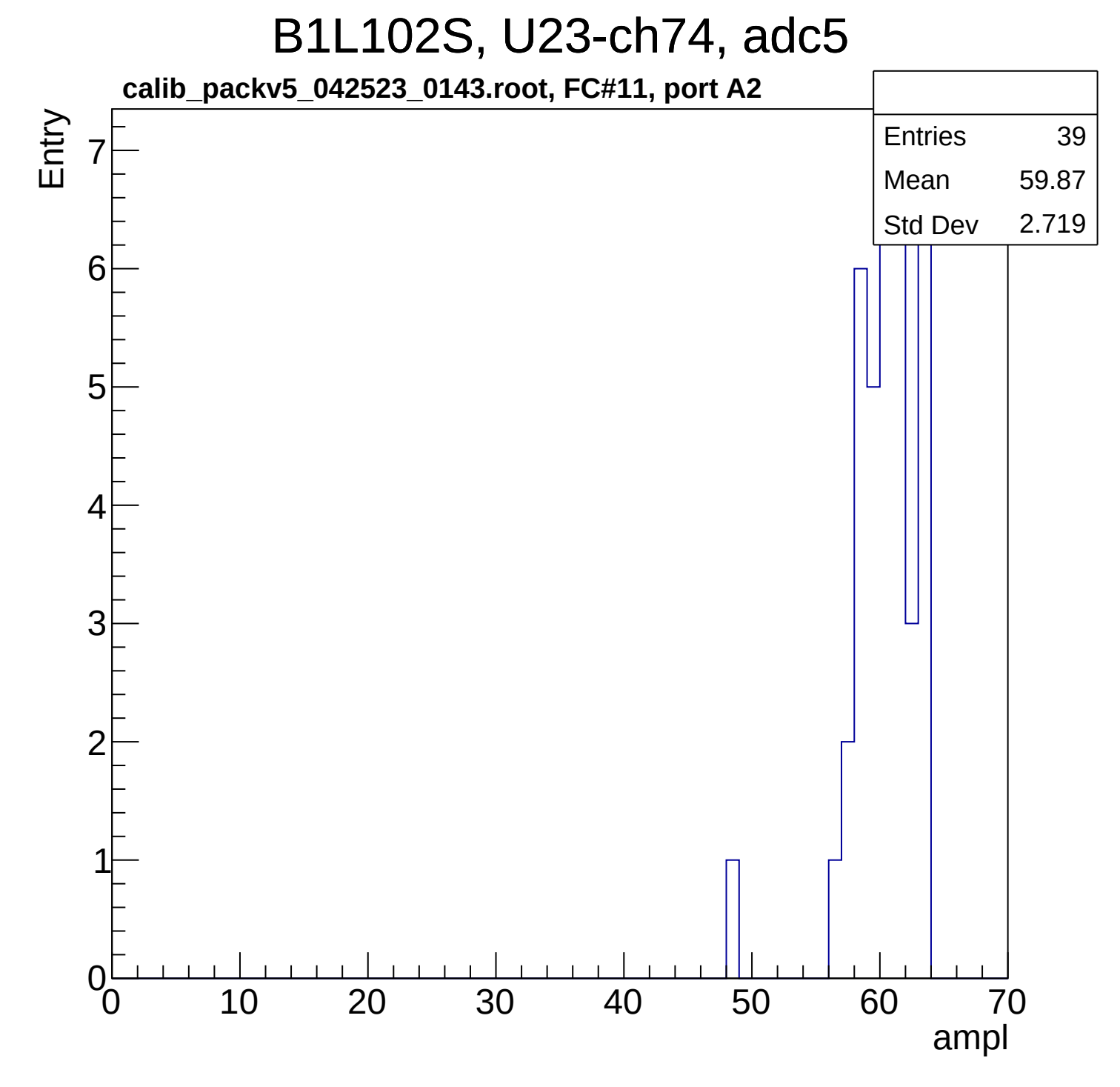

B1L102S, U23-ch74, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 29.32
Std Dev 3.077
Gaus mean : 29.8499
Gaus Width: 3.3341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

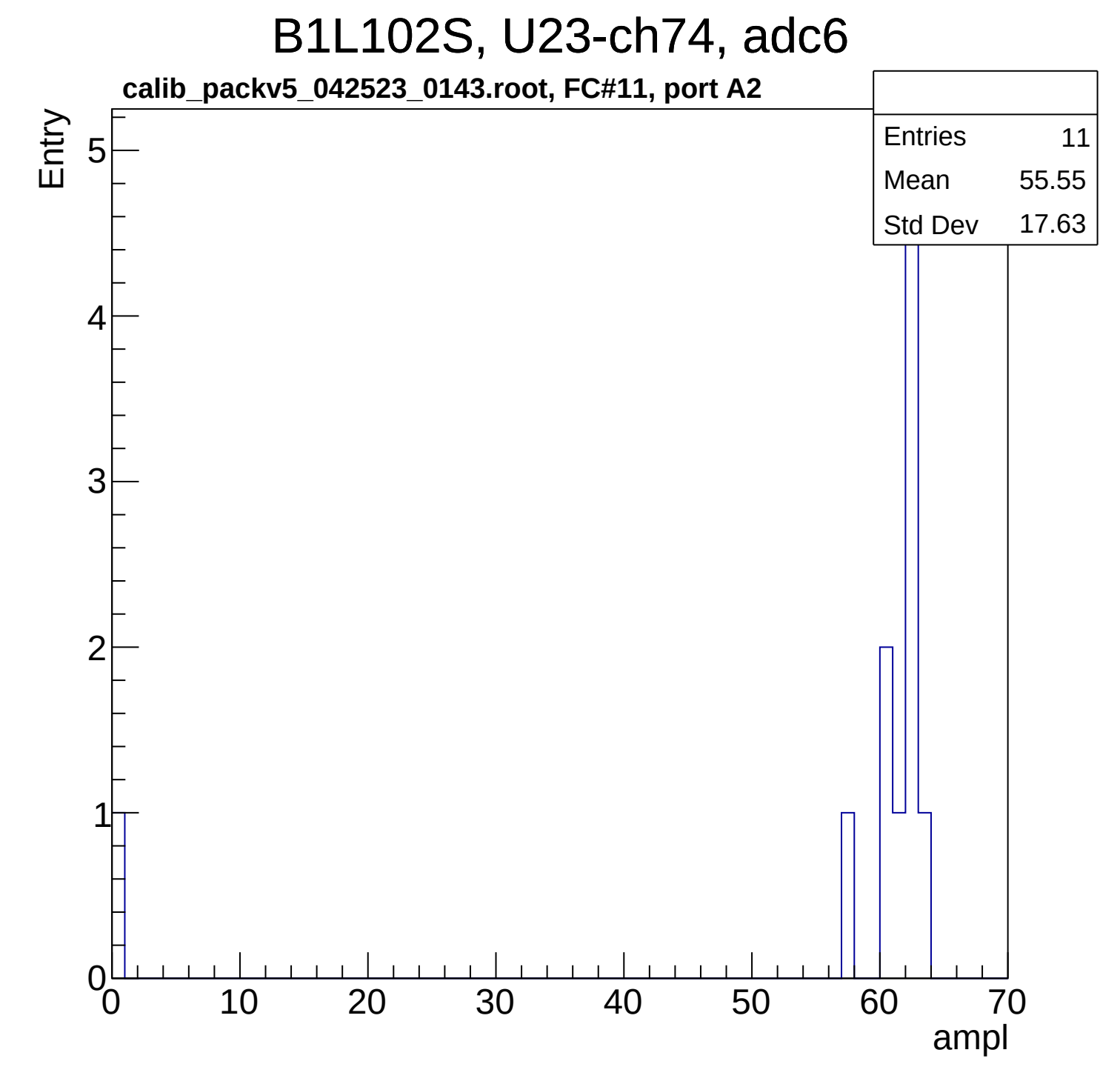

B1L102S, U23-ch74, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 35.24
Std Dev 3.98
Gaus mean : 35.7656
Gaus Width: 4.3231
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

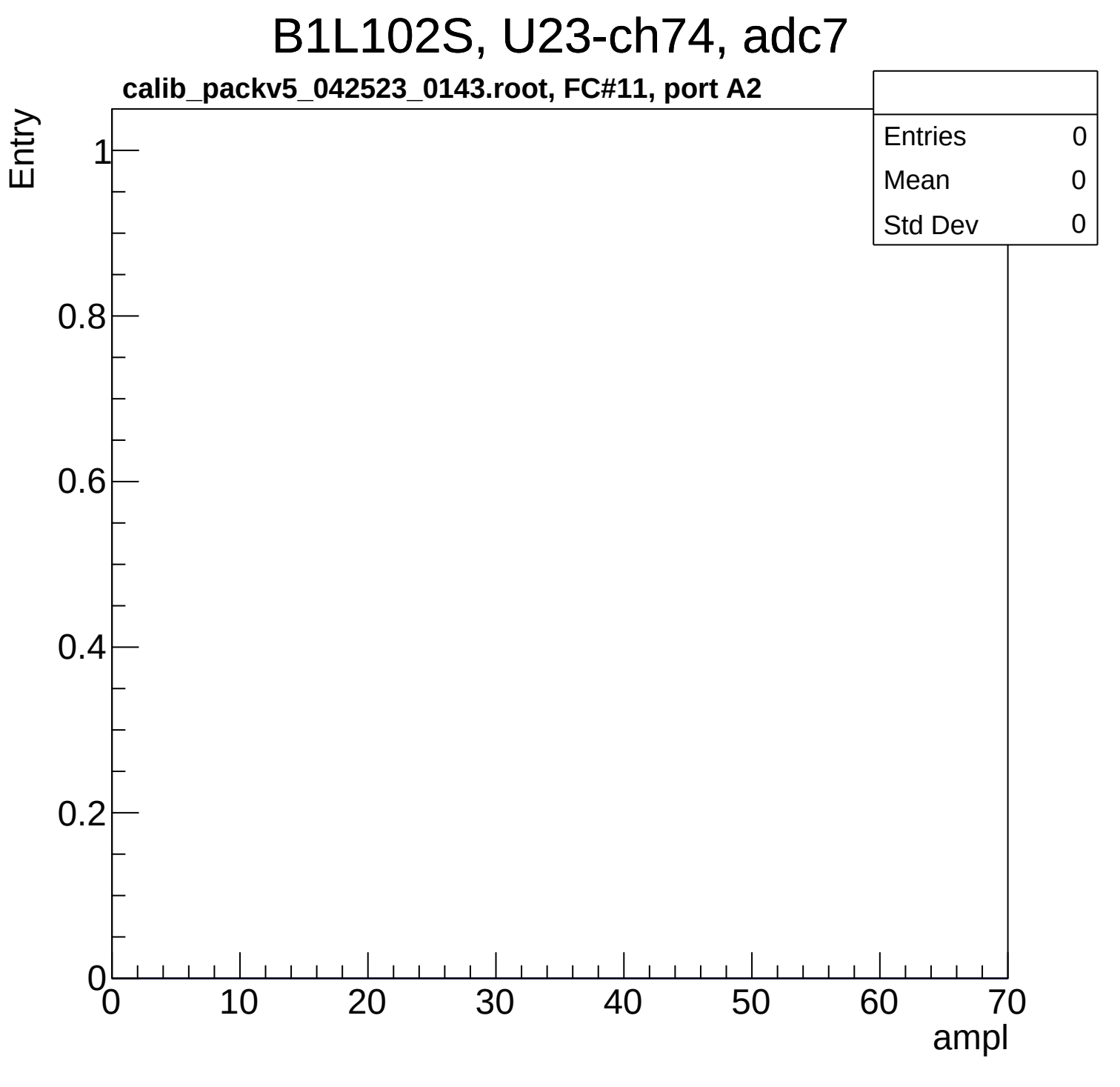

B1L102S, U23-ch74, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 41.84
Std Dev 3.828
Gaus mean : 41.8451
Gaus Width: 3.4559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

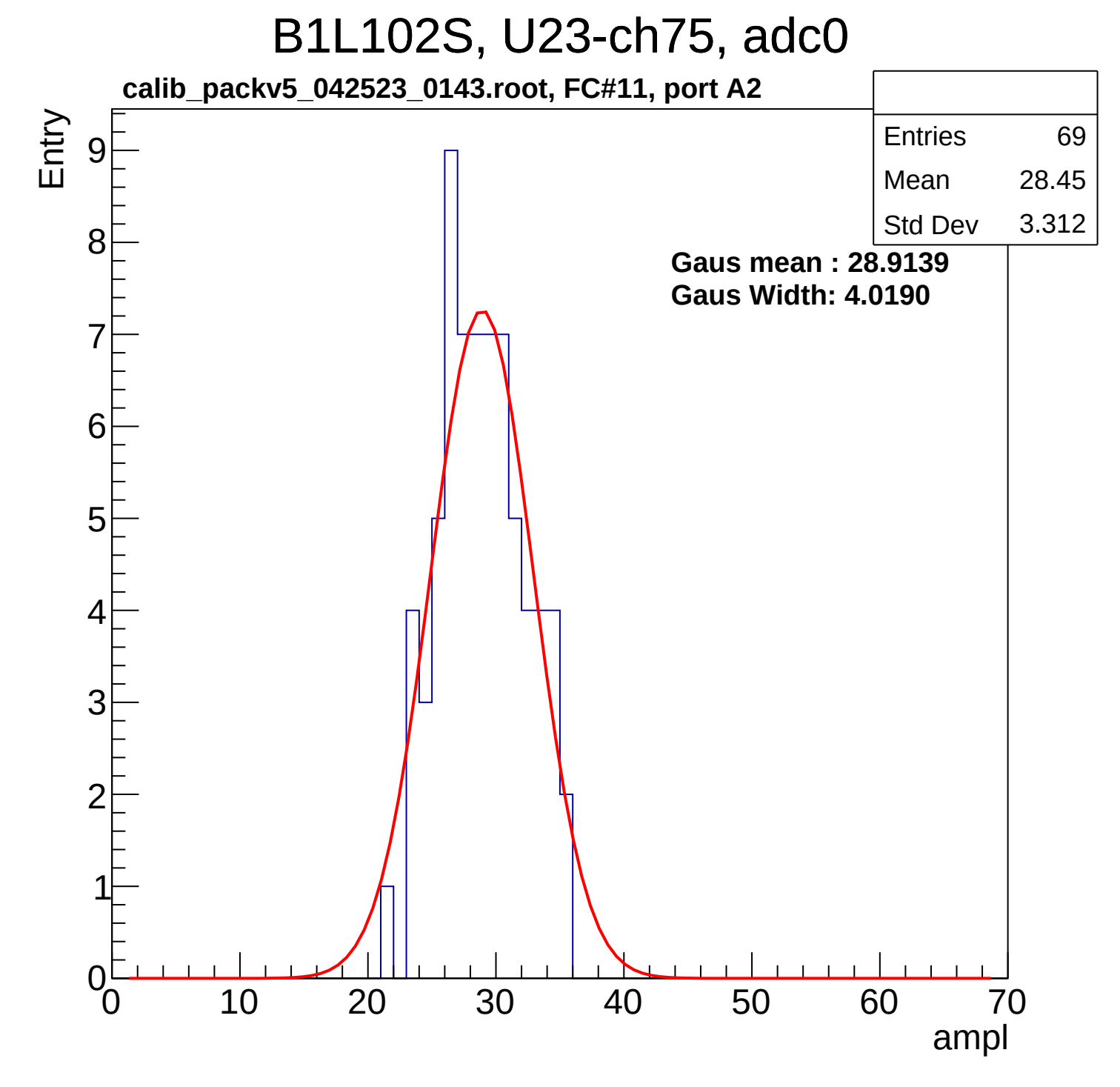

B1L102S, U23-ch74, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 48.06
Std Dev 3.321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

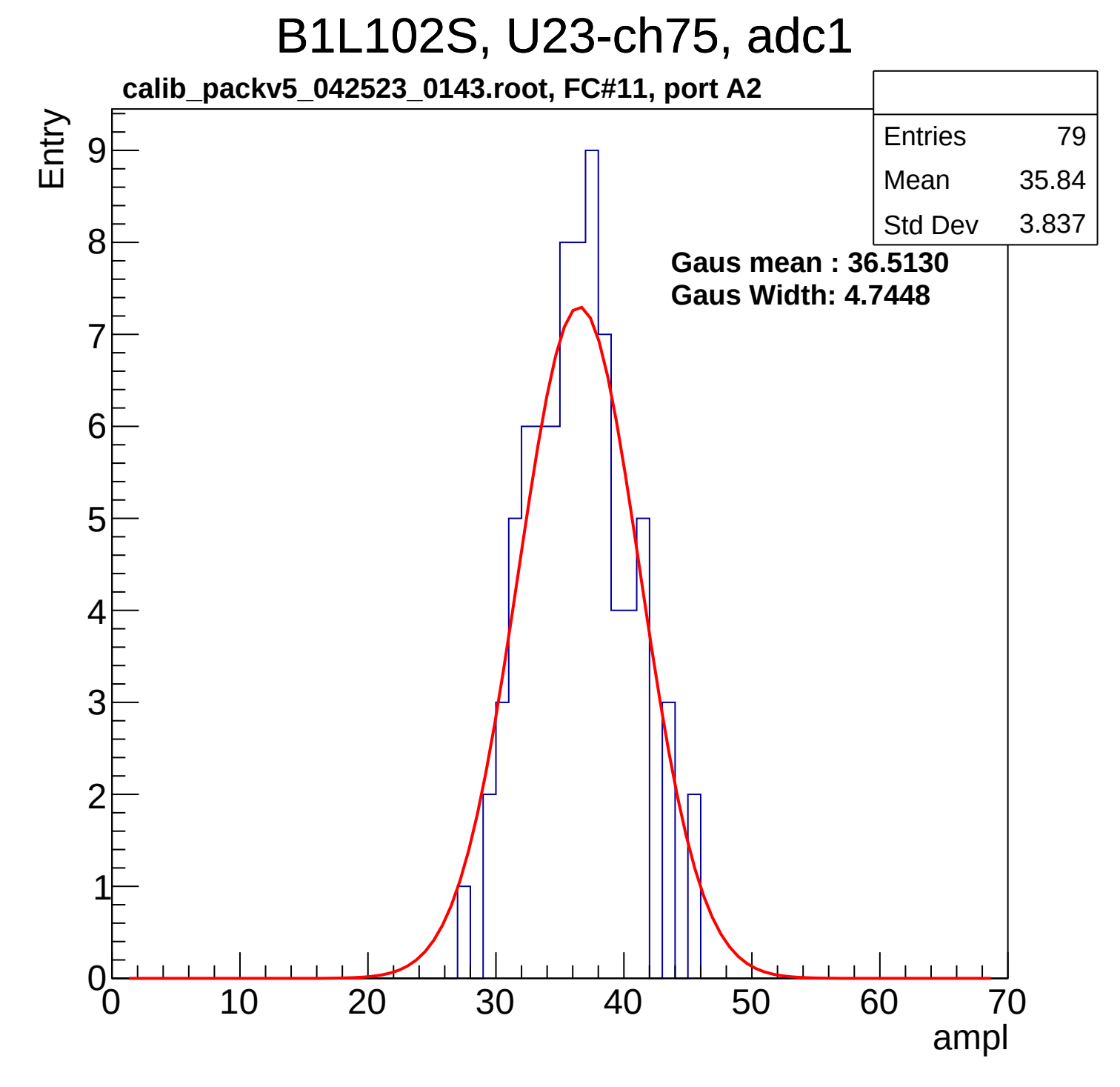

B1L102S, U23-ch74, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 55.26
Std Dev 3.324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

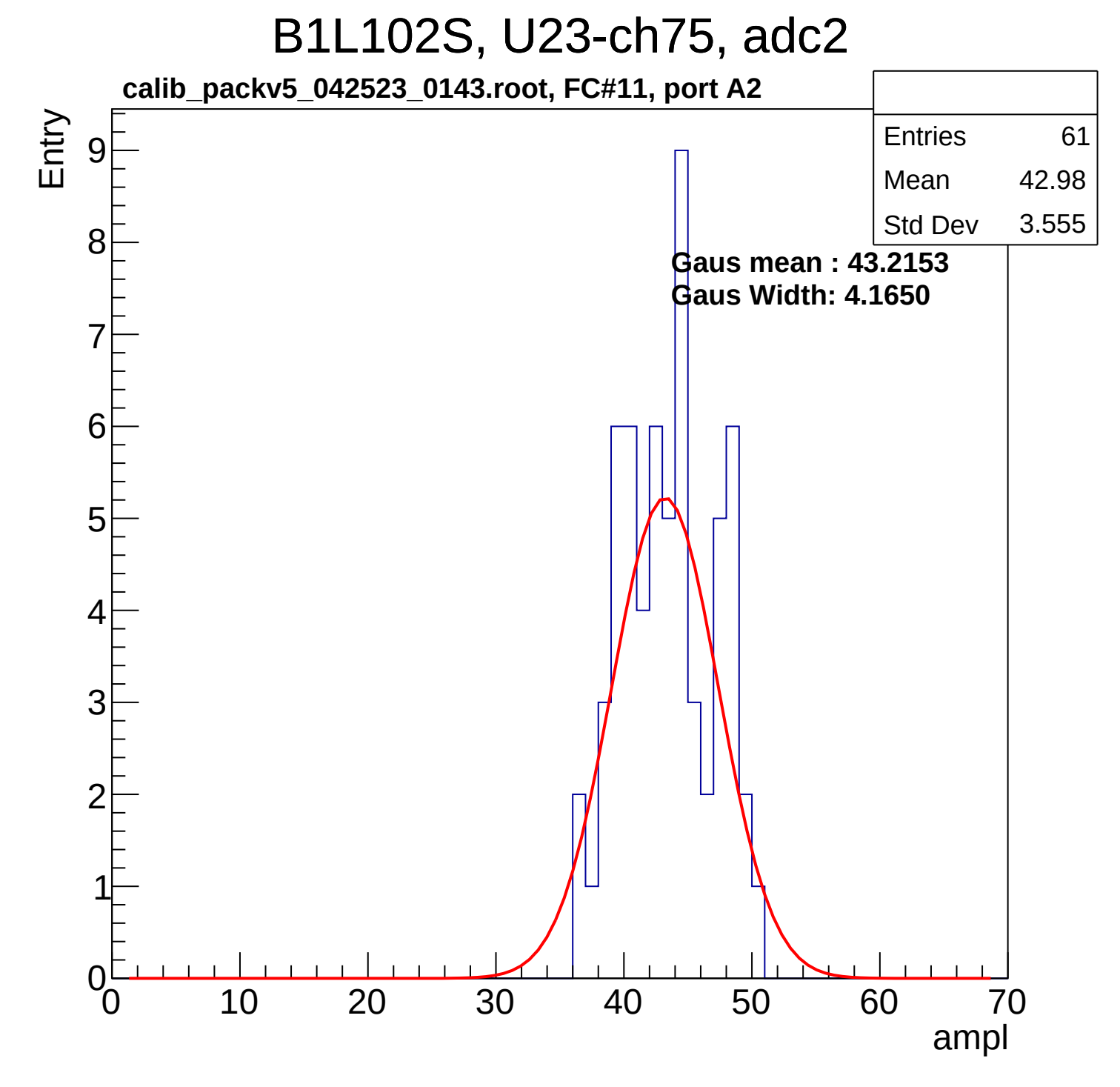

B1L102S, U23-ch74, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 59.87
Std Dev 2.719
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

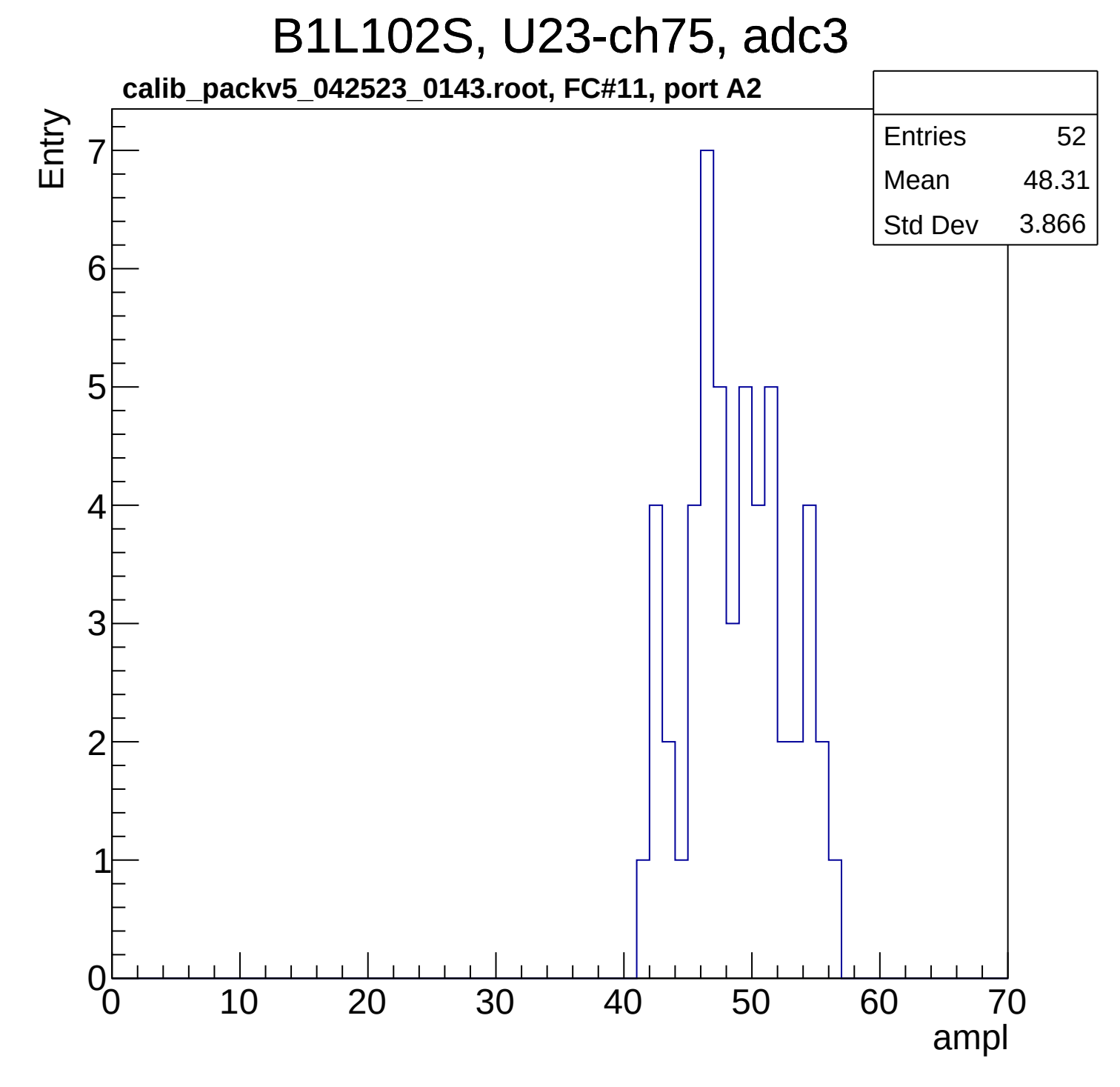

B1L102S, U23-ch74, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 55.55
Std Dev 17.63
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

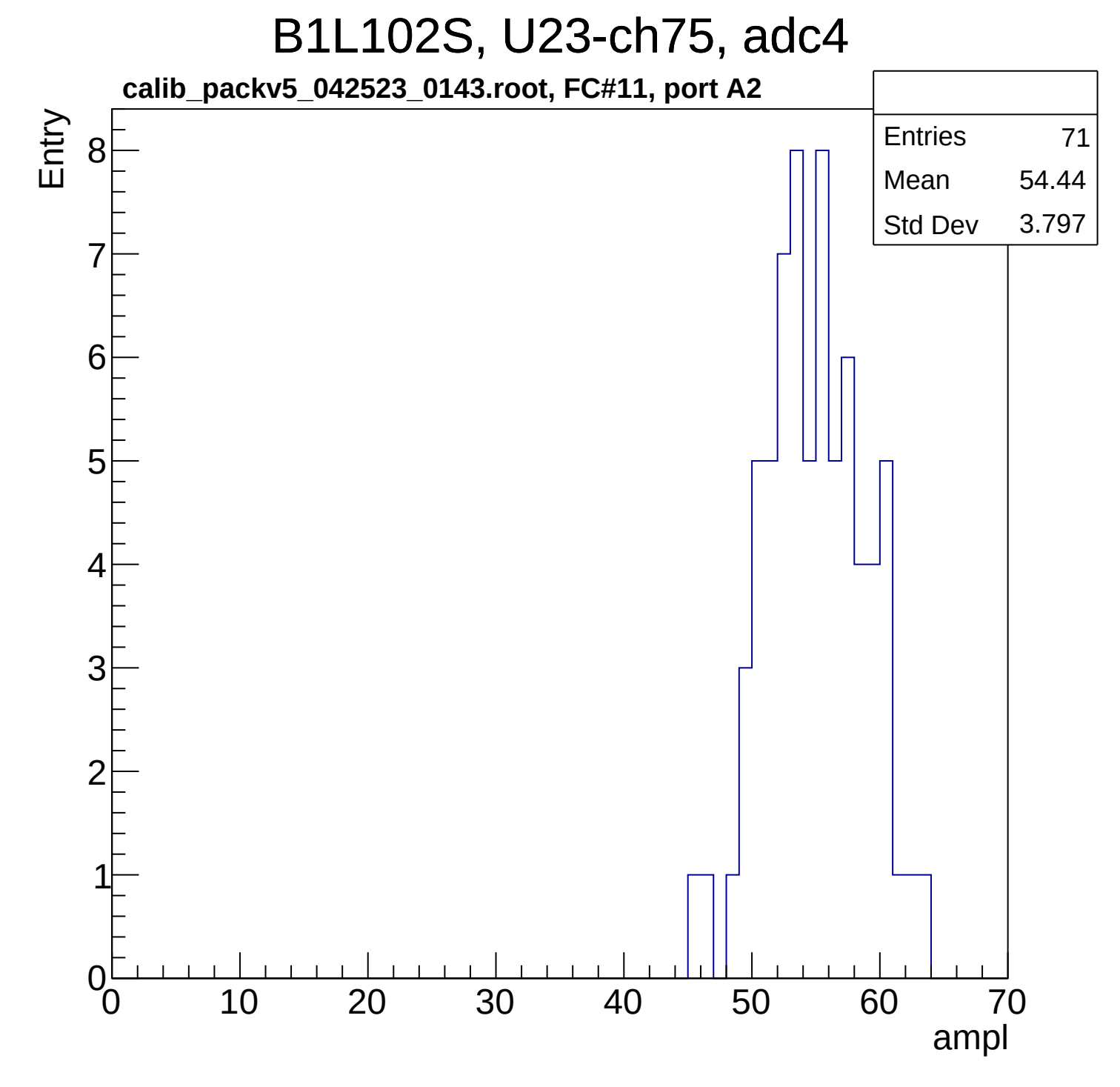

B1L102S, U23-ch74, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

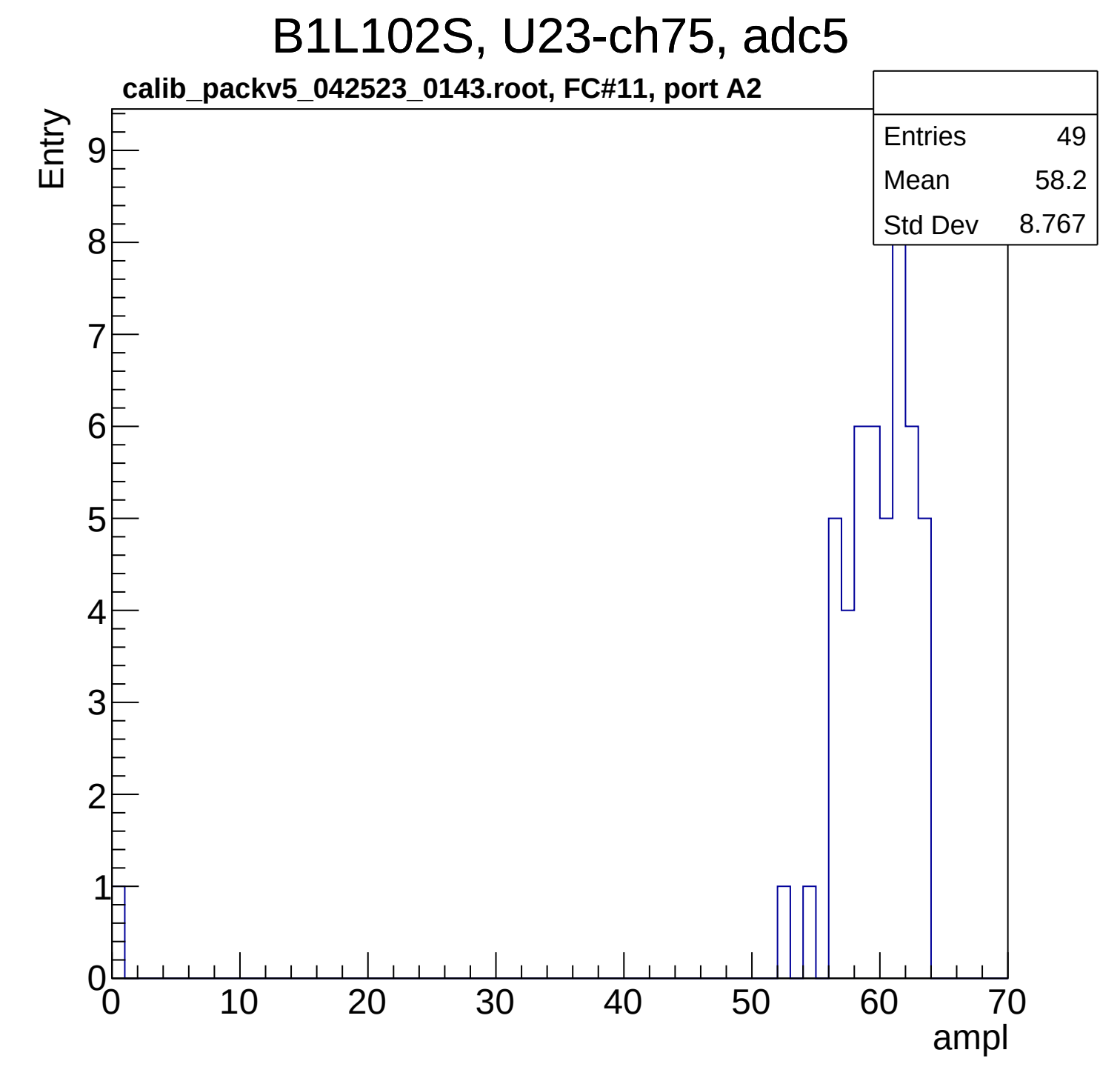

B1L102S, U23-ch75, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.45
Std Dev 3.312
Gaus mean : 28.9139
Gaus Width: 4.0190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

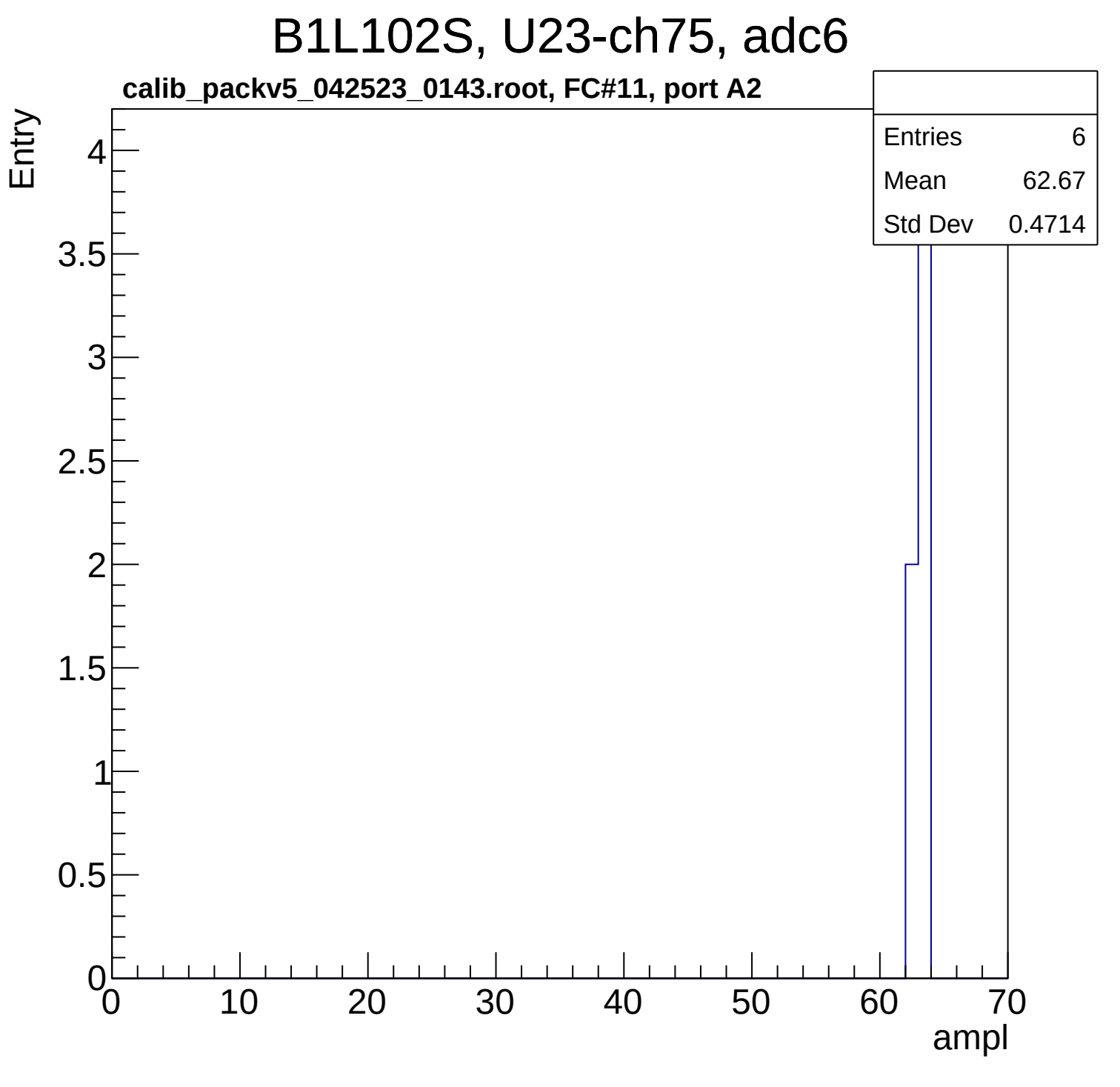

B1L102S, U23-ch75, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 35.84
Std Dev 3.837
Gaus mean : 36.5130
Gaus Width: 4.7448
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

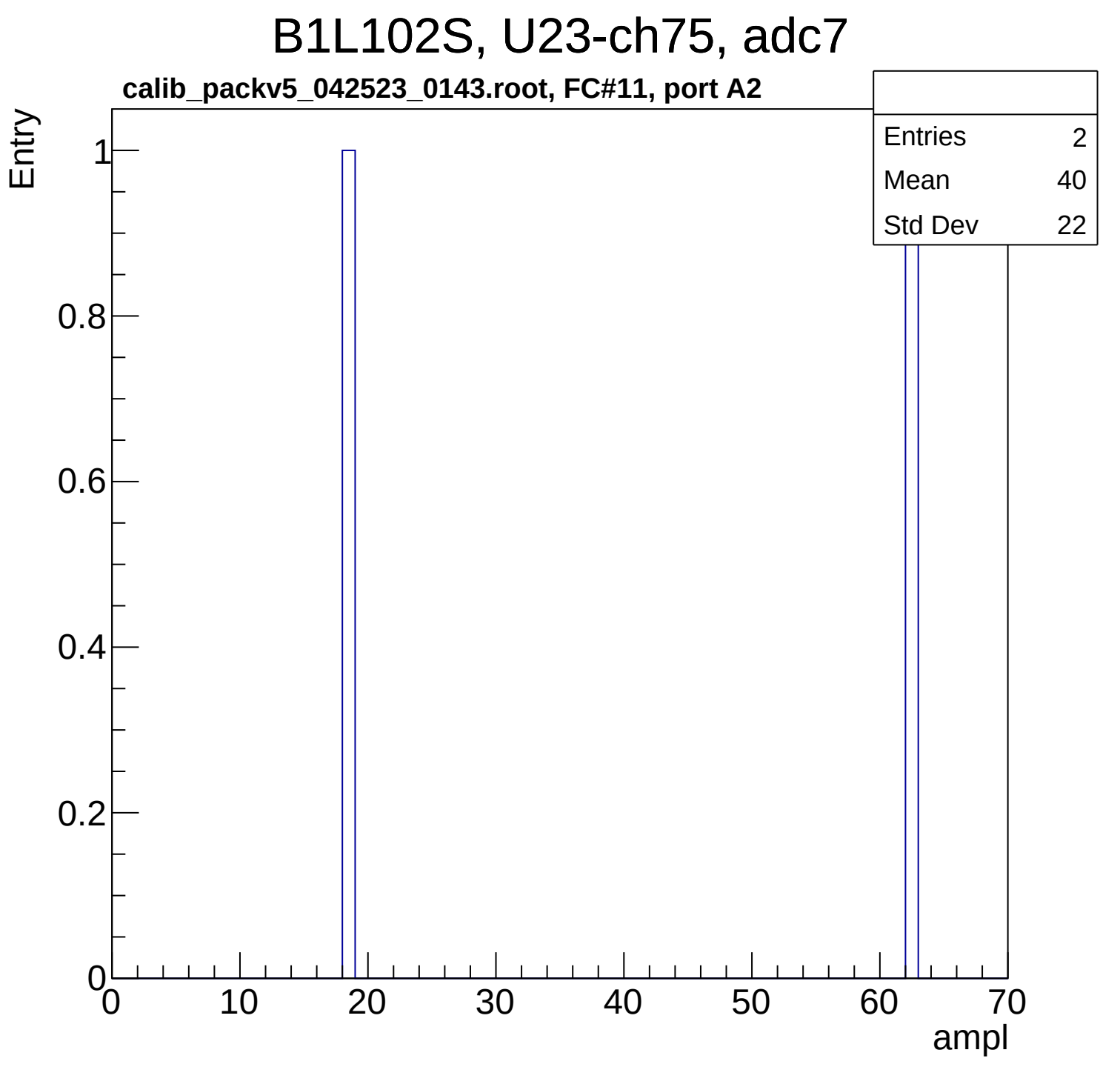

B1L102S, U23-ch75, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 42.98
Std Dev 3.555
Gaus mean : 43.2153
Gaus Width: 4.1650
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

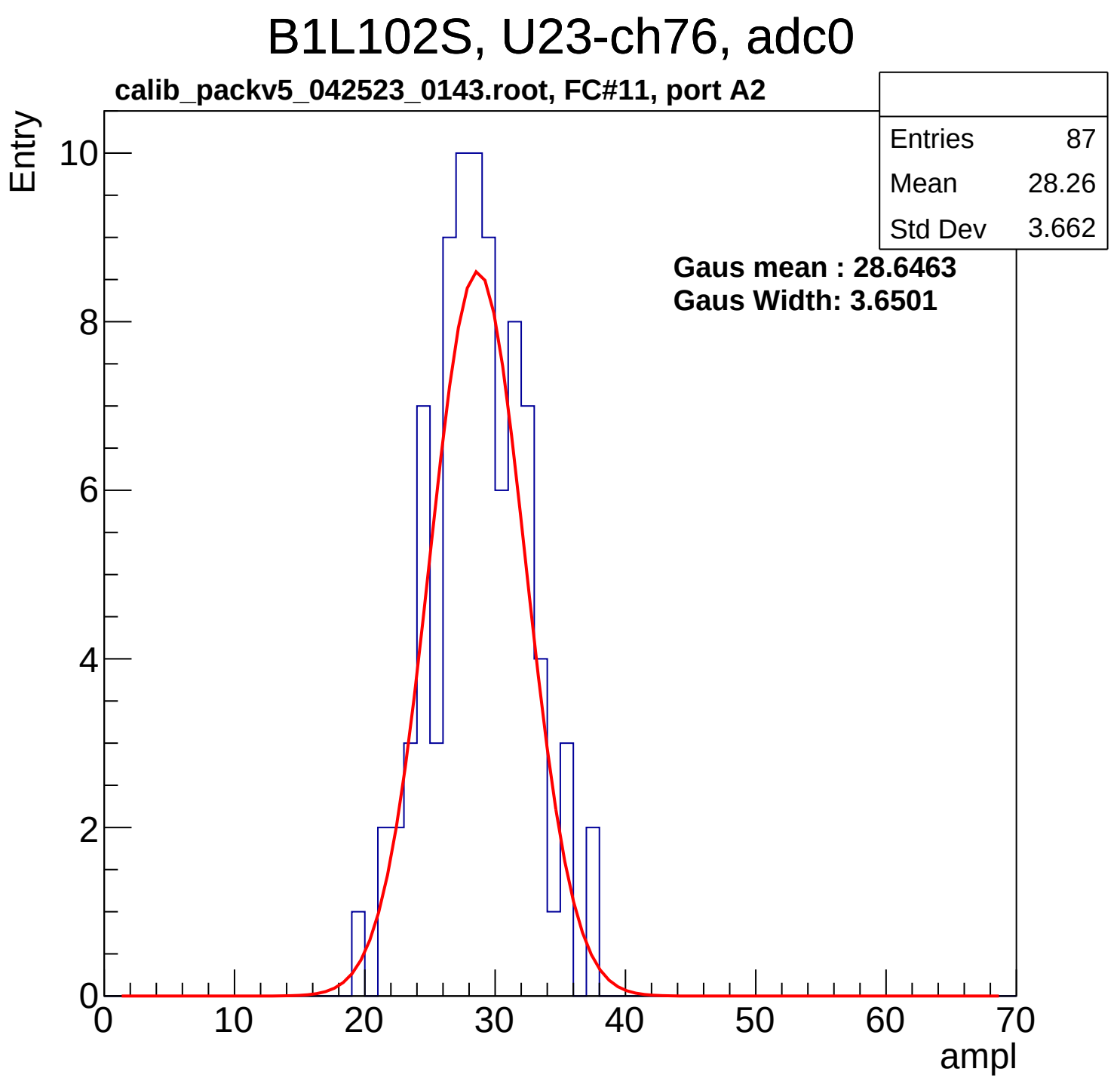

B1L102S, U23-ch75, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 48.31
Std Dev 3.866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

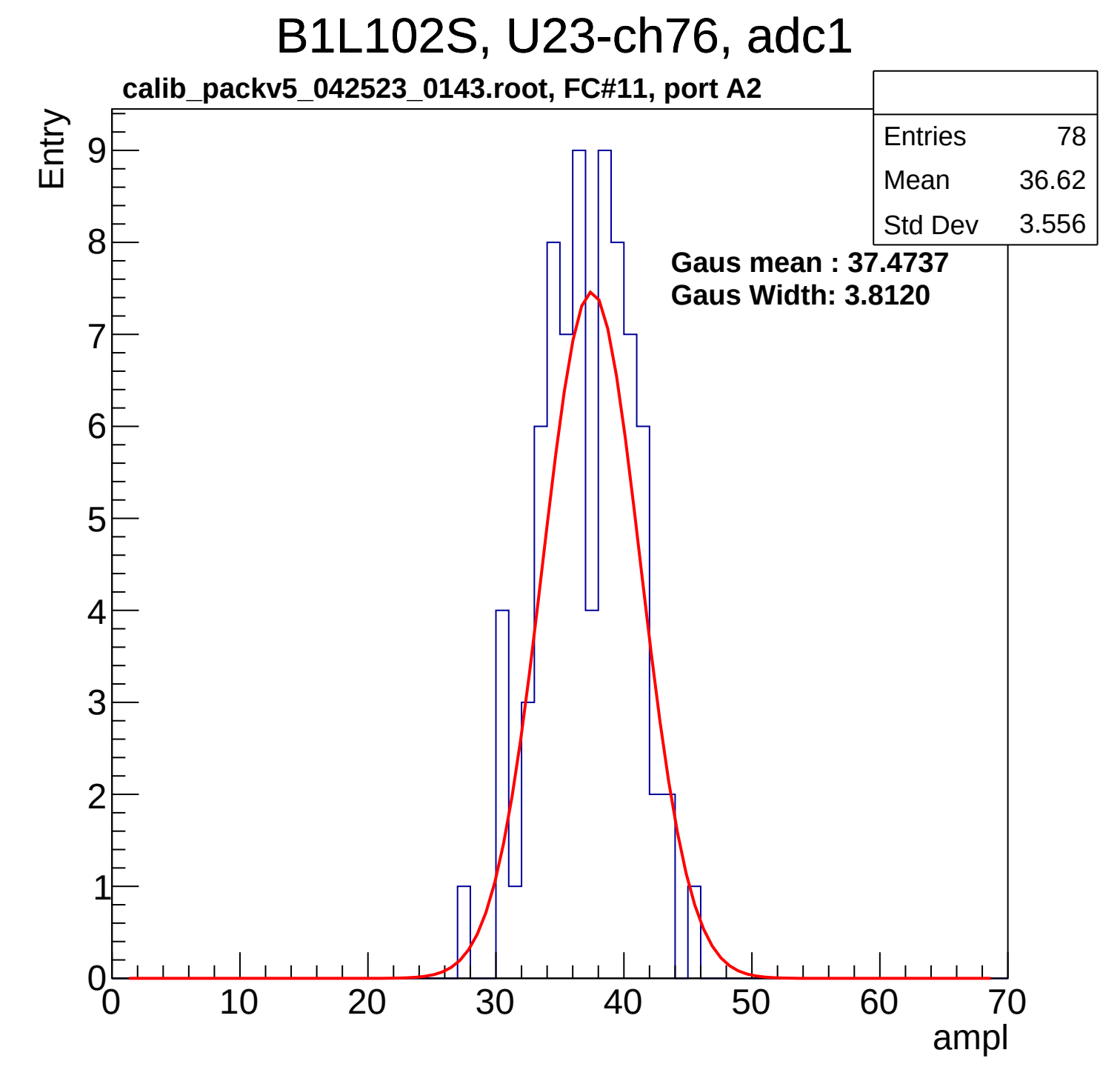

B1L102S, U23-ch75, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 54.44
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

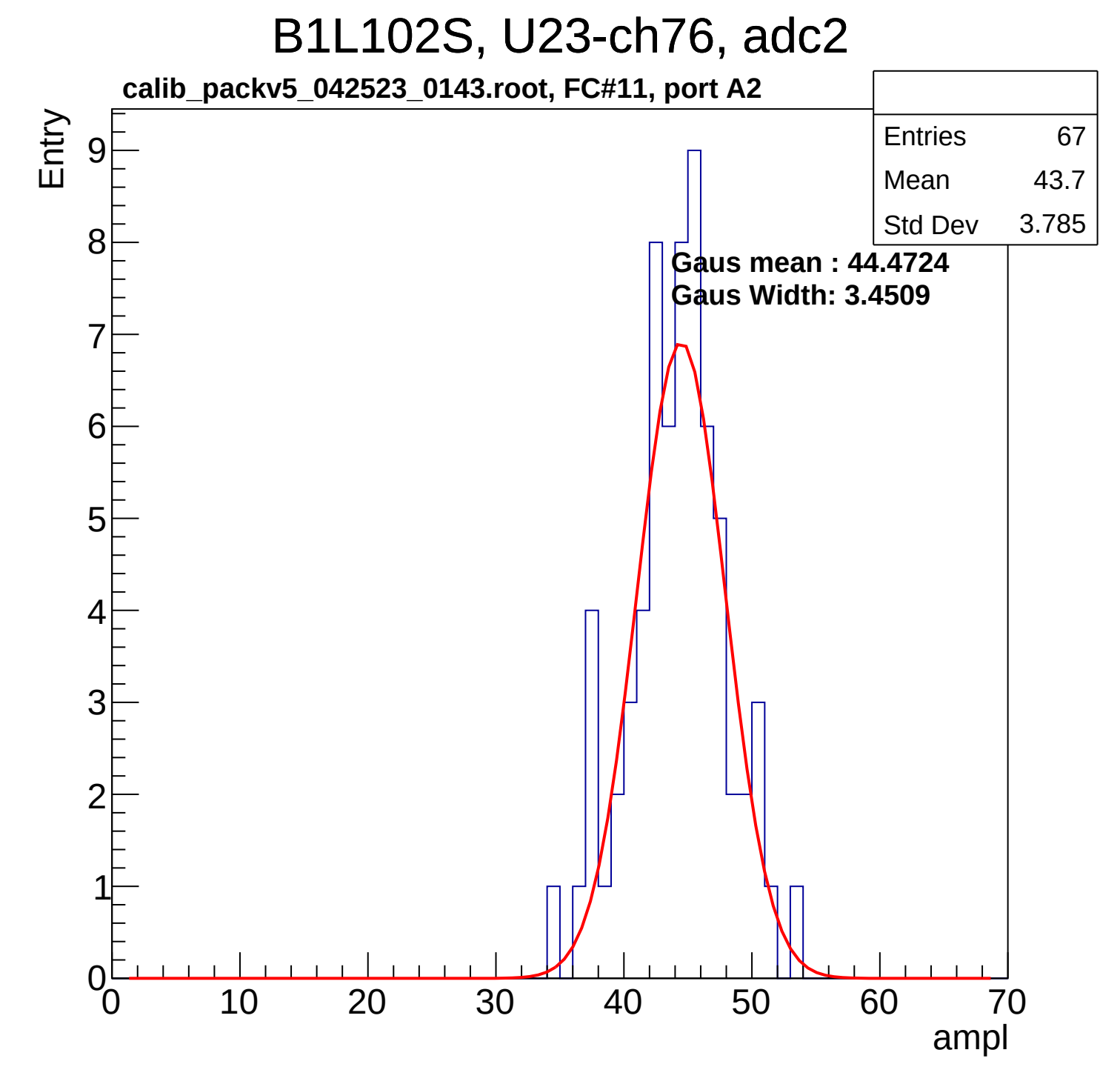

B1L102S, U23-ch75, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.2
Std Dev 8.767
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

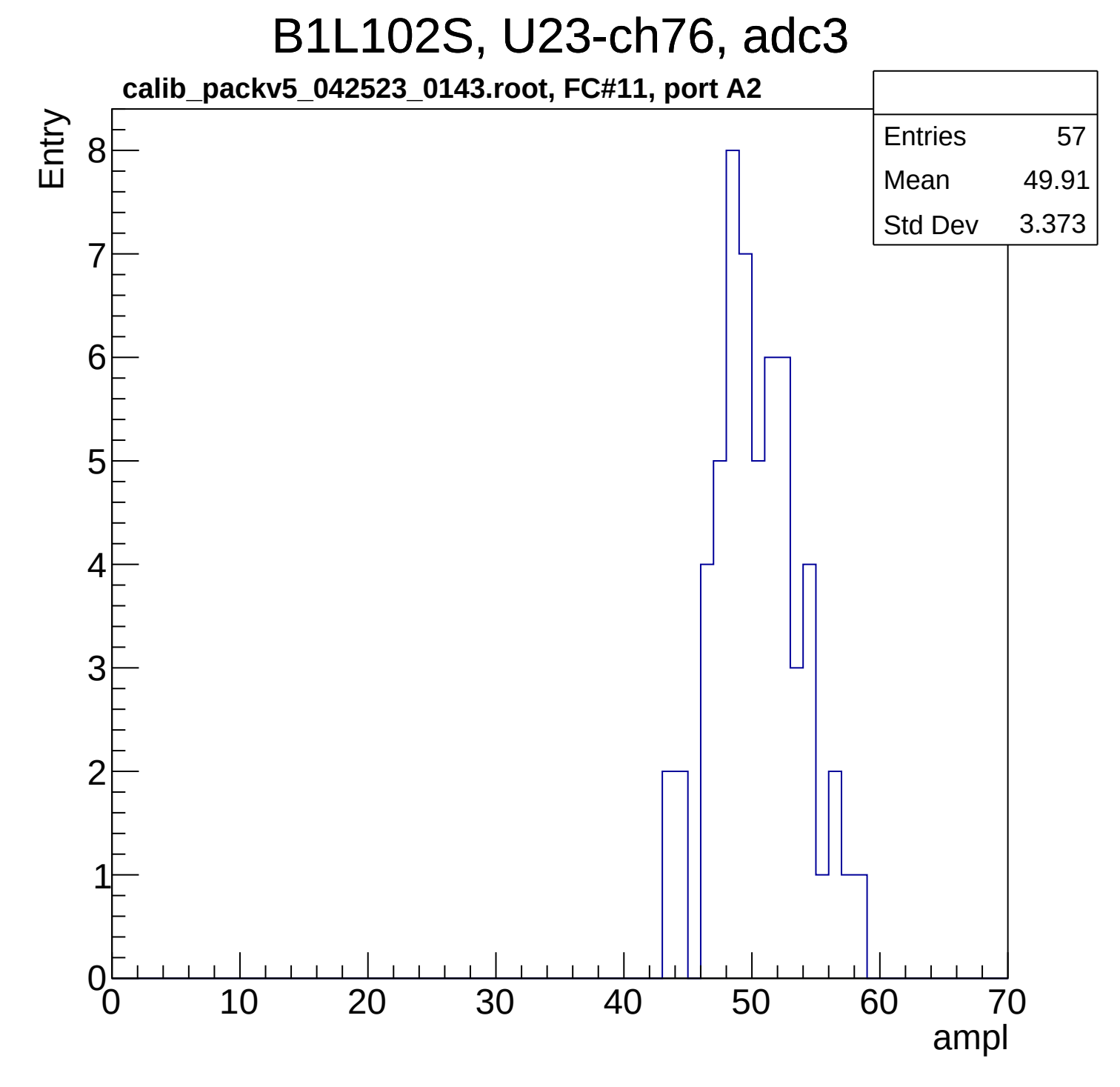

B1L102S, U23-ch75, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

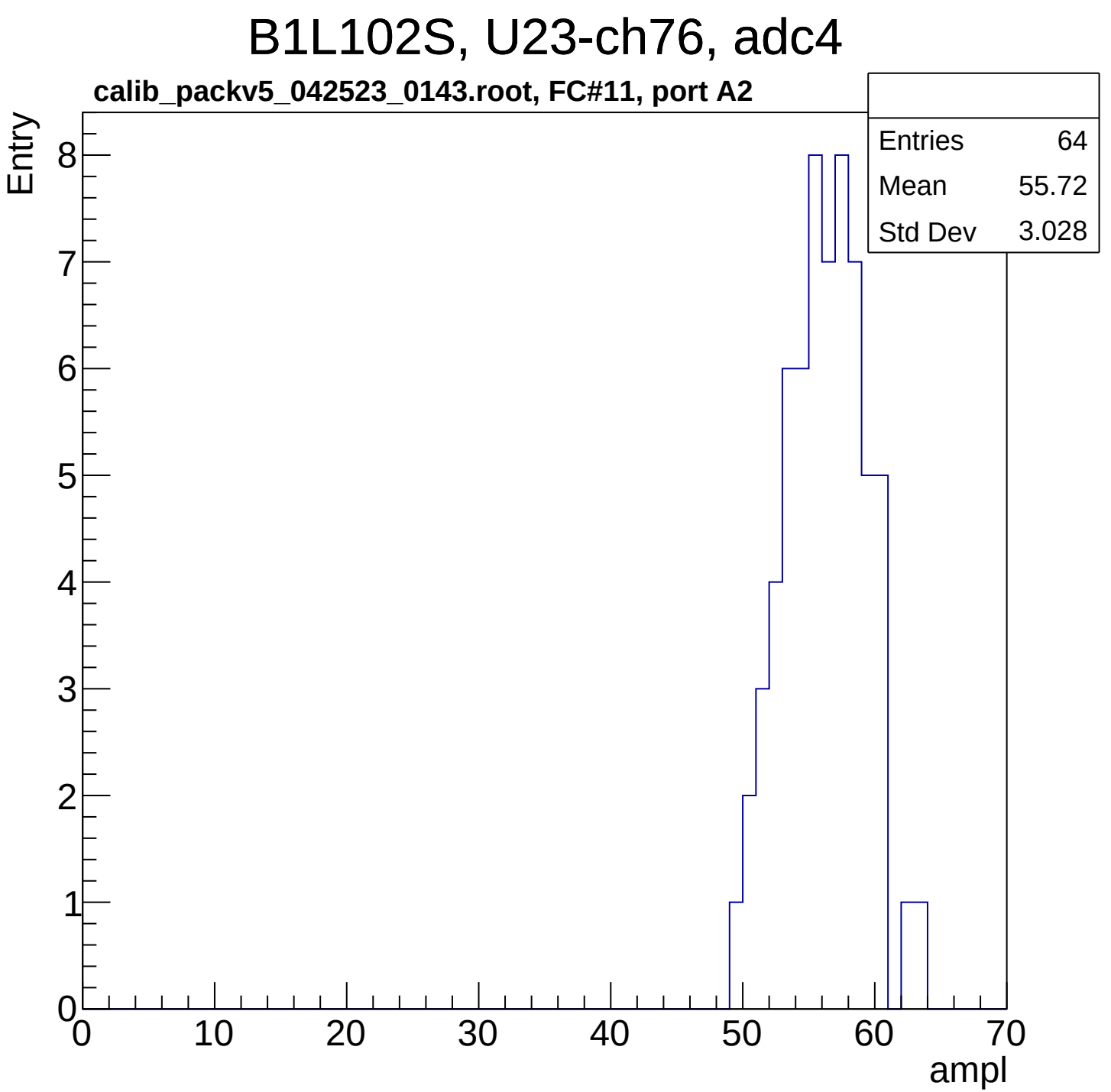

B1L102S, U23-ch75, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 40
Std Dev 22
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

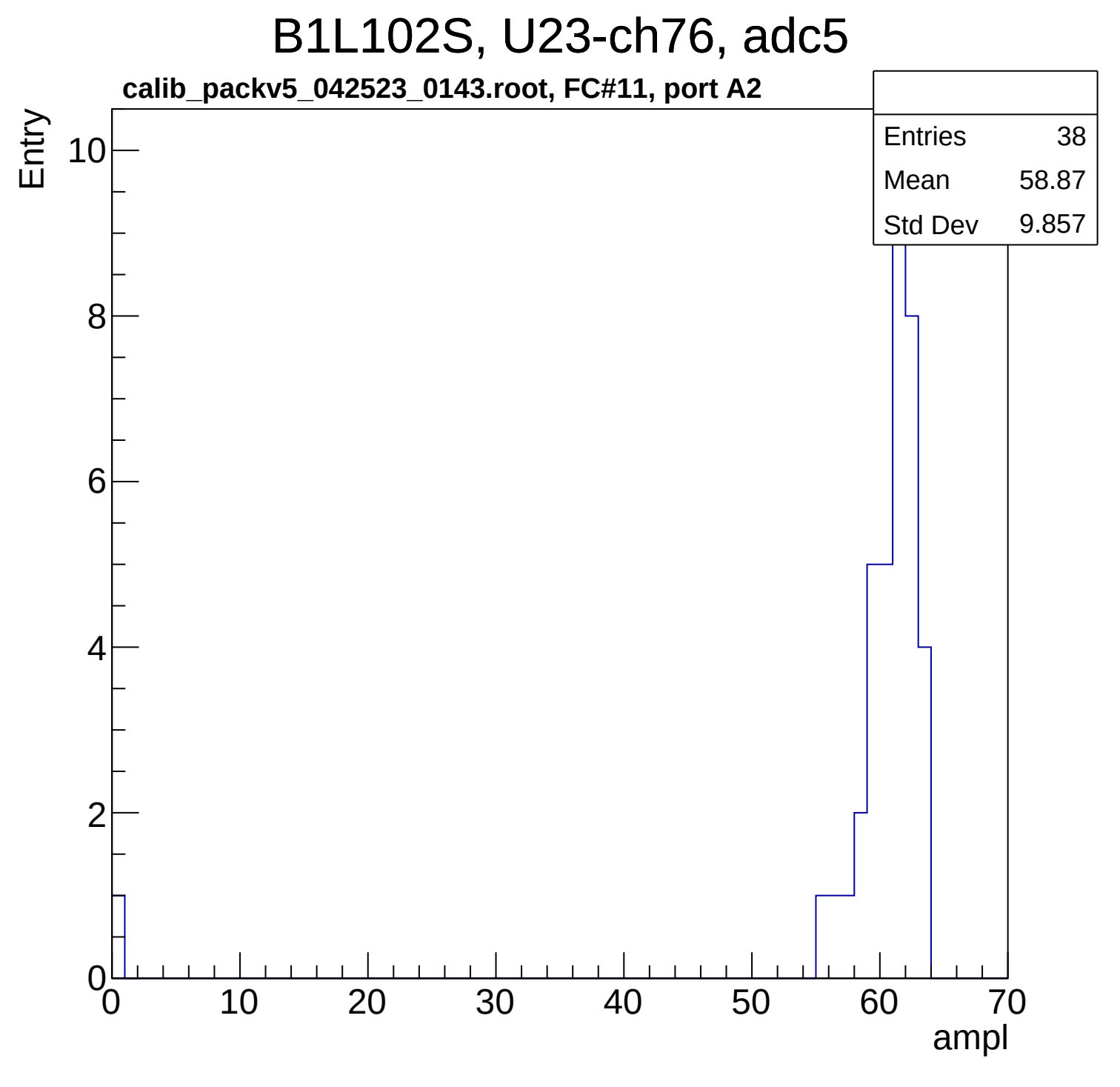

B1L102S, U23-ch76, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 87
Mean 28.26
Std Dev 3.662
Gaus mean : 28.6463
Gaus Width: 3.6501
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

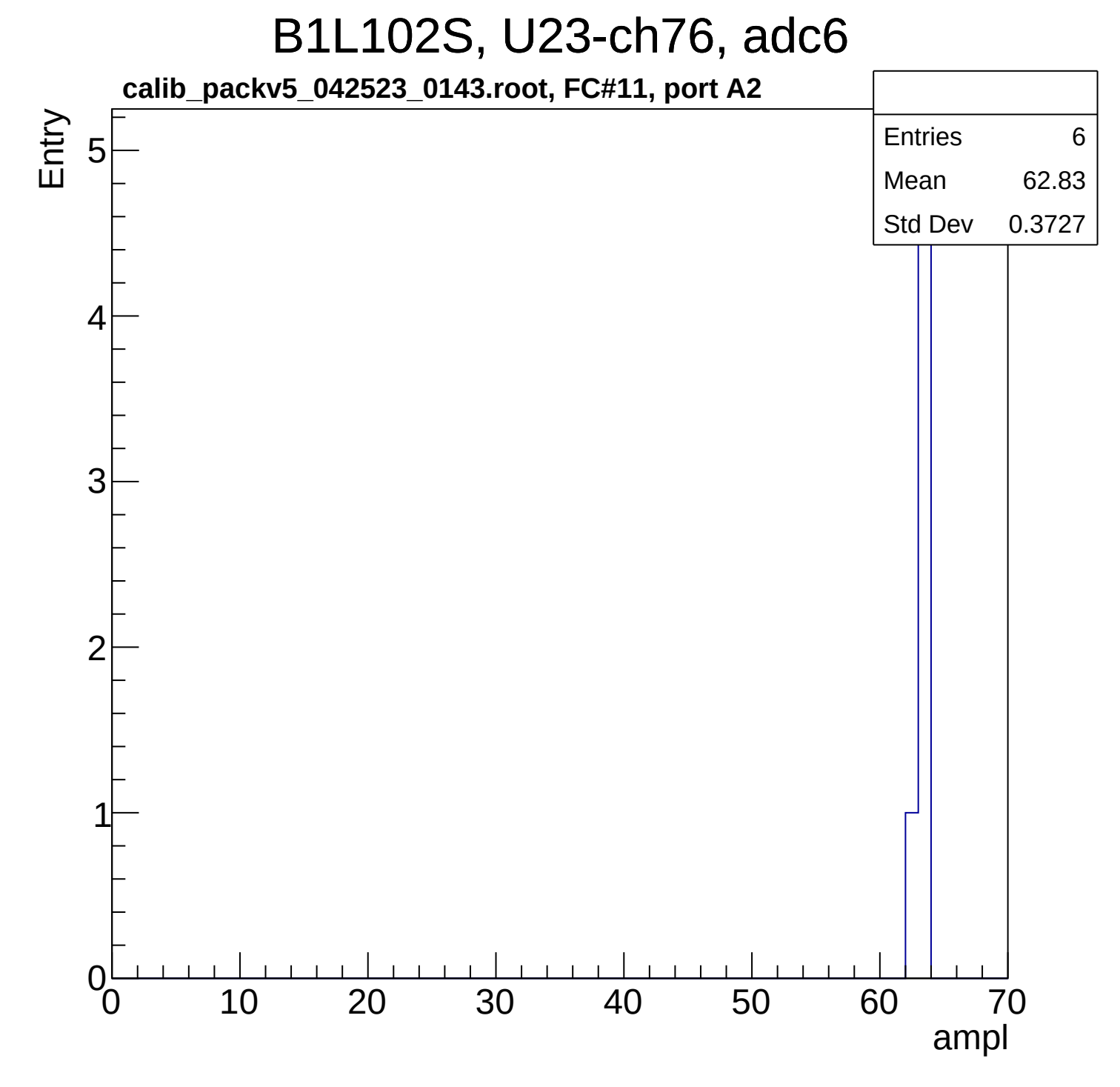

B1L102S, U23-ch76, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 36.62
Std Dev 3.556
Gaus mean : 37.4737
Gaus Width: 3.8120
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

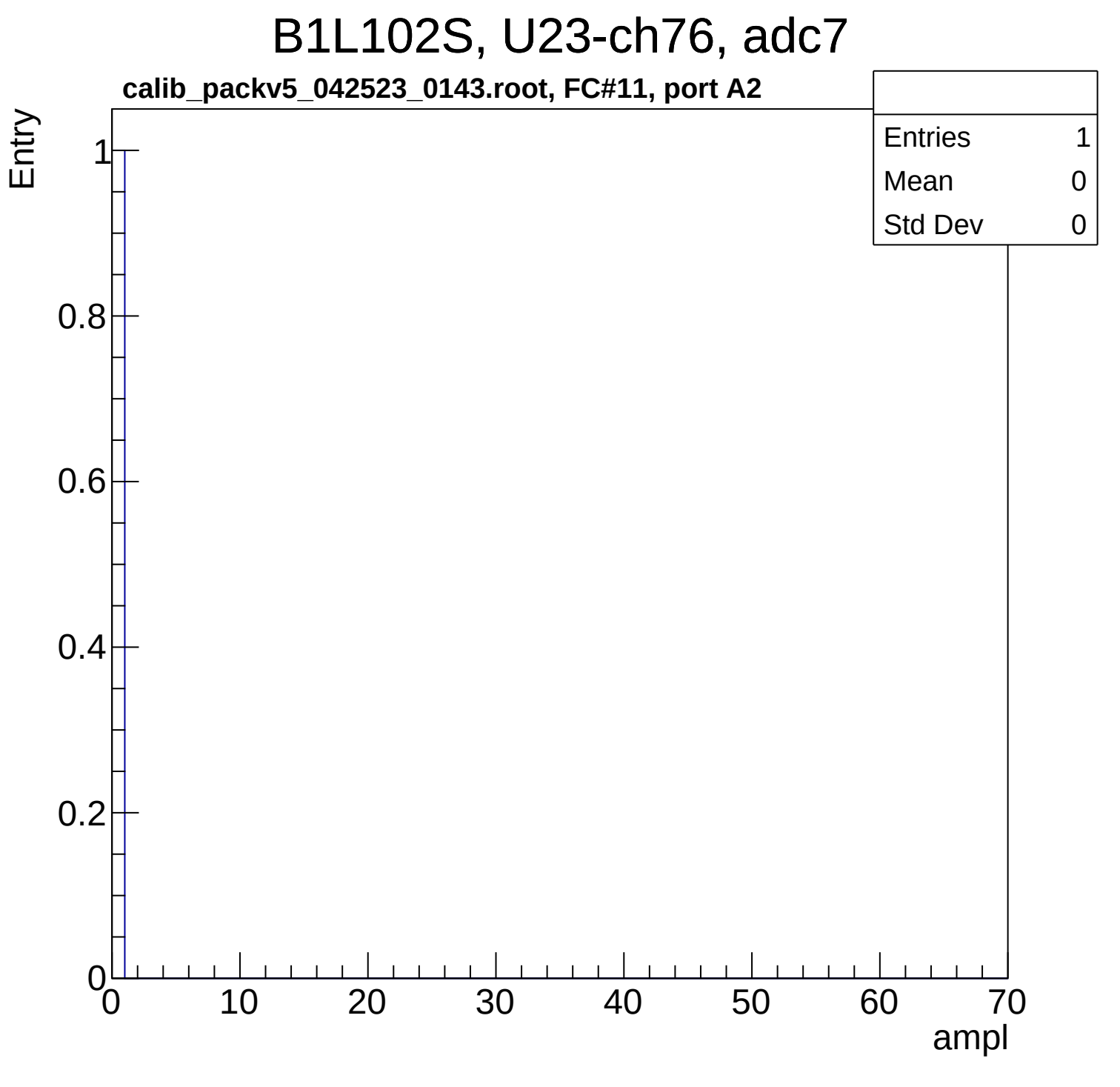

B1L102S, U23-ch76, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 43.7
Std Dev 3.785
Gaus mean : 44.4724
Gaus Width: 3.4509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

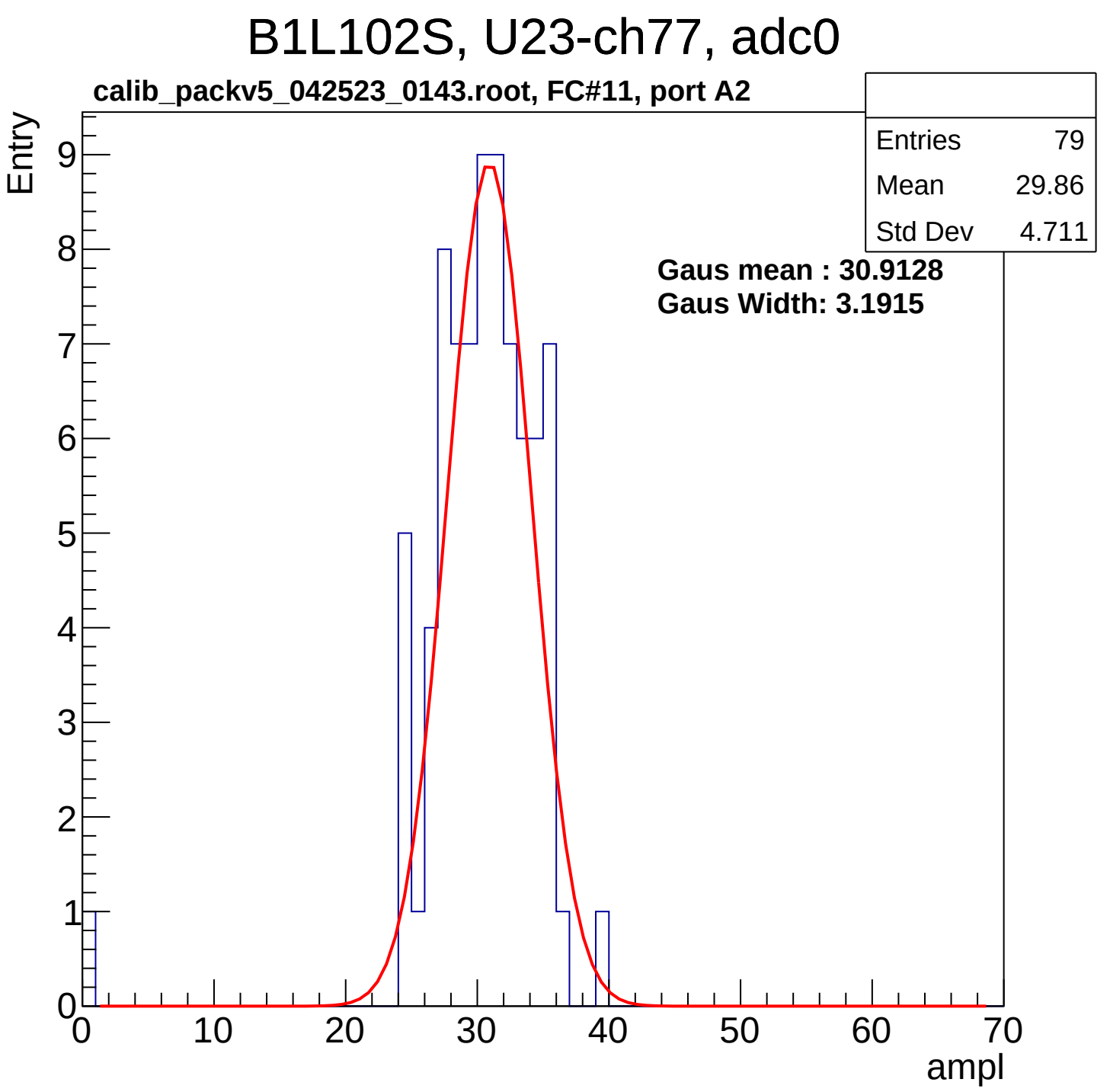

B1L102S, U23-ch76, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 49.91
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

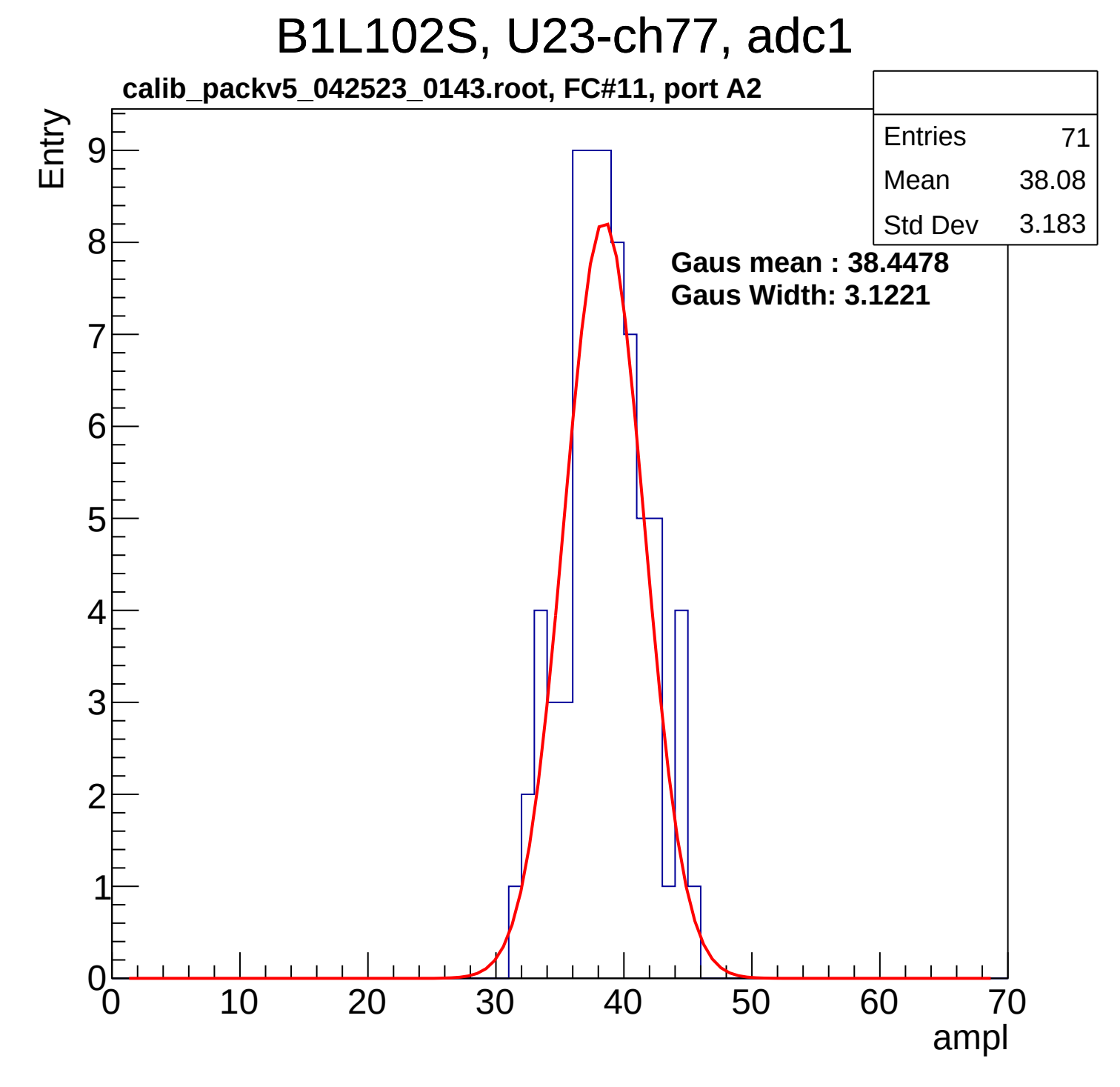

B1L102S, U23-ch76, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 55.72
Std Dev 3.028
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

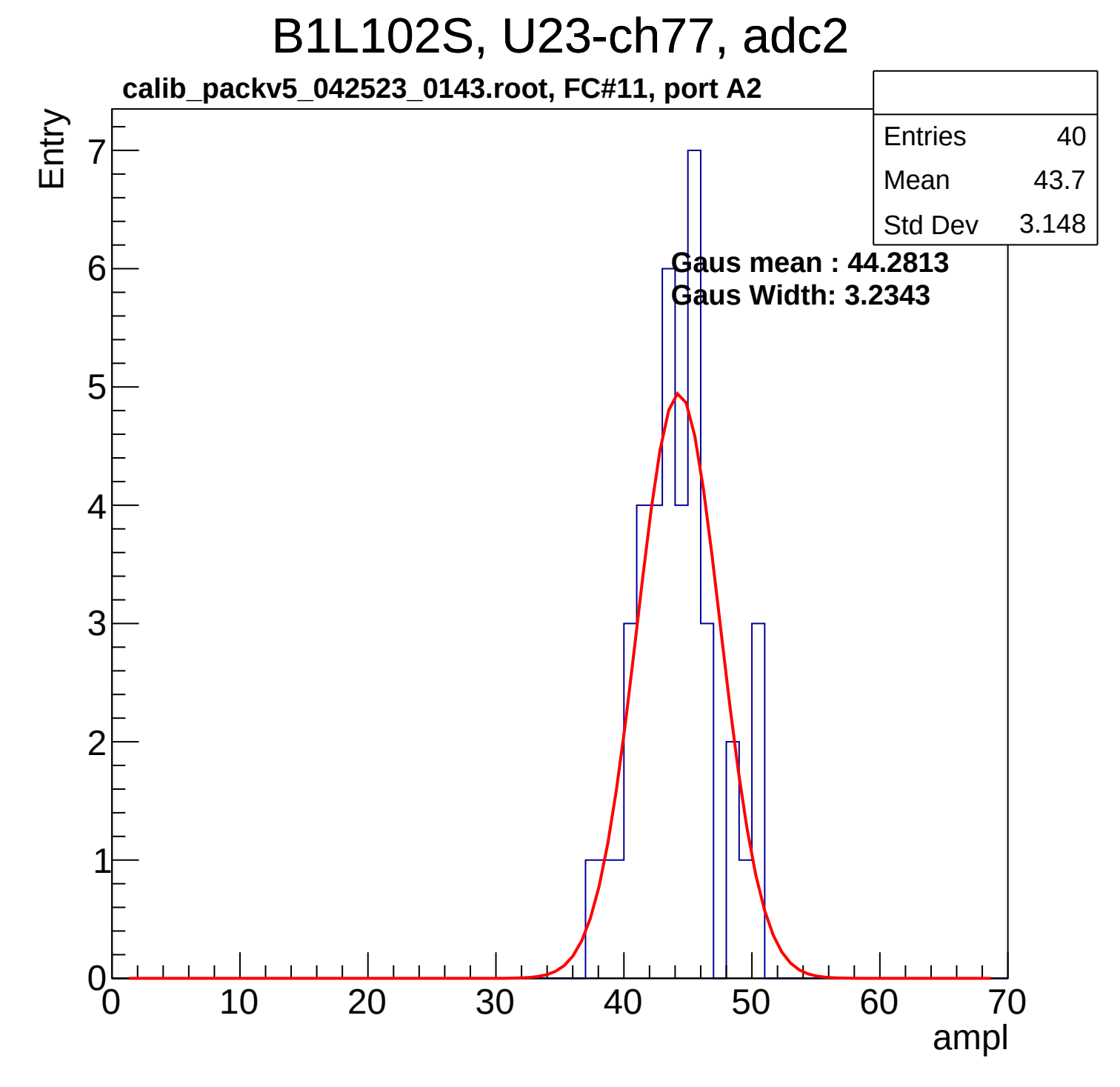

B1L102S, U23-ch76, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.87
Std Dev 9.857
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

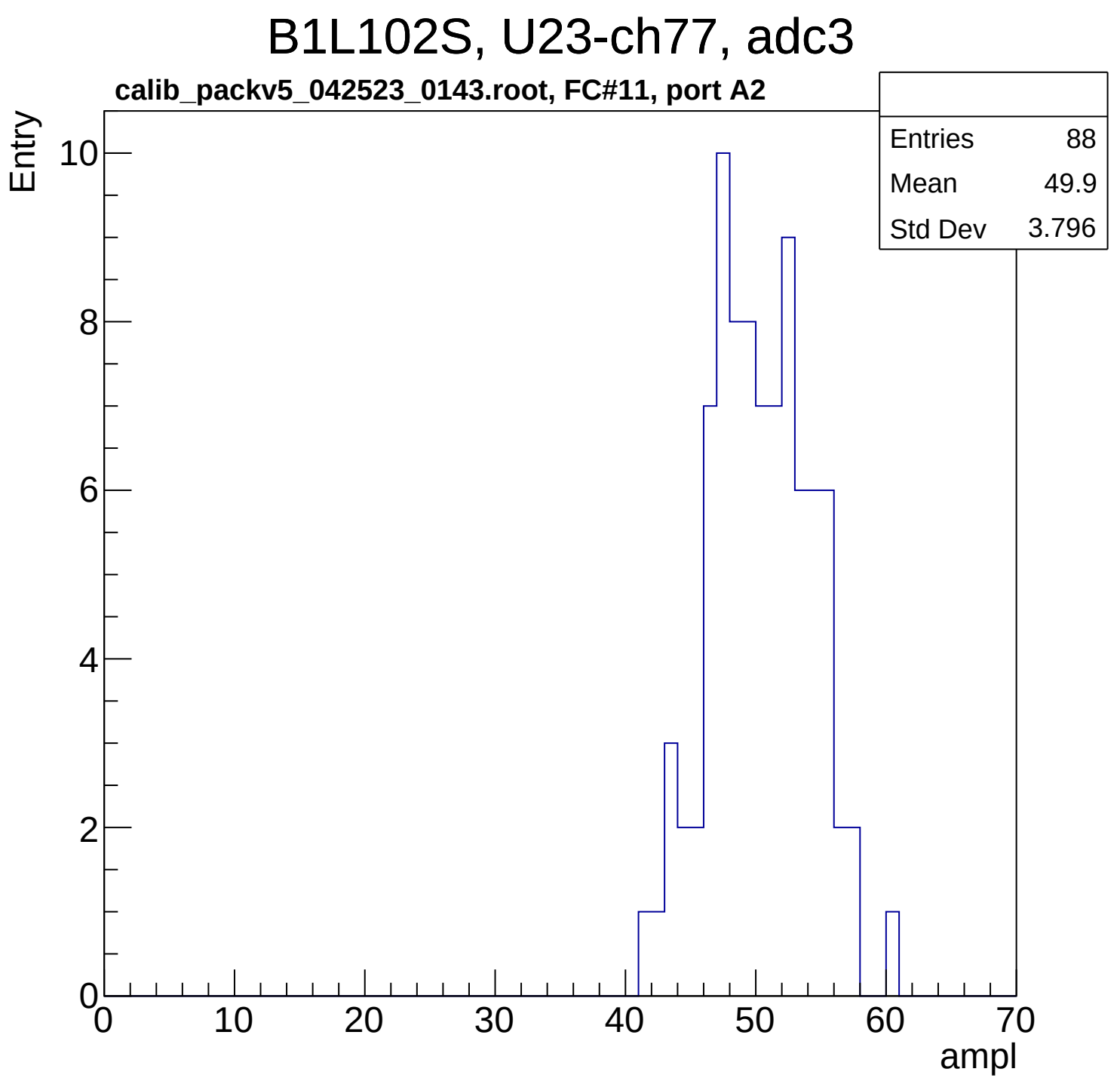

B1L102S, U23-ch76, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

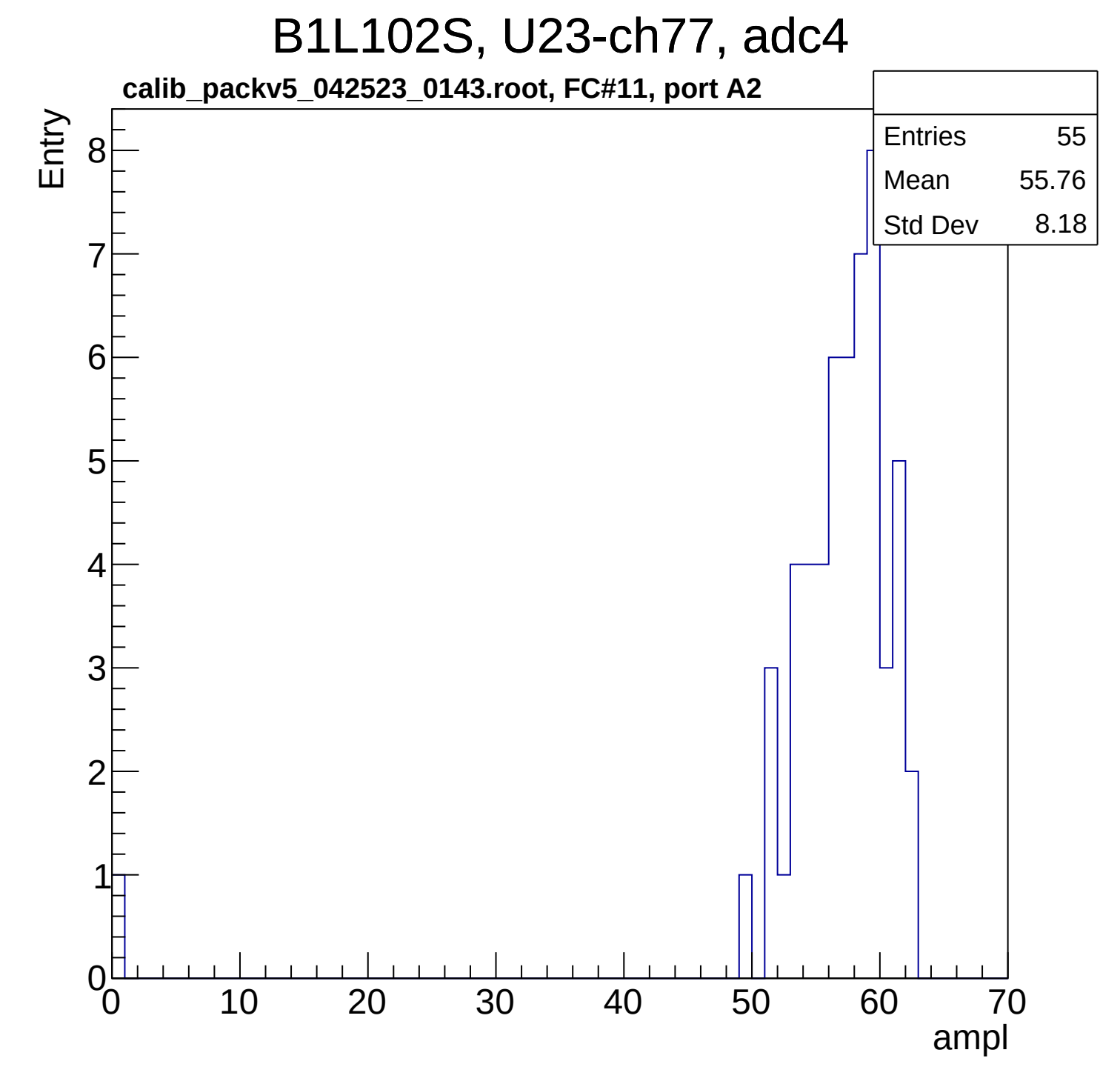

B1L102S, U23-ch76, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

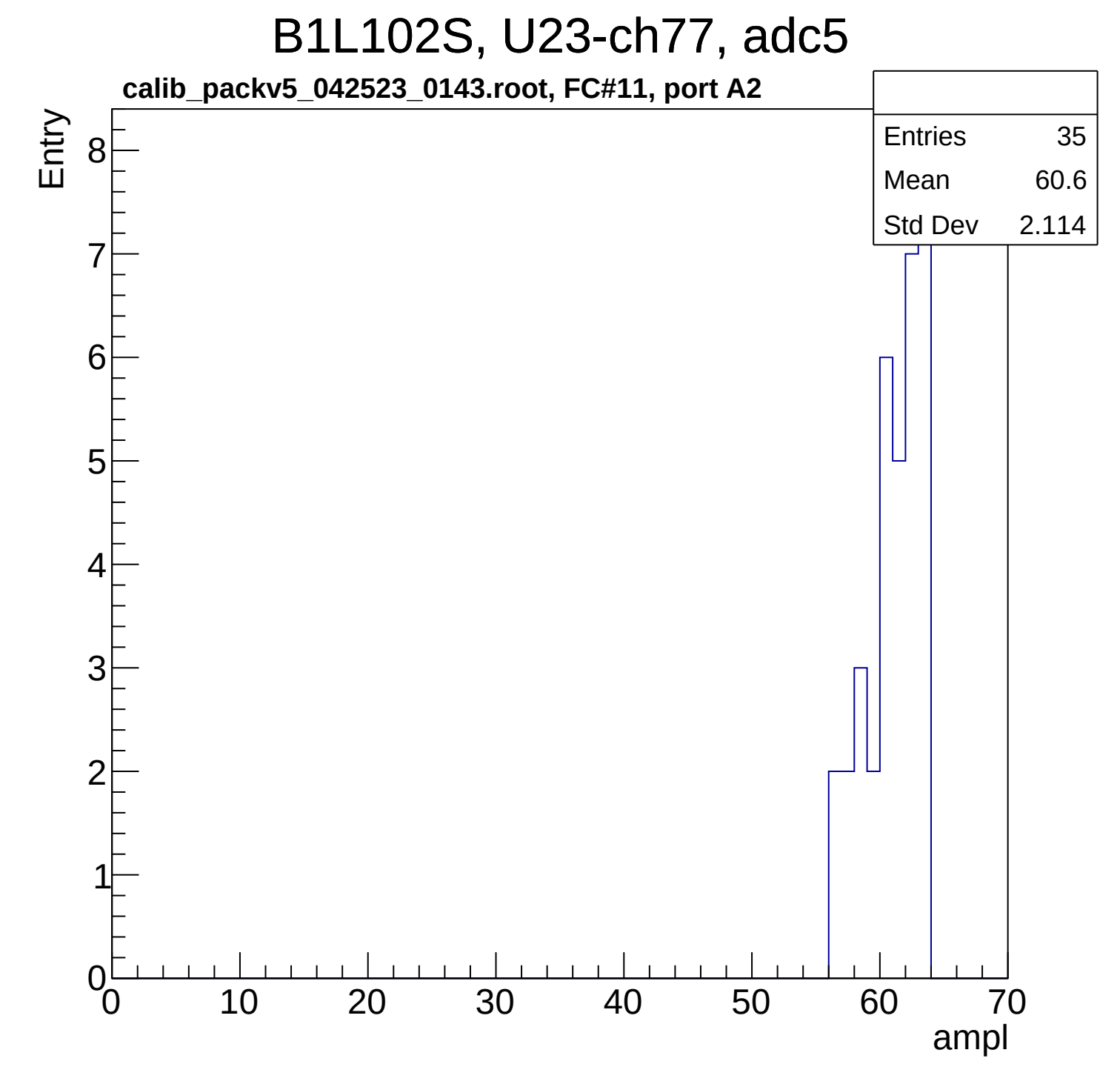

B1L102S, U23-ch77, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 29.86
Std Dev 4.711
Gaus mean : 30.9128
Gaus Width: 3.1915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

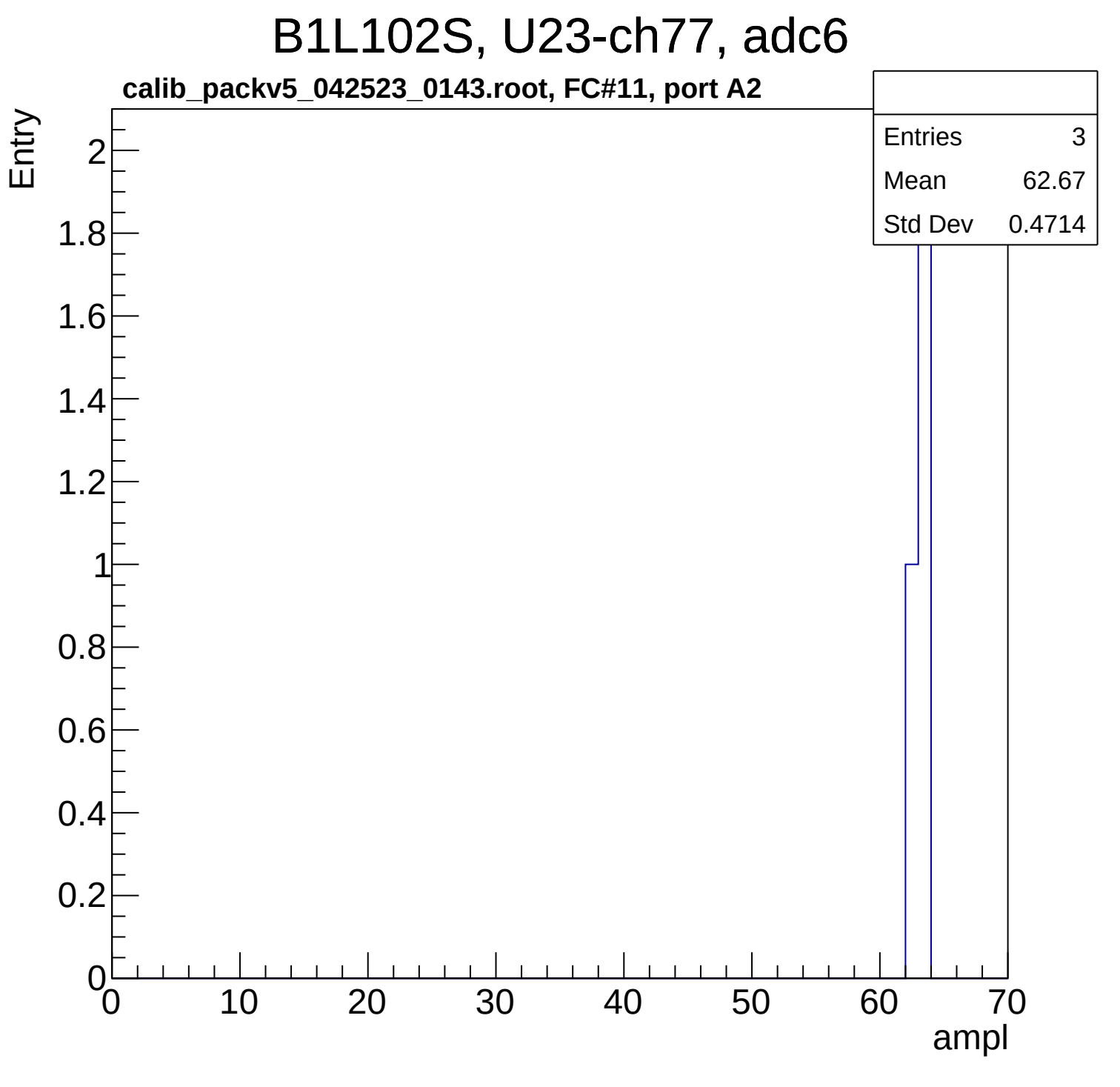

B1L102S, U23-ch77, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 38.08
Std Dev 3.183
Gaus mean : 38.4478
Gaus Width: 3.1221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

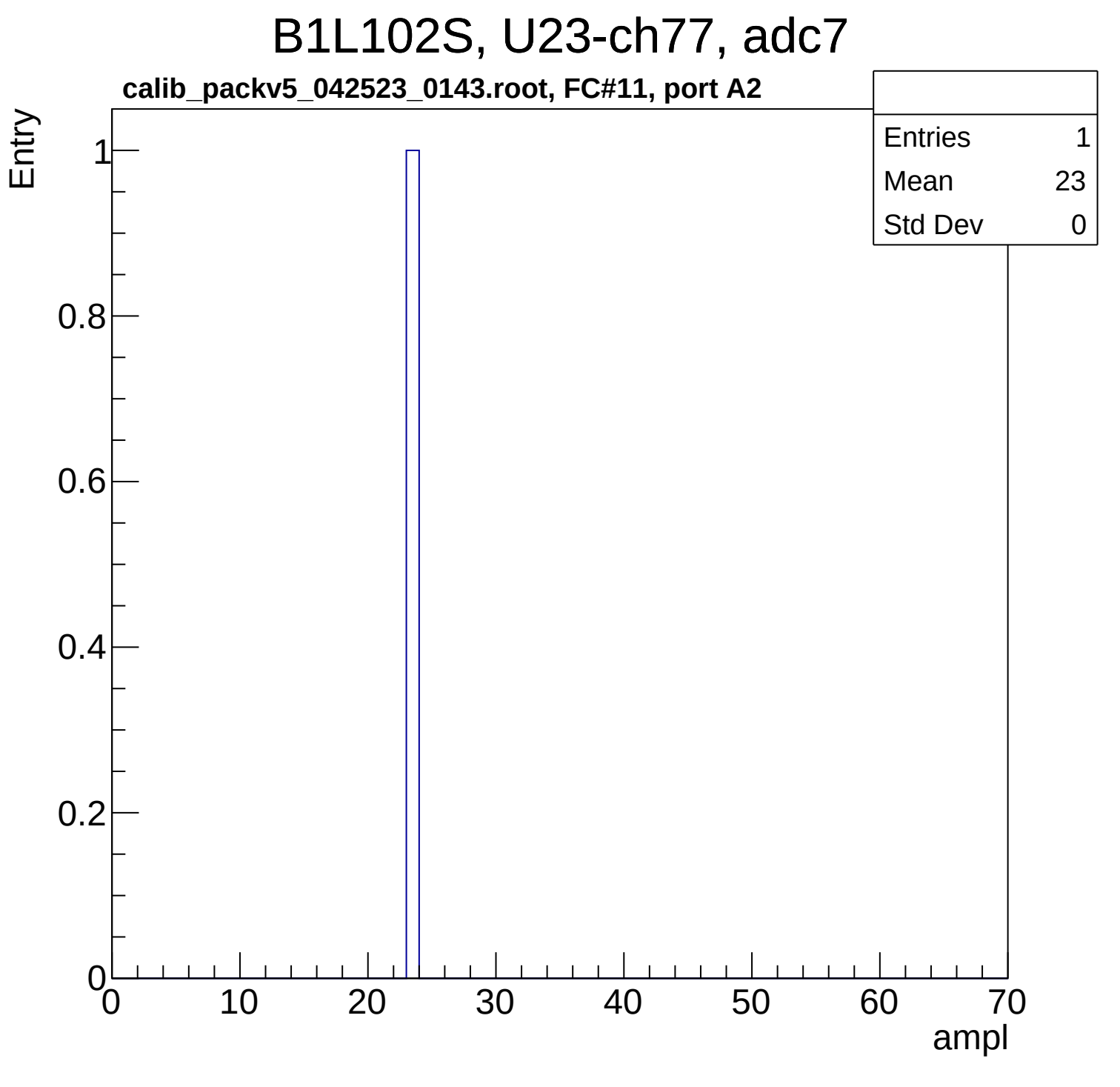

B1L102S, U23-ch77, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 43.7
Std Dev 3.148
Gaus mean : 44.2813
Gaus Width: 3.2343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

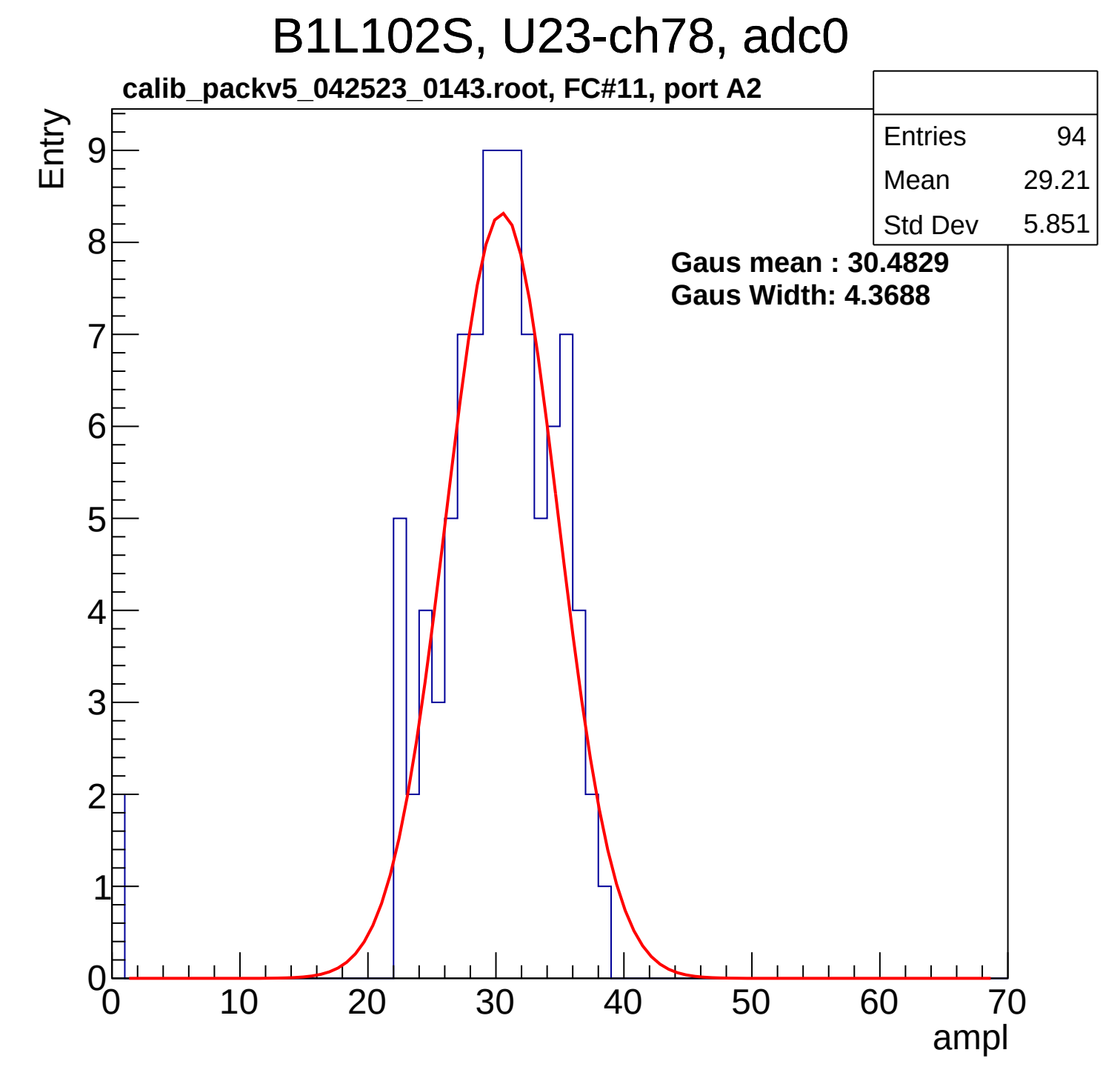

B1L102S, U23-ch77, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 49.9
Std Dev 3.796
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

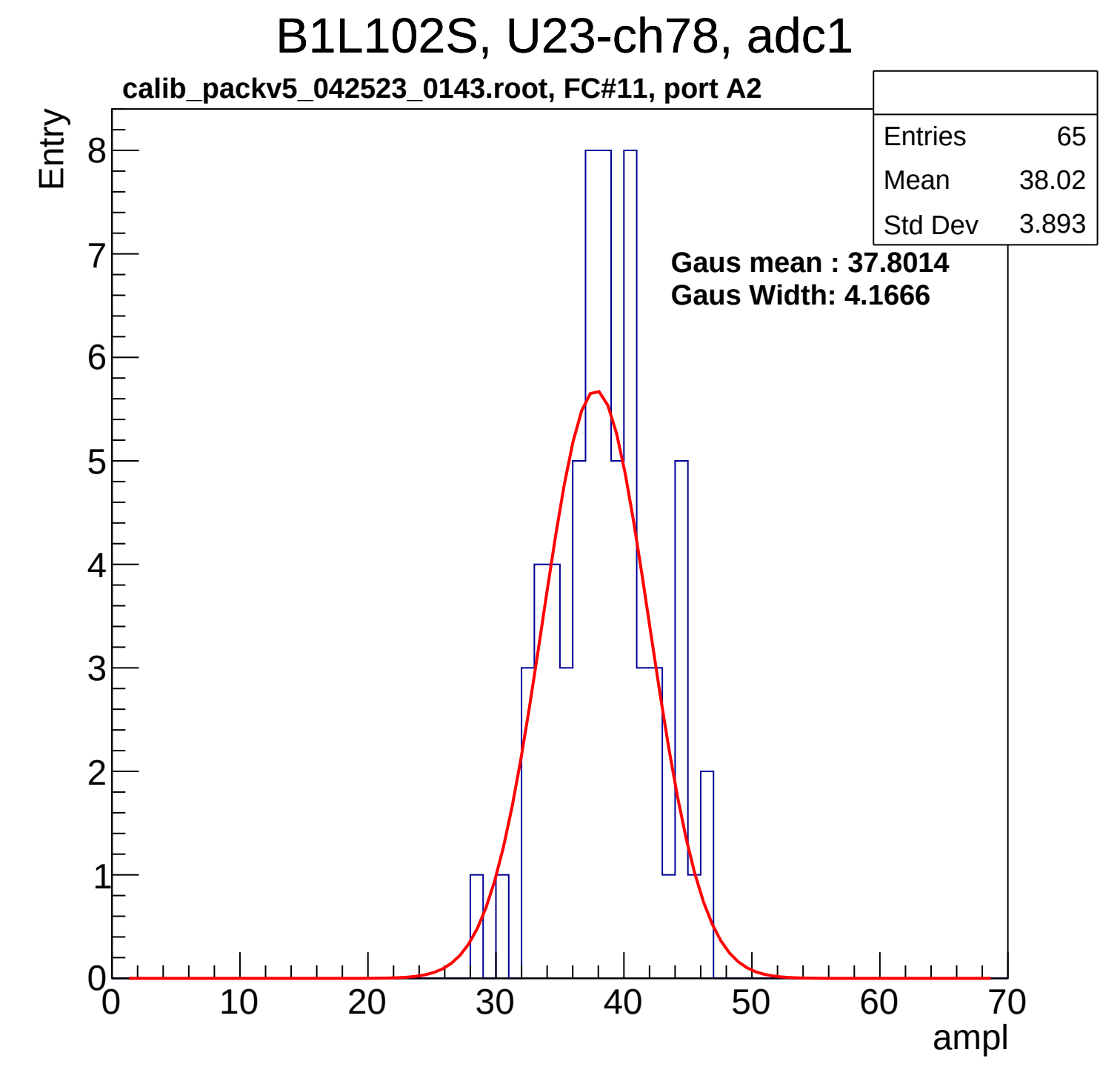

B1L102S, U23-ch77, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 55.76
Std Dev 8.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

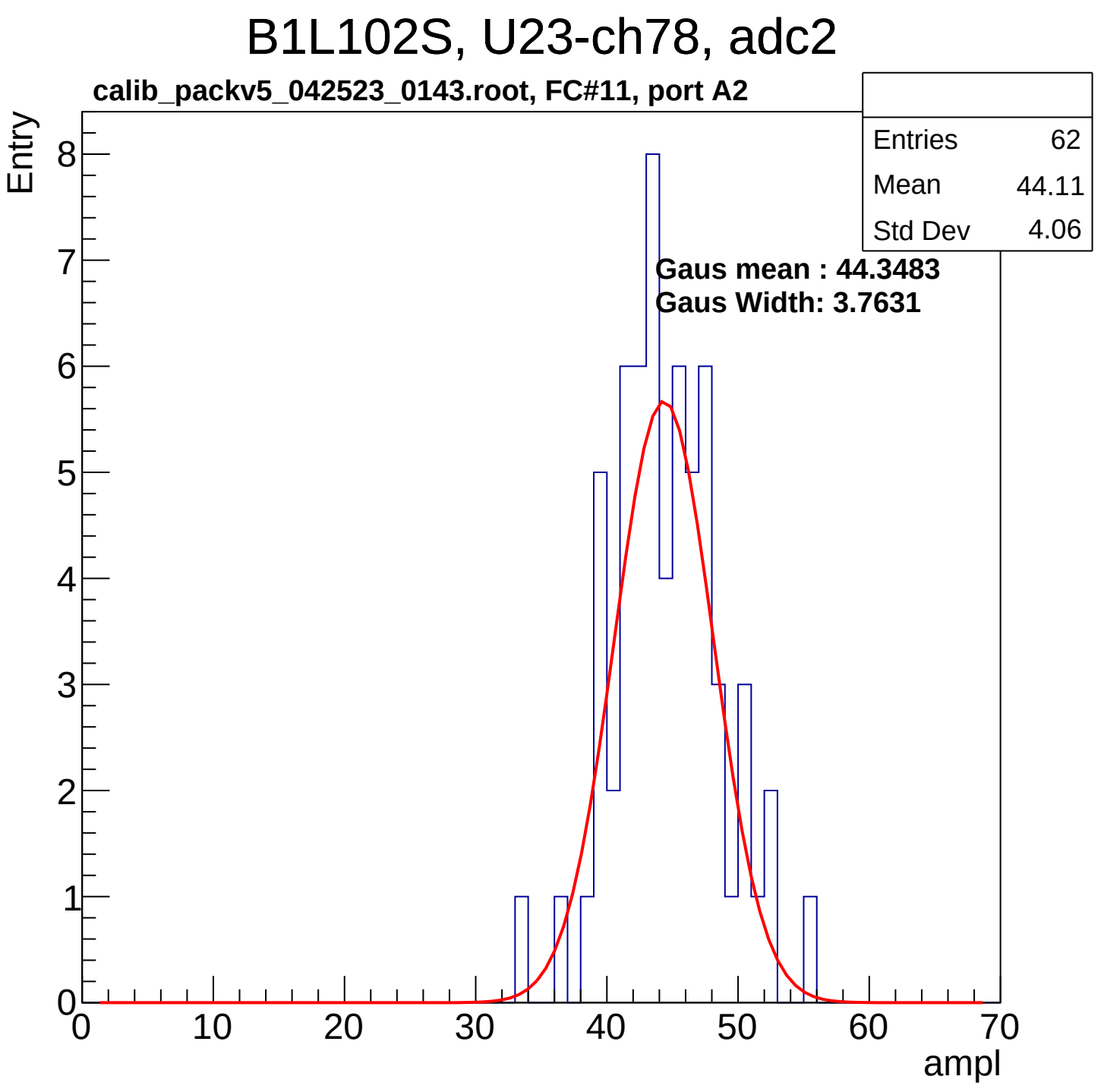

B1L102S, U23-ch77, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 60.6
Std Dev 2.114
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

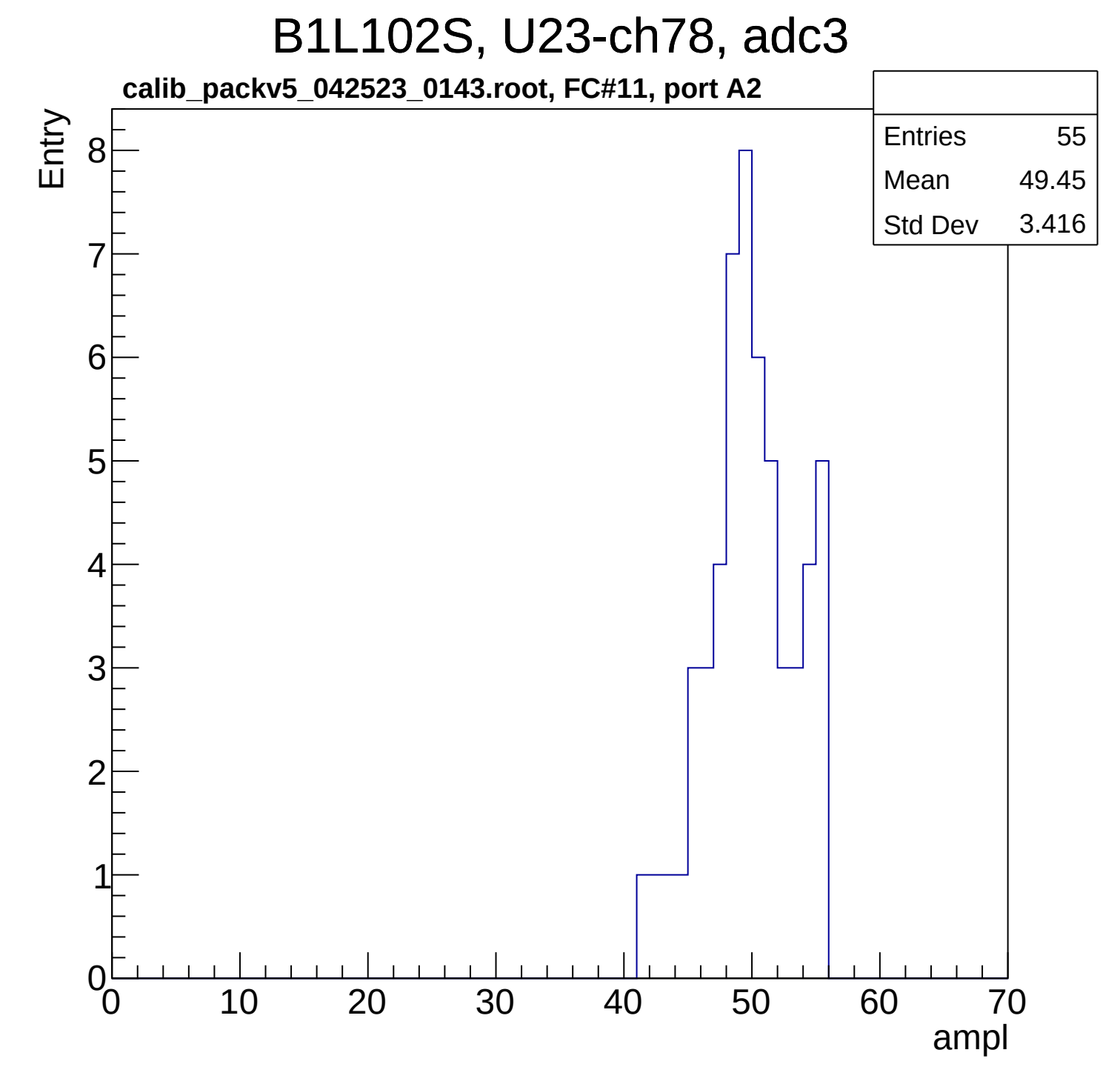

B1L102S, U23-ch77, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

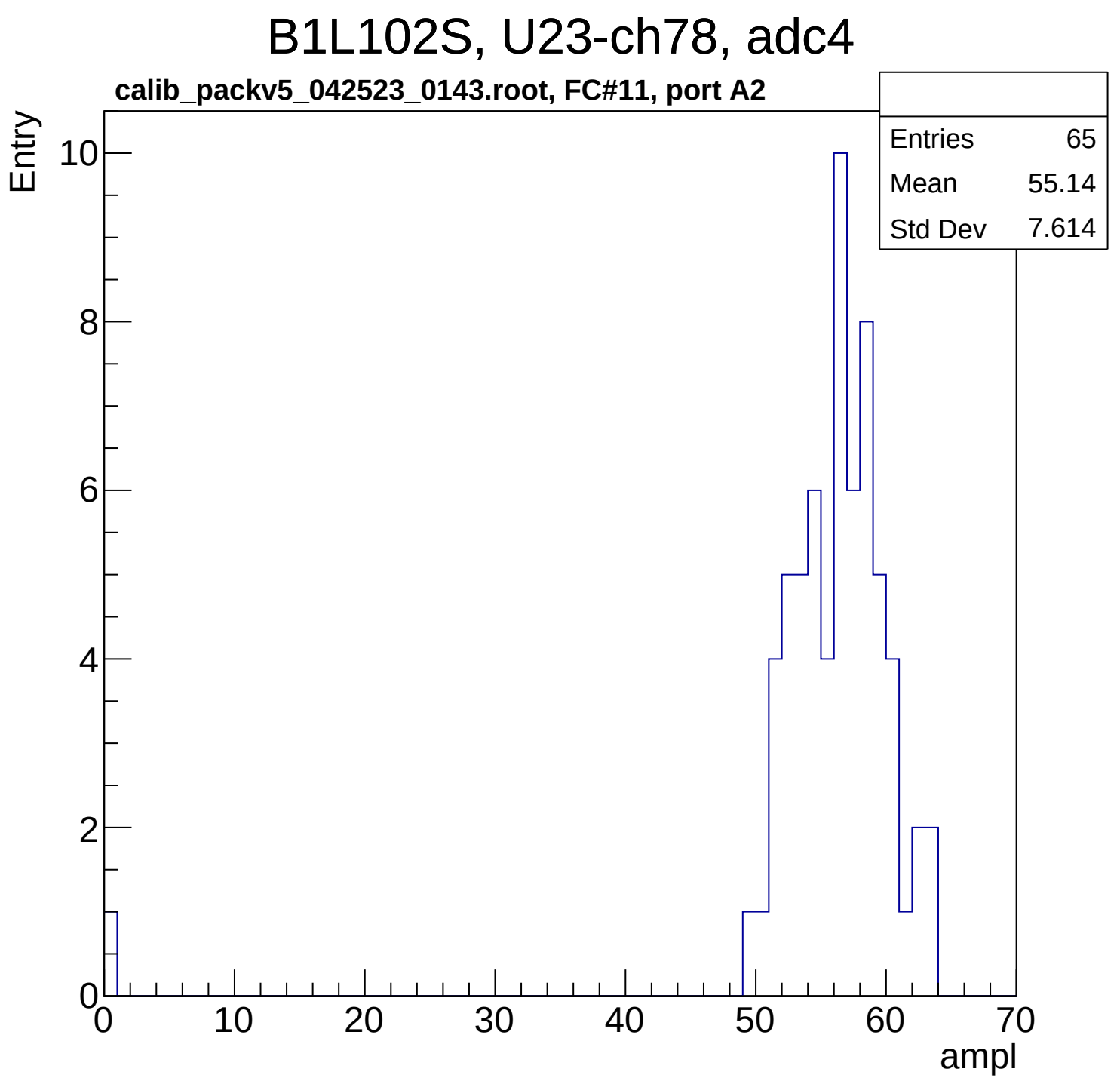

B1L102S, U23-ch77, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

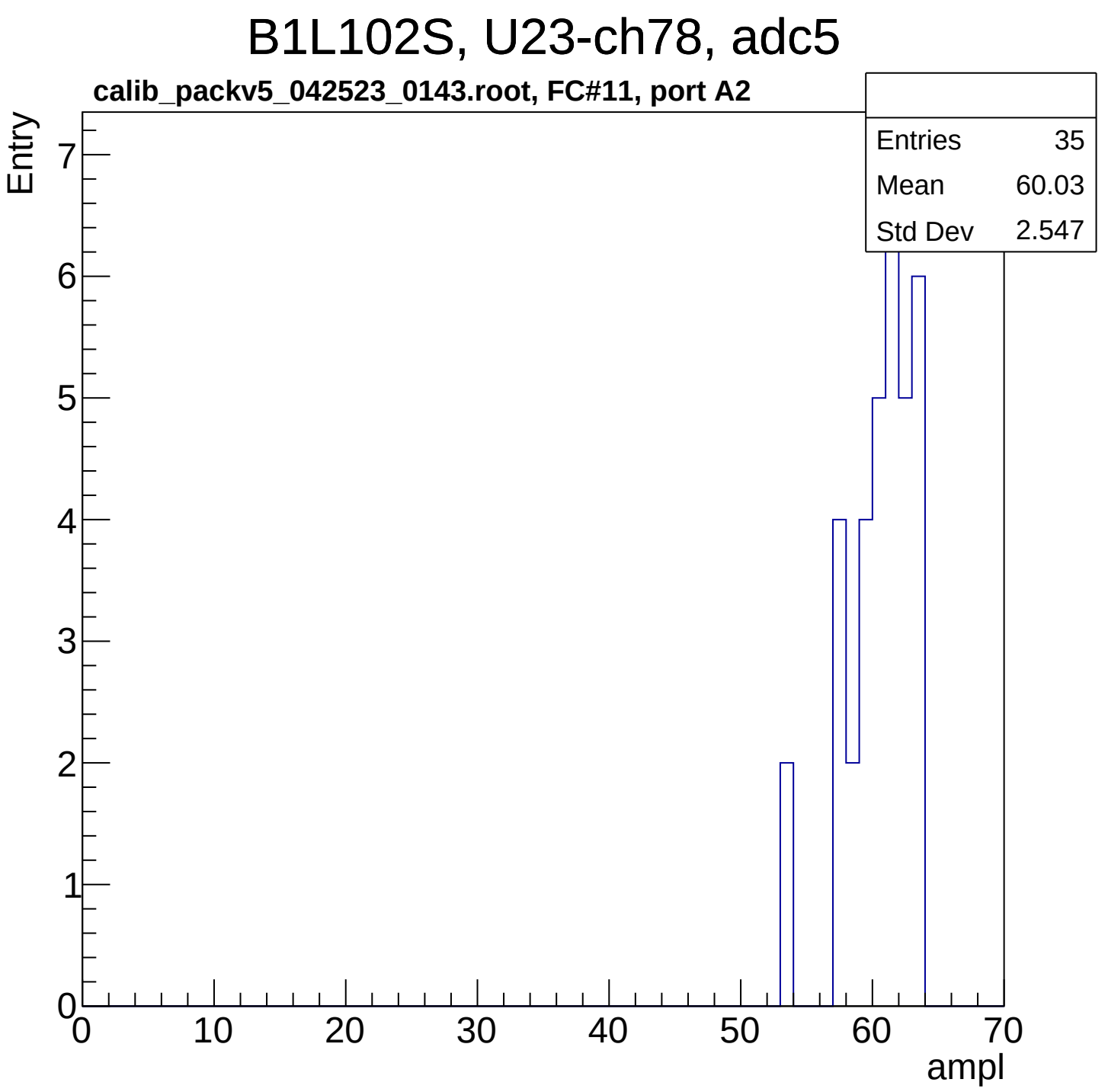

B1L102S, U23-ch78, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 94
Mean 29.21
Std Dev 5.851
Gaus mean : 30.4829
Gaus Width: 4.3688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

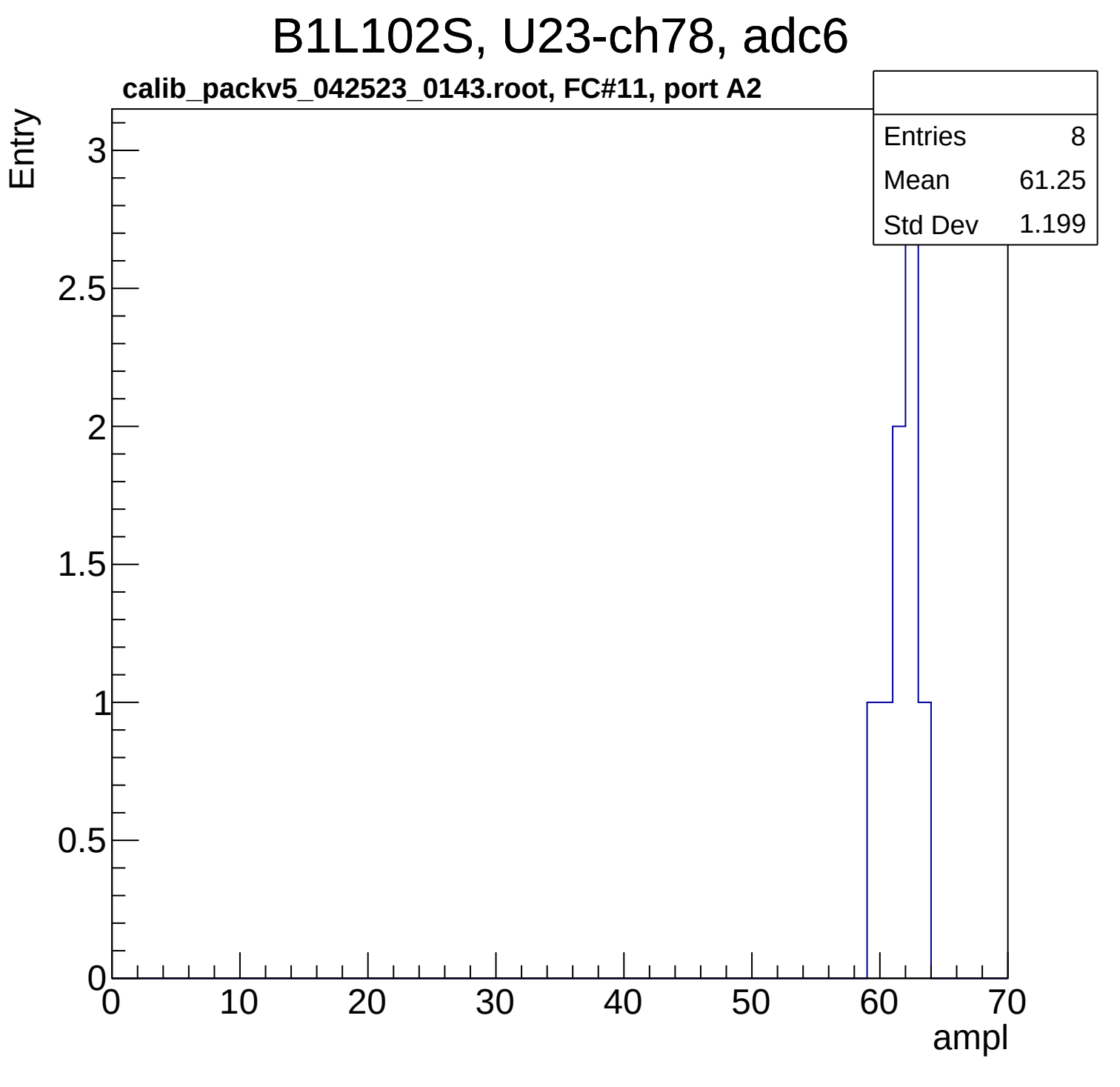

B1L102S, U23-ch78, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 38.02
Std Dev 3.893
Gaus mean : 37.8014
Gaus Width: 4.1666
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

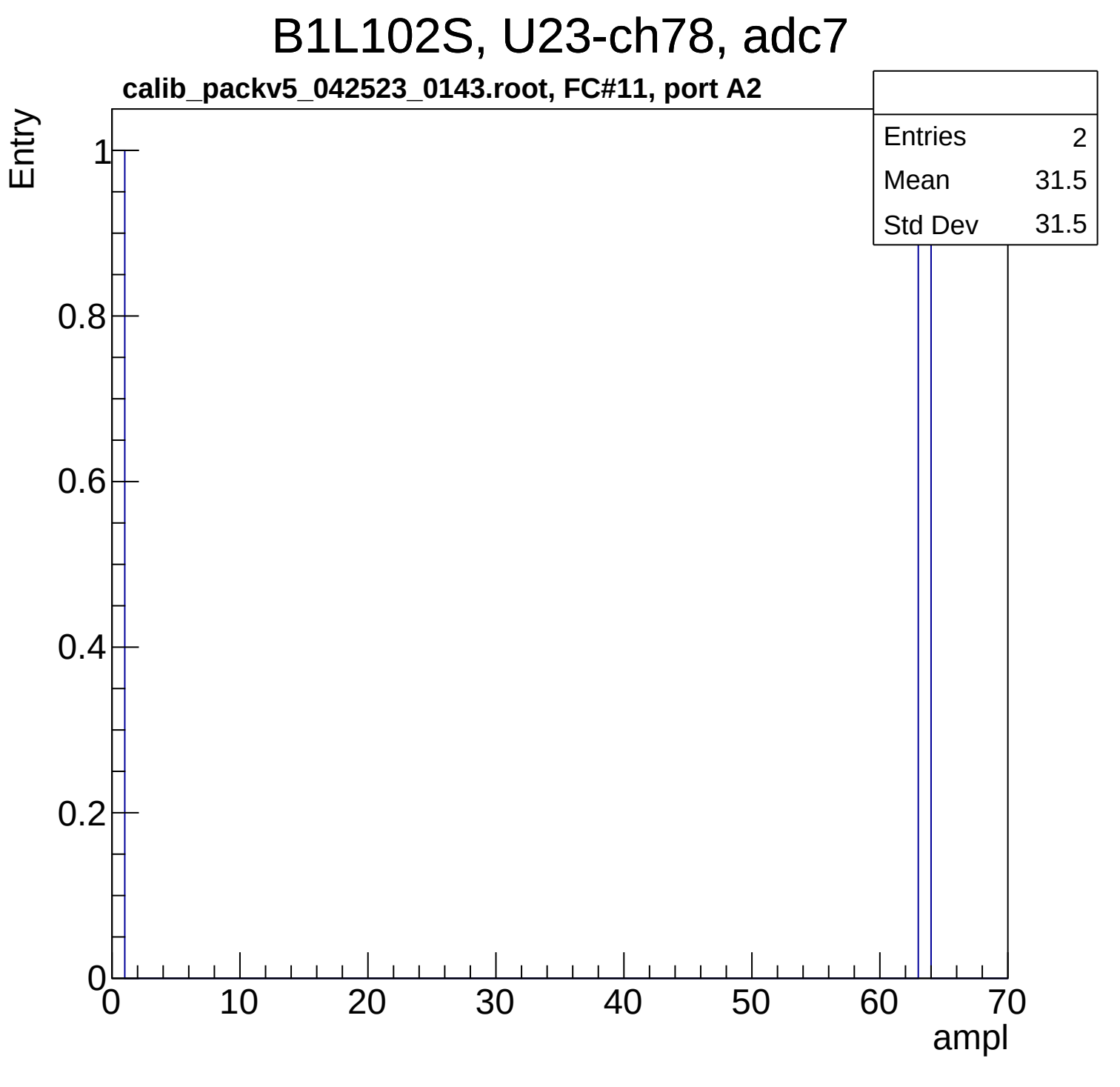

B1L102S, U23-ch78, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 44.11
Std Dev 4.06
Gaus mean : 44.3483
Gaus Width: 3.7631
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

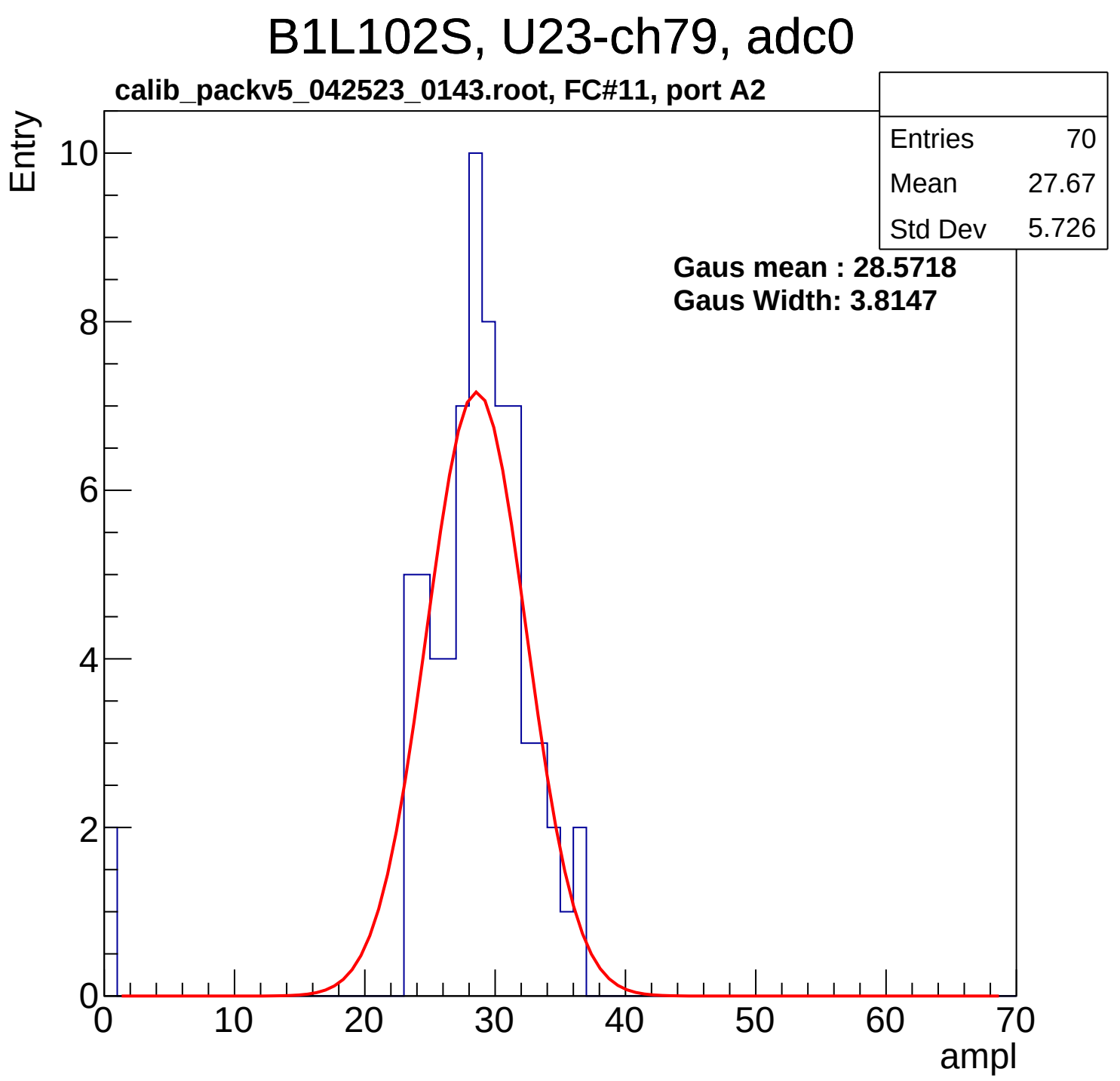

B1L102S, U23-ch78, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 49.45
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

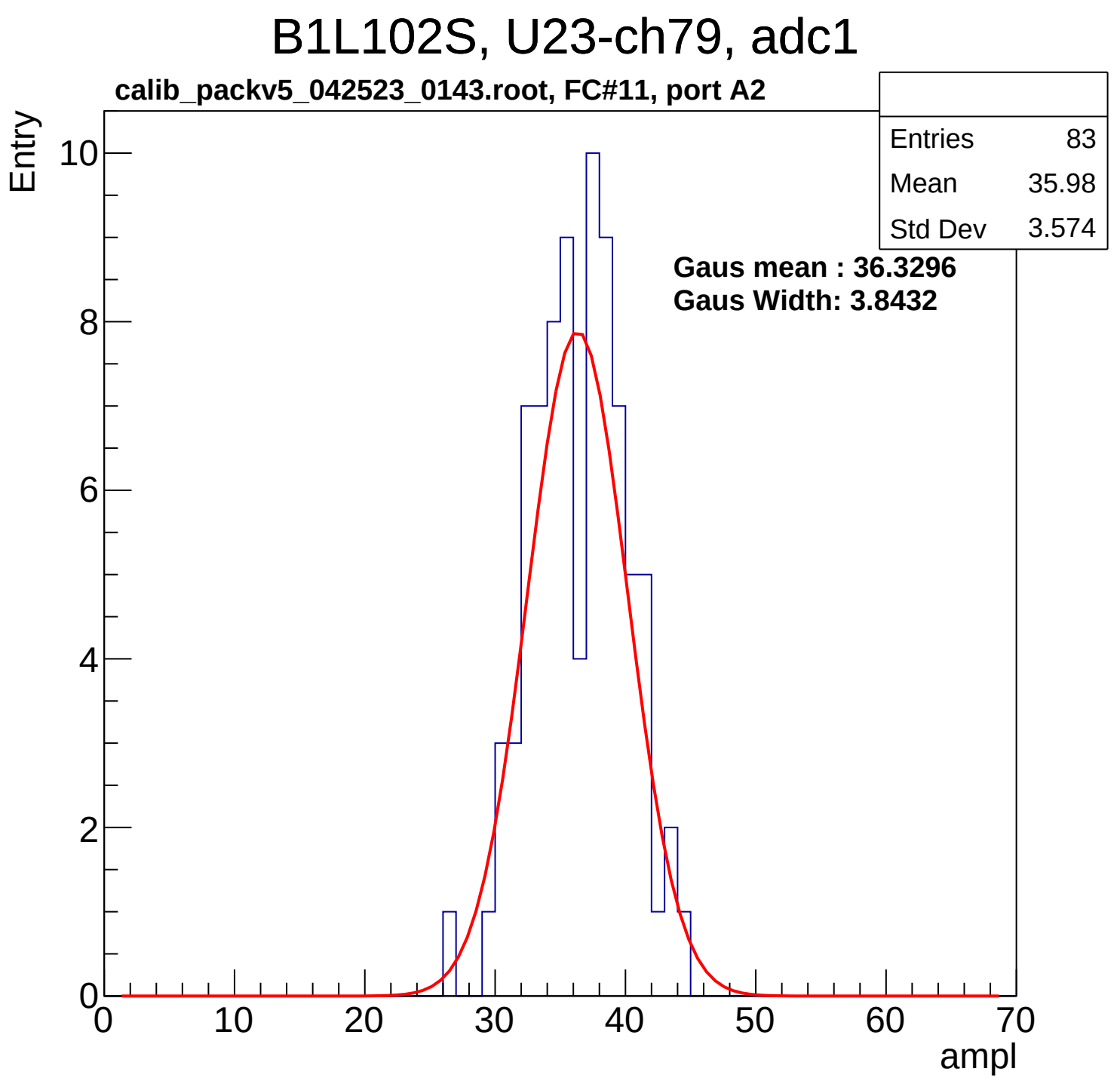

B1L102S, U23-ch78, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 55.14
Std Dev 7.614
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

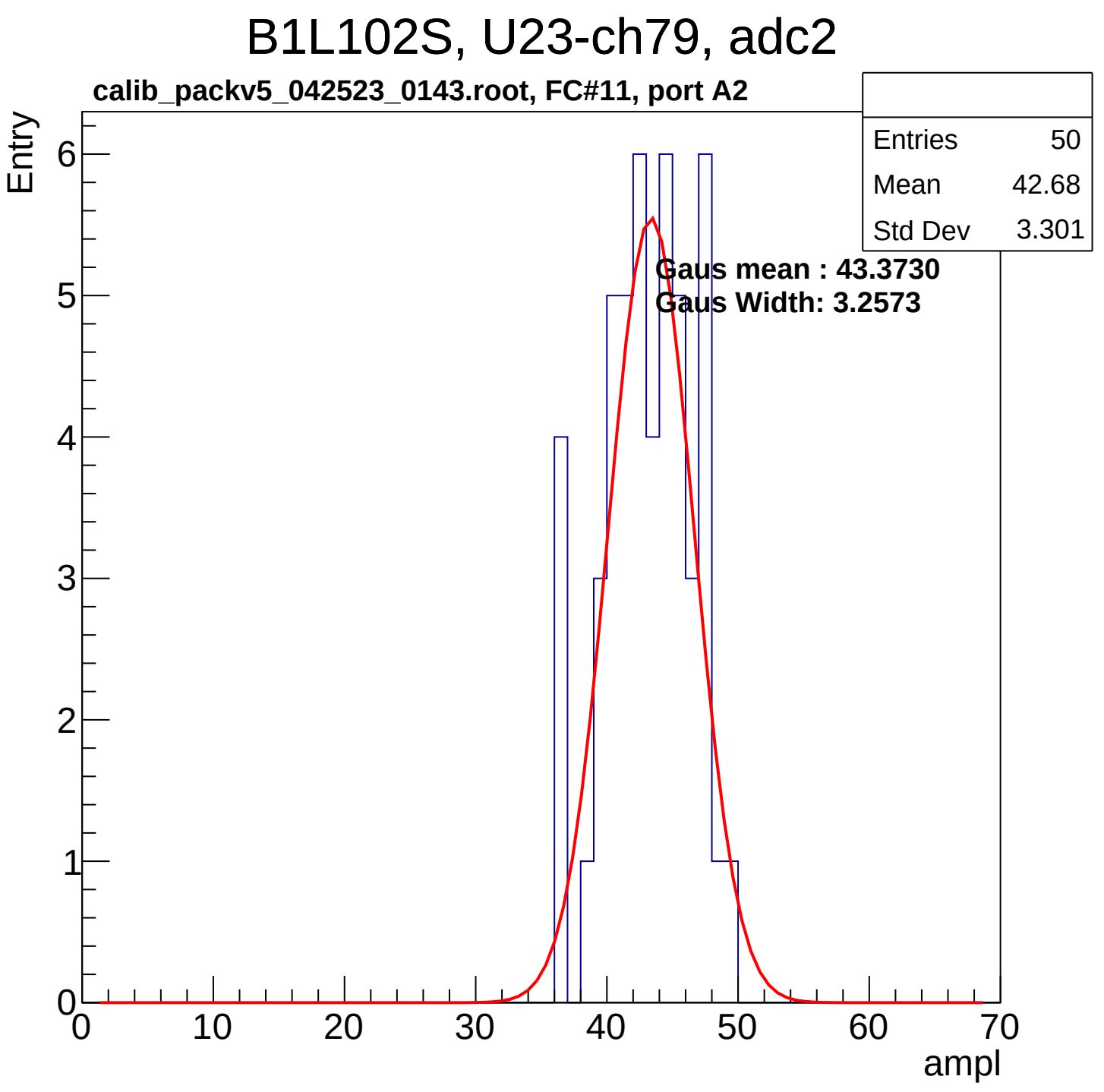

B1L102S, U23-ch78, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 60.03
Std Dev 2.547
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

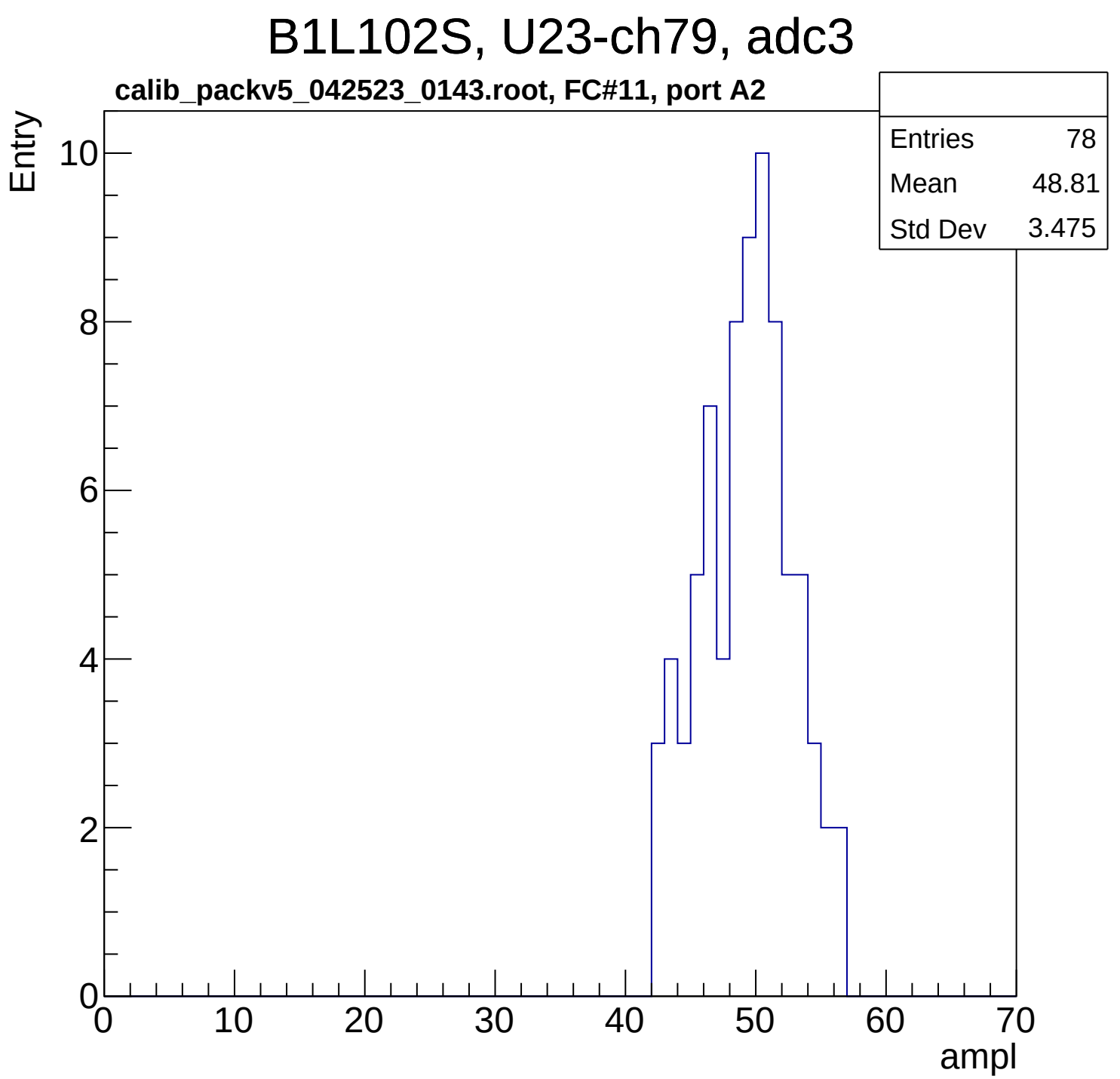

B1L102S, U23-ch78, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.25
Std Dev 1.199
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

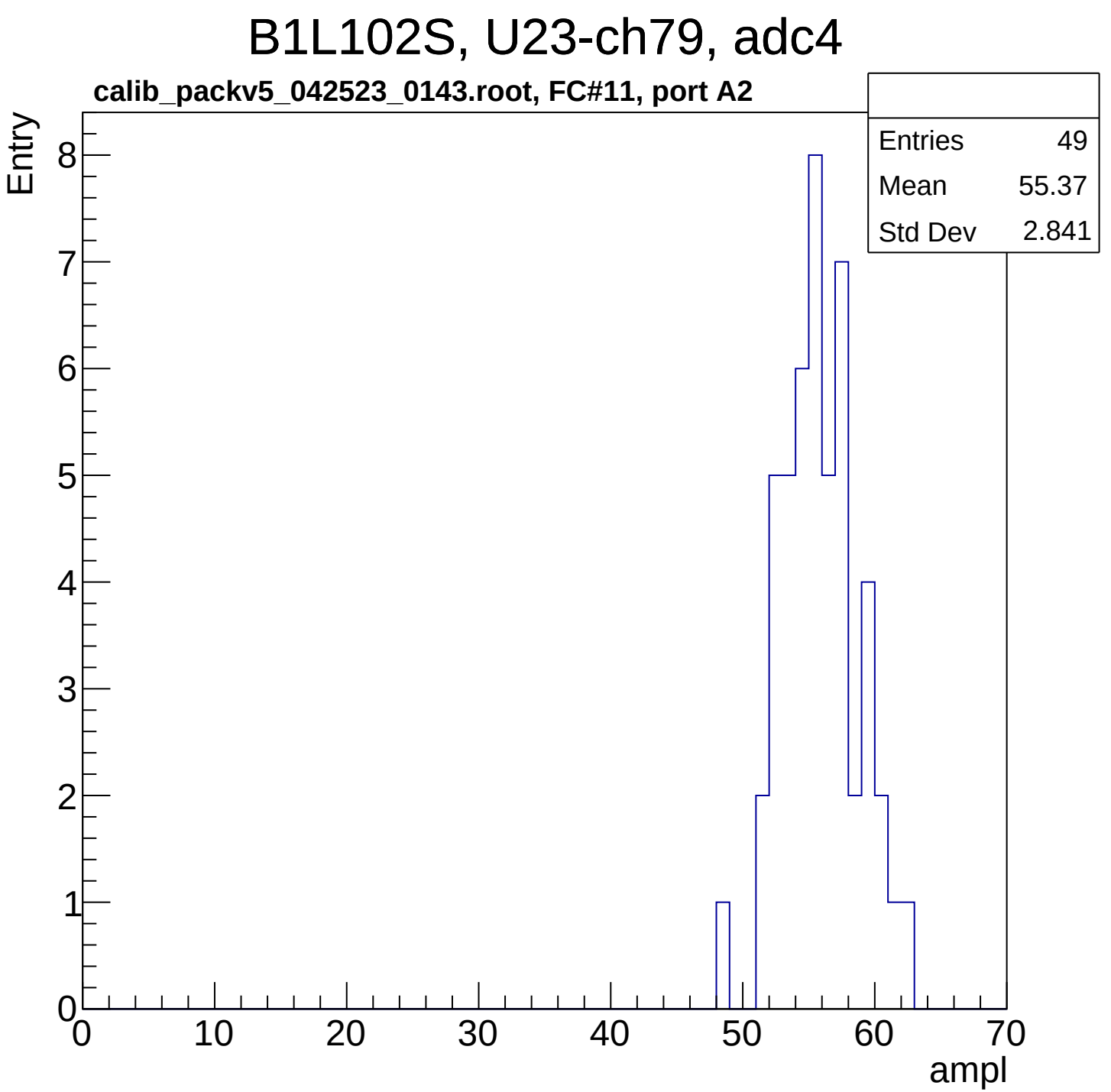

B1L102S, U23-ch78, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

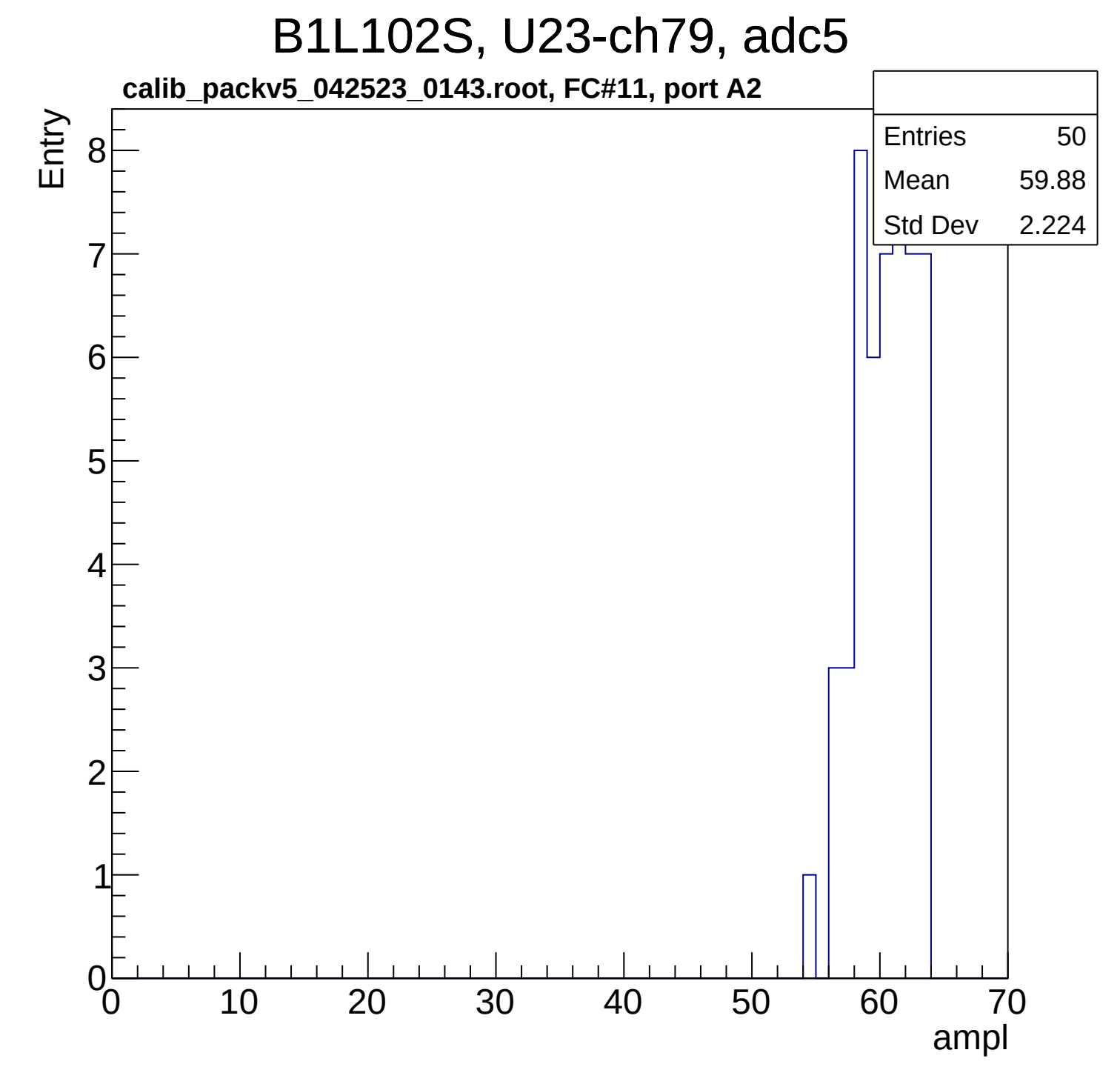

B1L102S, U23-ch79, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 27.67
Std Dev 5.726
Gaus mean : 28.5718
Gaus Width: 3.8147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

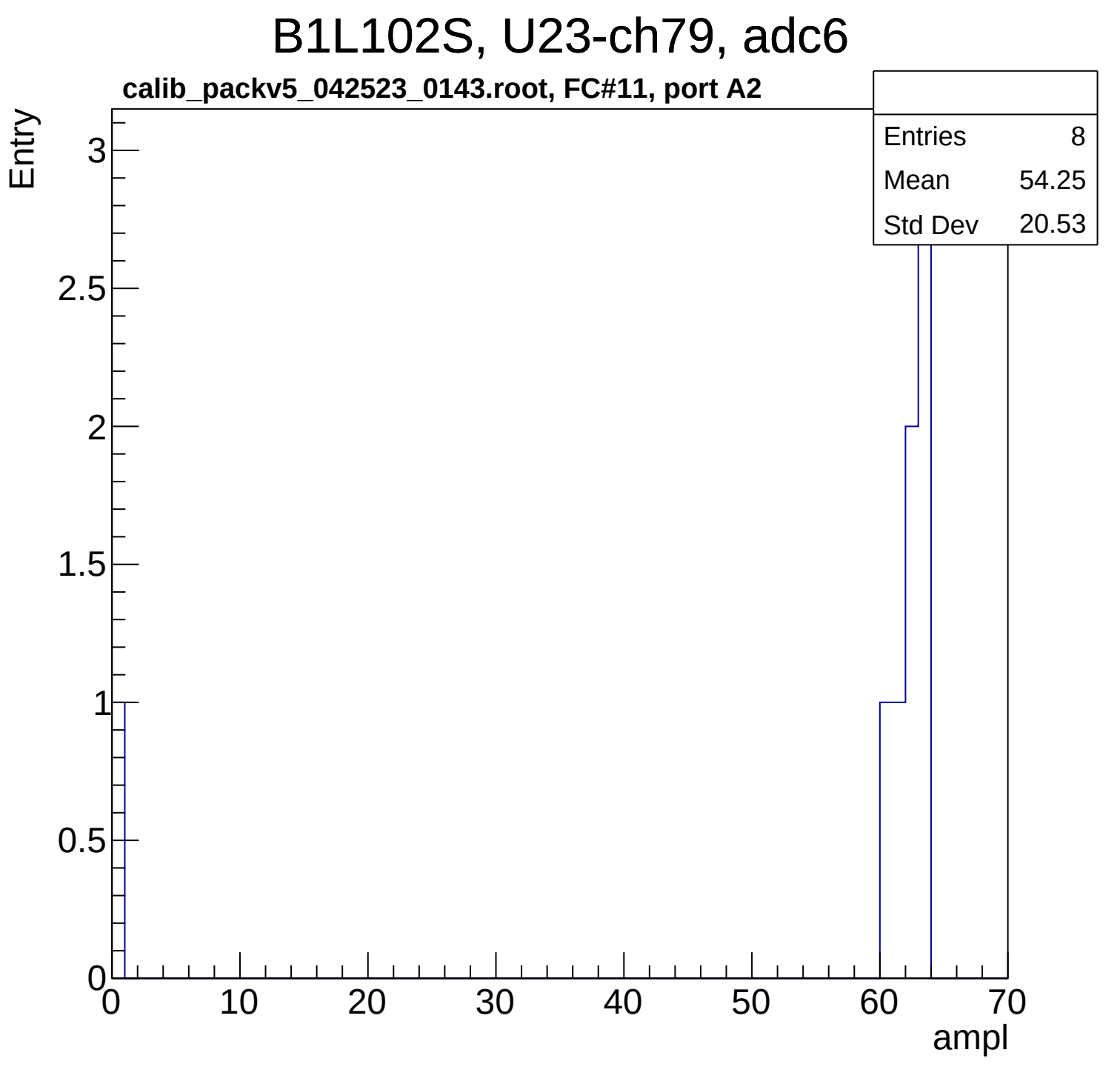

B1L102S, U23-ch79, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 35.98
Std Dev 3.574
Gaus mean : 36.3296
Gaus Width: 3.8432
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

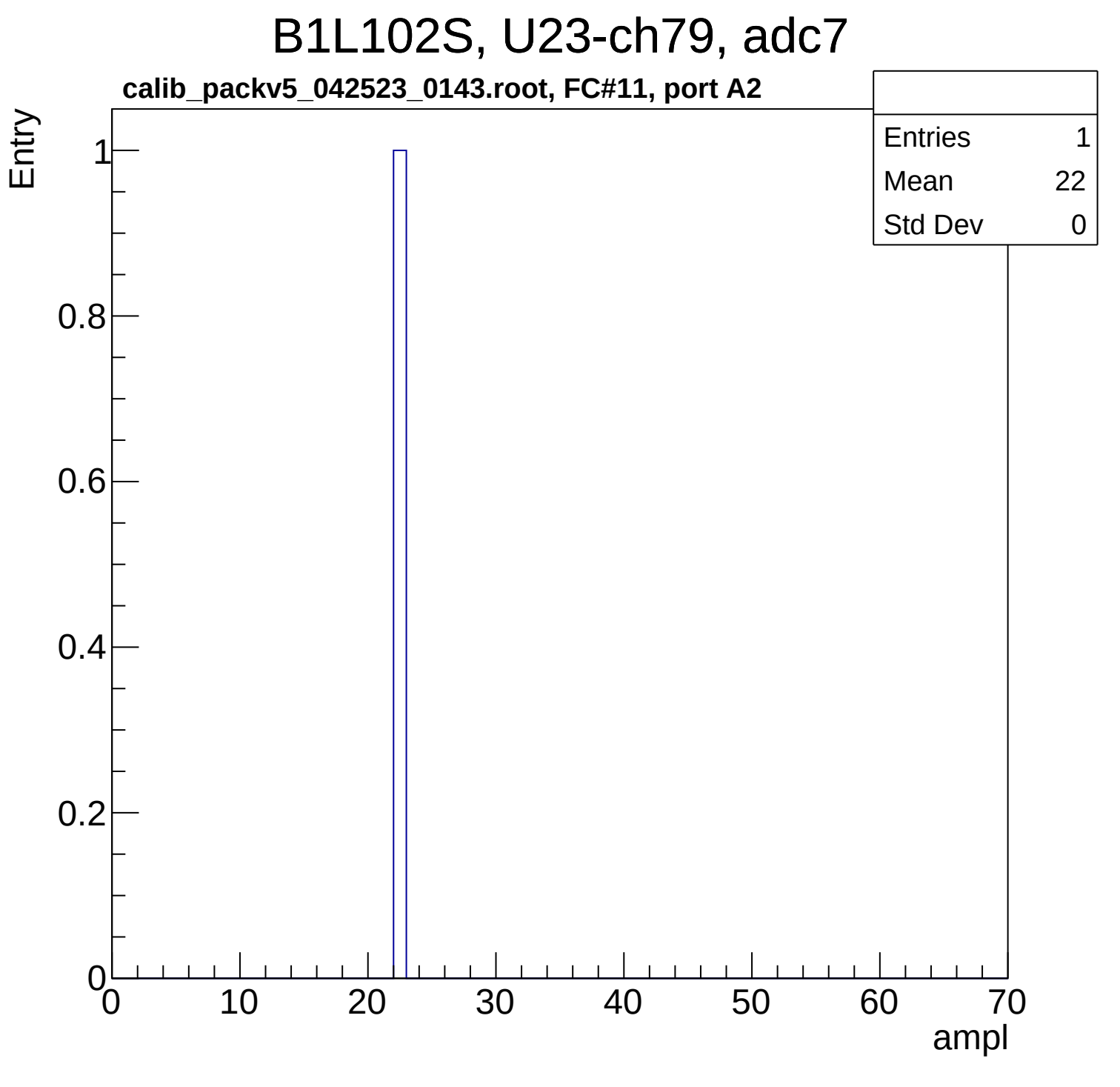

B1L102S, U23-ch79, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 42.68
Std Dev 3.301
Gaus mean : 43.3730
Gaus Width: 3.2573
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

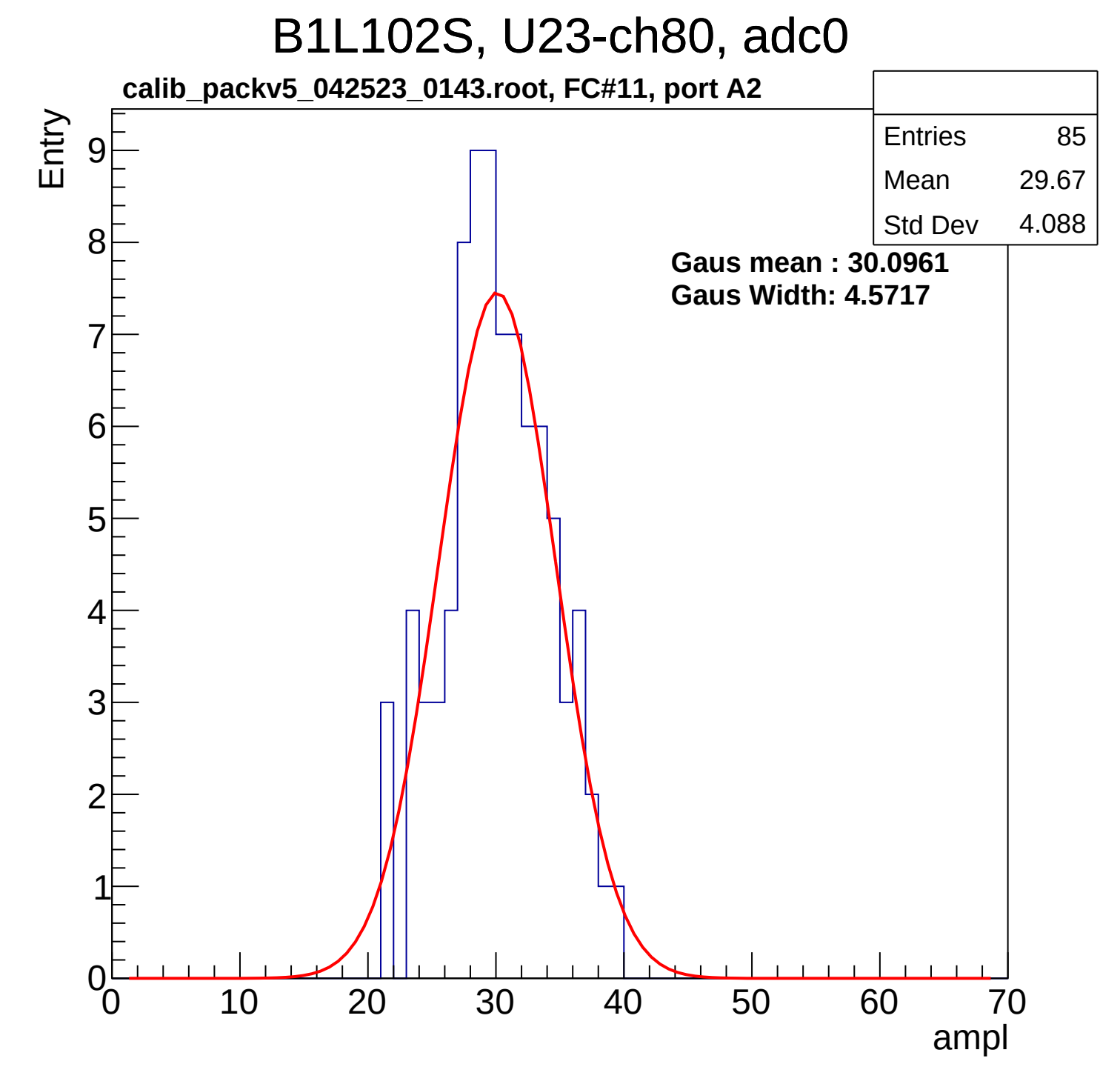

B1L102S, U23-ch79, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 48.81
Std Dev 3.475
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

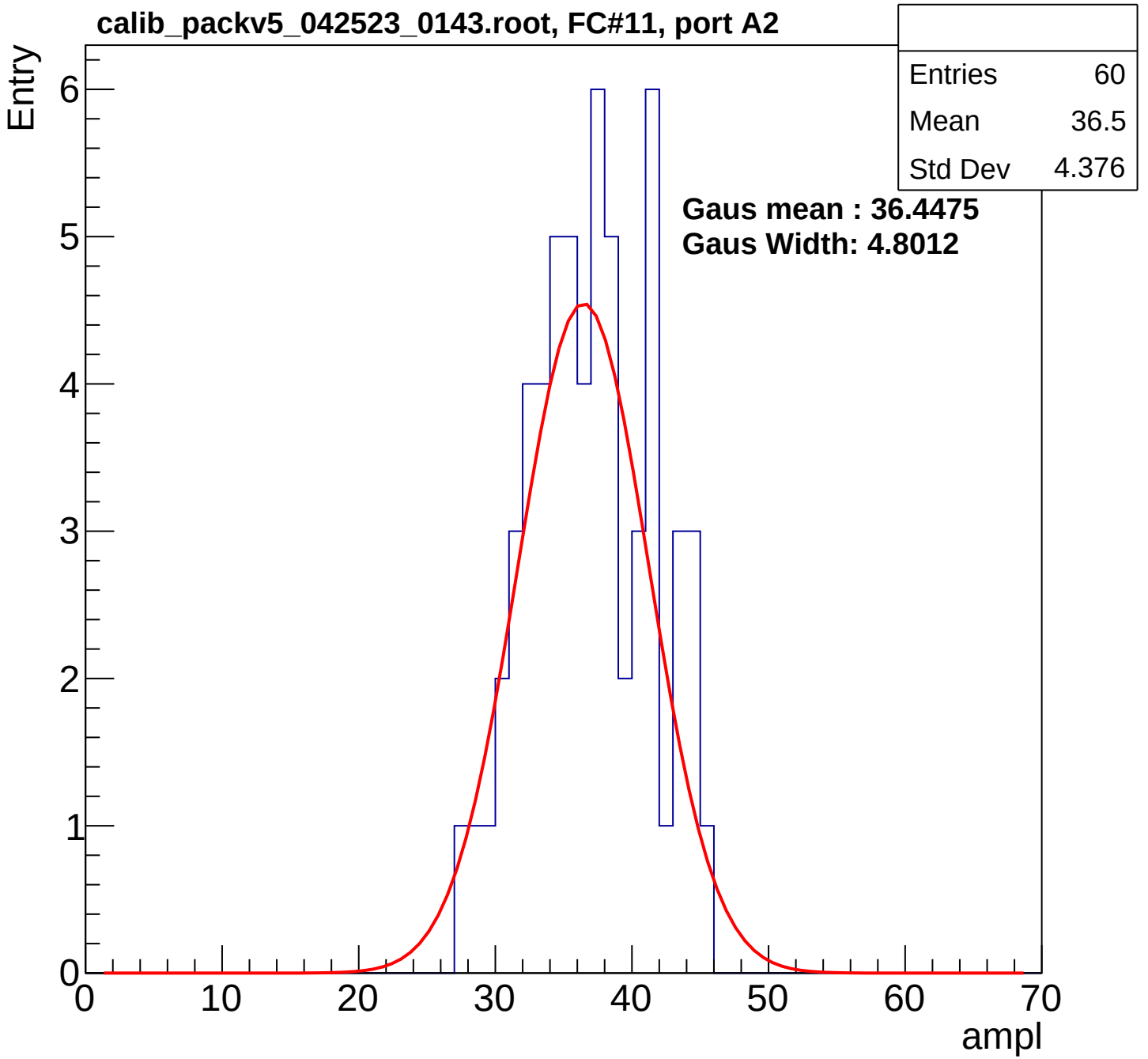

B1L102S, U23-ch79, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 55.37
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

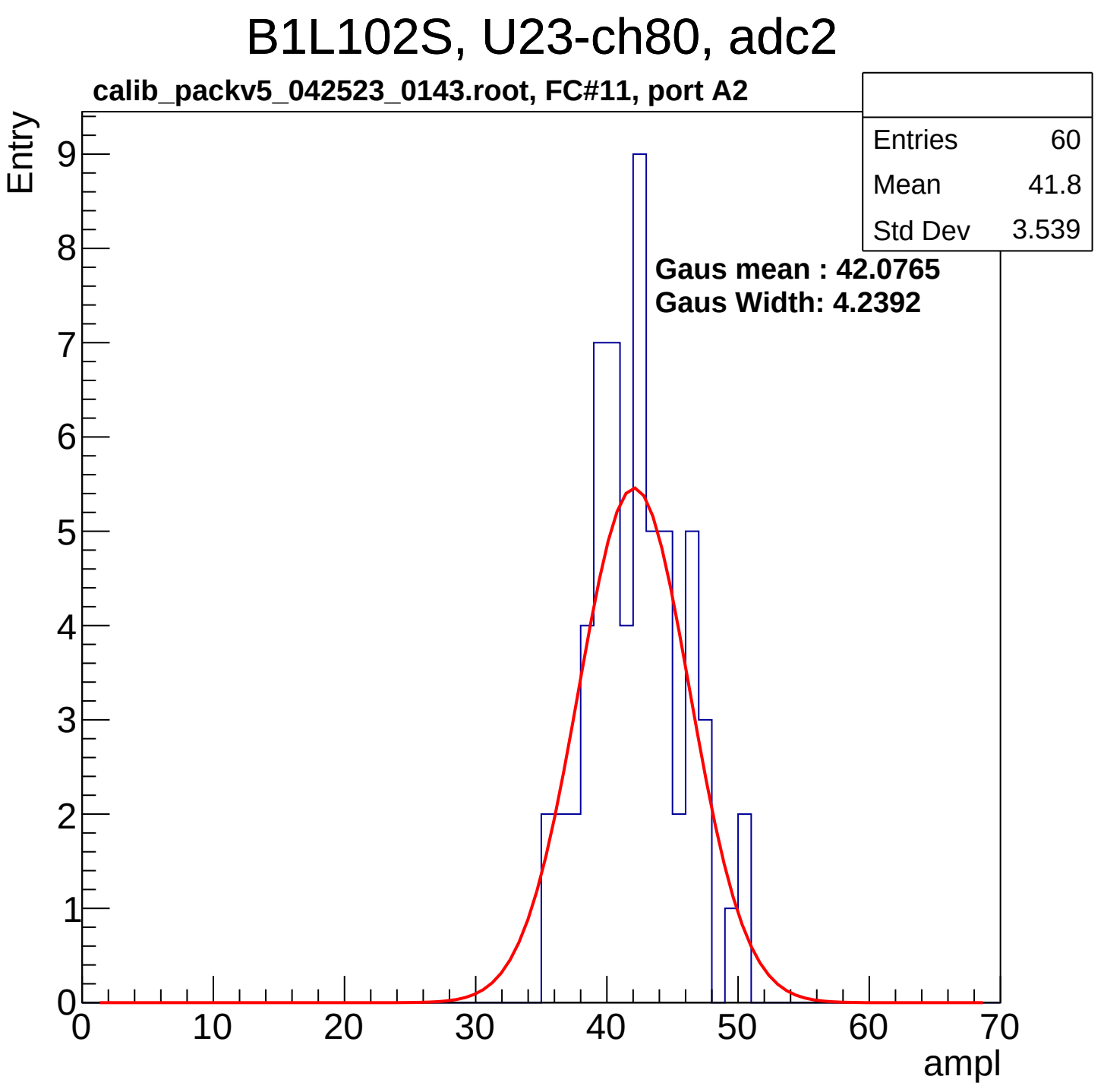

B1L102S, U23-ch79, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 59.88
Std Dev 2.224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

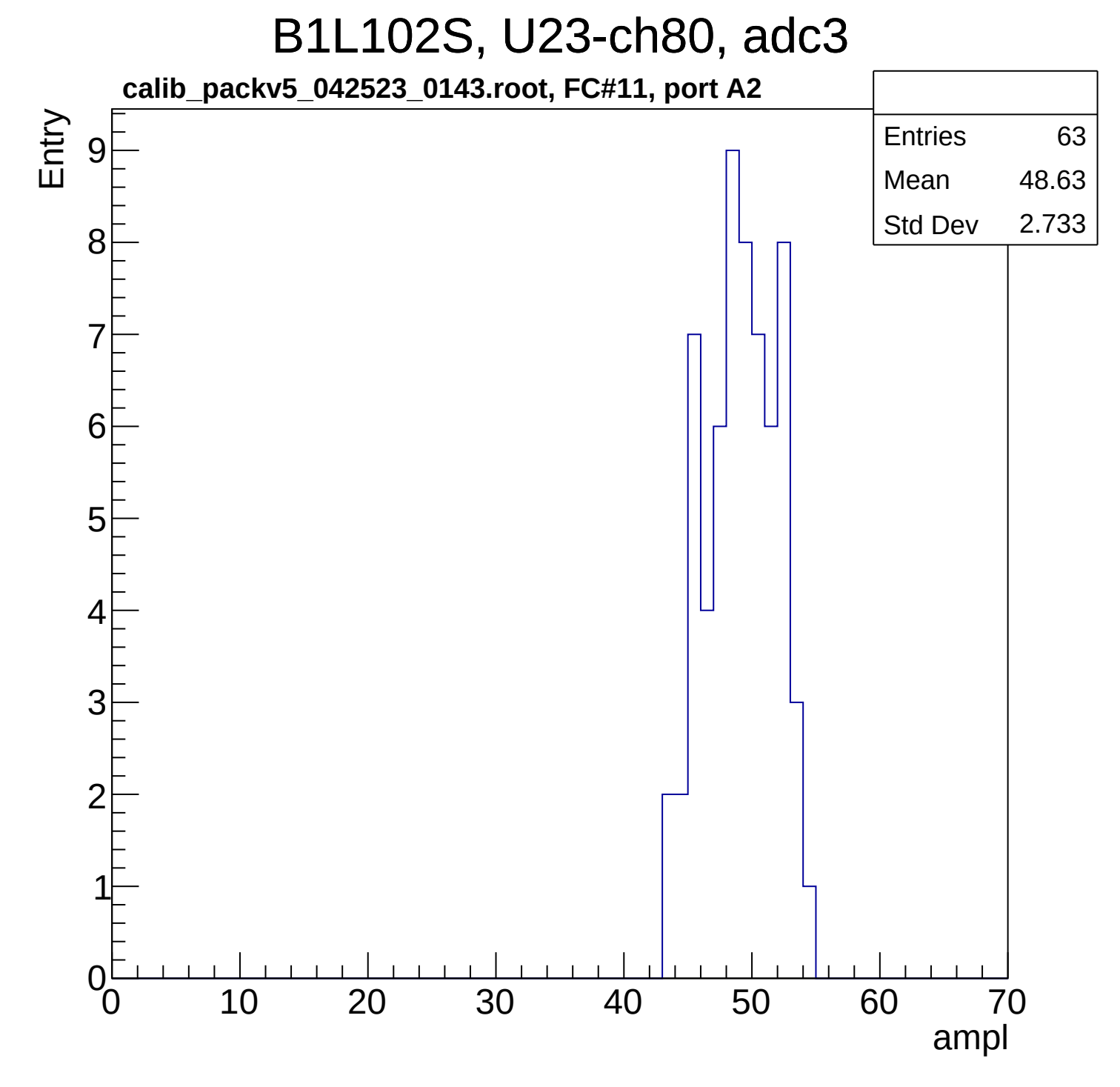

B1L102S, U23-ch79, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 54.25
Std Dev 20.53
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

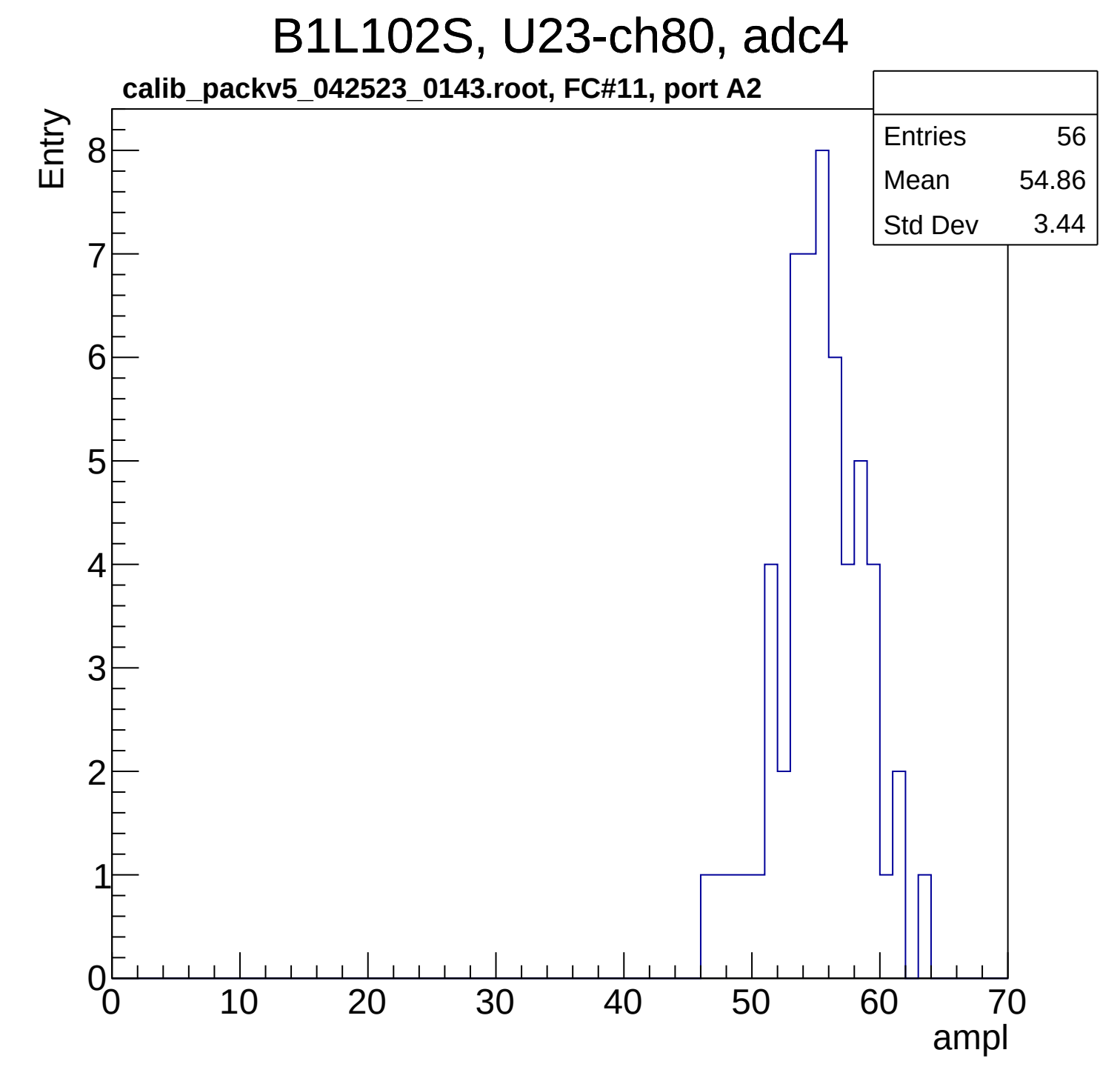

B1L102S, U23-ch79, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

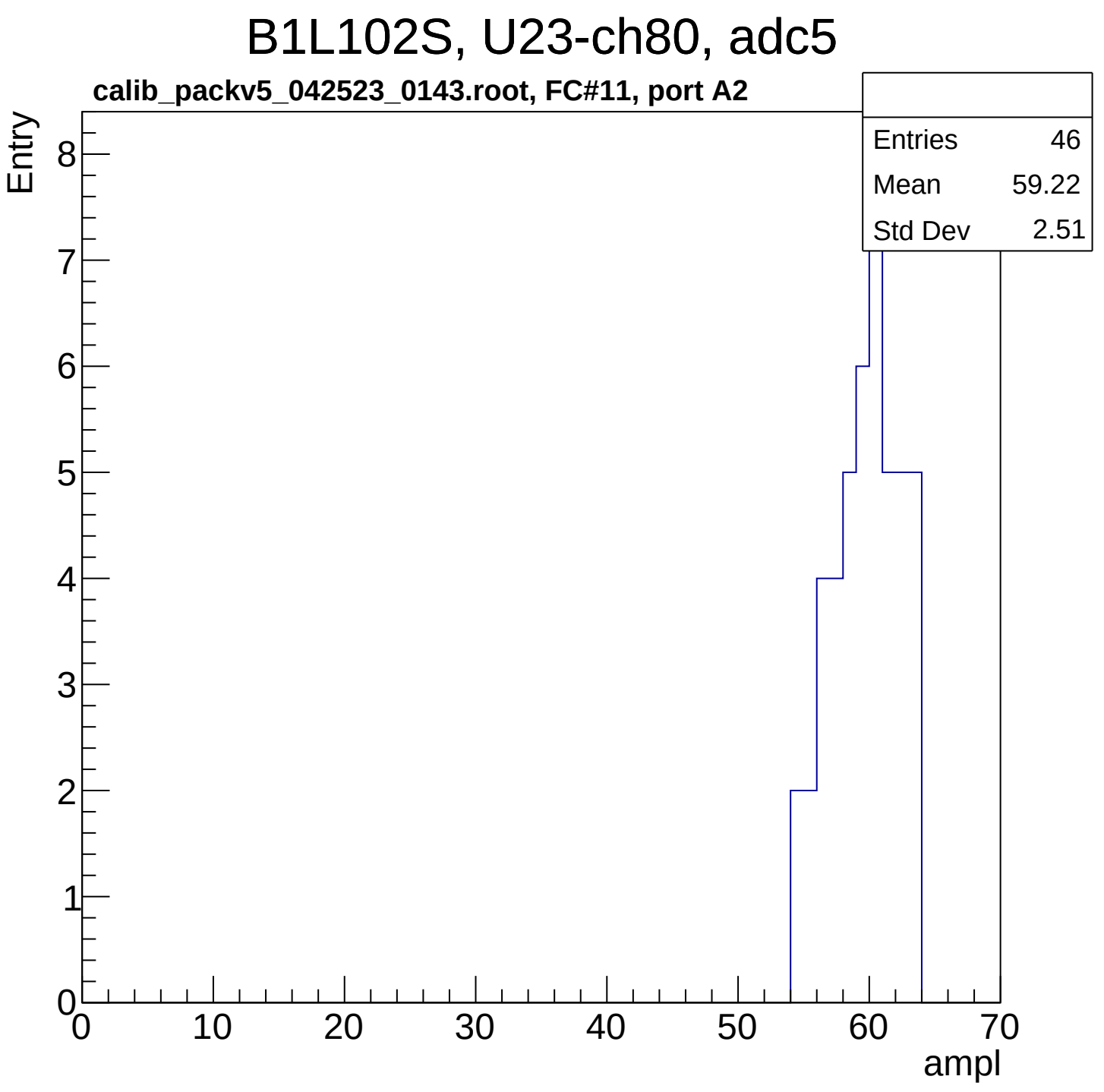

B1L102S, U23-ch80, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 29.67
Std Dev 4.088
Gaus mean : 30.0961
Gaus Width: 4.5717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

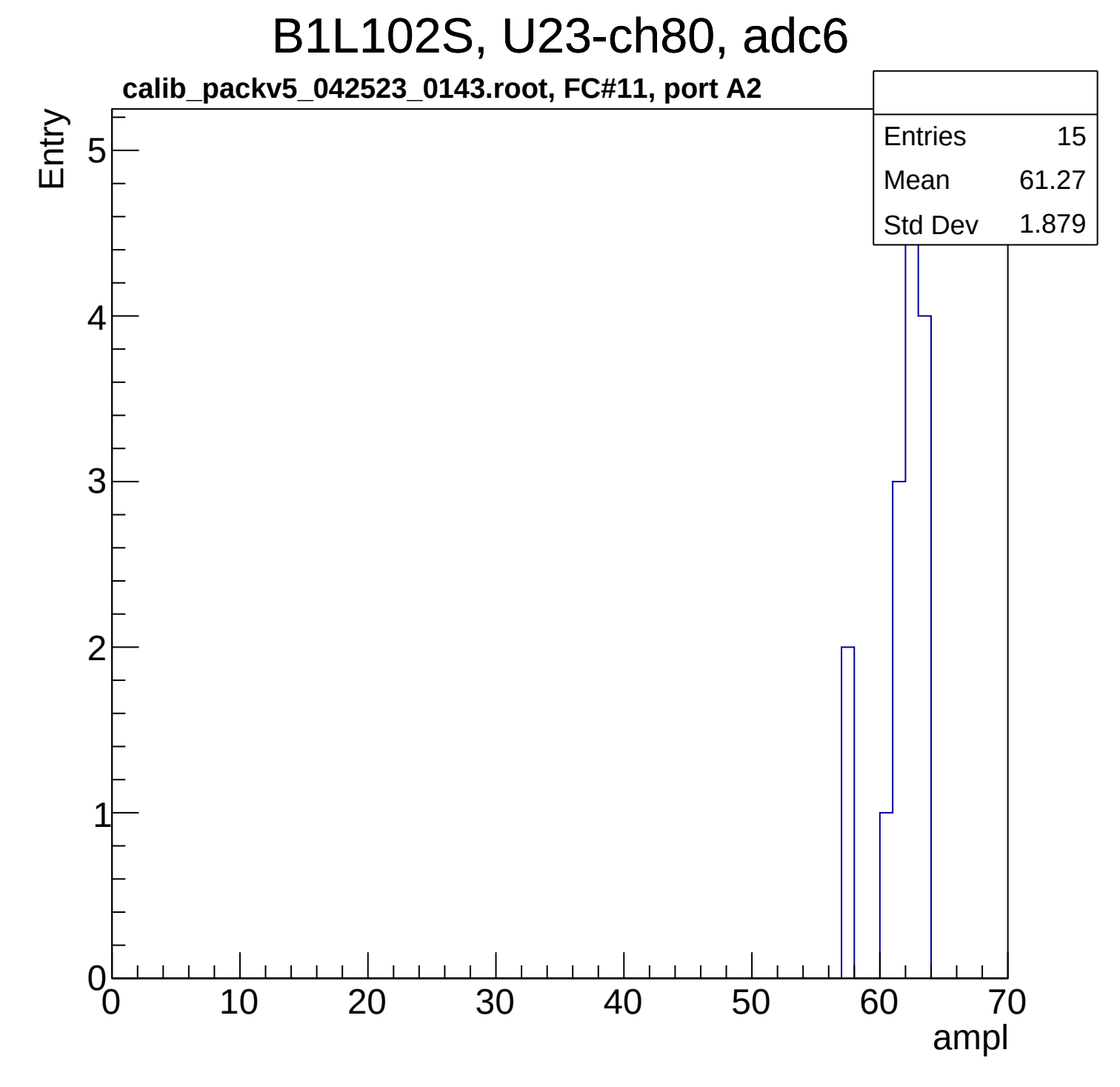

B1L102S, U23-ch80, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 36.5
Std Dev 4.376
Gaus mean : 36.4475
Gaus Width: 4.8012
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

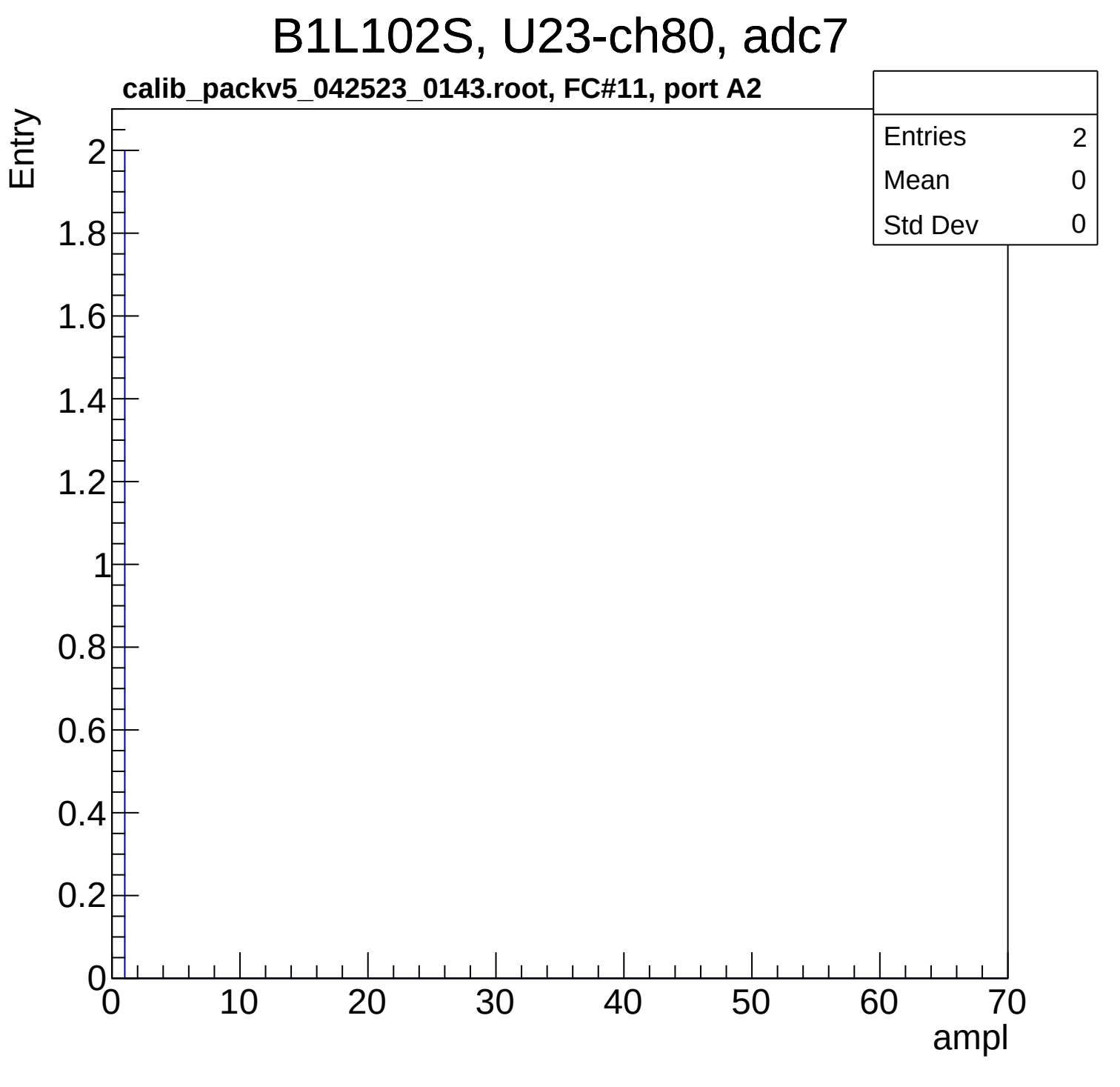

B1L102S, U23-ch80, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 41.8
Std Dev 3.539
Gaus mean : 42.0765
Gaus Width: 4.2392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

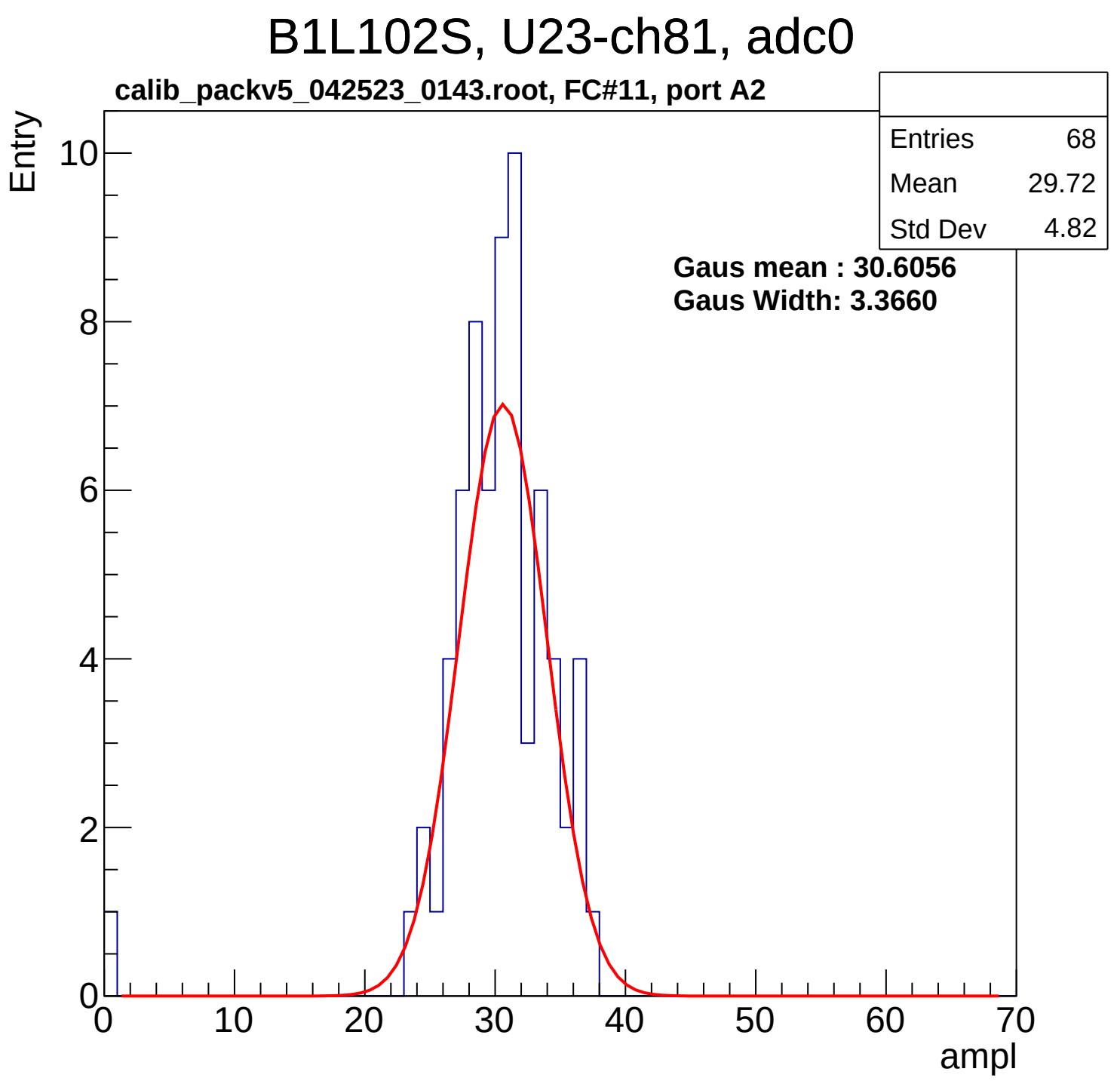

B1L102S, U23-ch80, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 48.63
Std Dev 2.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

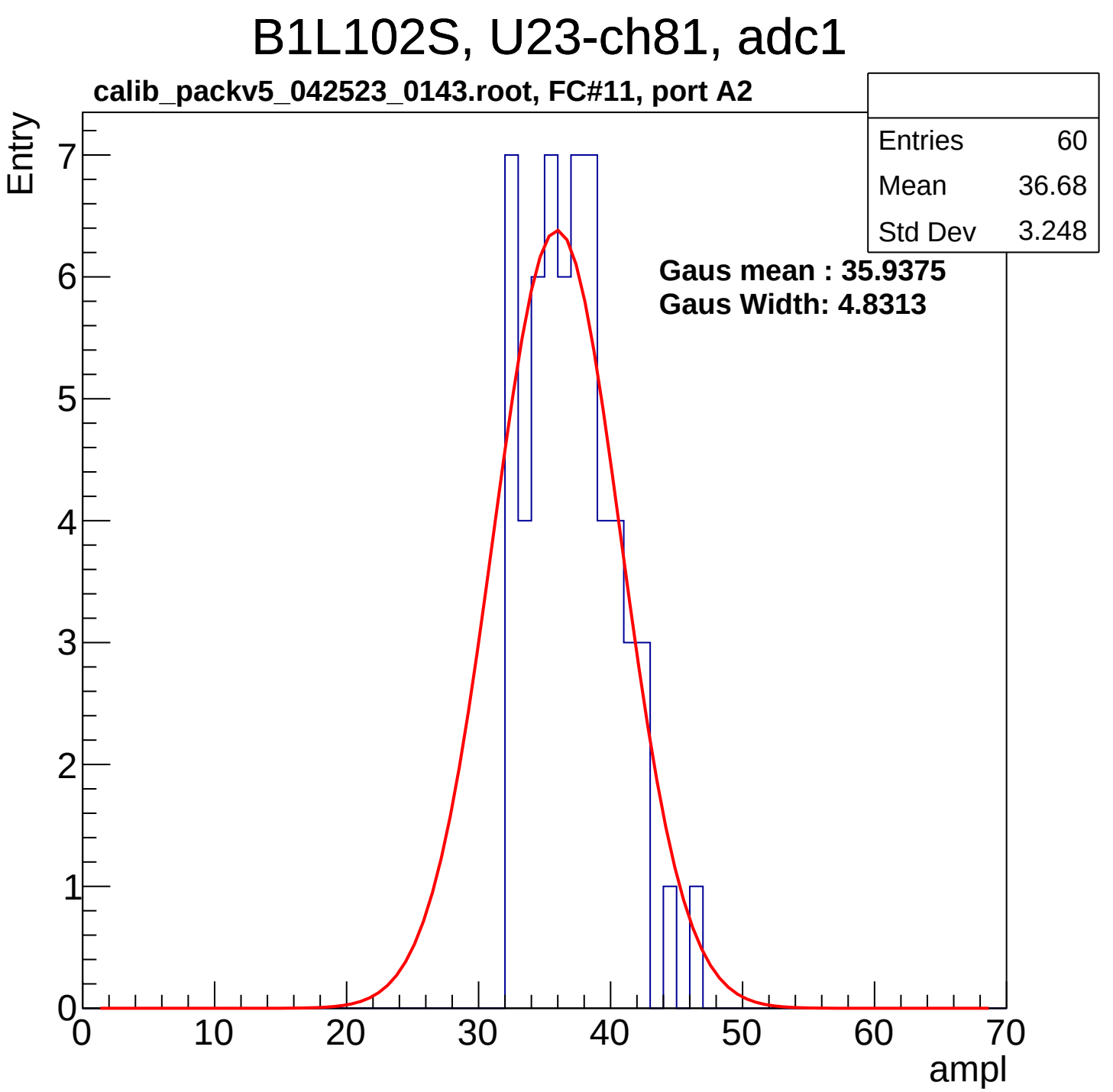

B1L102S, U23-ch80, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 54.86
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

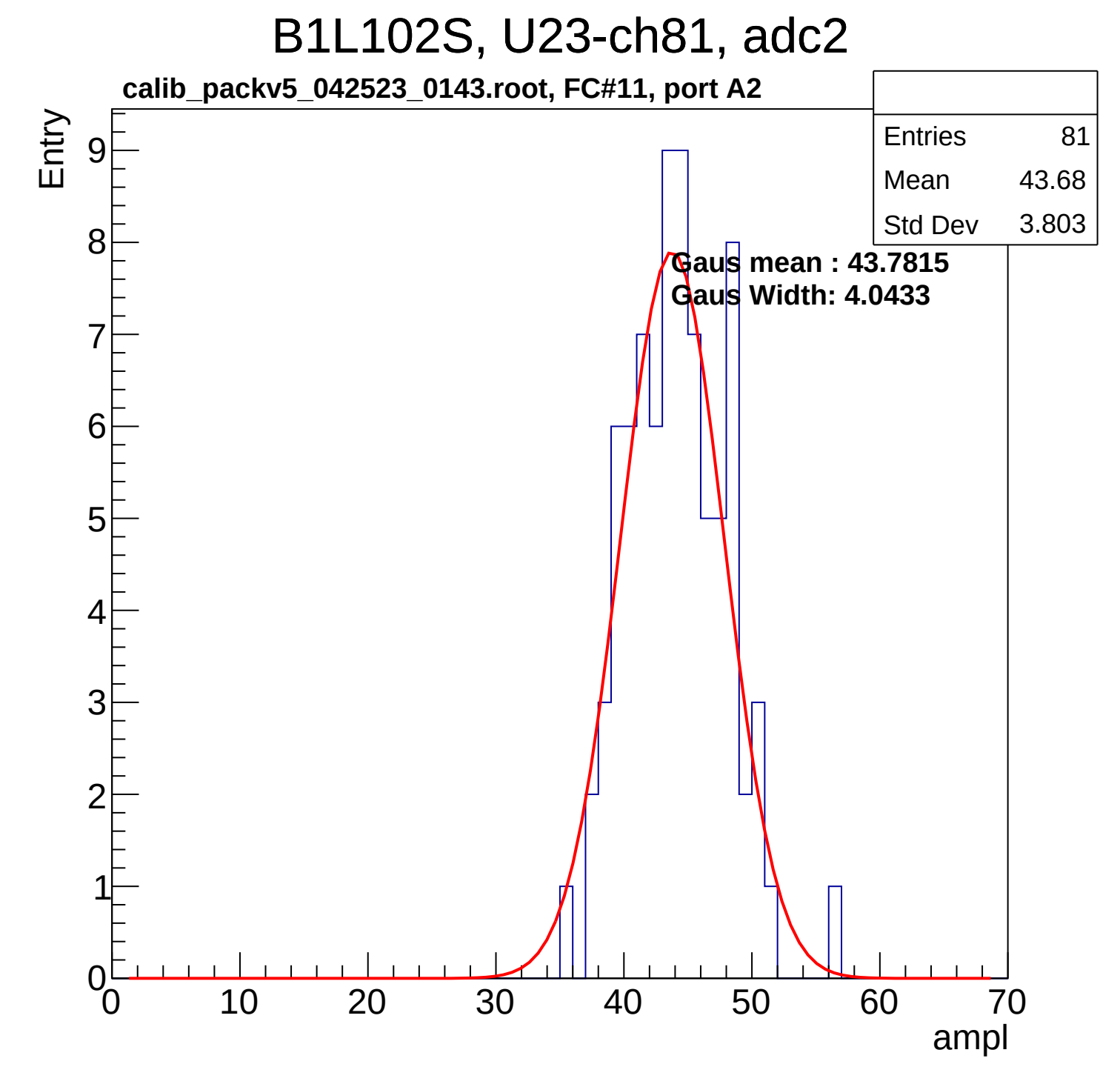

B1L102S, U23-ch80, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 59.22
Std Dev 2.51
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

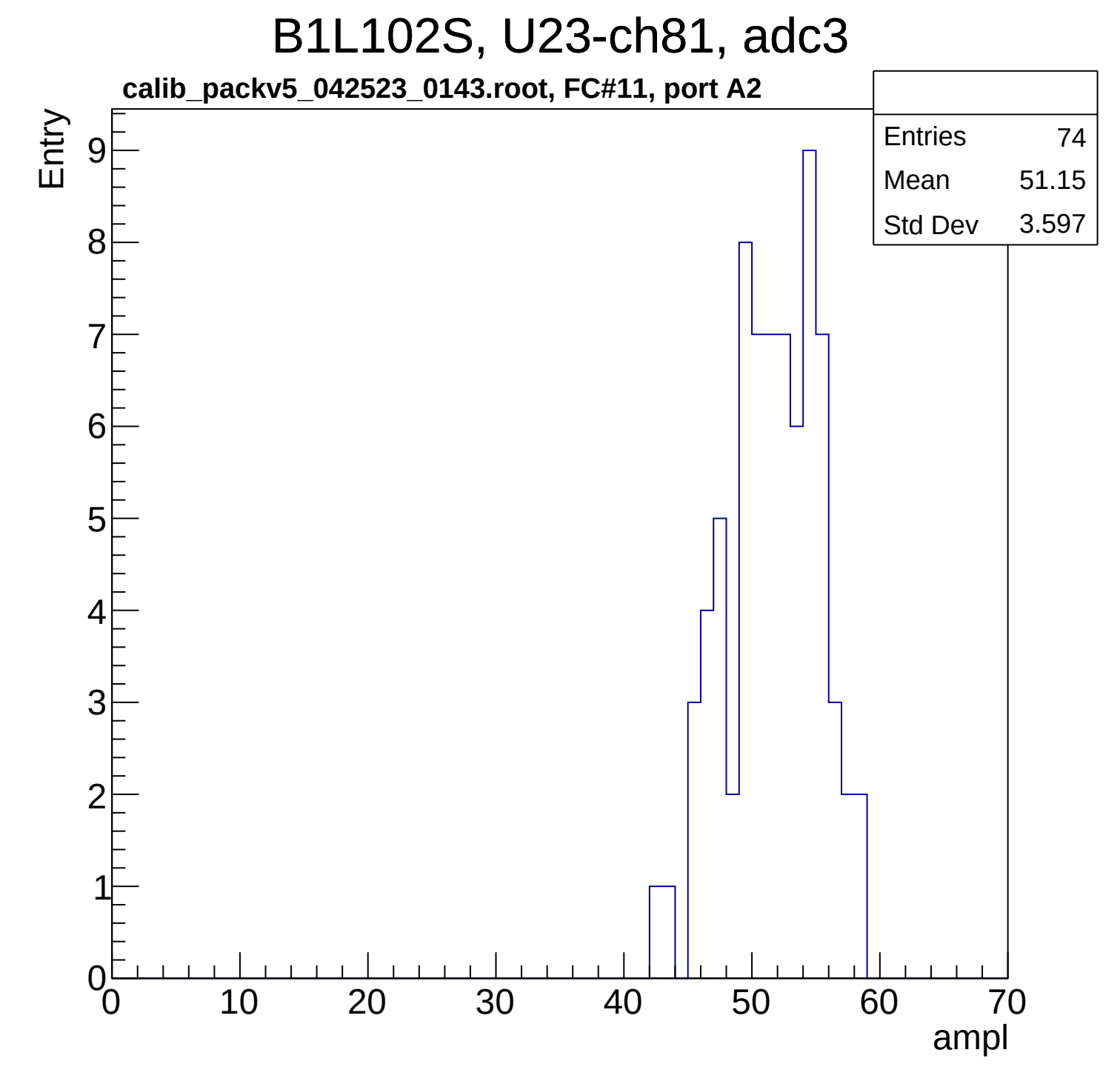

B1L102S, U23-ch80, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 61.27
Std Dev 1.879
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

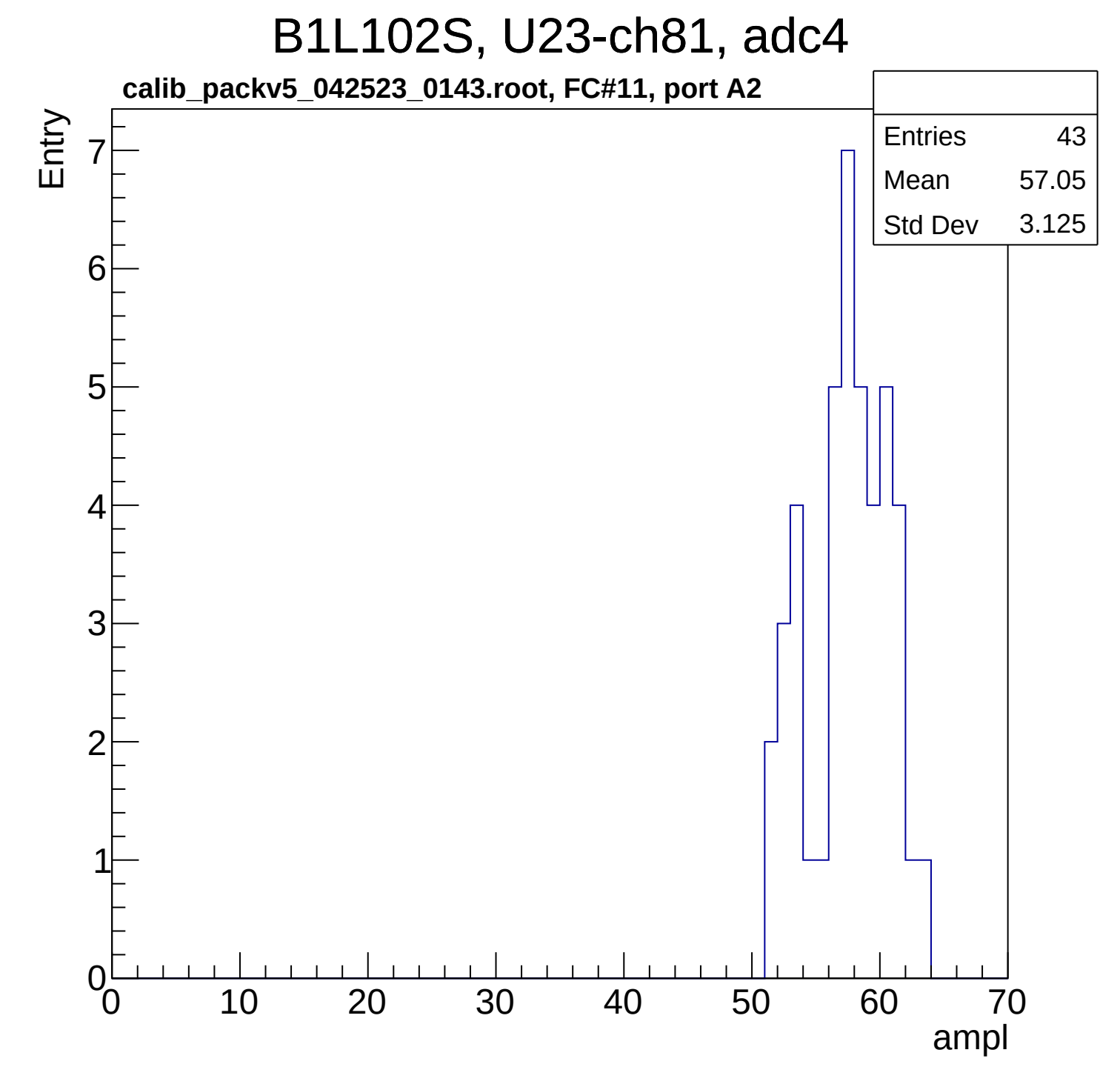

B1L102S, U23-ch80, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

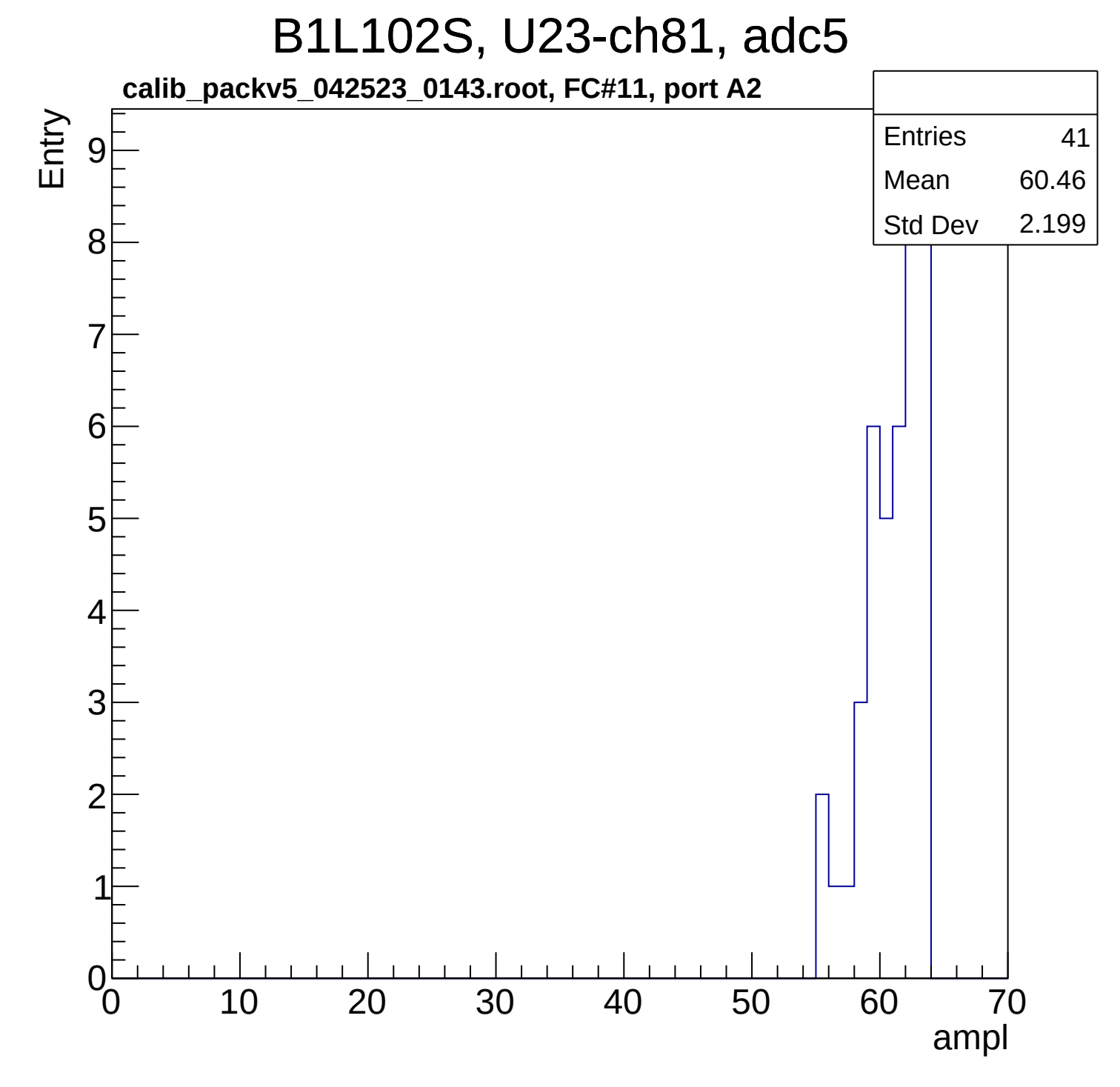

B1L102S, U23-ch81, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.72
Std Dev 4.82
Gaus mean : 30.6056
Gaus Width: 3.3660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

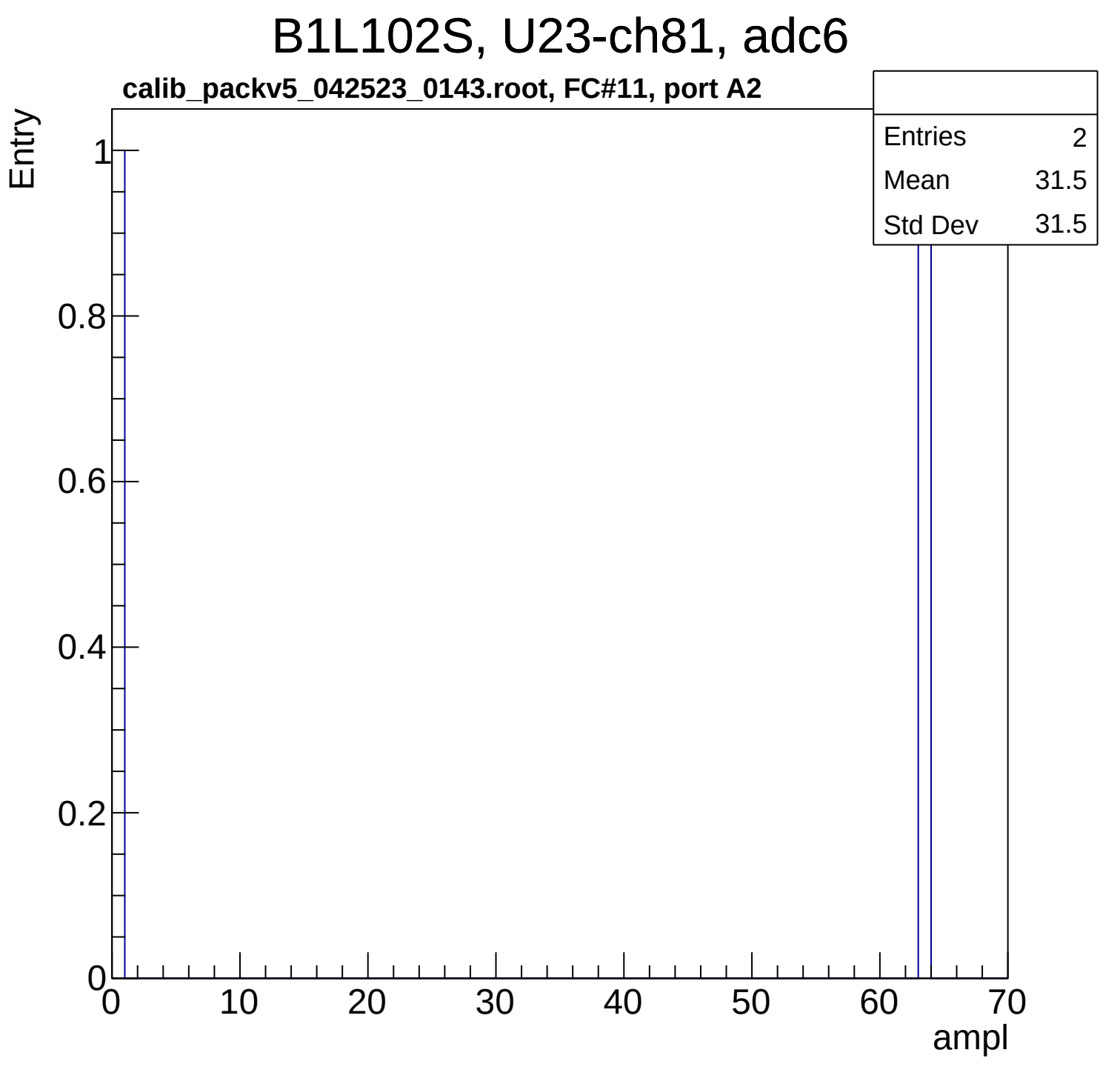

B1L102S, U23-ch81, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 36.68
Std Dev 3.248
Gaus mean : 35.9375
Gaus Width: 4.8313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

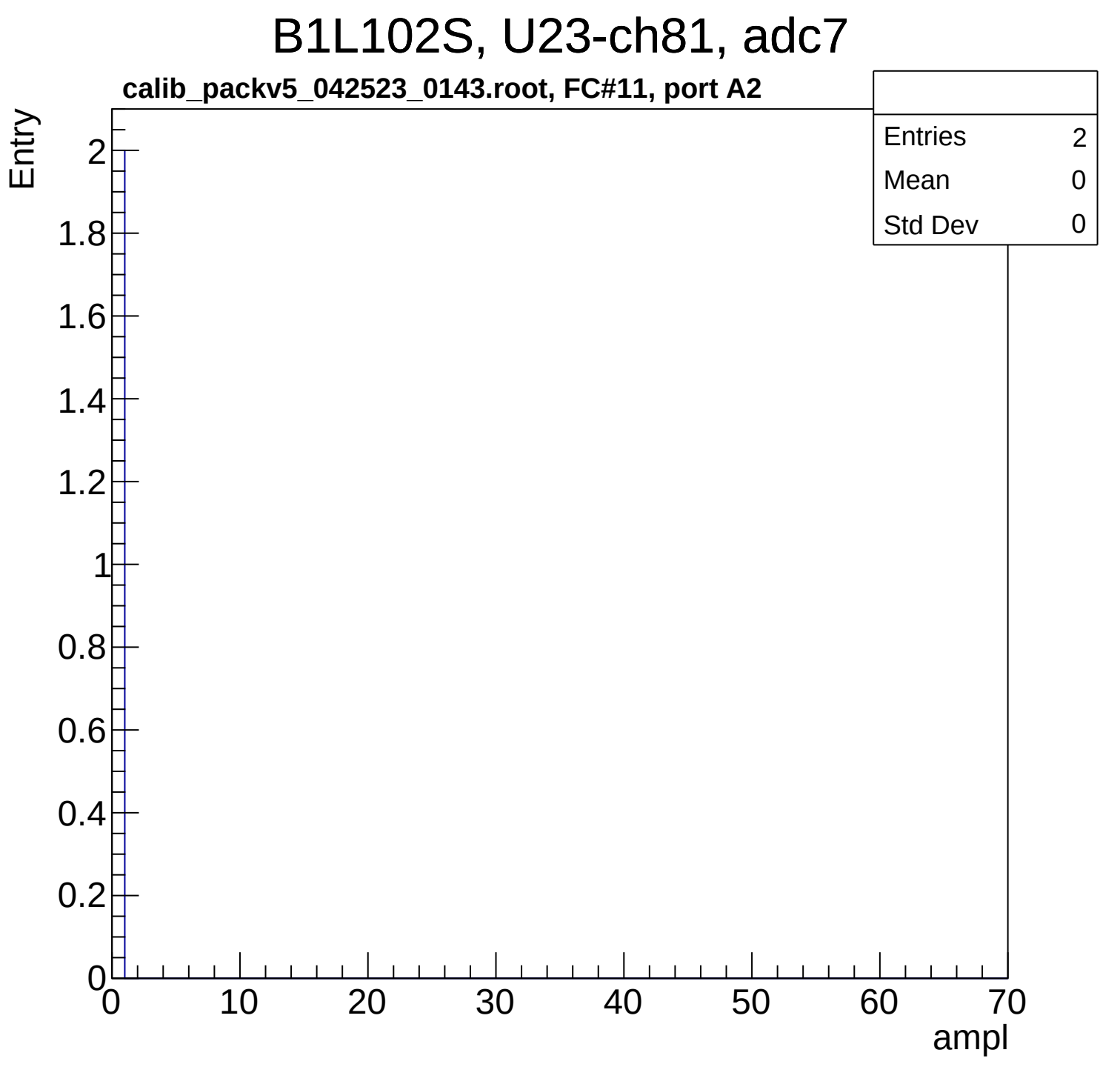

B1L102S, U23-ch81, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 43.68
Std Dev 3.803
Gaus mean : 43.7815
Gaus Width: 4.0433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

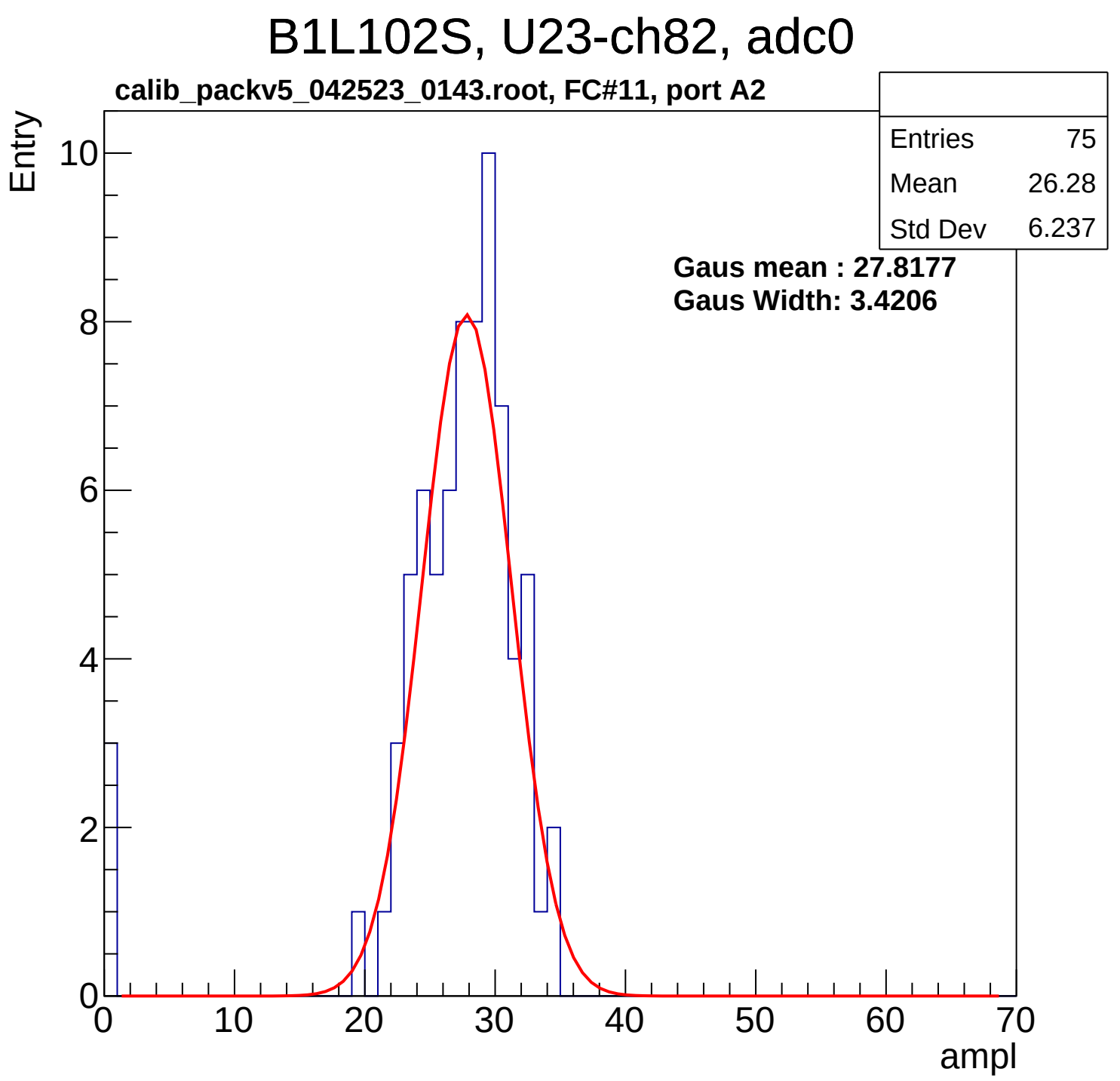

B1L102S, U23-ch81, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 51.15
Std Dev 3.597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

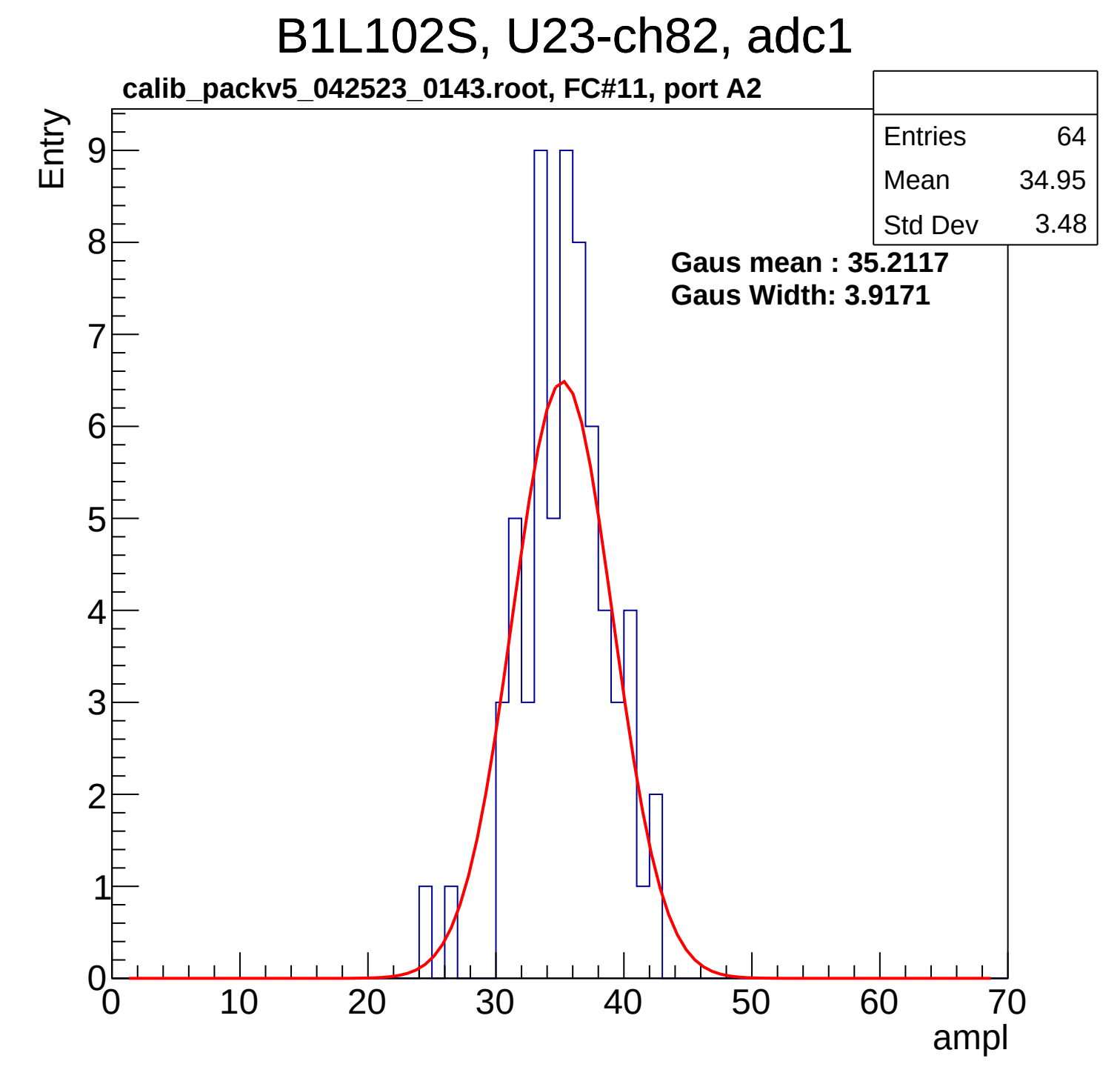

B1L102S, U23-ch81, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 57.05
Std Dev 3.125
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

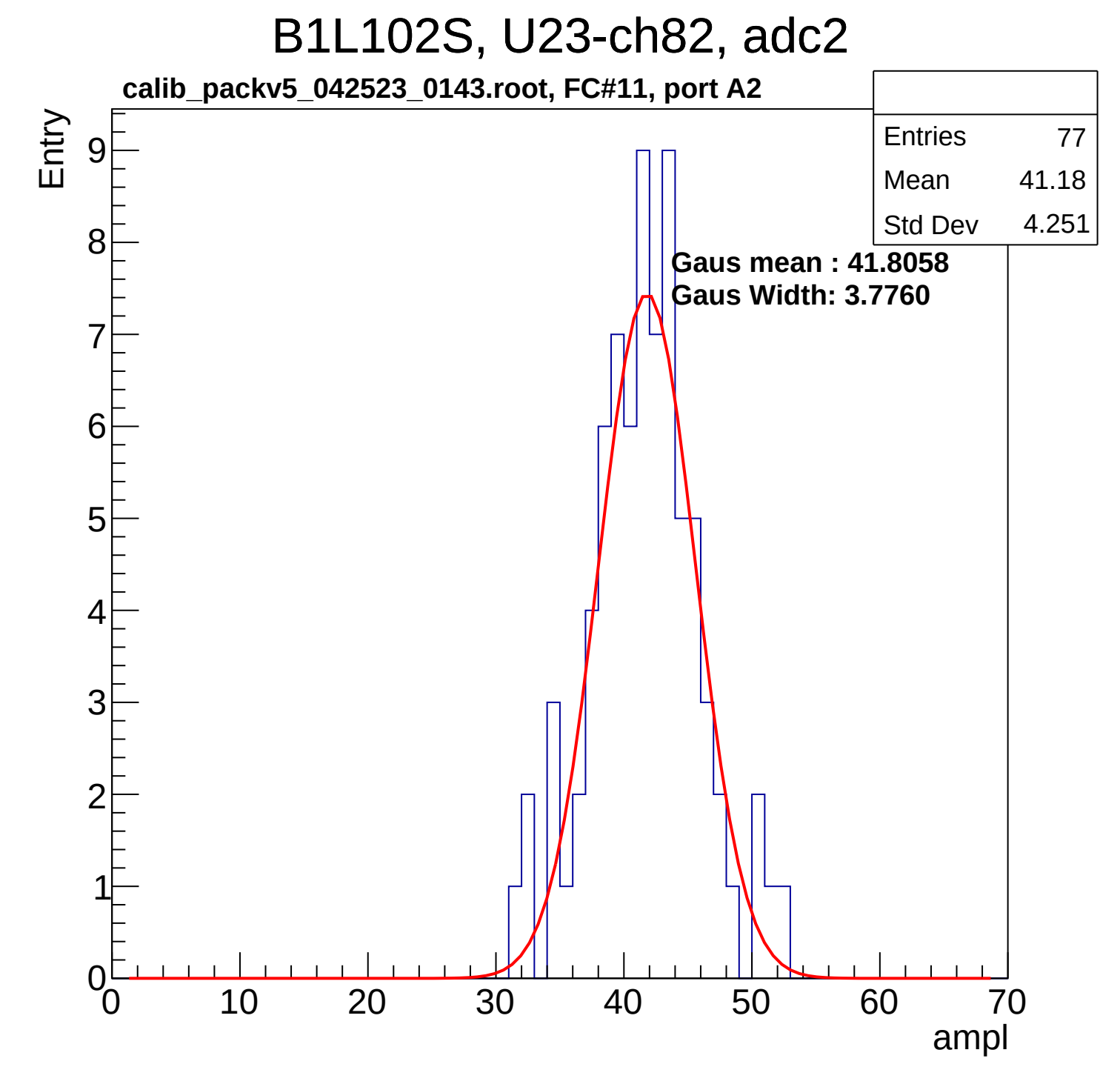

B1L102S, U23-ch81, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 60.46
Std Dev 2.199
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

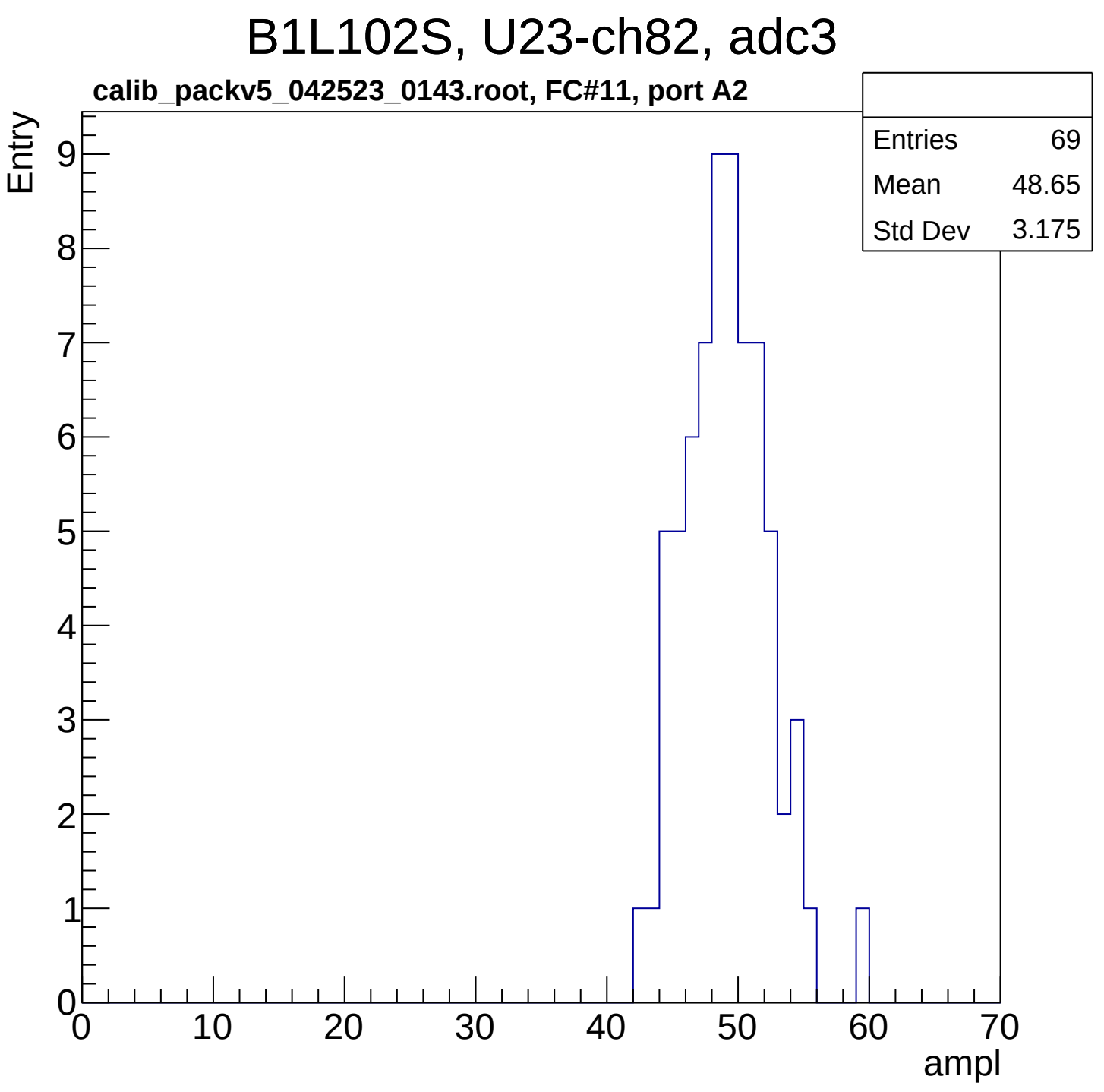

B1L102S, U23-ch81, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

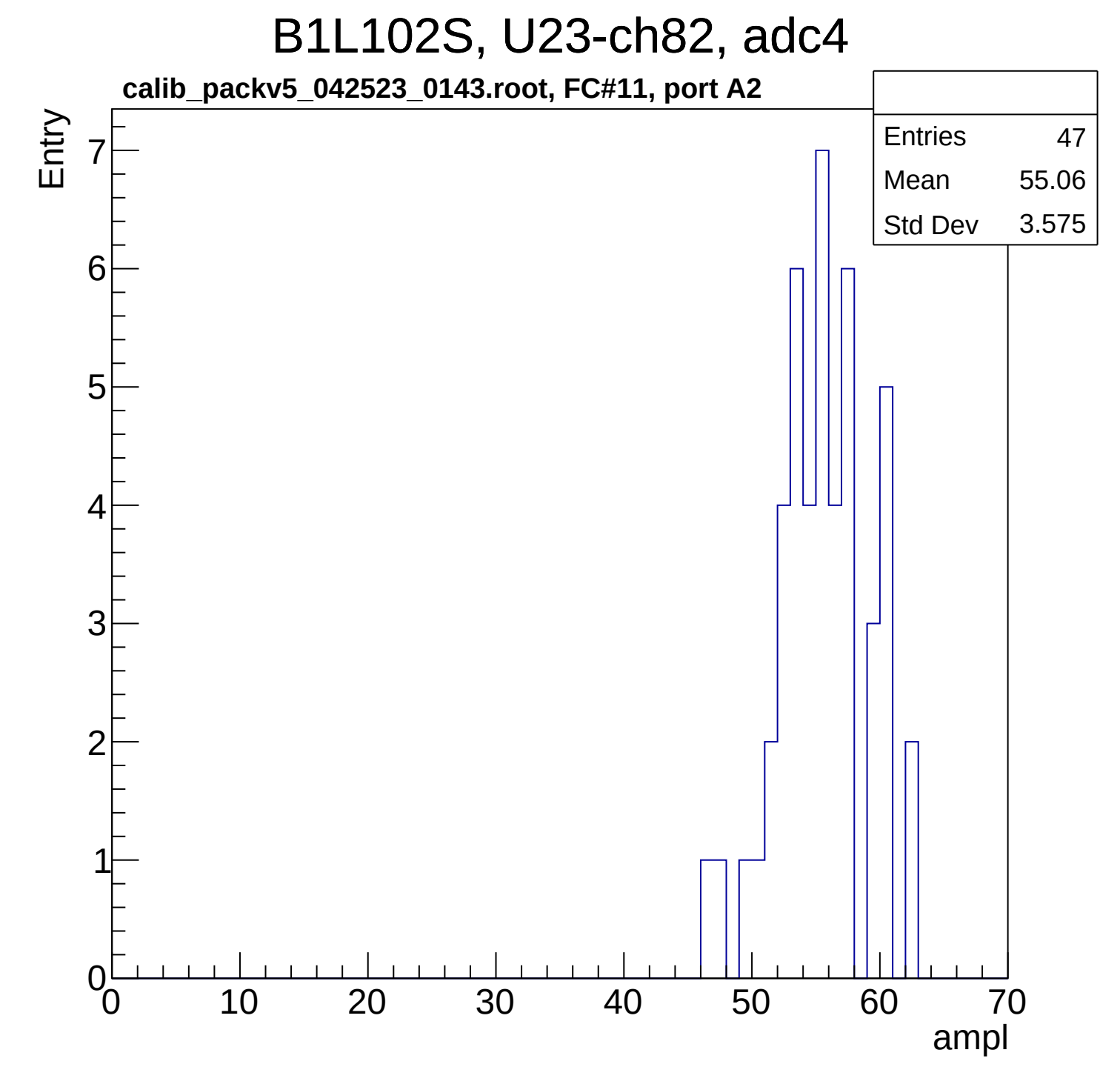

B1L102S, U23-ch81, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

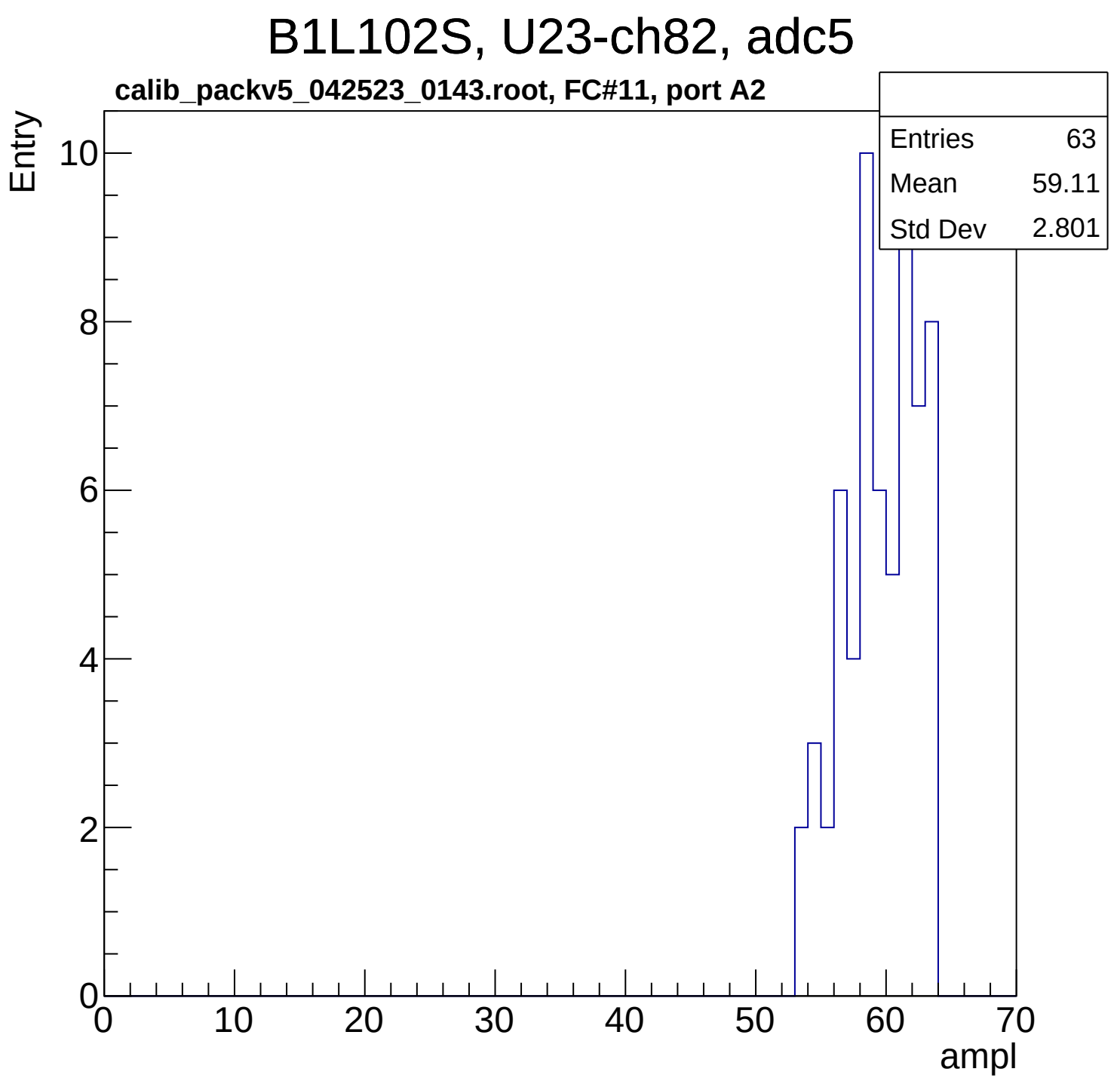

B1L102S, U23-ch82, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 26.28
Std Dev 6.237
Gaus mean : 27.8177
Gaus Width: 3.4206
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

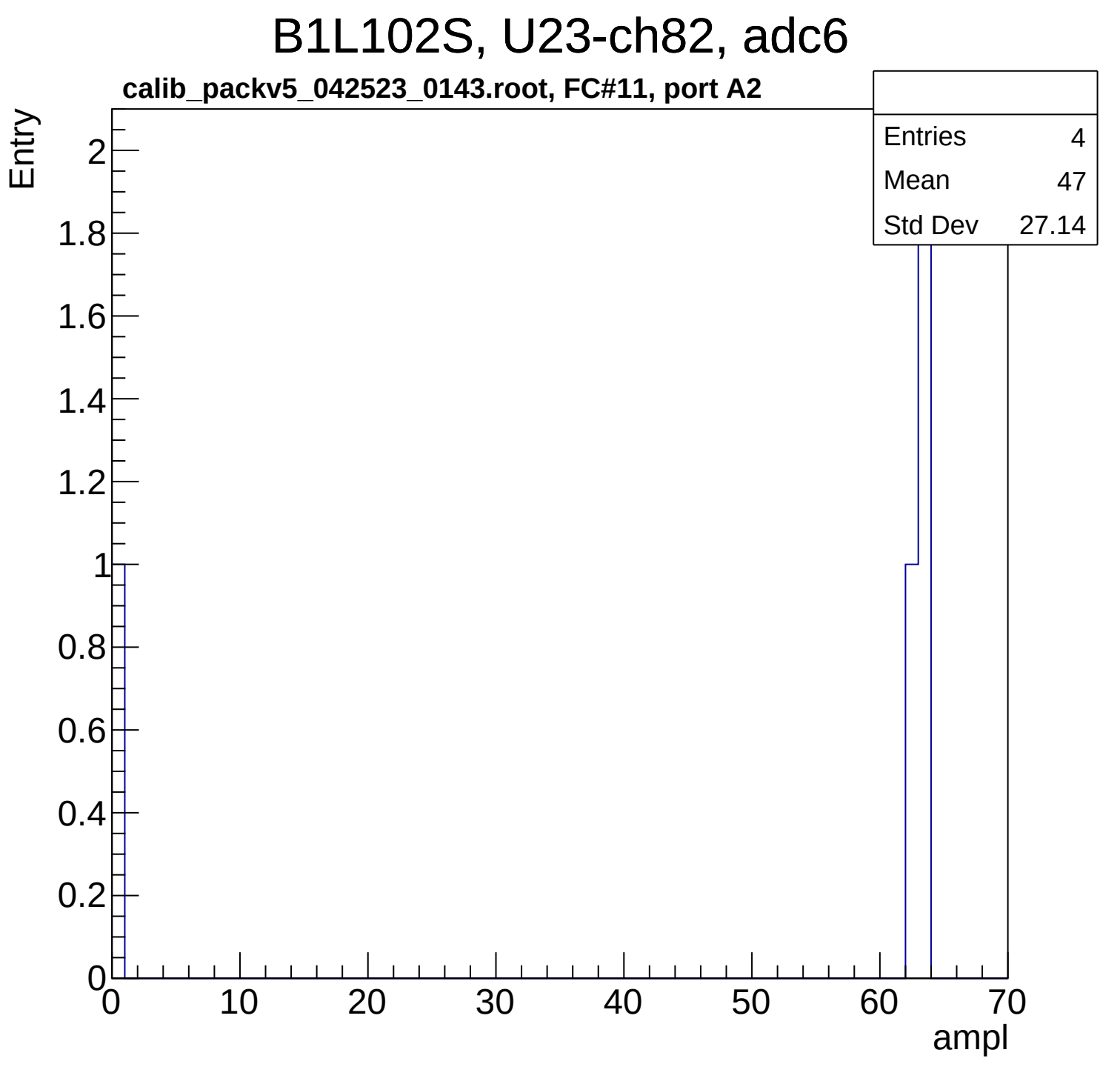

B1L102S, U23-ch82, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 34.95
Std Dev 3.48
Gaus mean : 35.2117
Gaus Width: 3.9171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

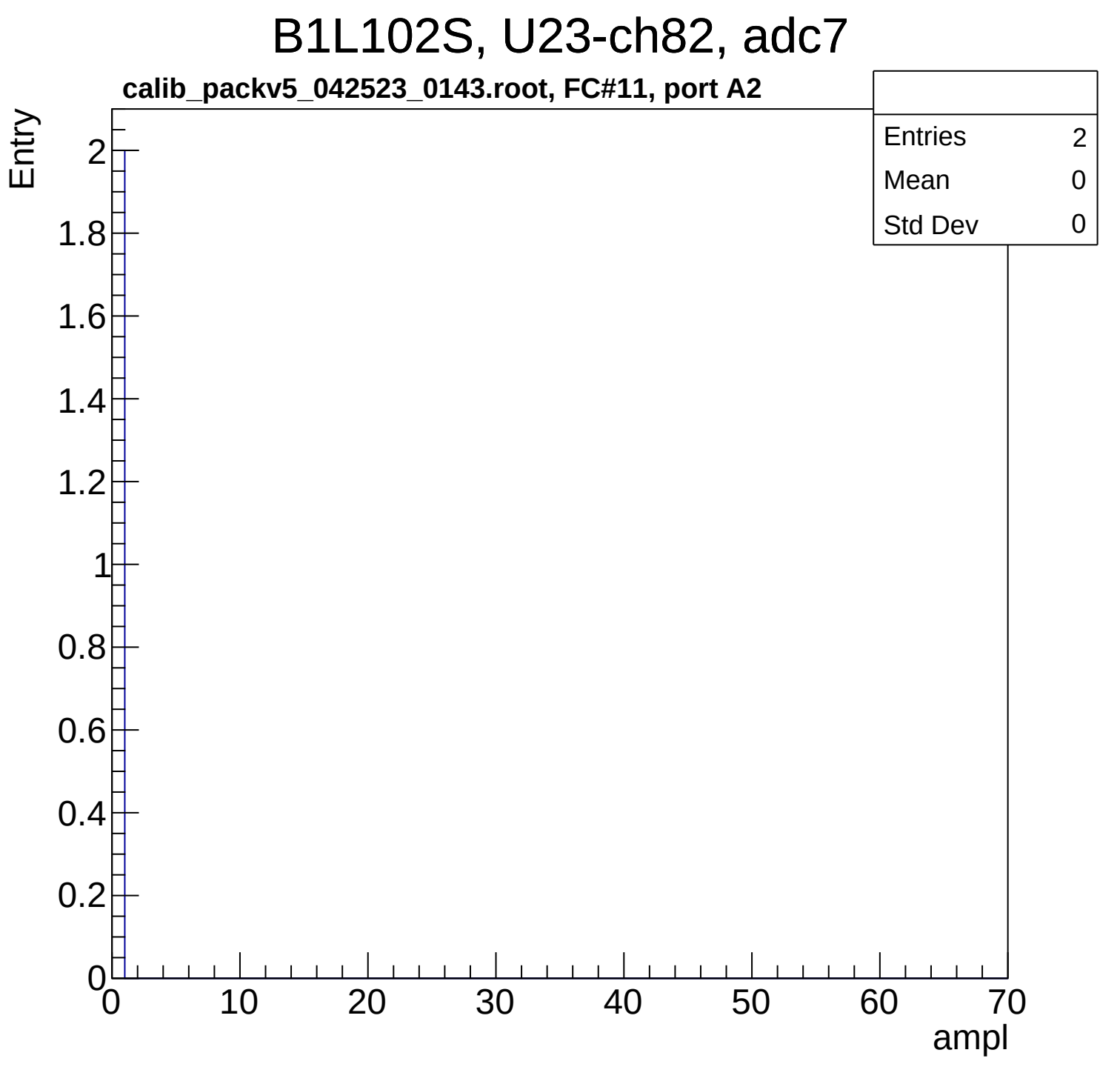

B1L102S, U23-ch82, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 41.18
Std Dev 4.251
Gaus mean : 41.8058
Gaus Width: 3.7760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

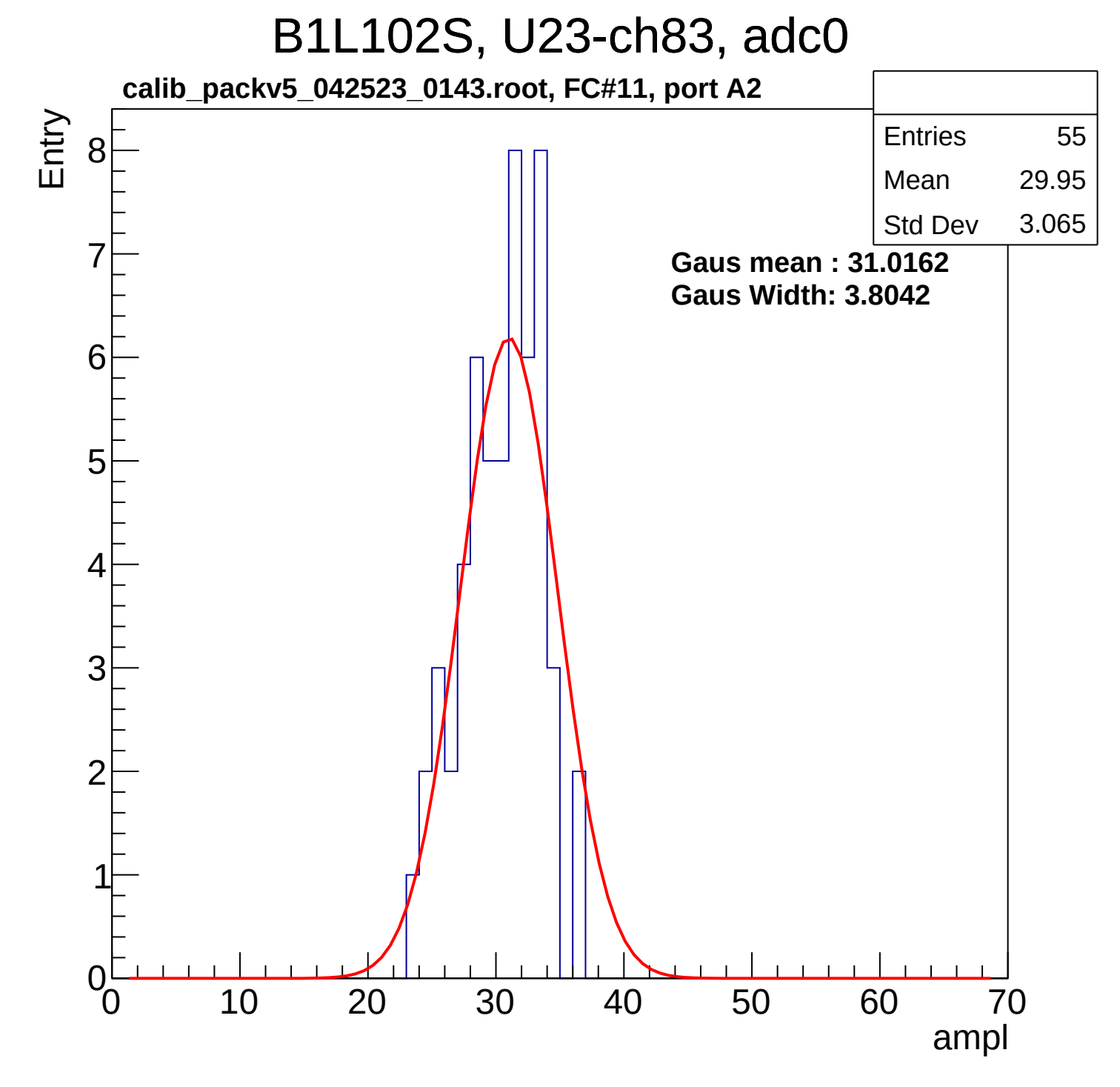

B1L102S, U23-ch82, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 48.65
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

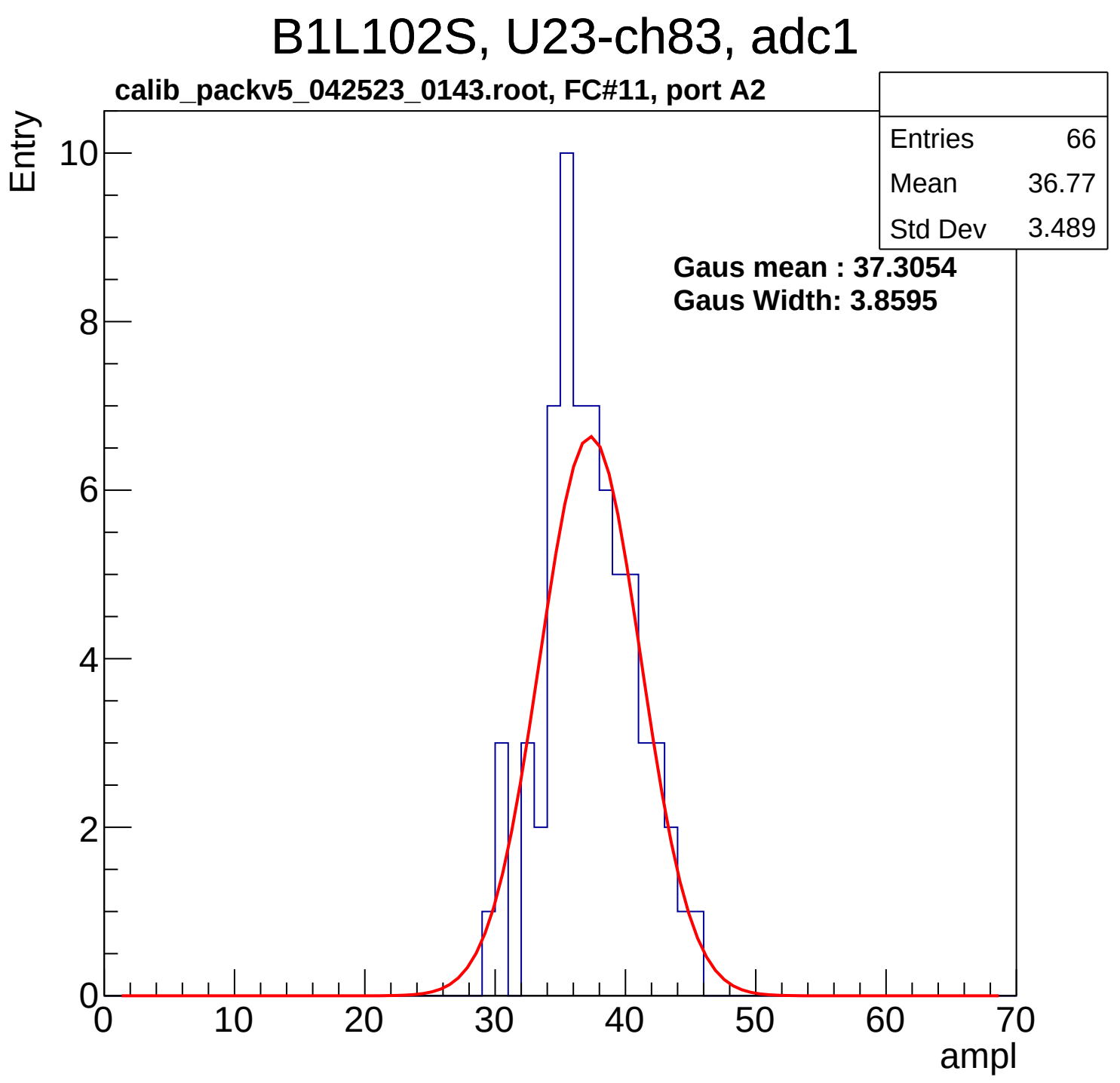

B1L102S, U23-ch82, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 55.06
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

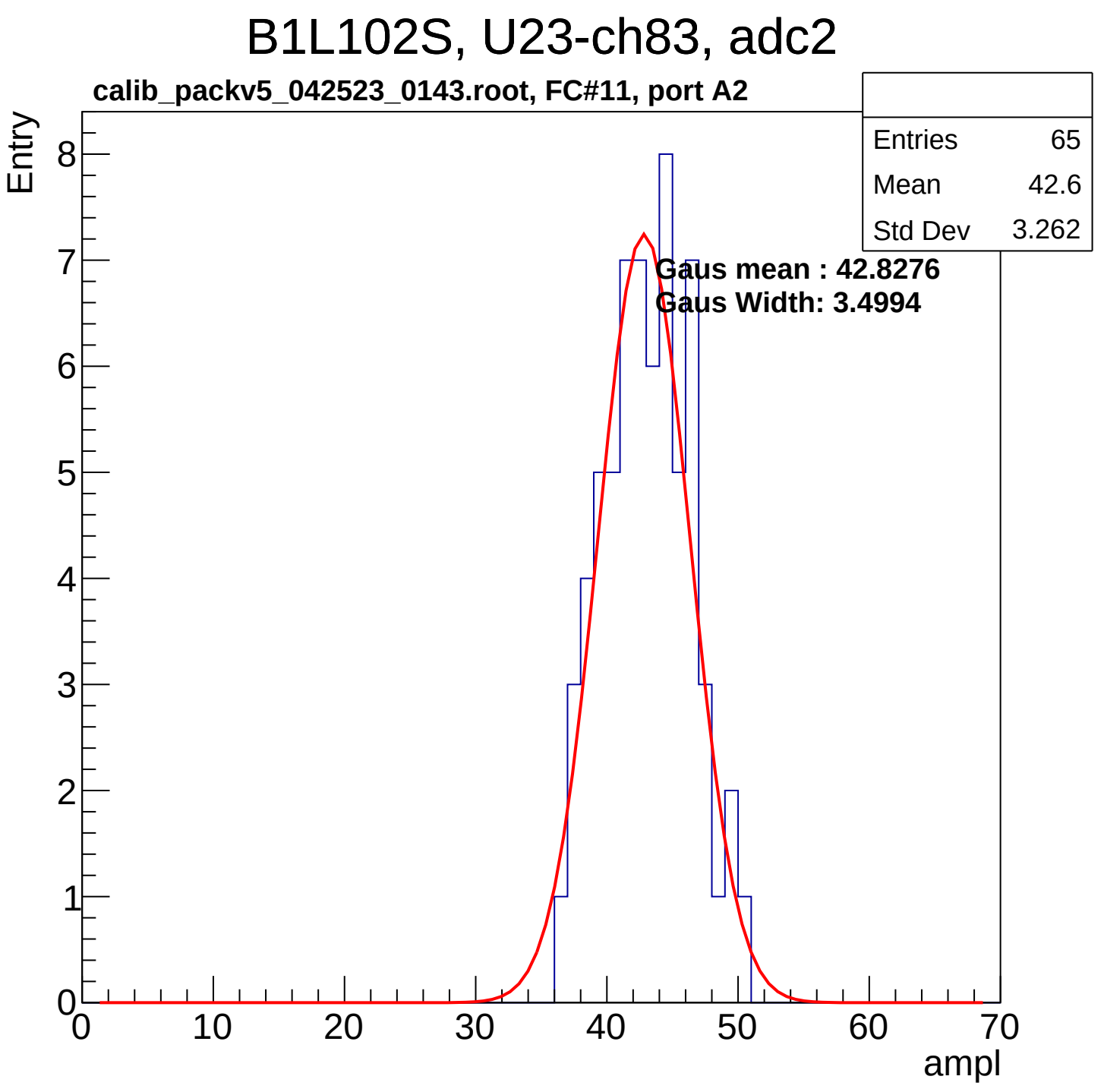

B1L102S, U23-ch82, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 59.11
Std Dev 2.801
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

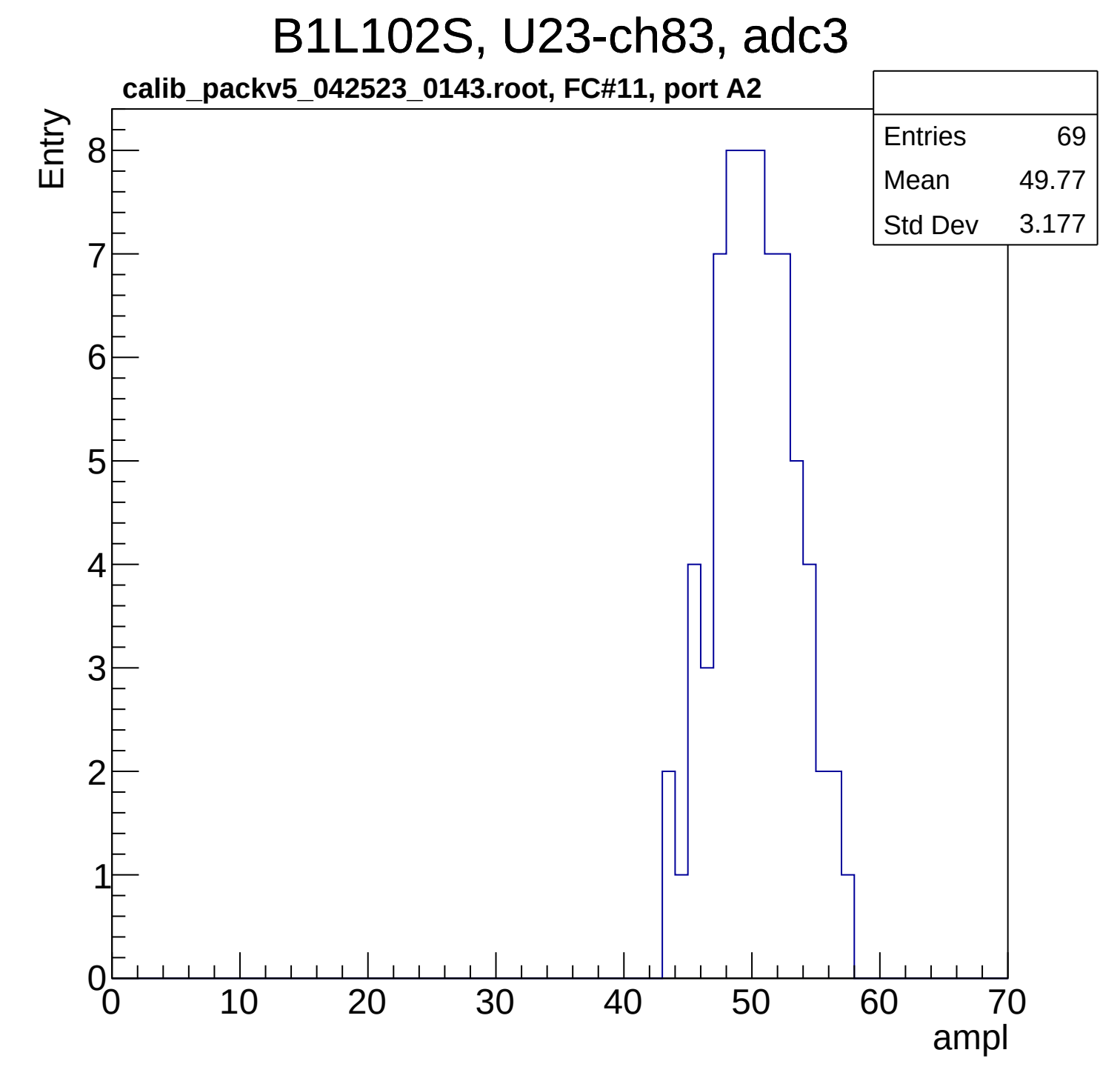

B1L102S, U23-ch82, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

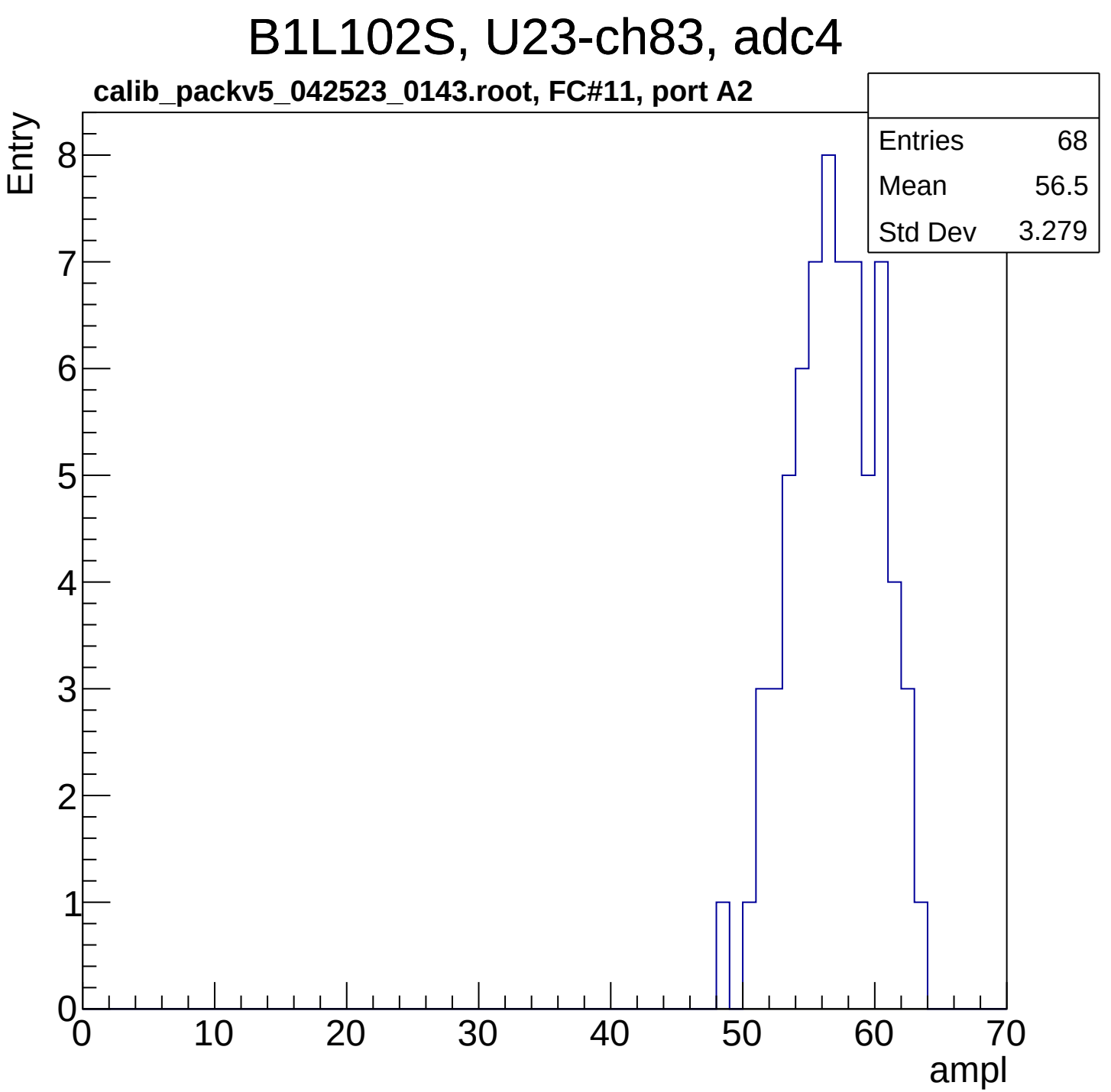

B1L102S, U23-ch82, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

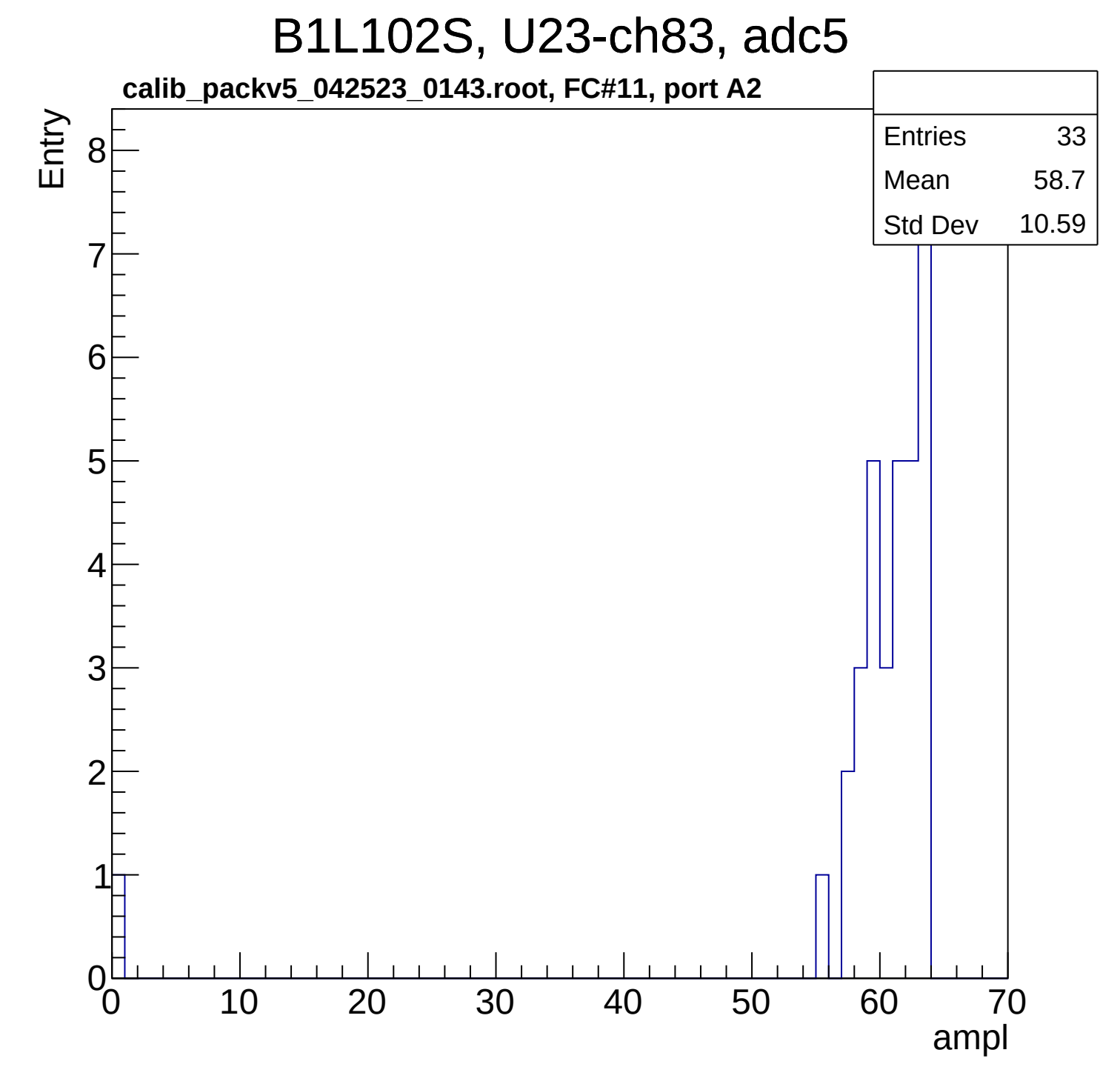

B1L102S, U23-ch83, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 29.95
Std Dev 3.065
Gaus mean : 31.0162
Gaus Width: 3.8042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

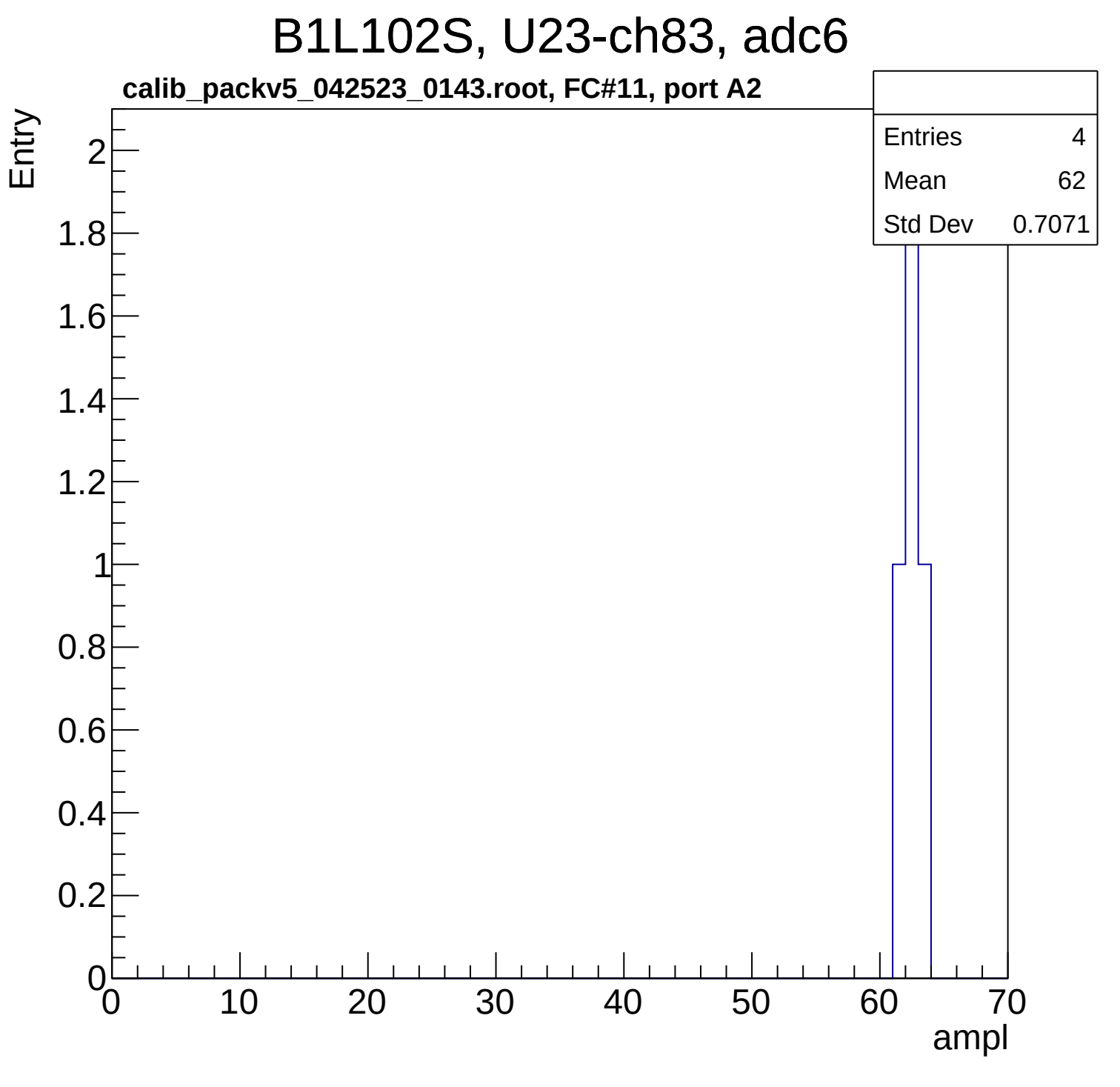

B1L102S, U23-ch83, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 36.77
Std Dev 3.489
Gaus mean : 37.3054
Gaus Width: 3.8595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

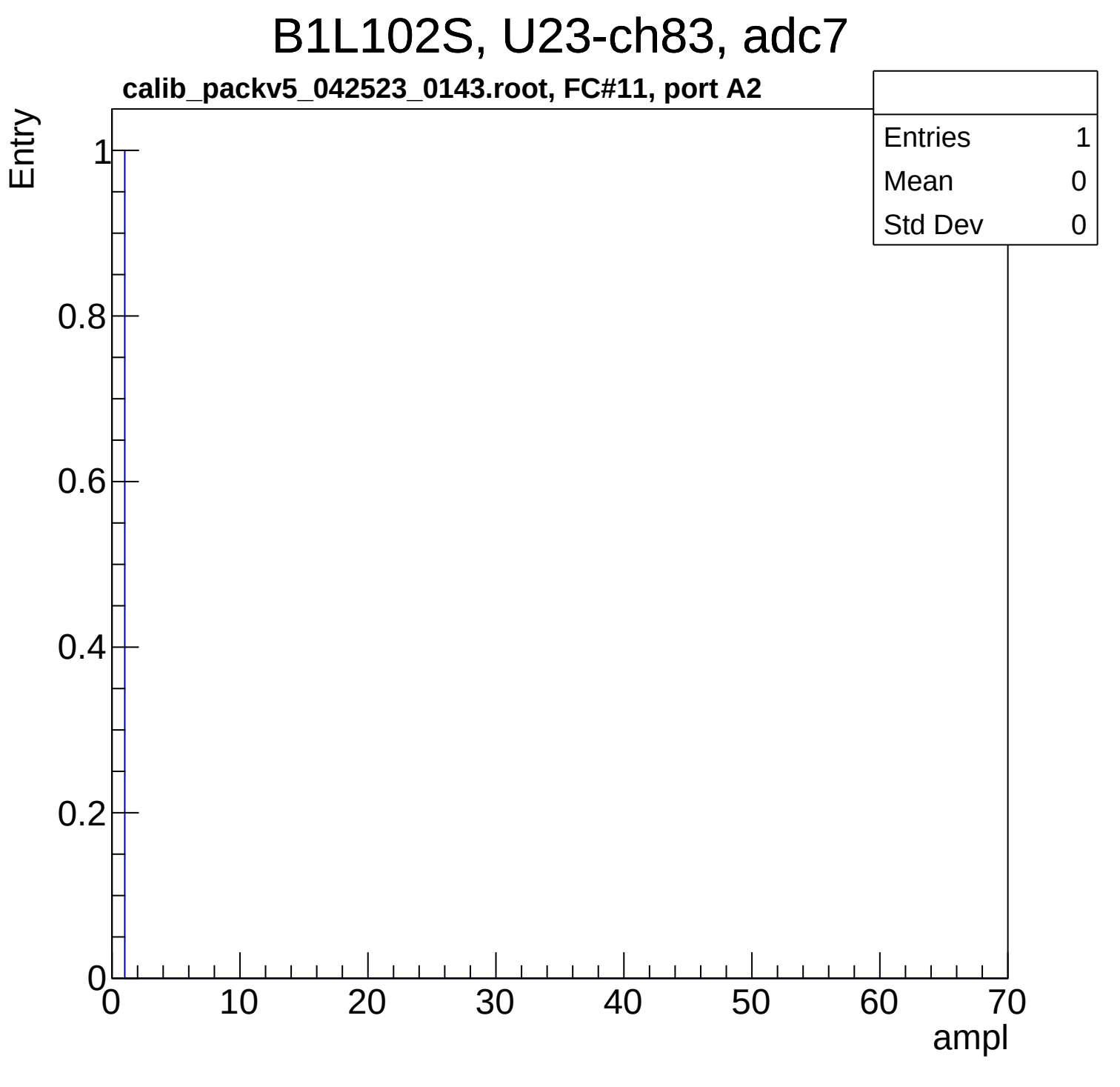

B1L102S, U23-ch83, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 42.6
Std Dev 3.262
Gaus mean : 42.8276
Gaus Width: 3.4994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

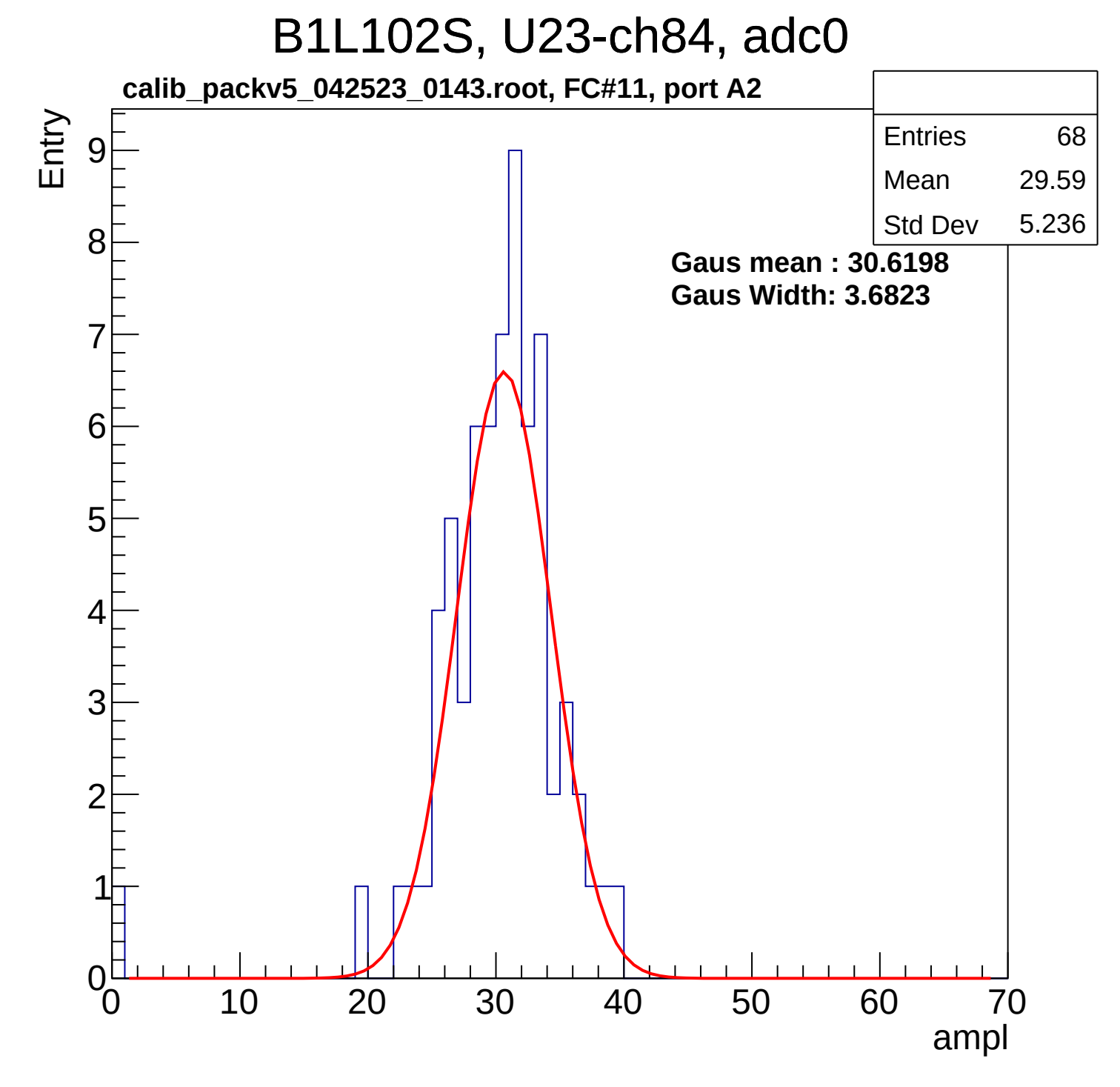

B1L102S, U23-ch83, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 49.77
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

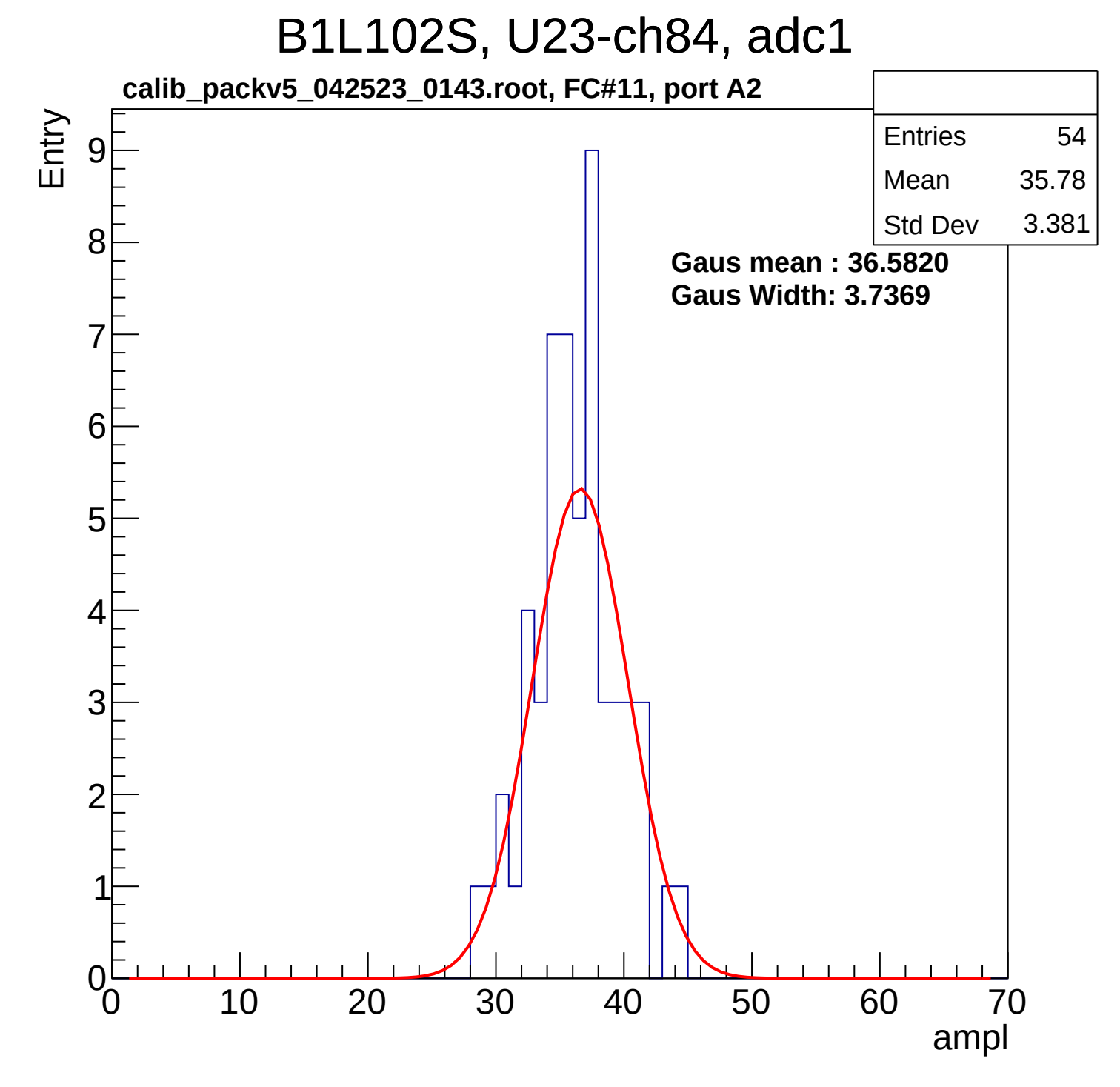

B1L102S, U23-ch83, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 56.5
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

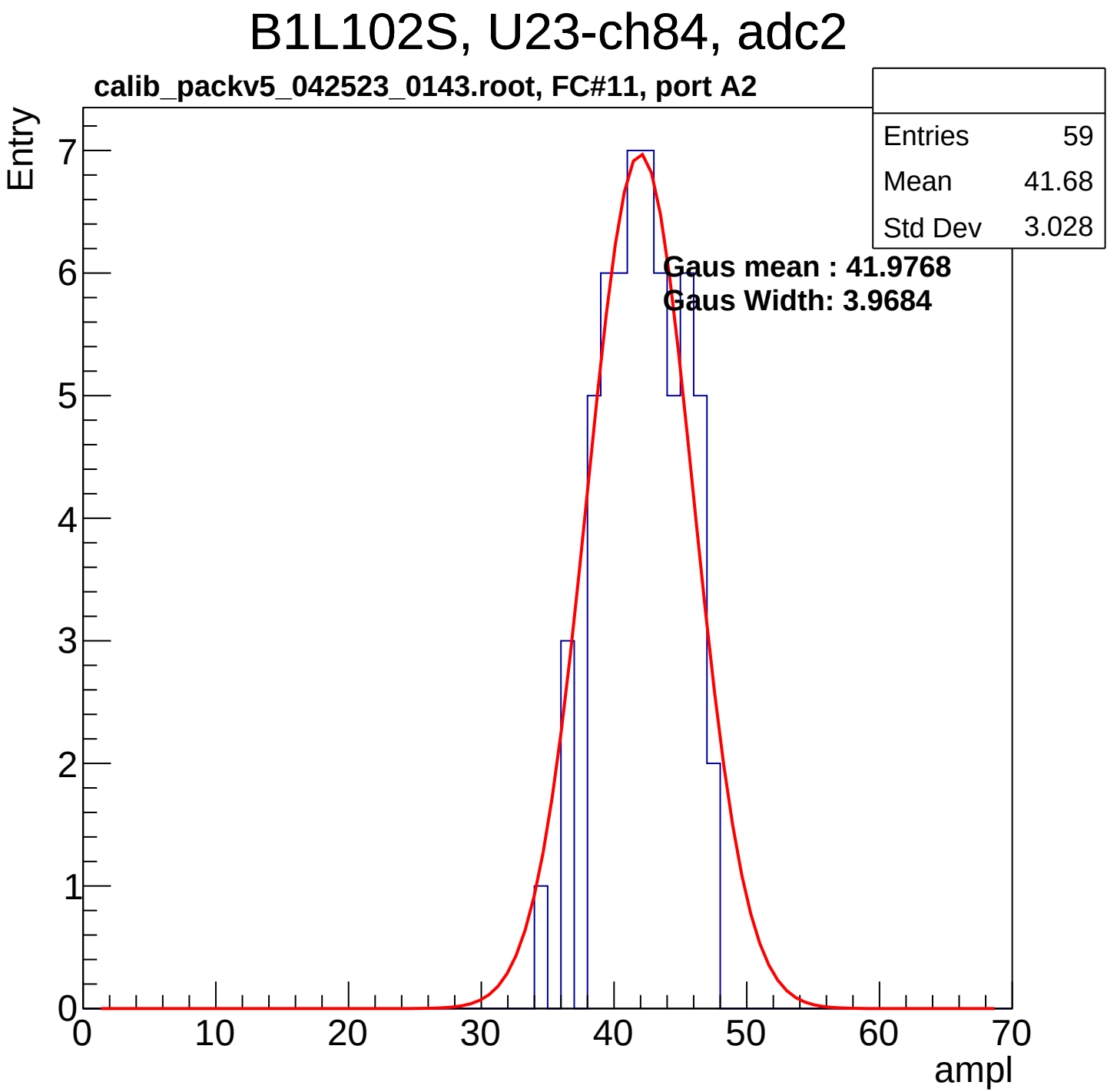

B1L102S, U23-ch83, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 33
Mean 58.7
Std Dev 10.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

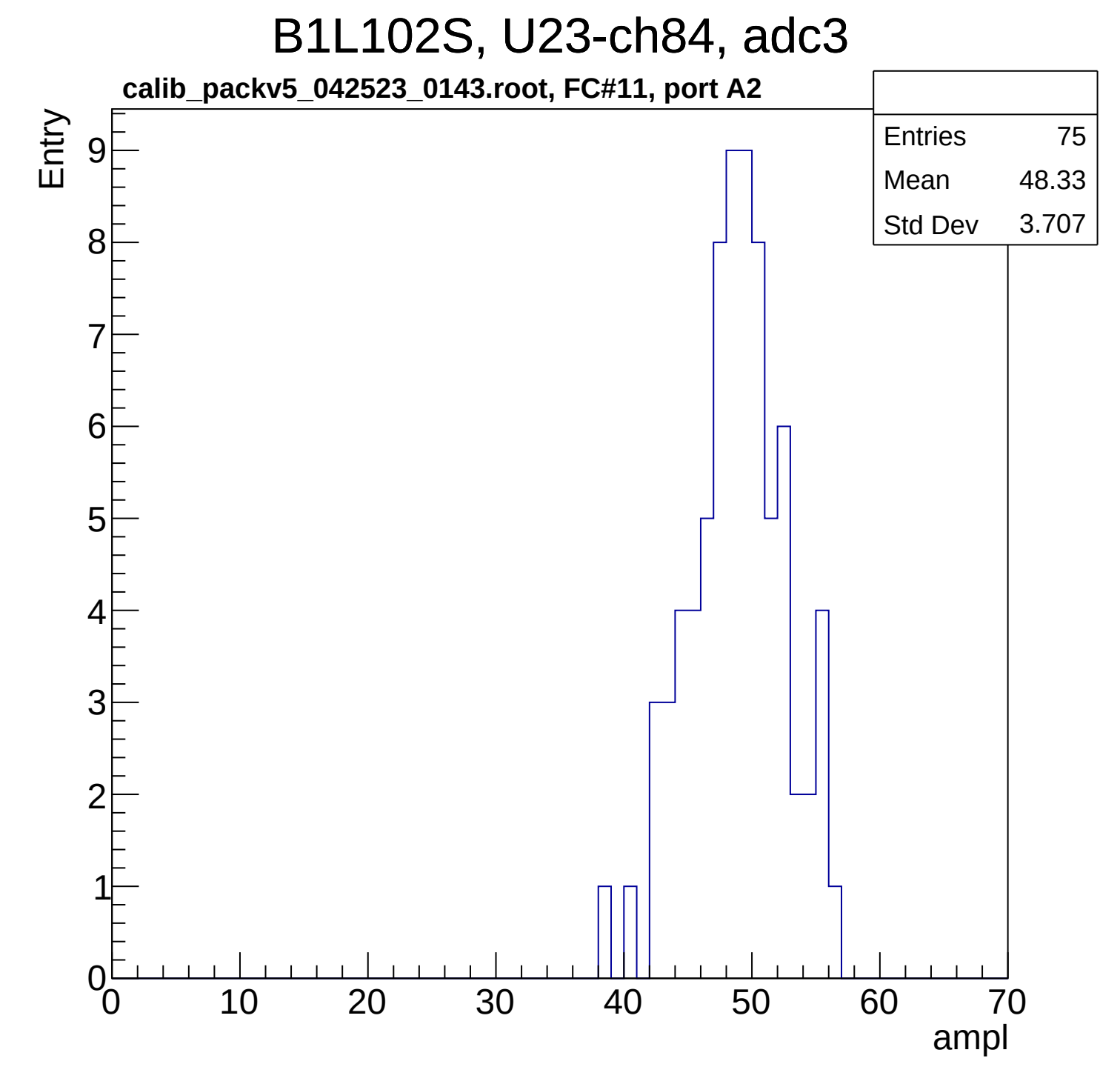

B1L102S, U23-ch83, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

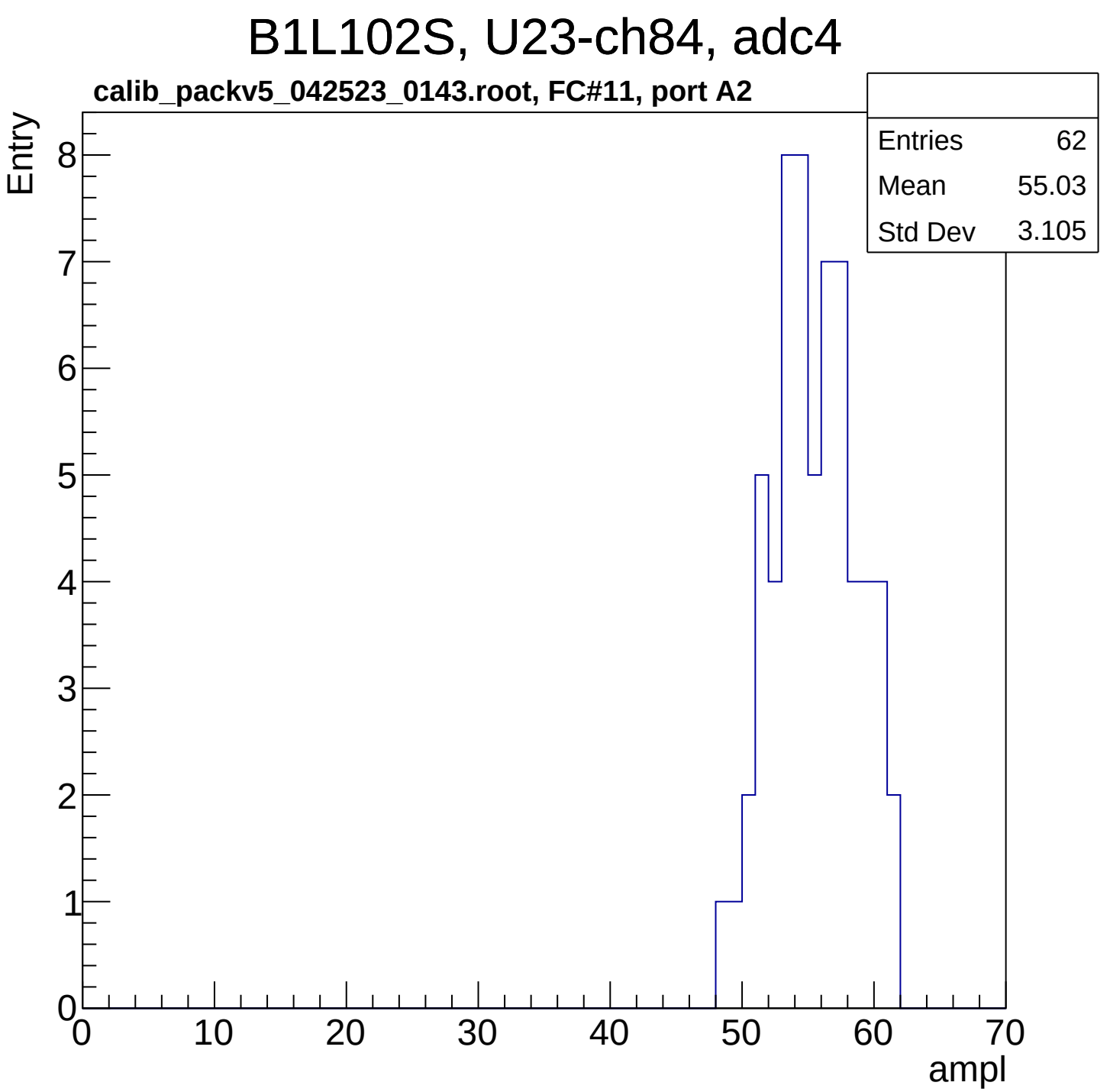

B1L102S, U23-ch83, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

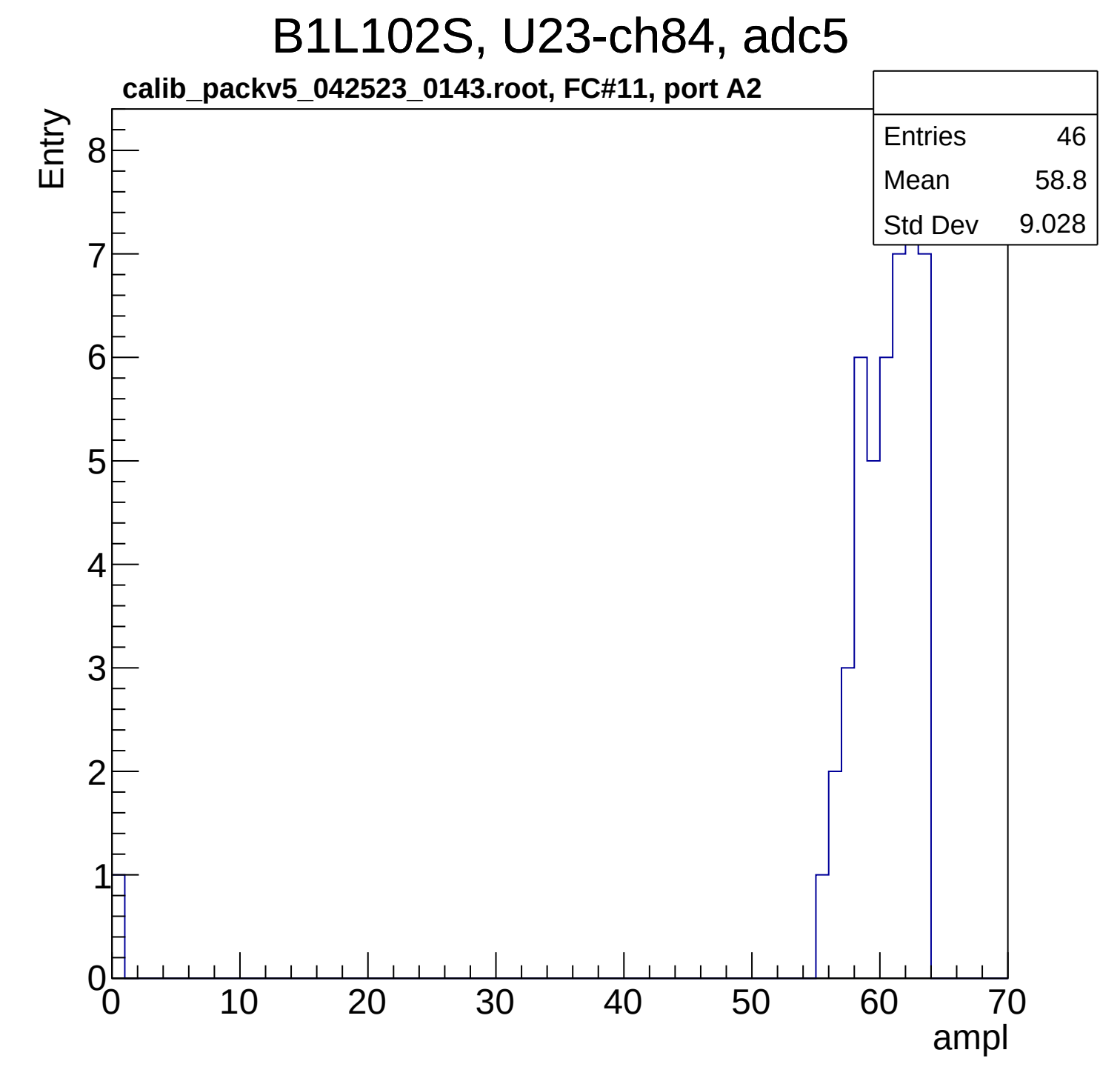

B1L102S, U23-ch84, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.59
Std Dev 5.236
Gaus mean : 30.6198
Gaus Width: 3.6823
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

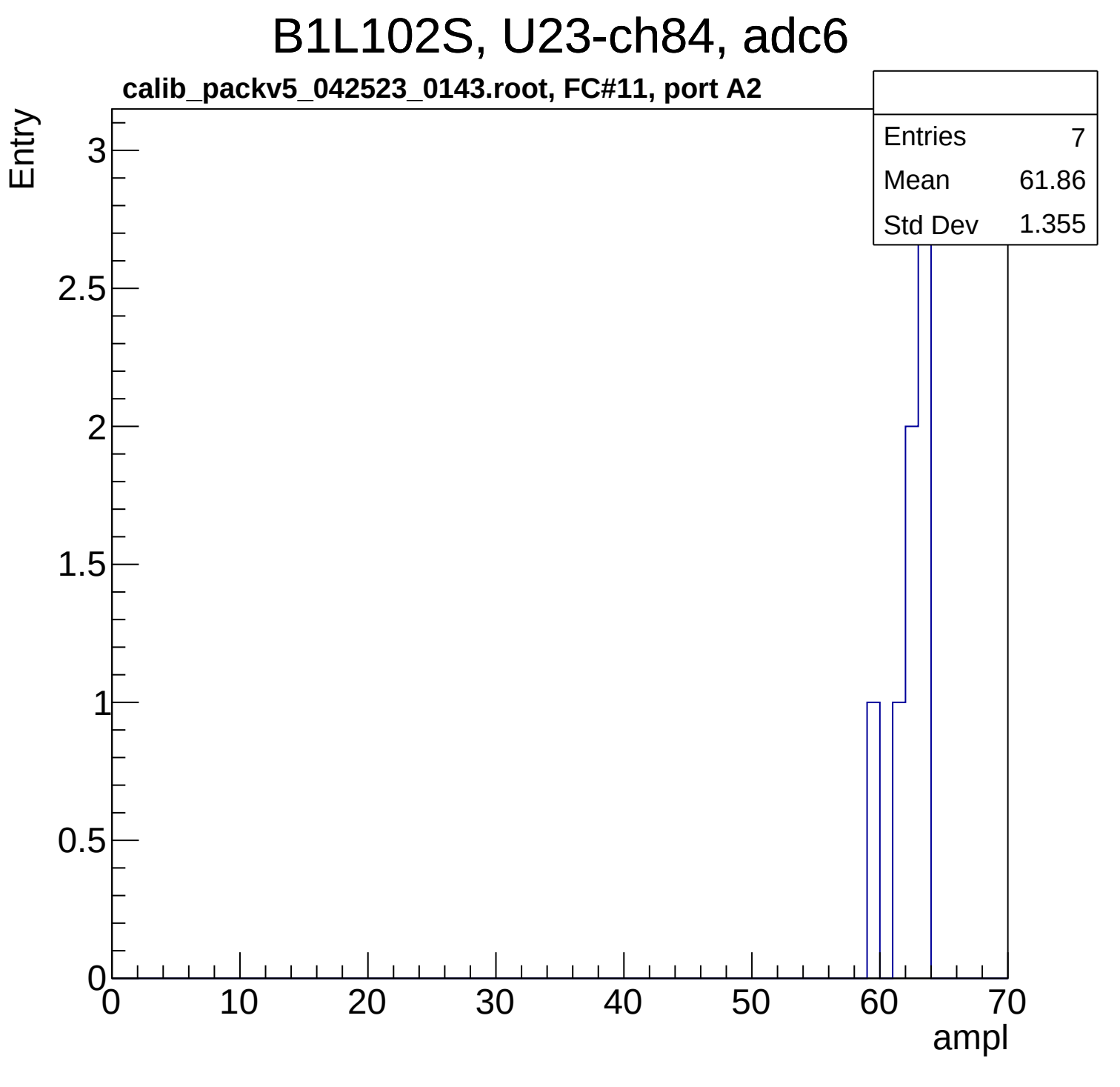

B1L102S, U23-ch84, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 35.78
Std Dev 3.381
Gaus mean : 36.5820
Gaus Width: 3.7369
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

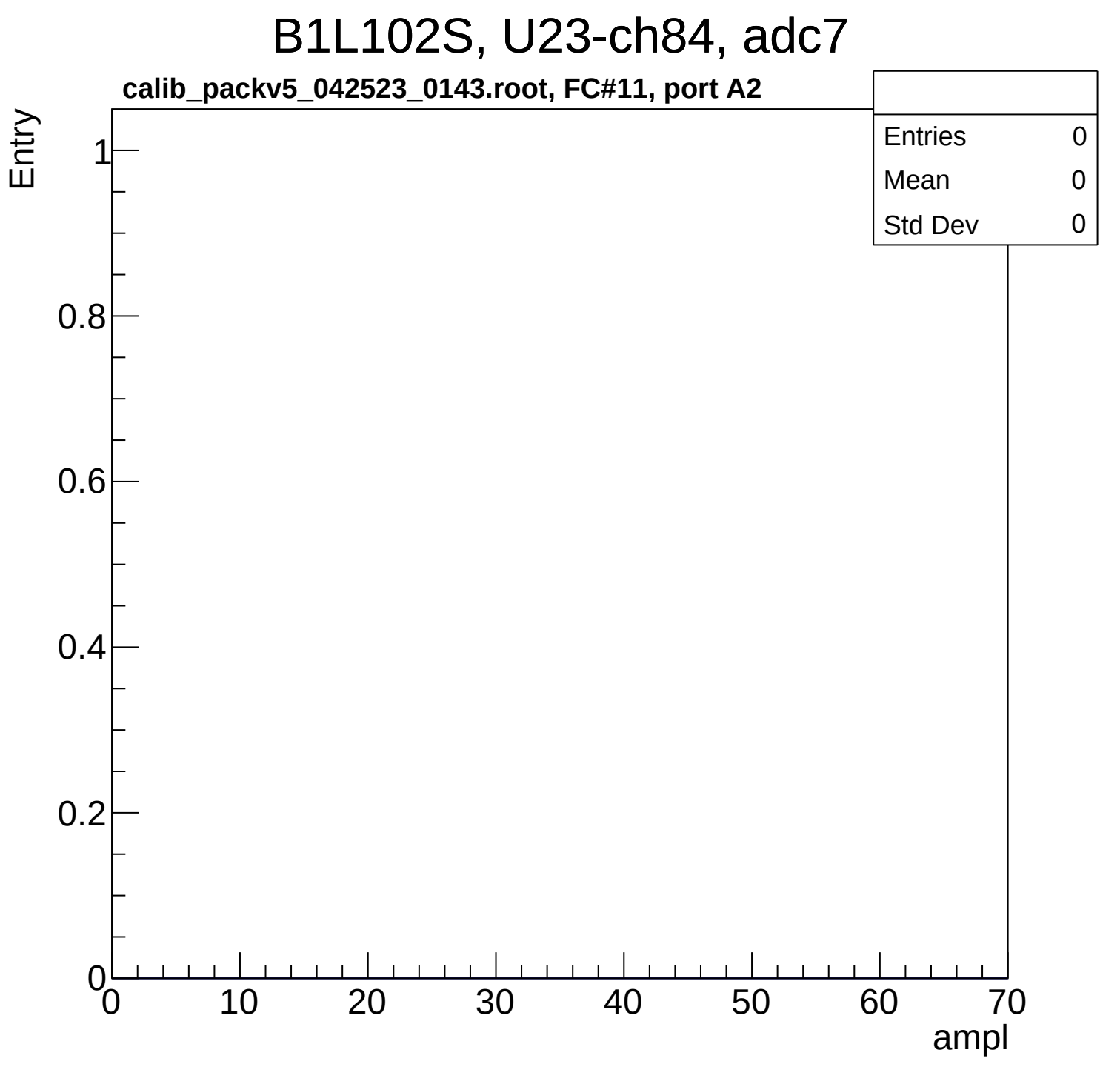

B1L102S, U23-ch84, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 41.68
Std Dev 3.028
Gaus mean : 41.9768
Gaus Width: 3.9684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

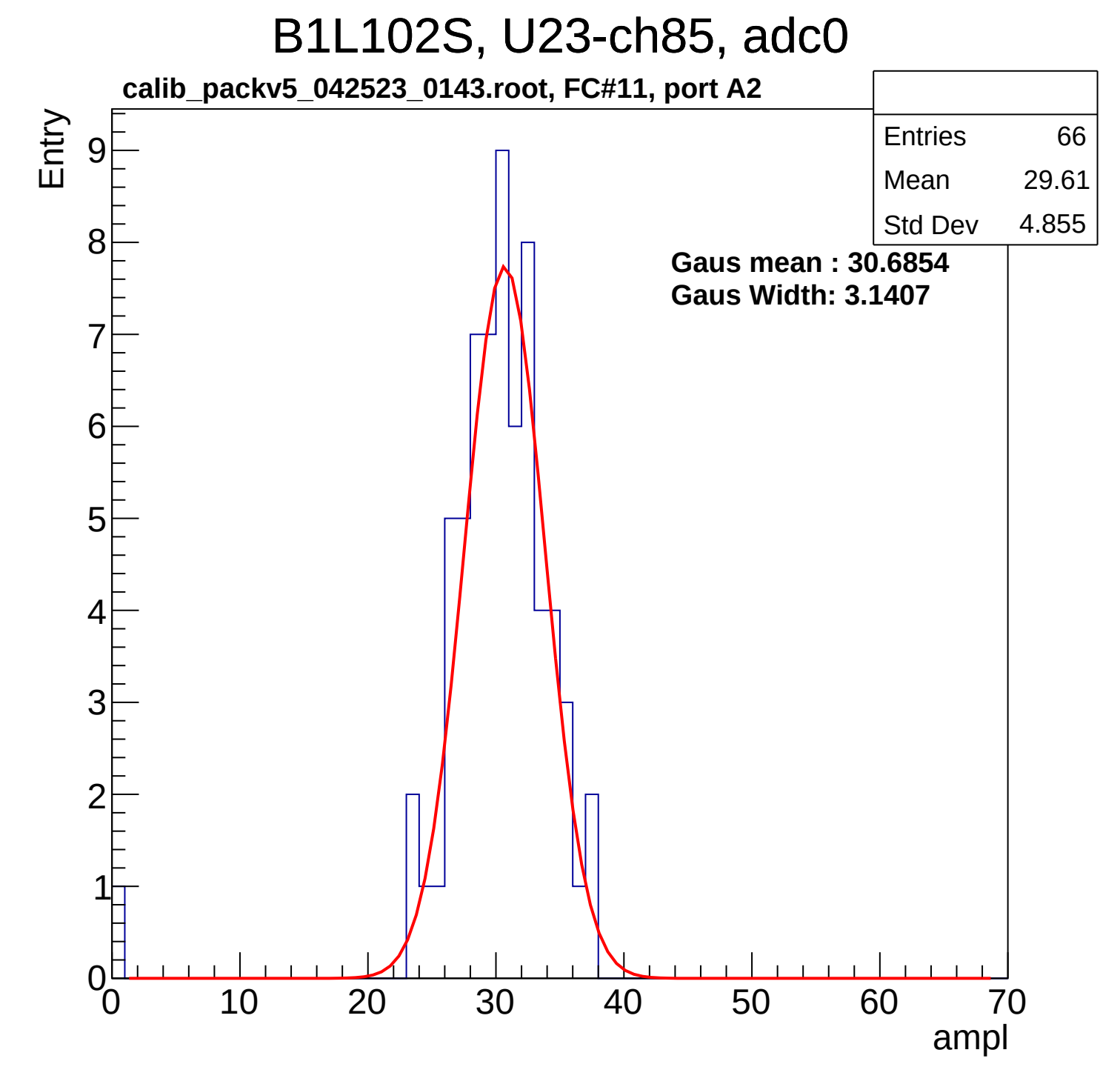

B1L102S, U23-ch84, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 48.33
Std Dev 3.707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

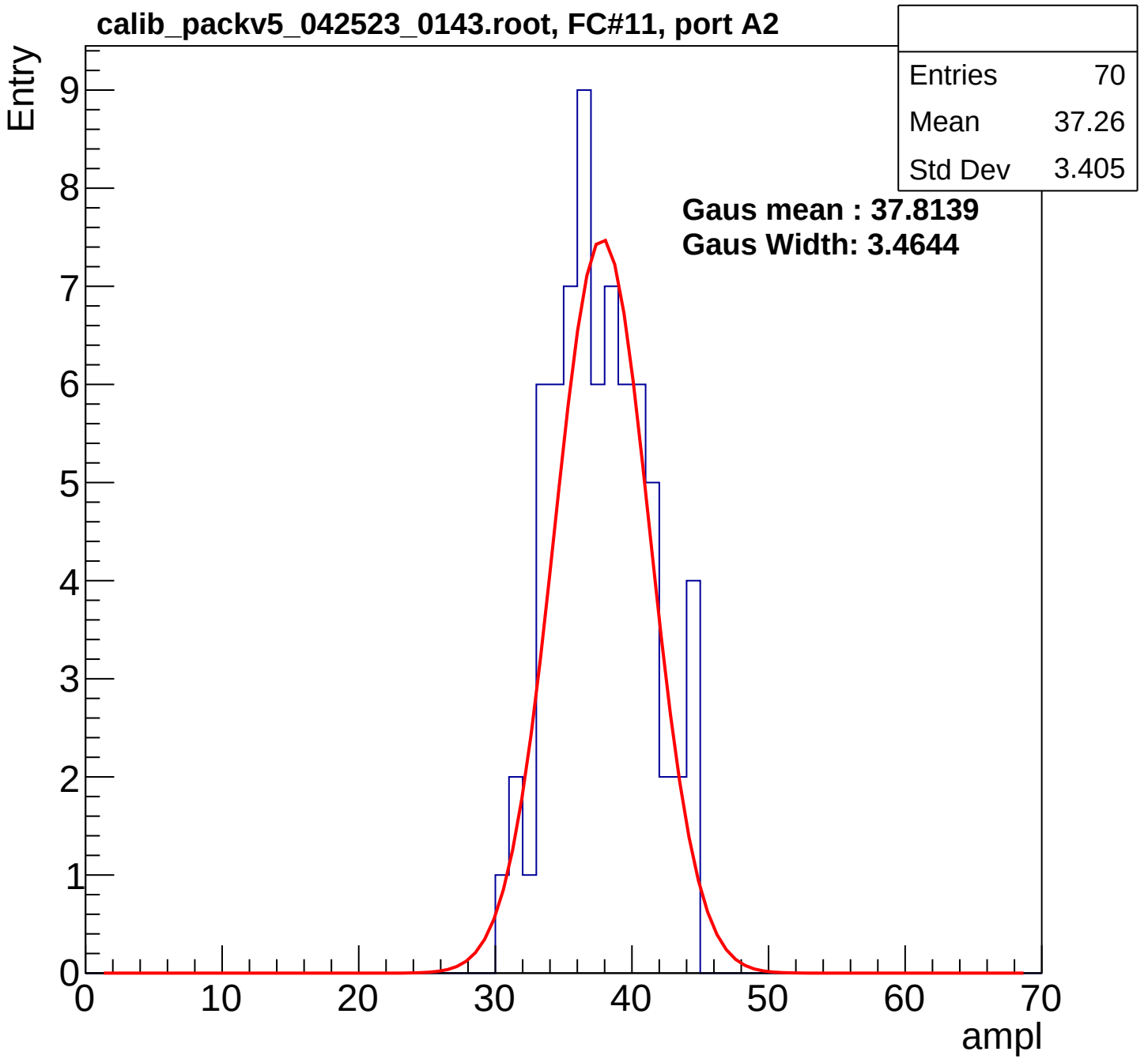

B1L102S, U23-ch84, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 55.03
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

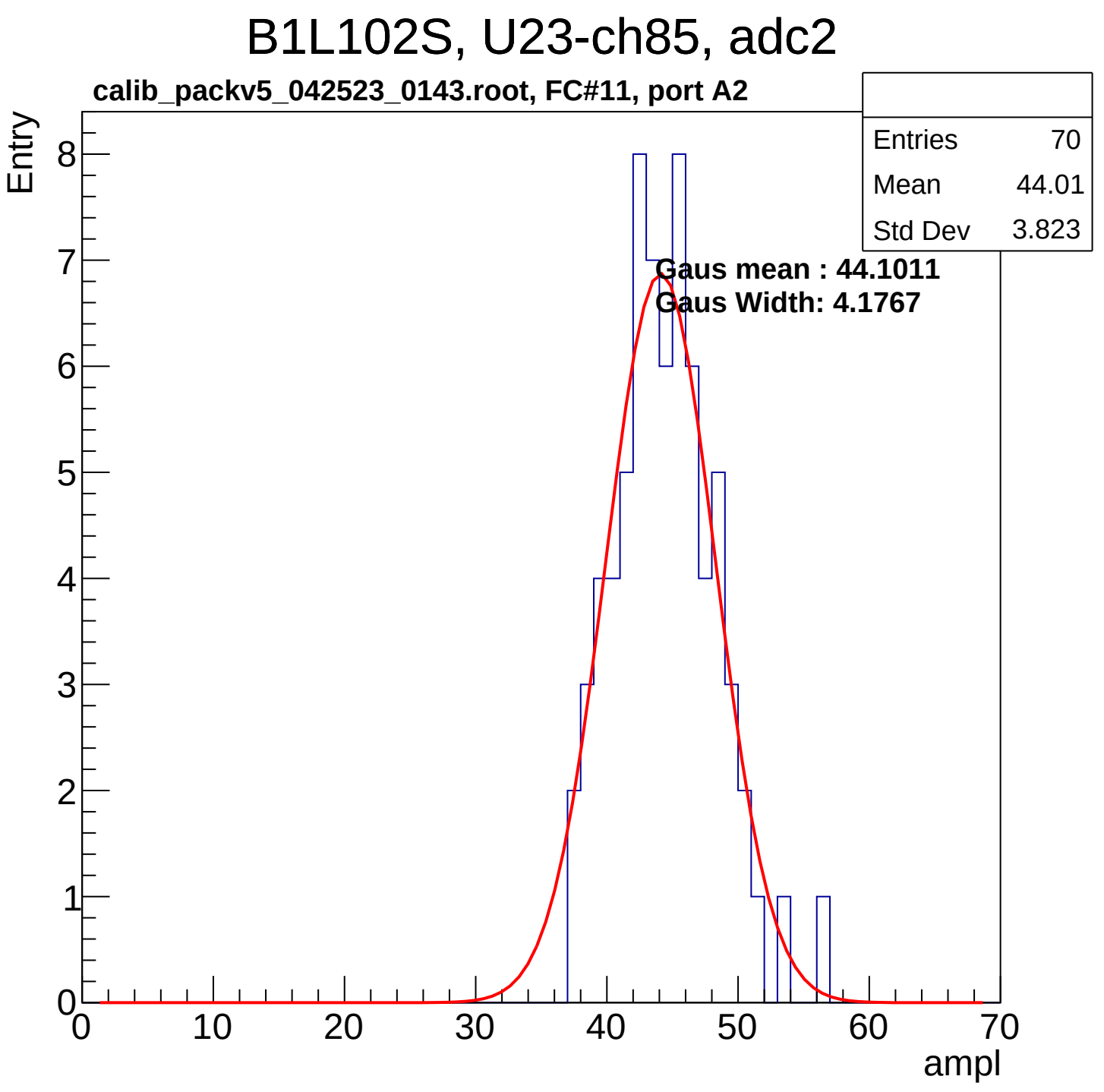

B1L102S, U23-ch84, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 58.8
Std Dev 9.028
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

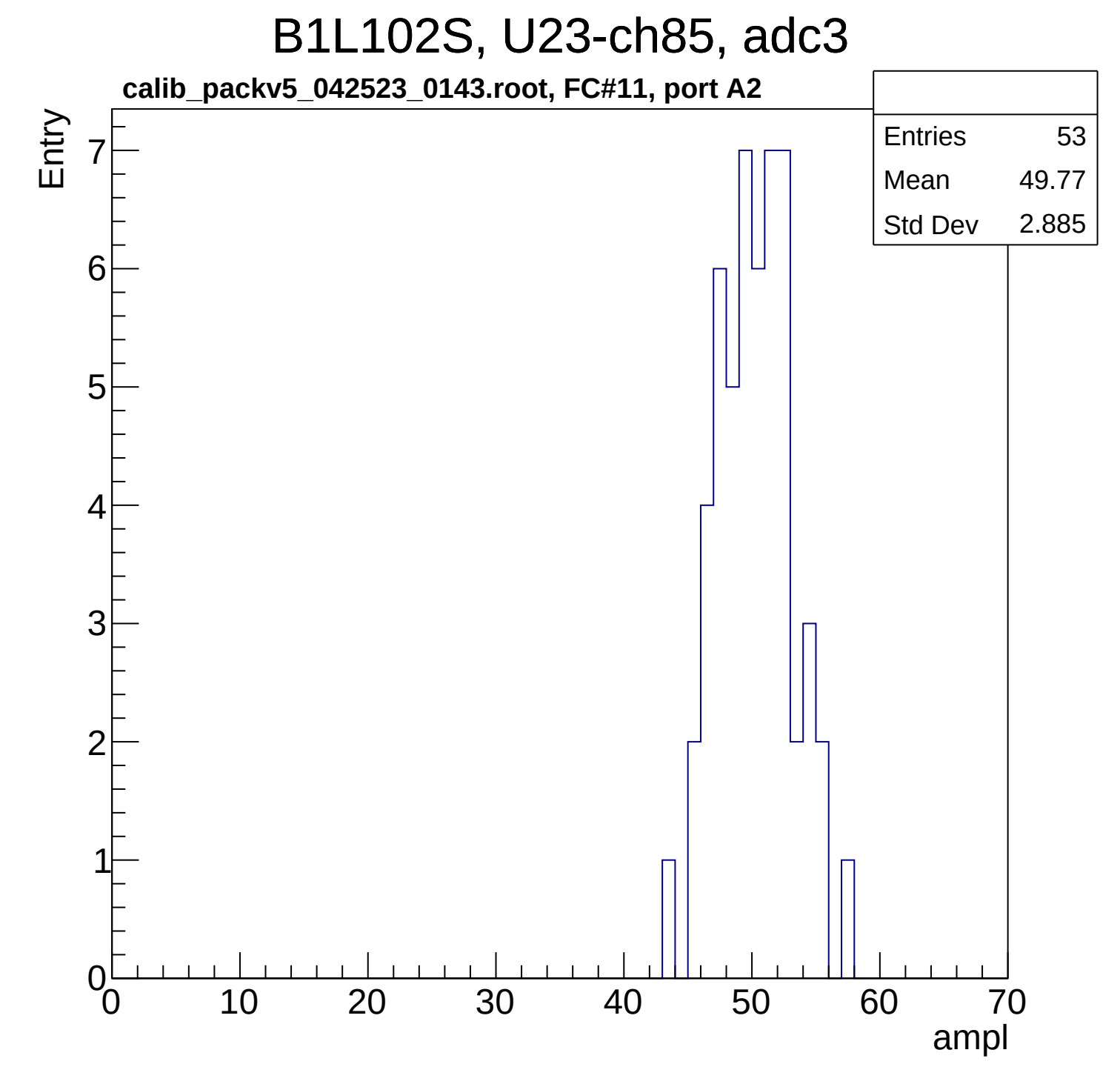

B1L102S, U23-ch84, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 61.86
Std Dev 1.355
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

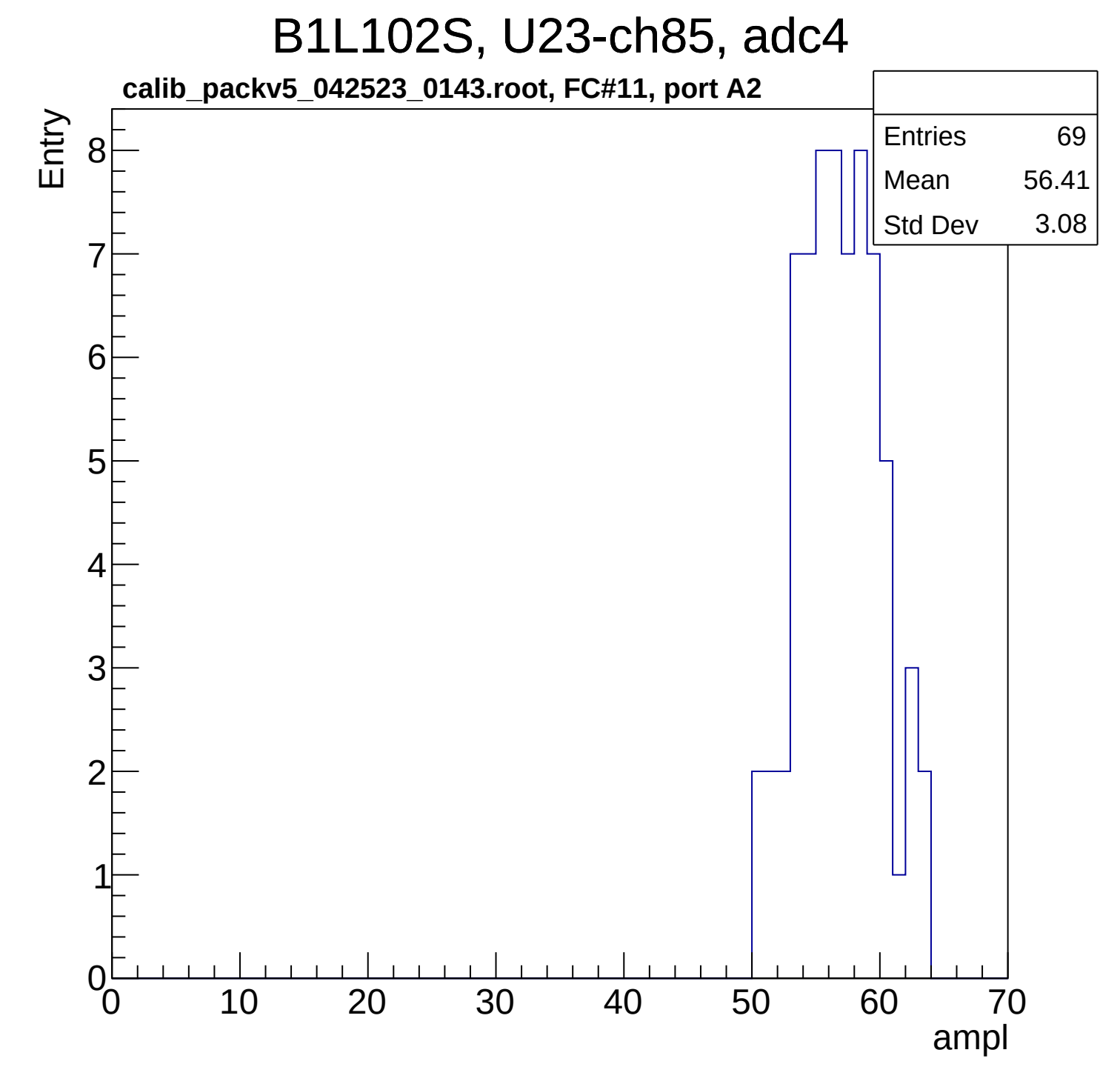

B1L102S, U23-ch84, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

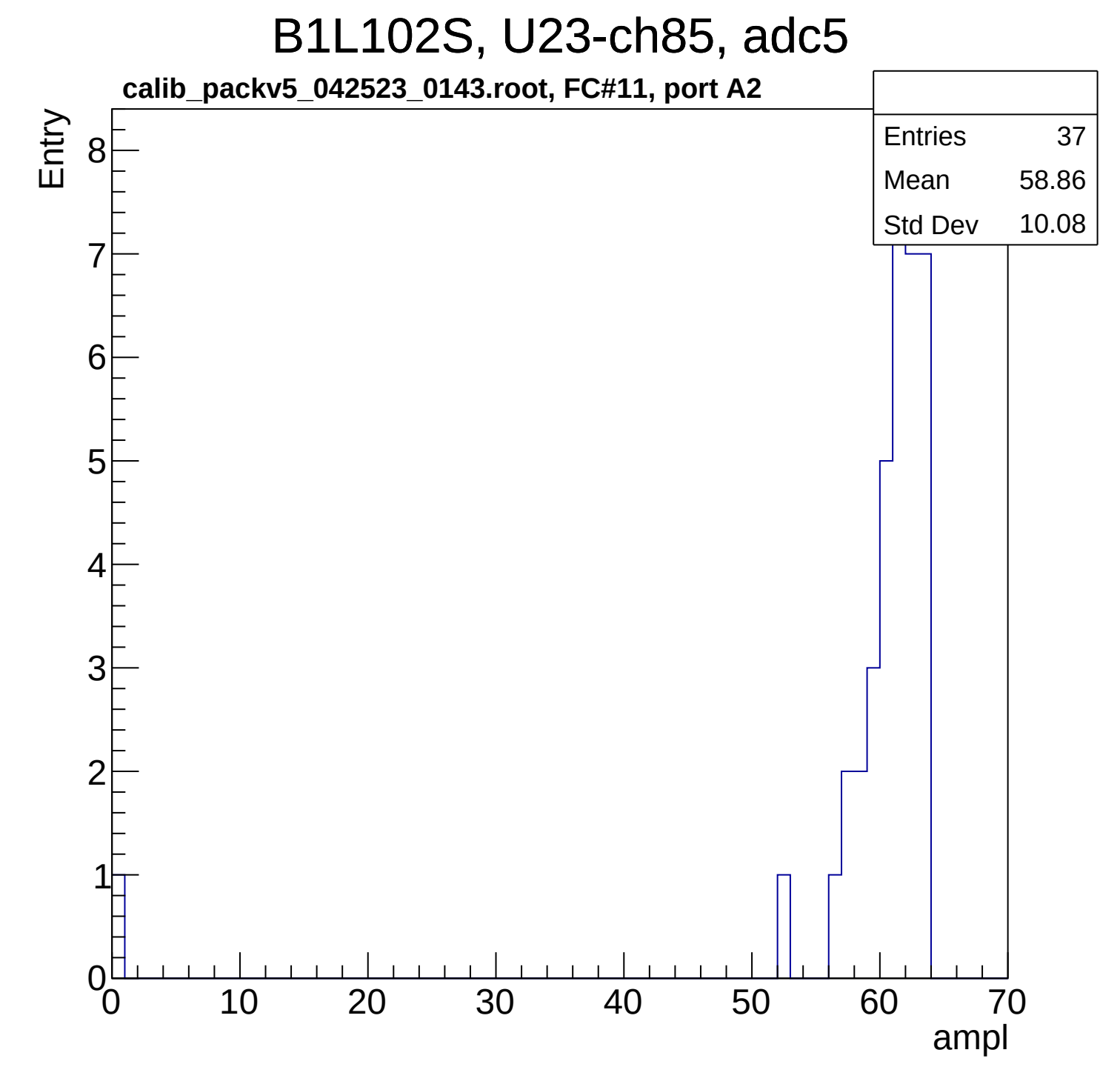

B1L102S, U23-ch85, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 29.61
Std Dev 4.855
Gaus mean : 30.6854
Gaus Width: 3.1407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

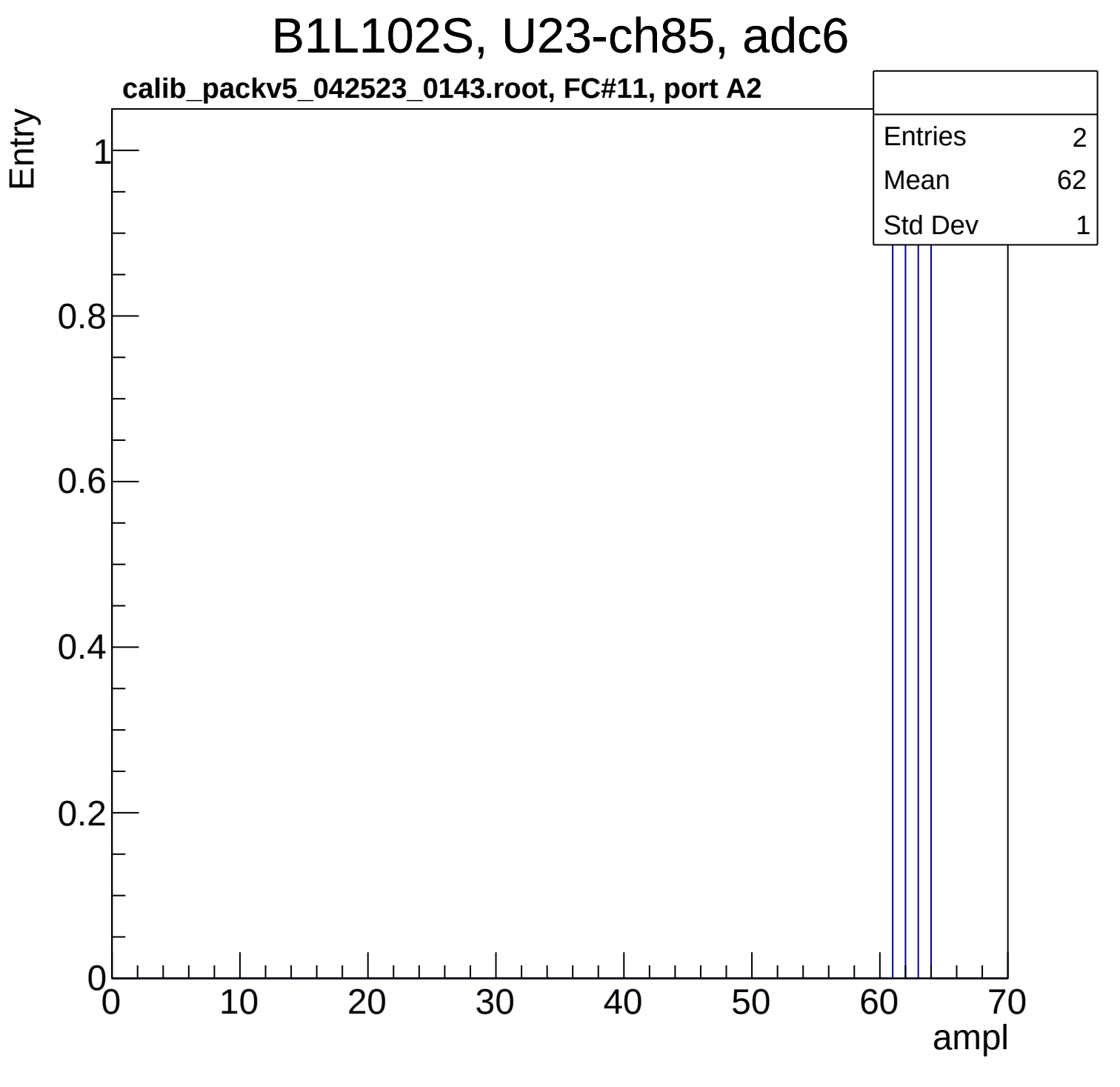

B1L102S, U23-ch85, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 37.26
Std Dev 3.405
Gaus mean : 37.8139
Gaus Width: 3.4644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

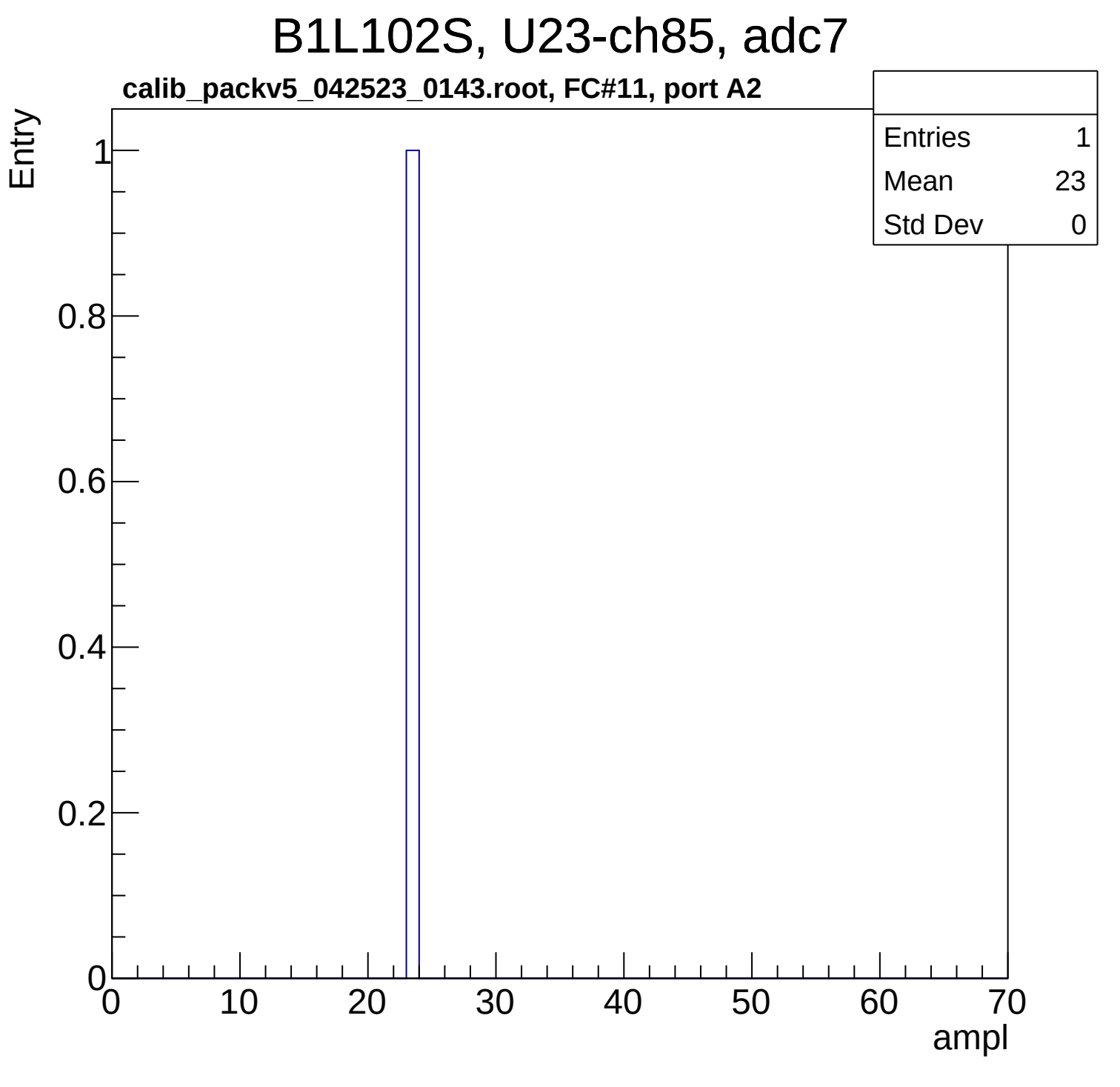

B1L102S, U23-ch85, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 44.01
Std Dev 3.823
Gaus mean : 44.1011
Gaus Width: 4.1767
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

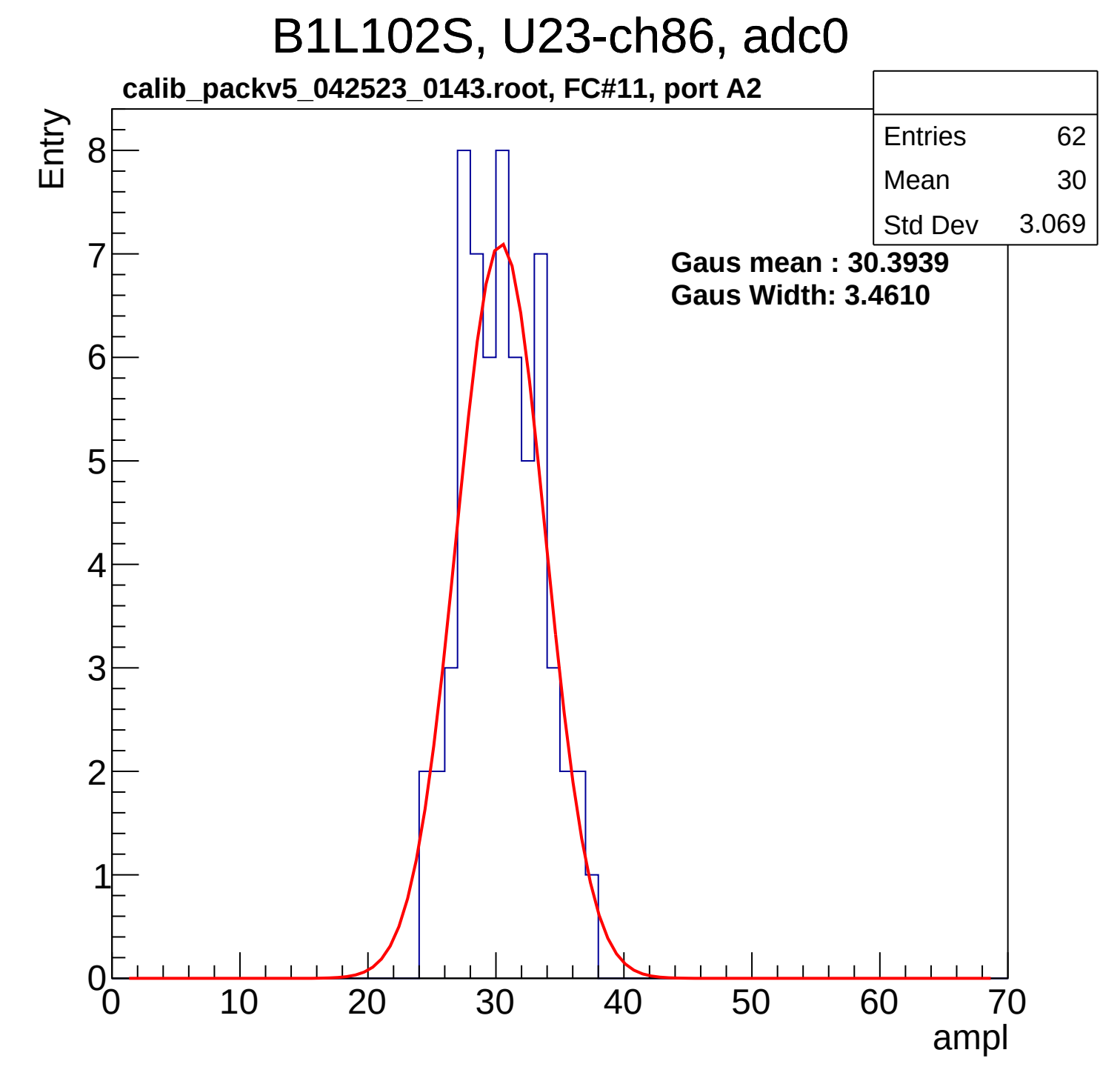

B1L102S, U23-ch85, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 49.77
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

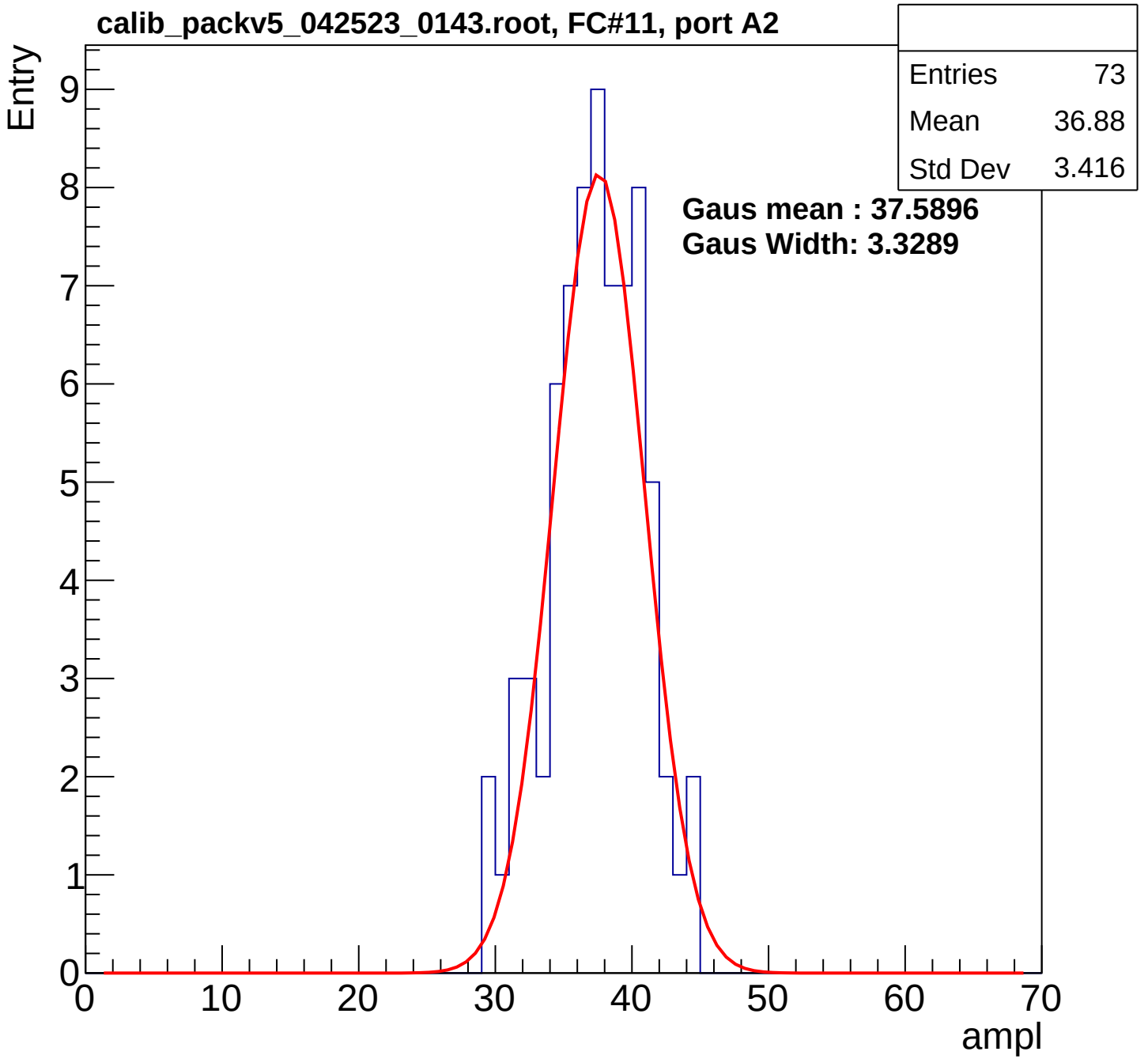

B1L102S, U23-ch85, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 56.41
Std Dev 3.08
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

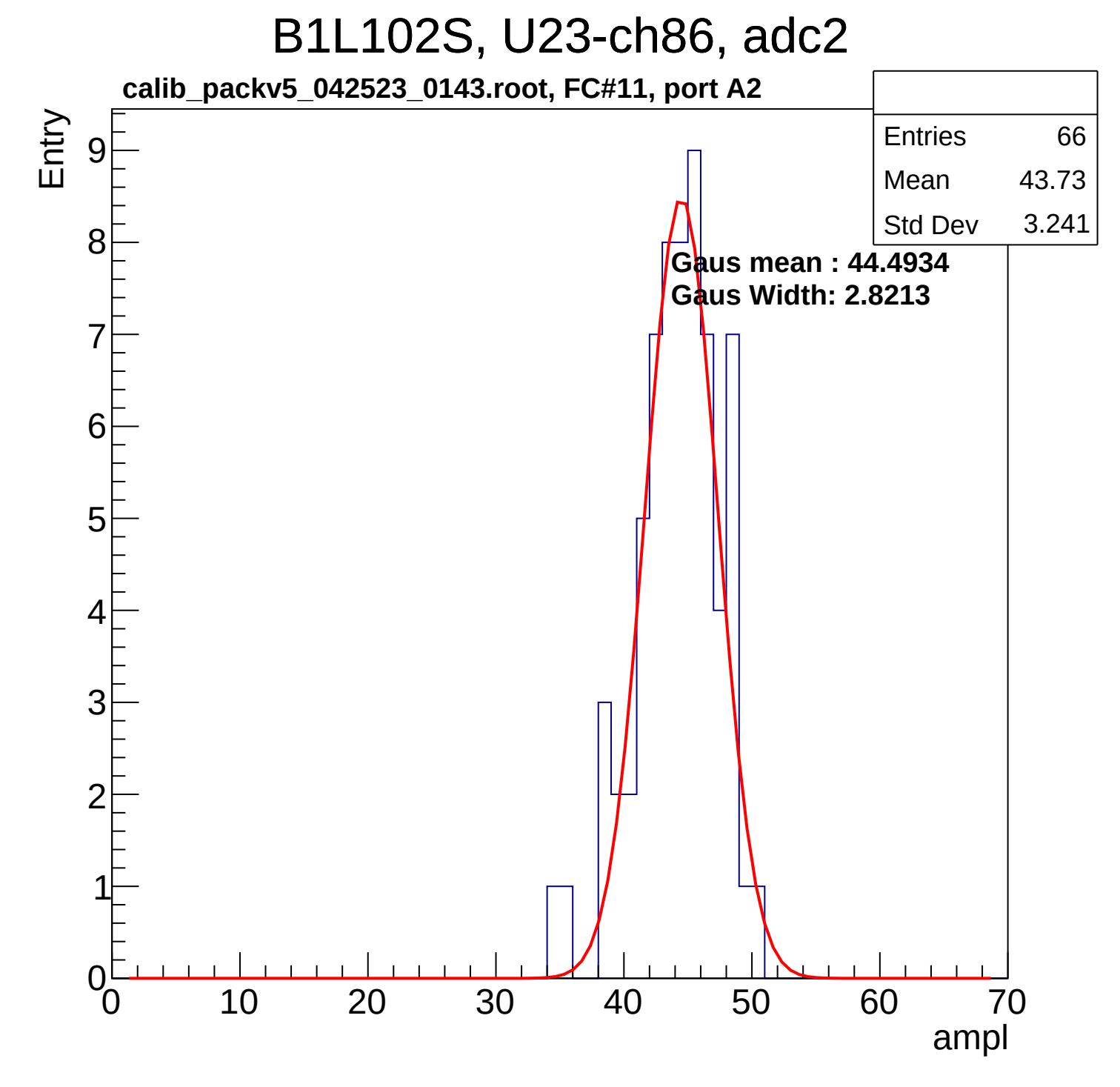

B1L102S, U23-ch85, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 58.86
Std Dev 10.08
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

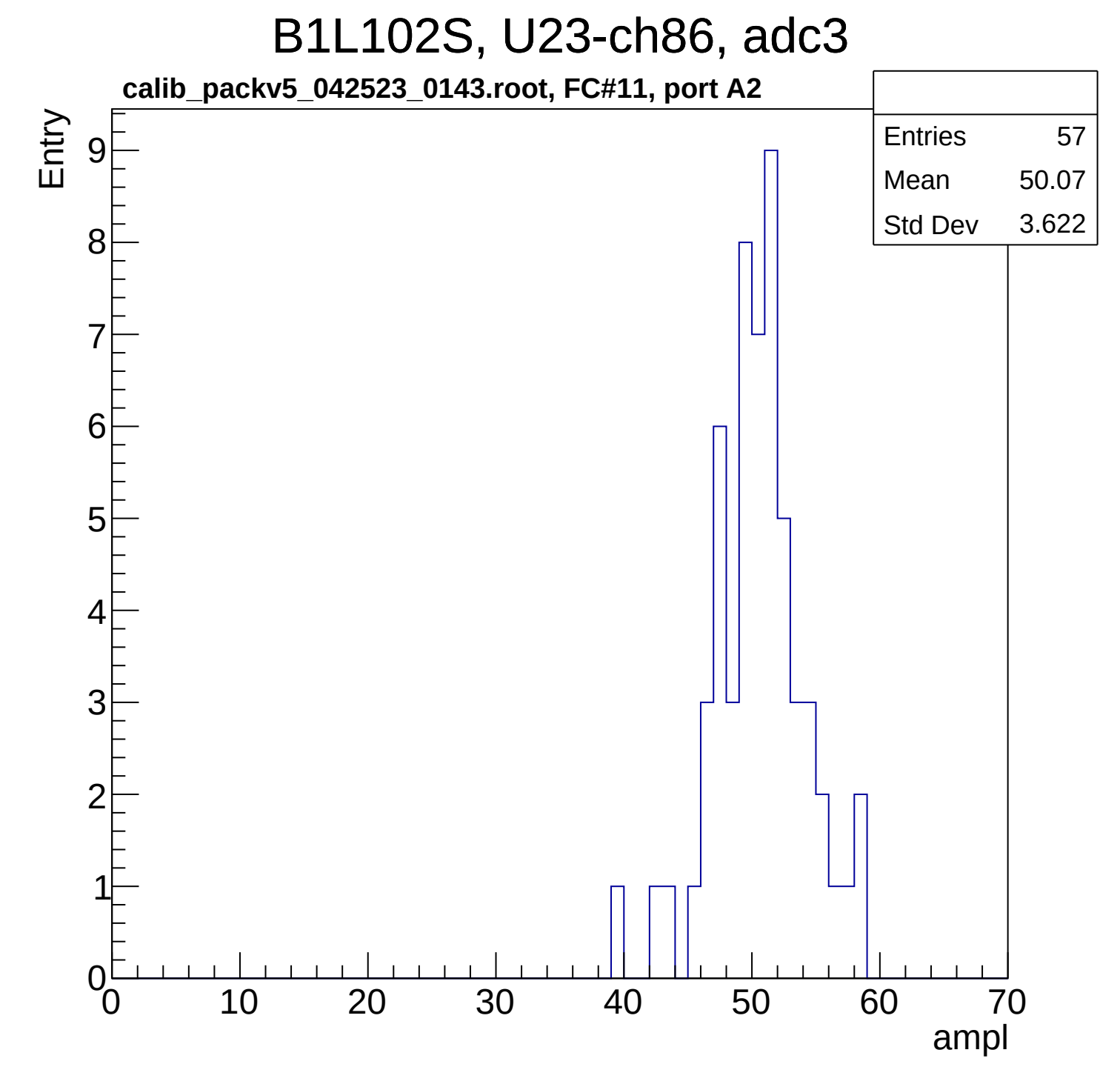

B1L102S, U23-ch85, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

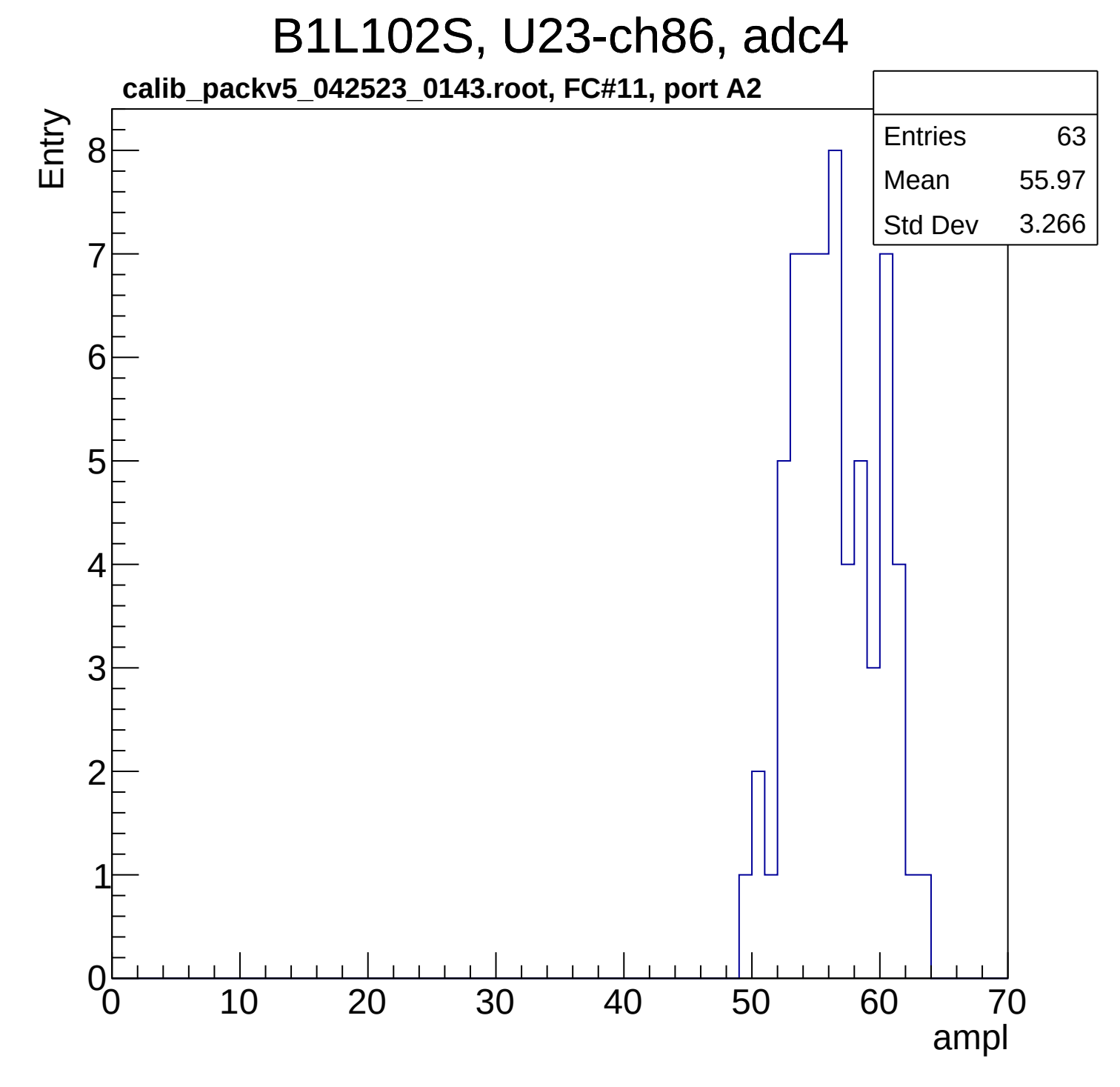

B1L102S, U23-ch85, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

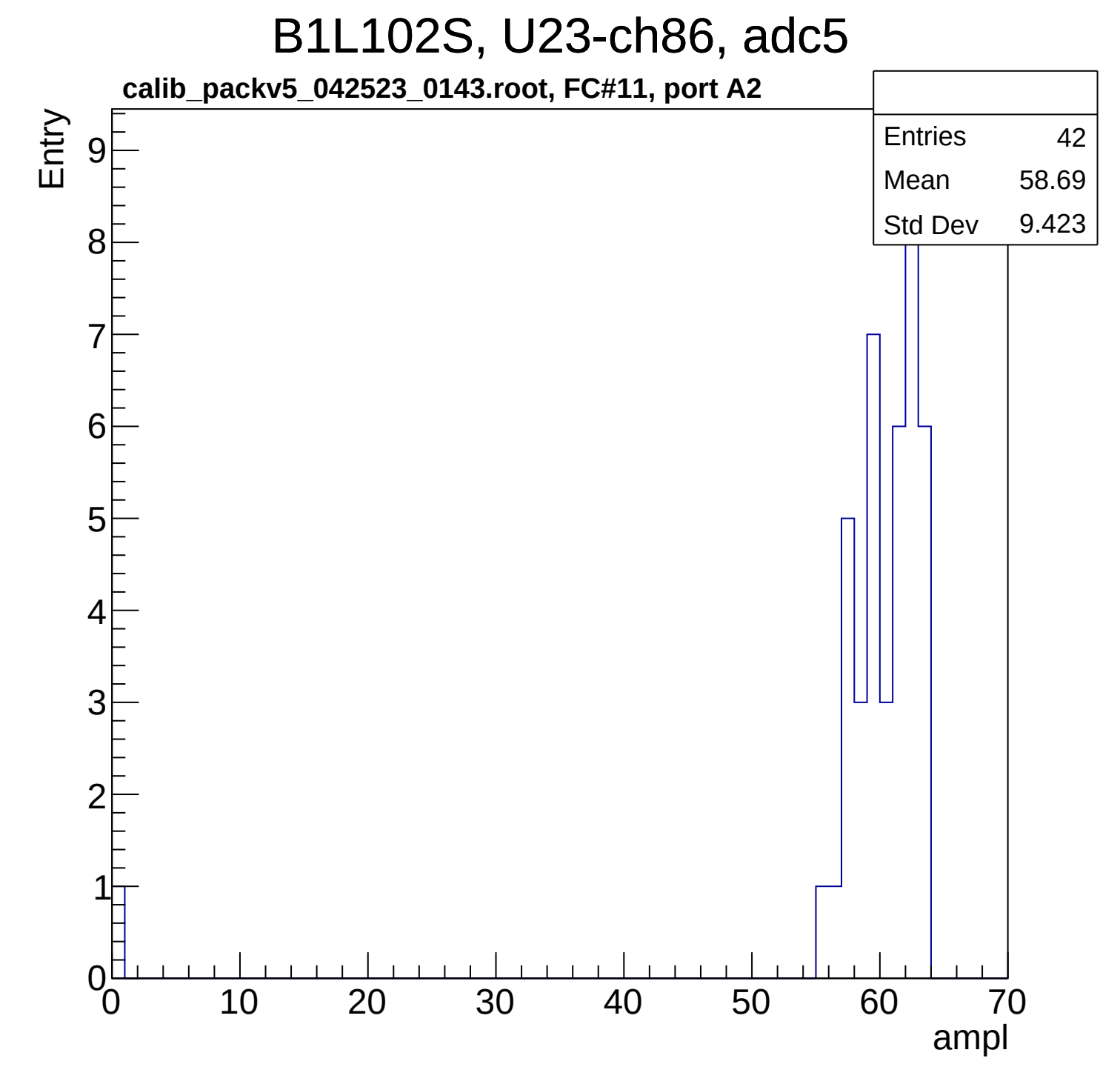

B1L102S, U23-ch86, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 30
Std Dev 3.069
Gaus mean : 30.3939
Gaus Width: 3.4610
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

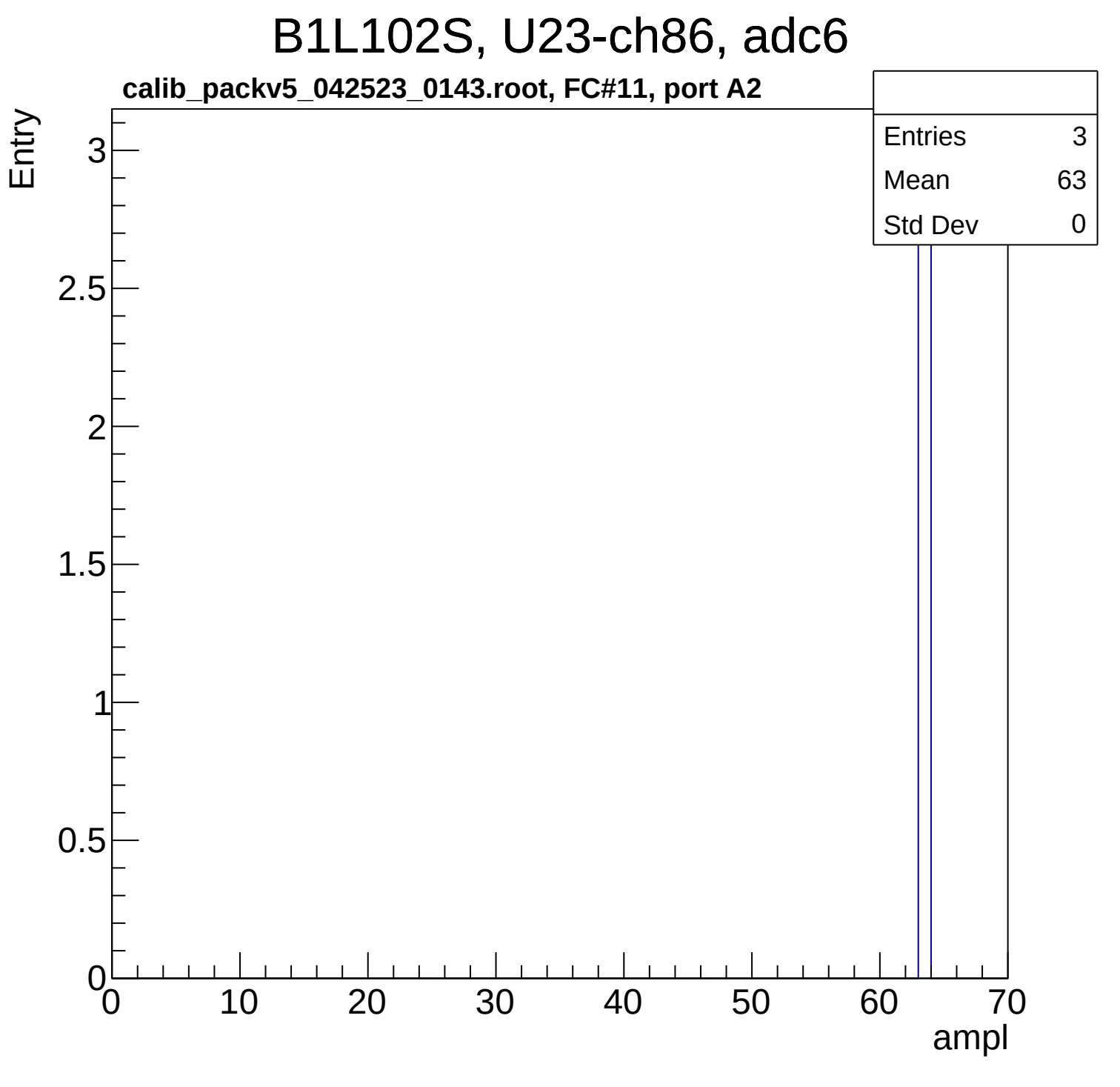

B1L102S, U23-ch86, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 36.88
Std Dev 3.416
Gaus mean : 37.5896
Gaus Width: 3.3289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

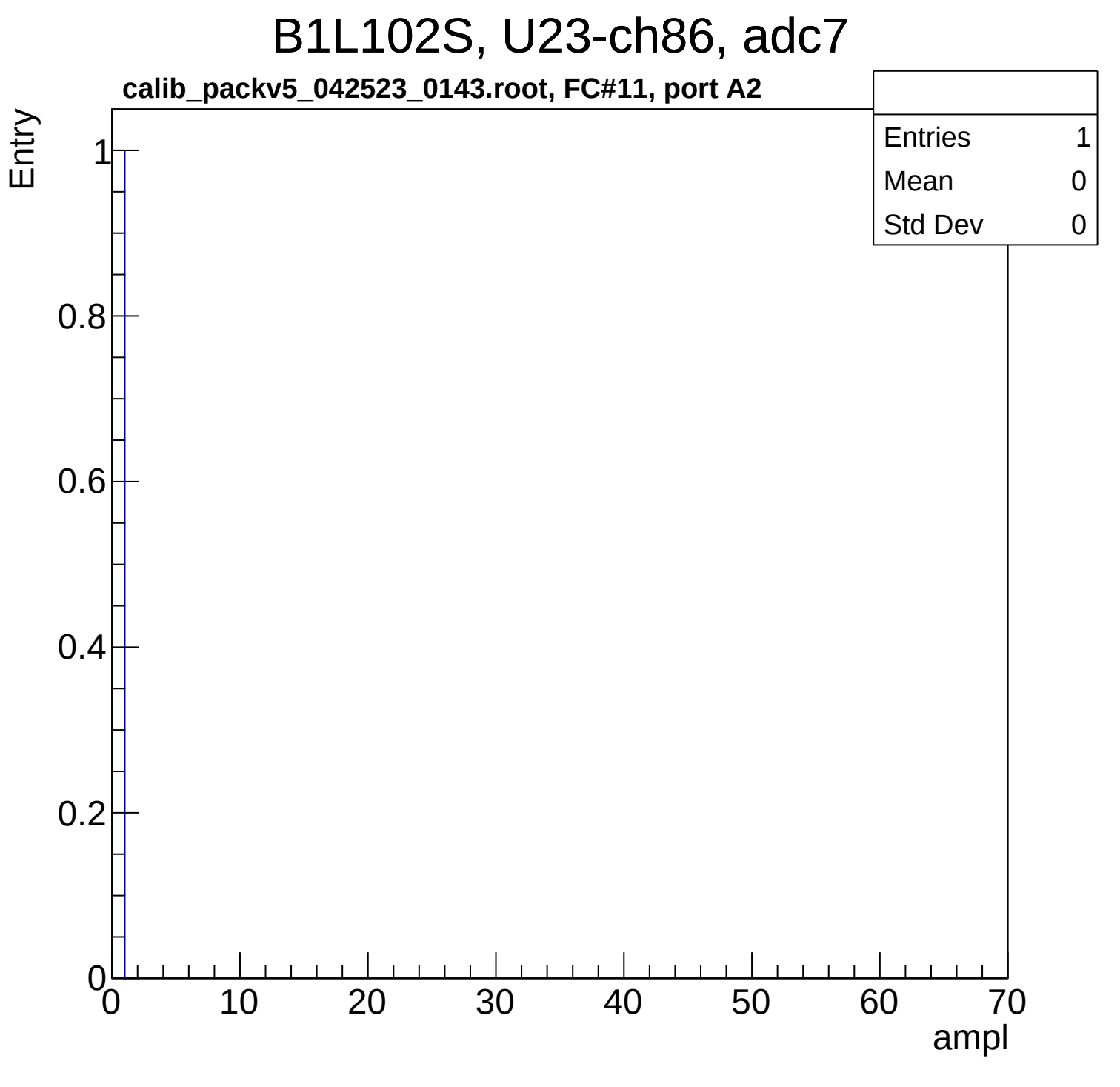

B1L102S, U23-ch86, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 43.73
Std Dev 3.241
Gaus mean : 44.4934
Gaus Width: 2.8213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

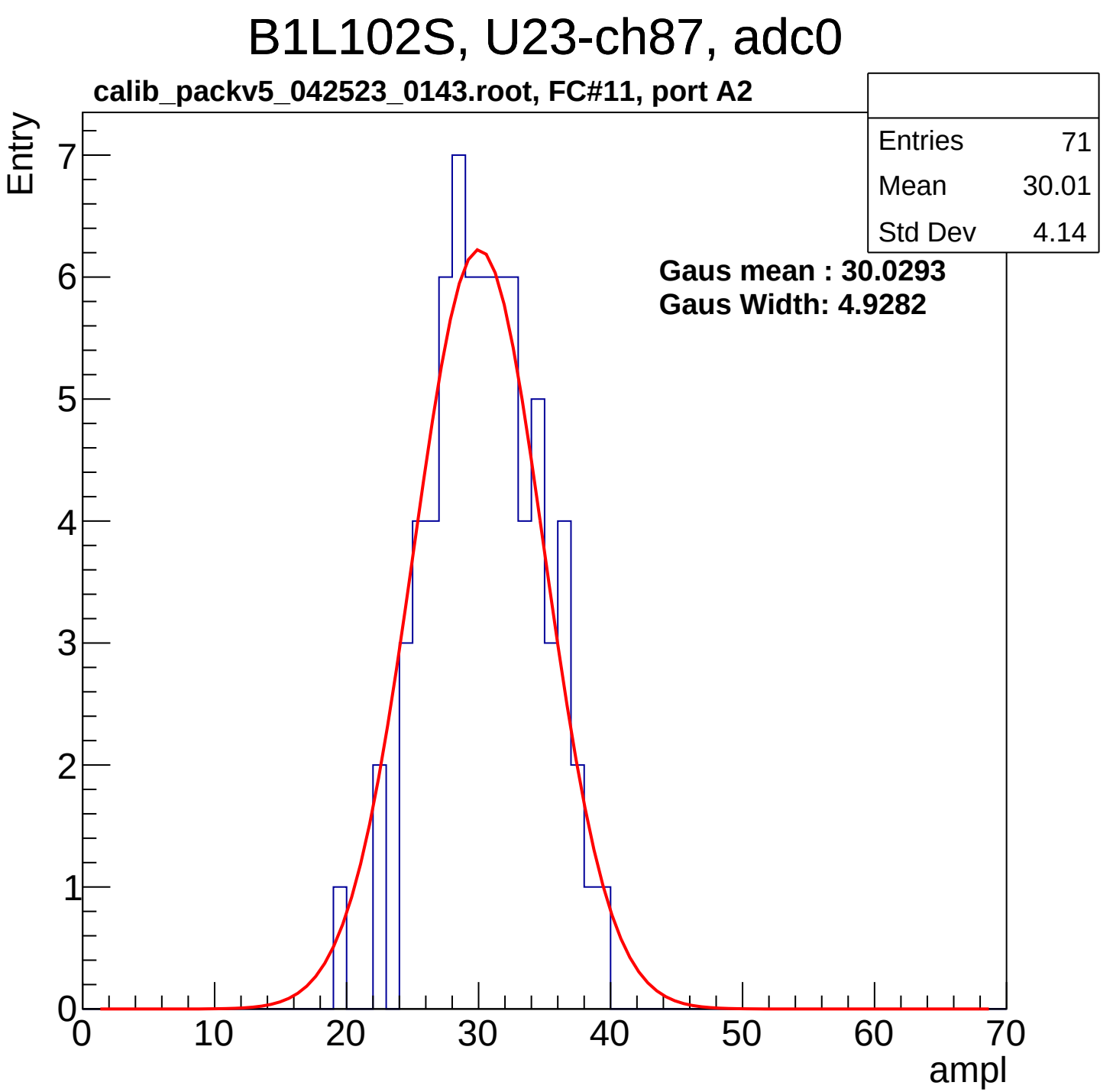

B1L102S, U23-ch86, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 50.07
Std Dev 3.622
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

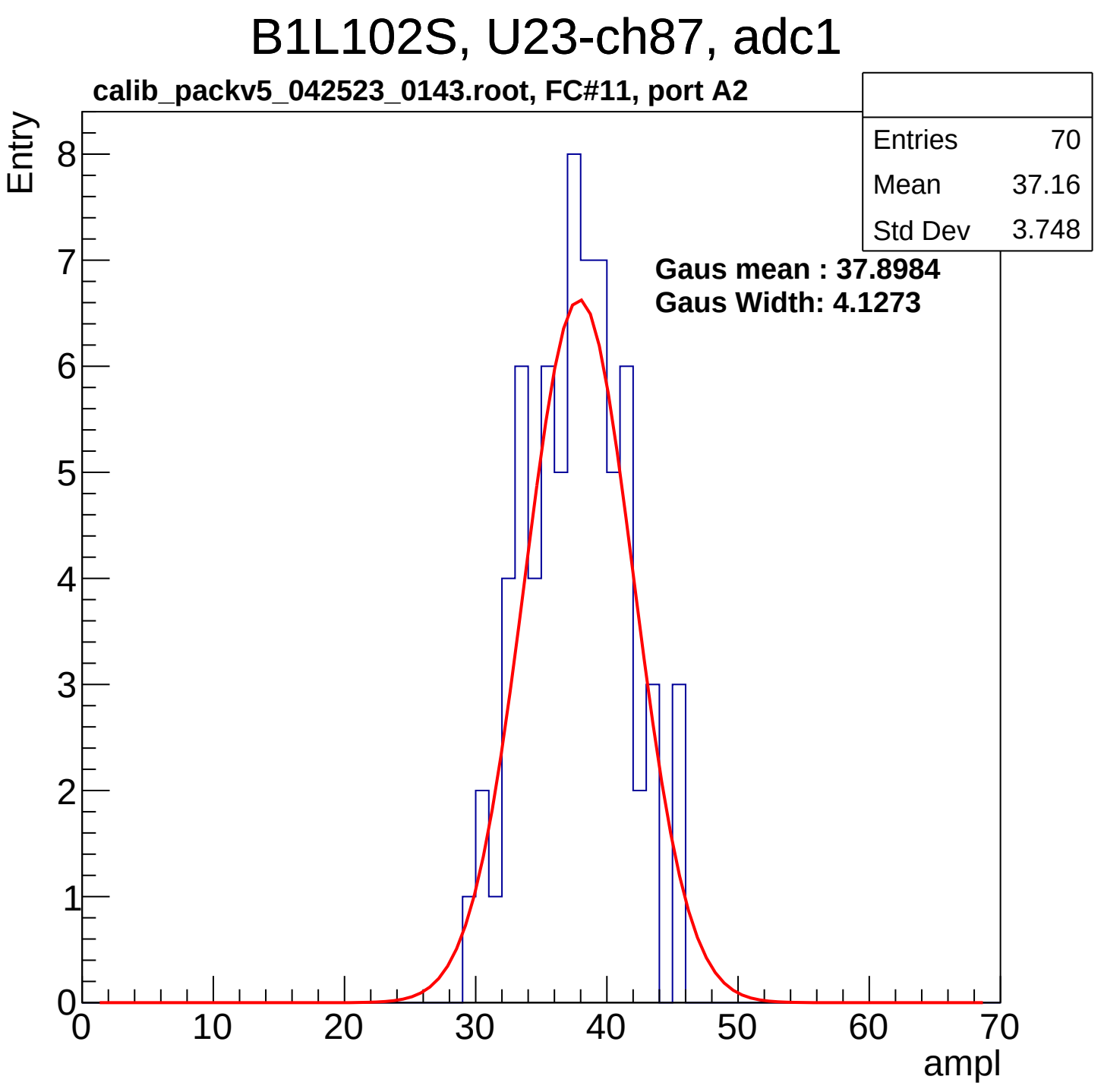

B1L102S, U23-ch86, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 55.97
Std Dev 3.266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

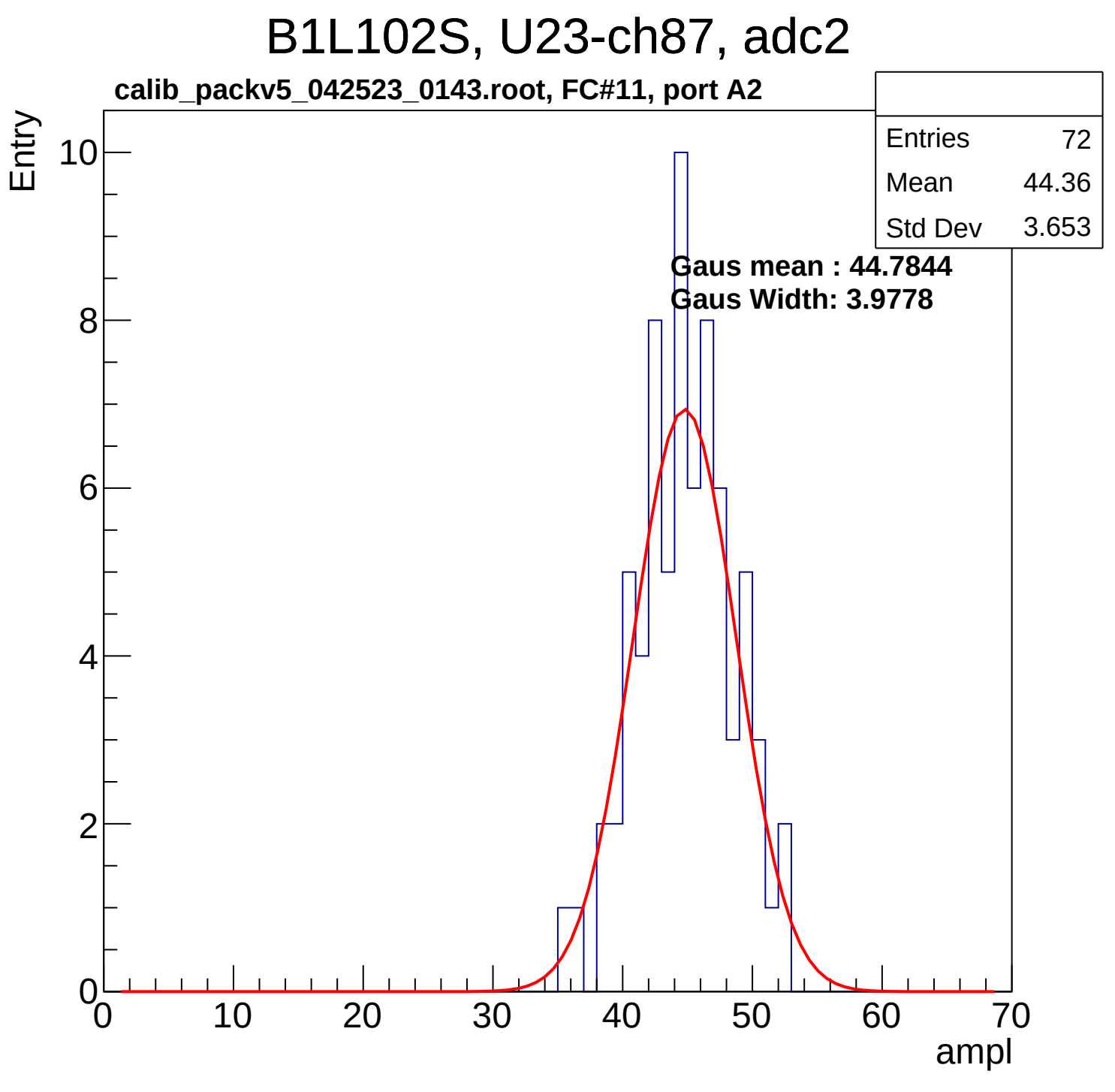

B1L102S, U23-ch86, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.69
Std Dev 9.423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

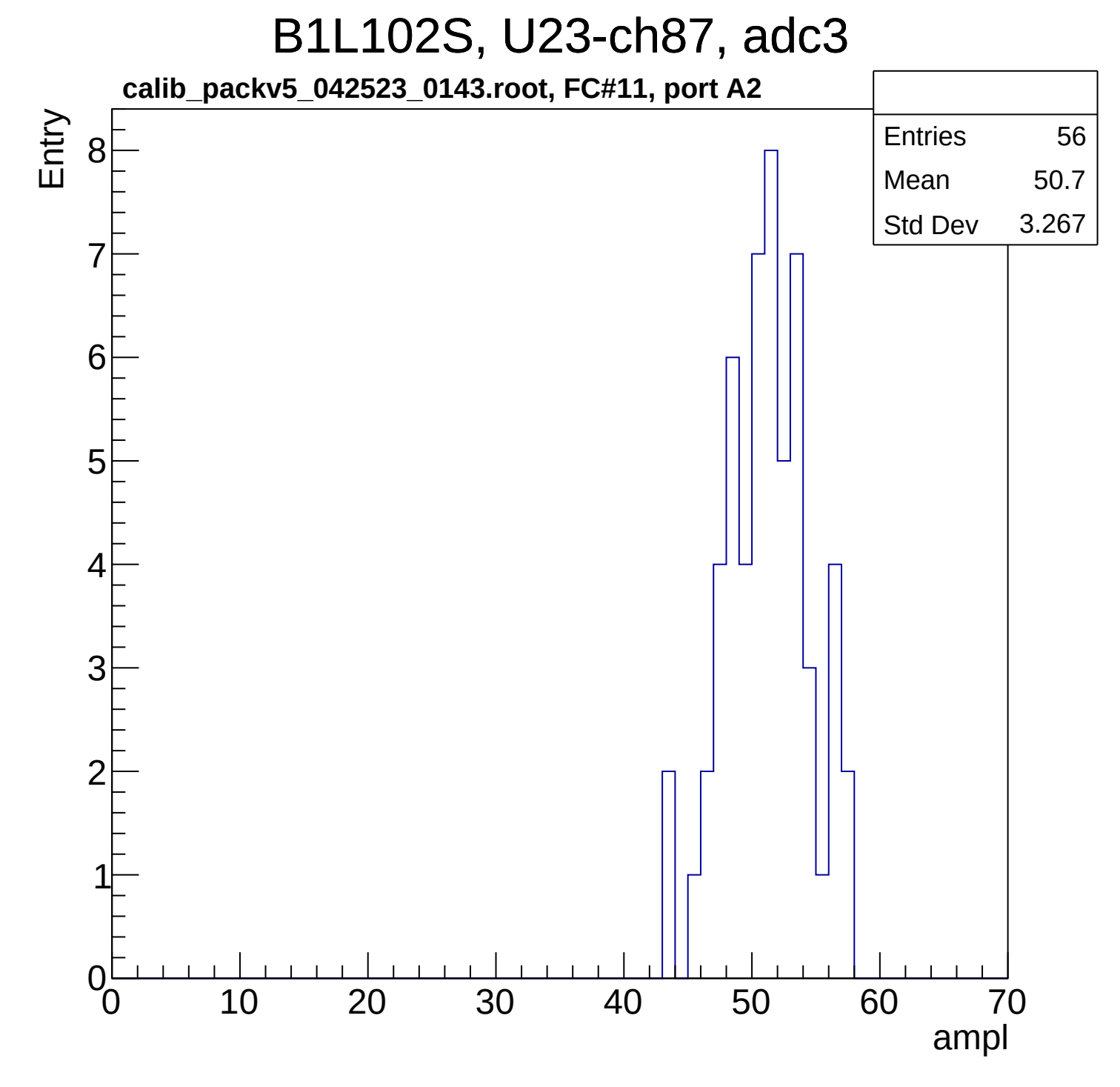

B1L102S, U23-ch86, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

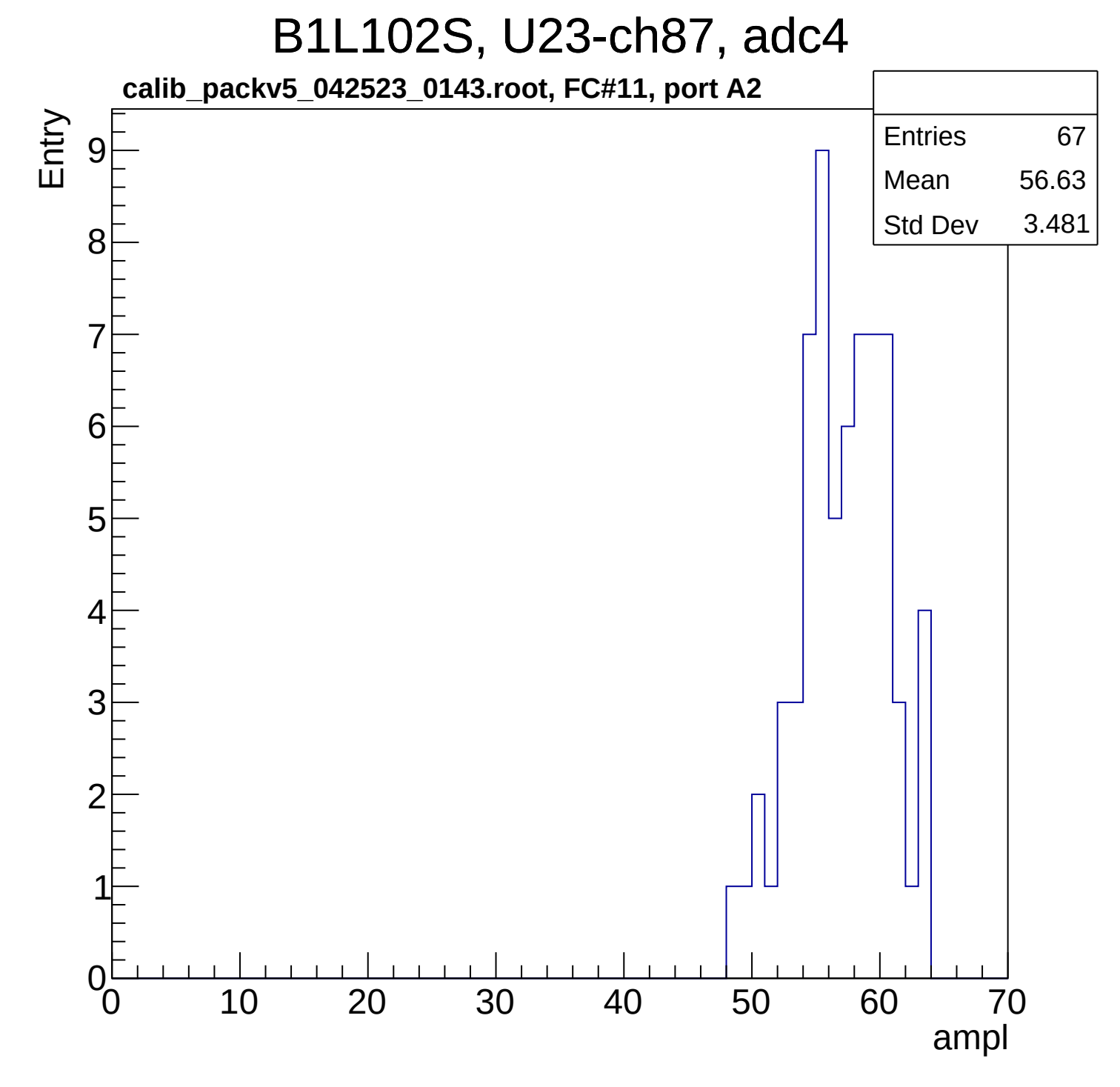

B1L102S, U23-ch86, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

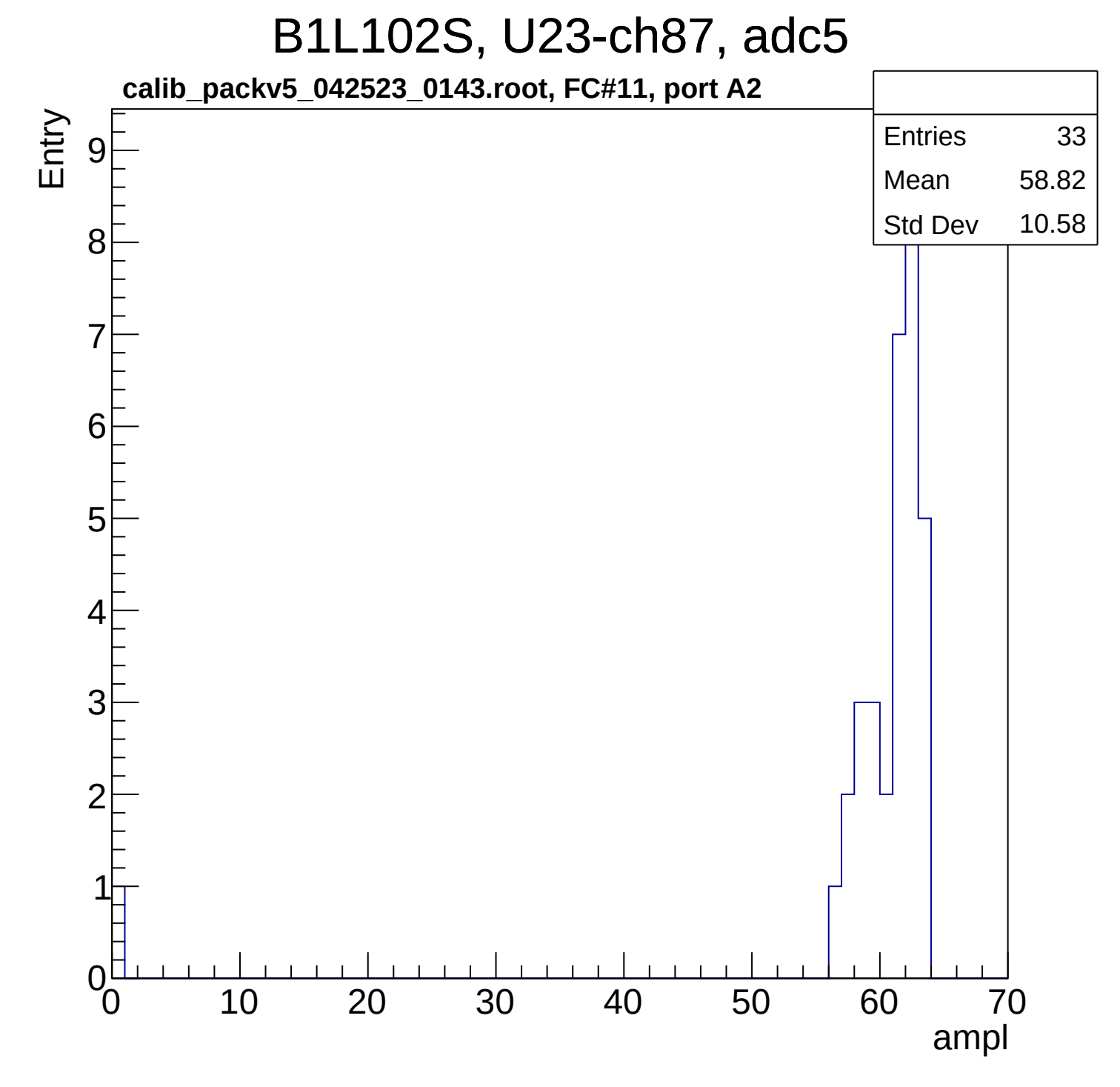

B1L102S, U23-ch87, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 30.01
Std Dev 4.14
Gaus mean : 30.0293
Gaus Width: 4.9282
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

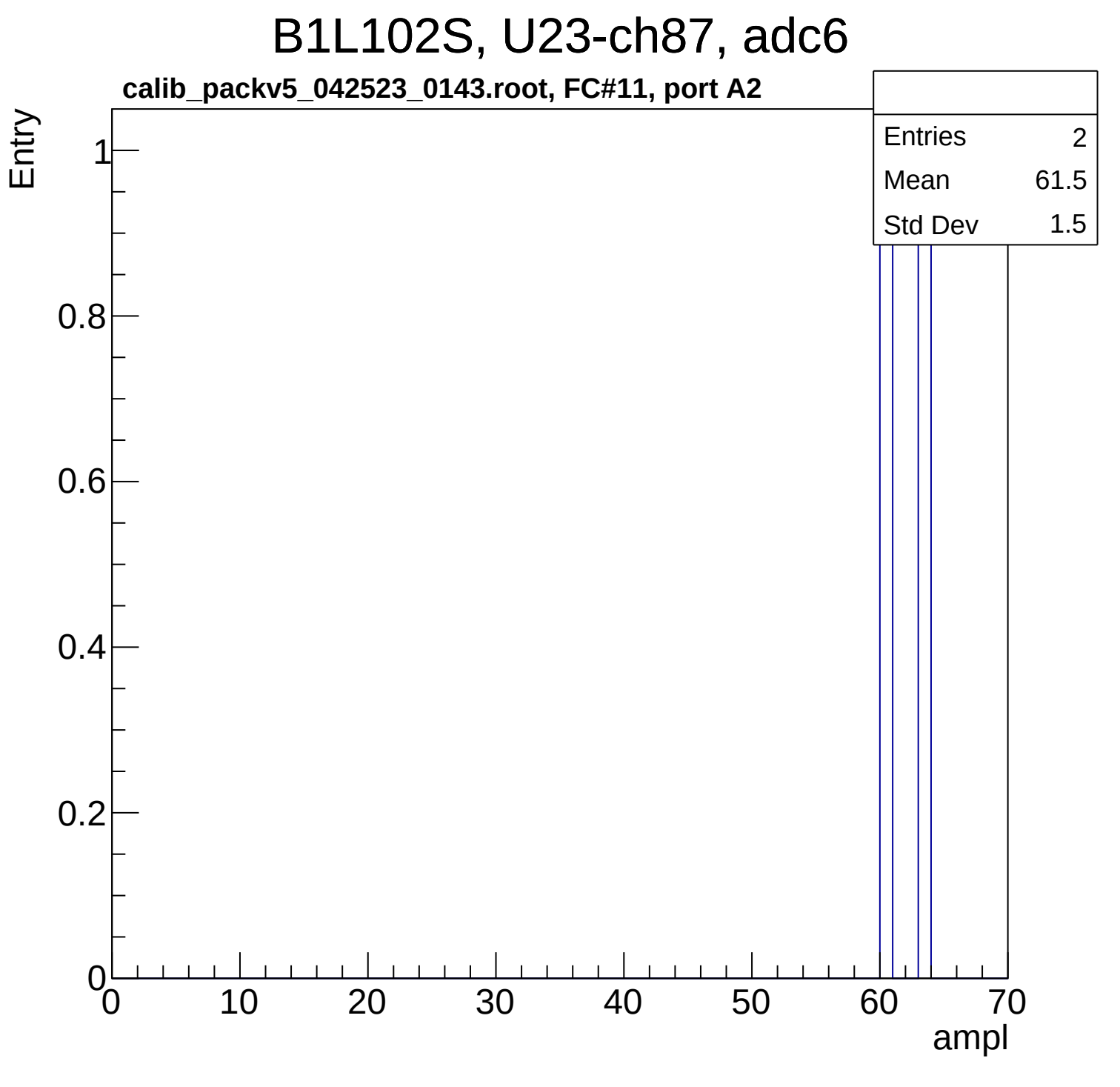

B1L102S, U23-ch87, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 37.16
Std Dev 3.748
Gaus mean : 37.8984
Gaus Width: 4.1273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

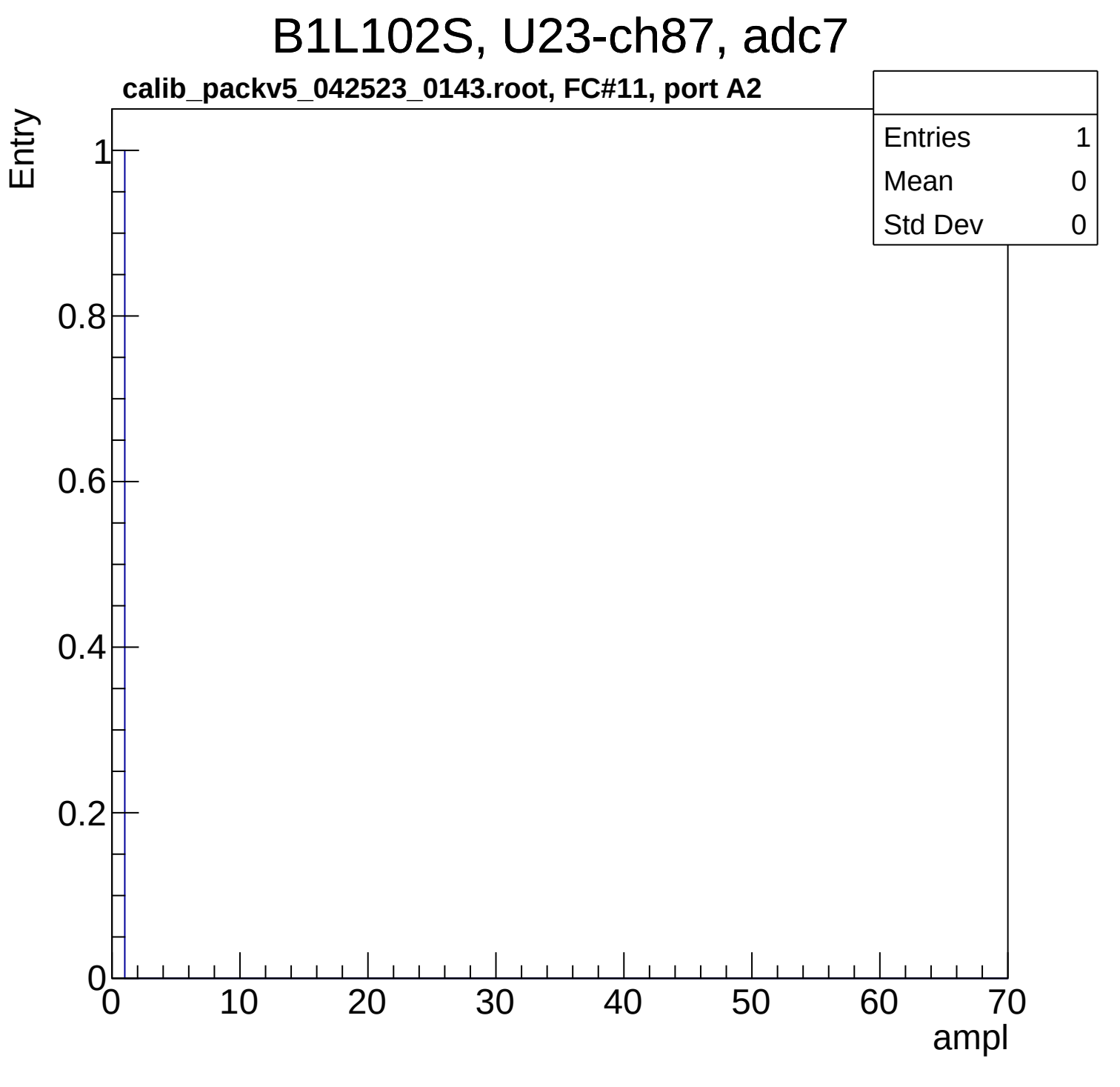

B1L102S, U23-ch87, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 44.36
Std Dev 3.653
Gaus mean : 44.7844
Gaus Width: 3.9778
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

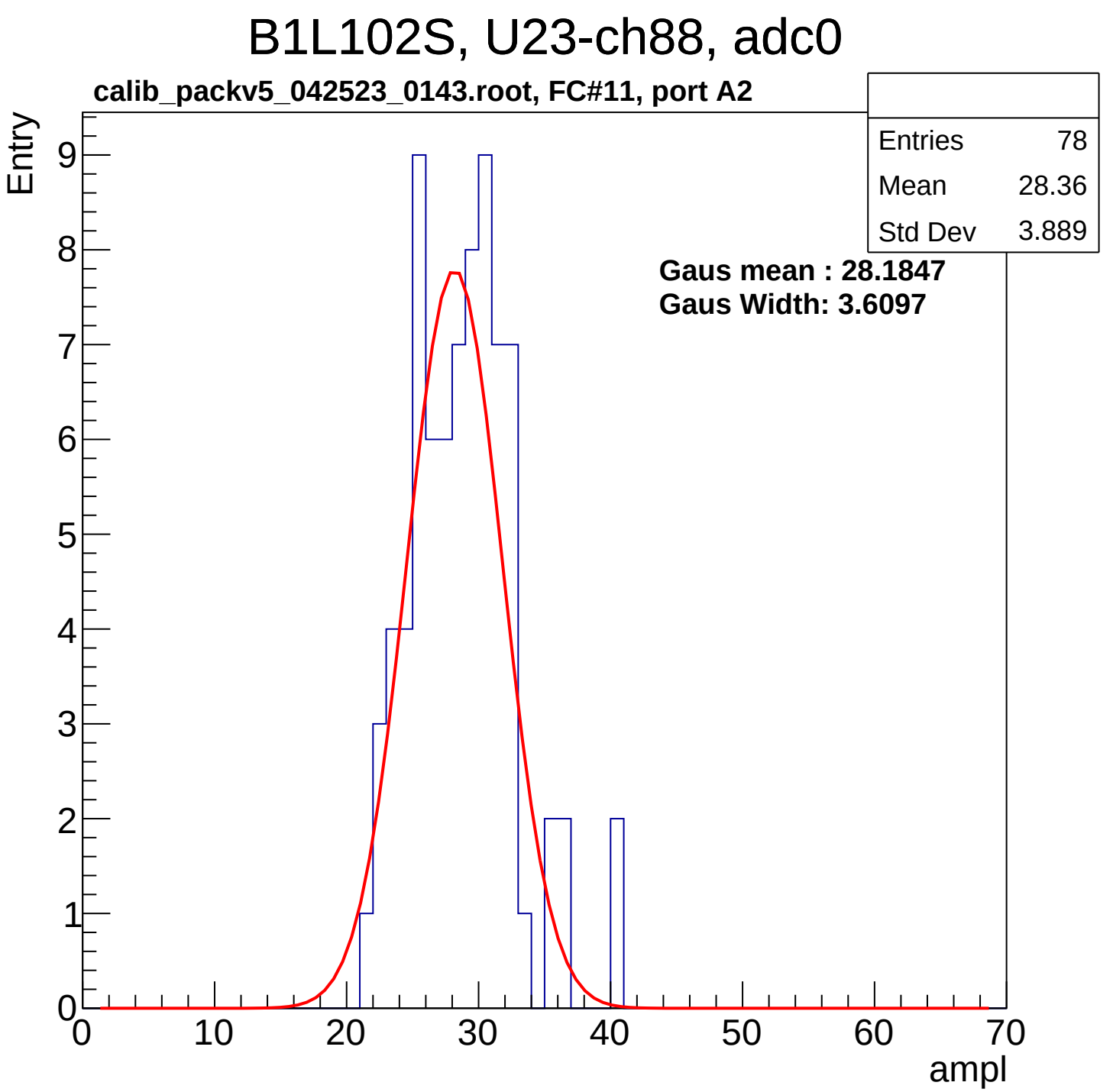

B1L102S, U23-ch87, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 50.7
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

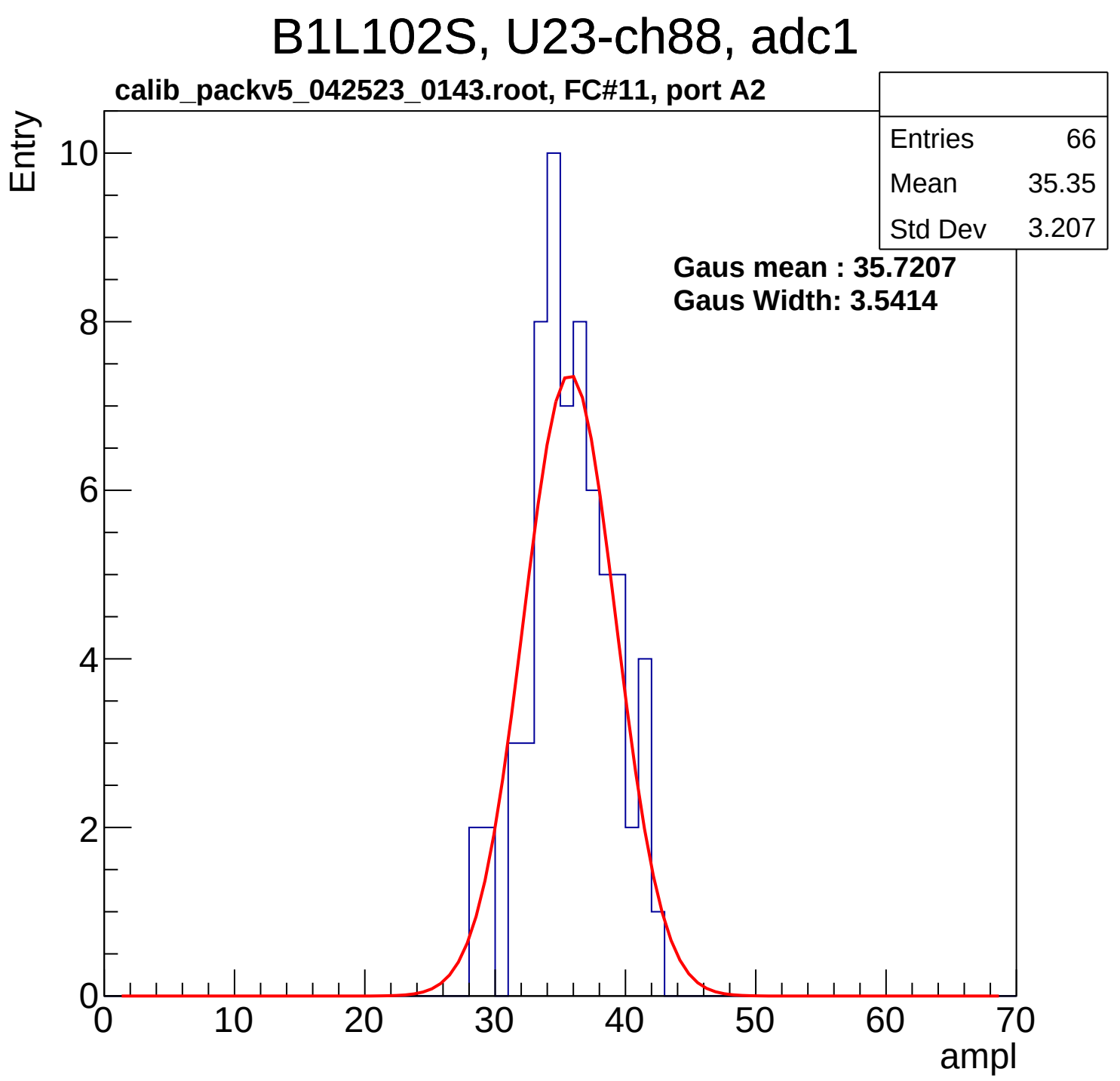

B1L102S, U23-ch87, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 56.63
Std Dev 3.481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

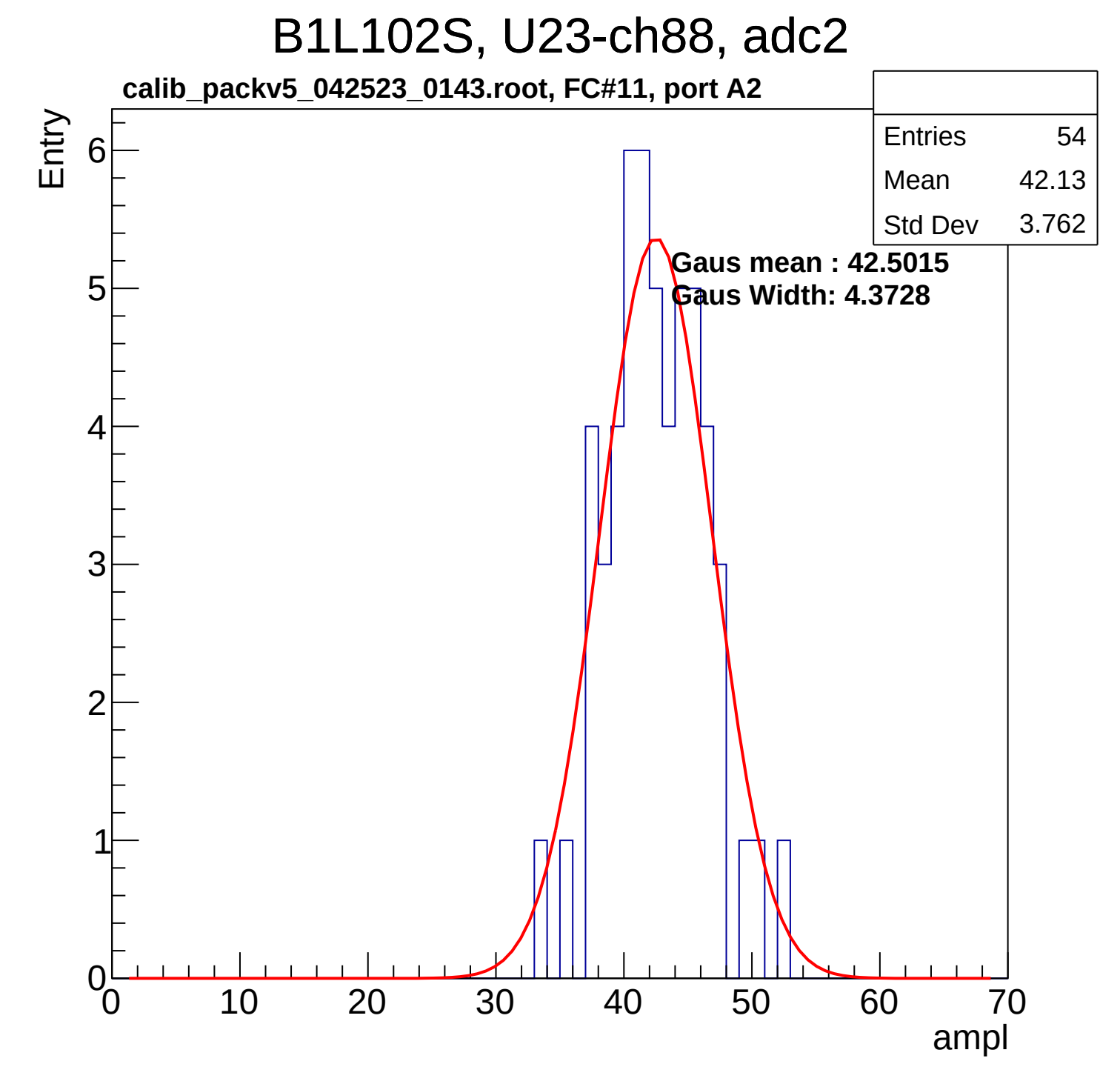

B1L102S, U23-ch87, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 33
Mean 58.82
Std Dev 10.58
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

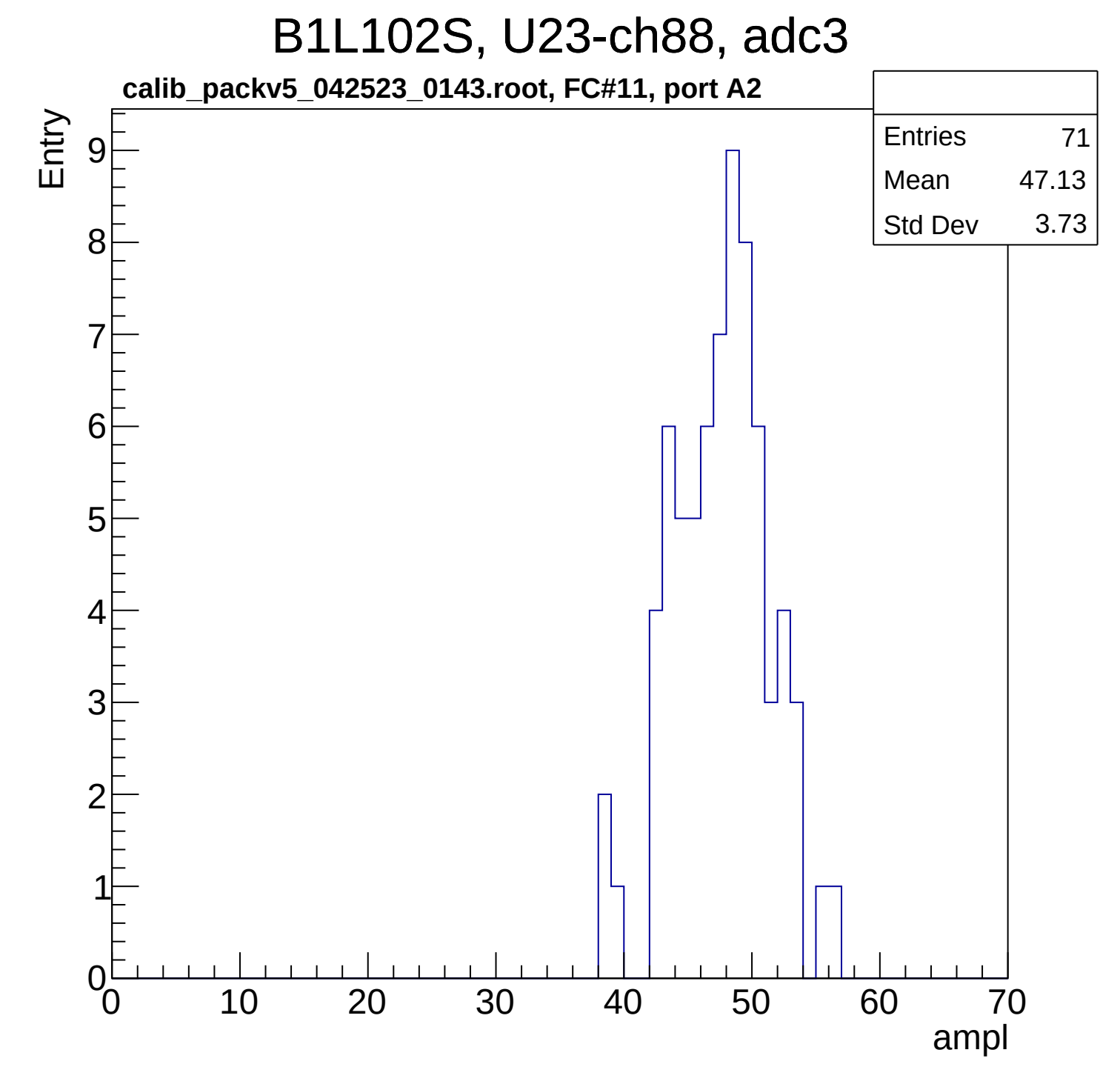

B1L102S, U23-ch87, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

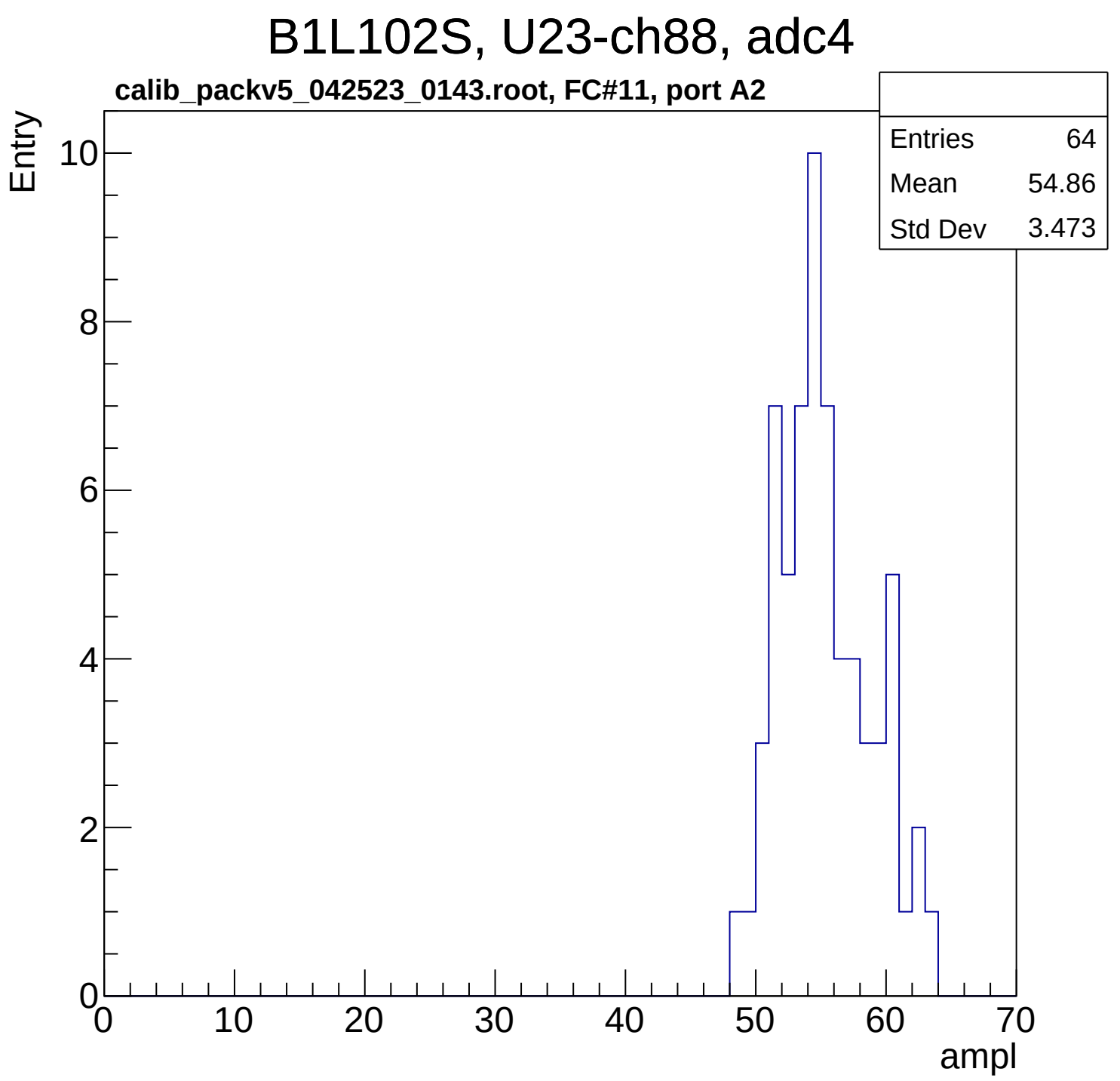

B1L102S, U23-ch87, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

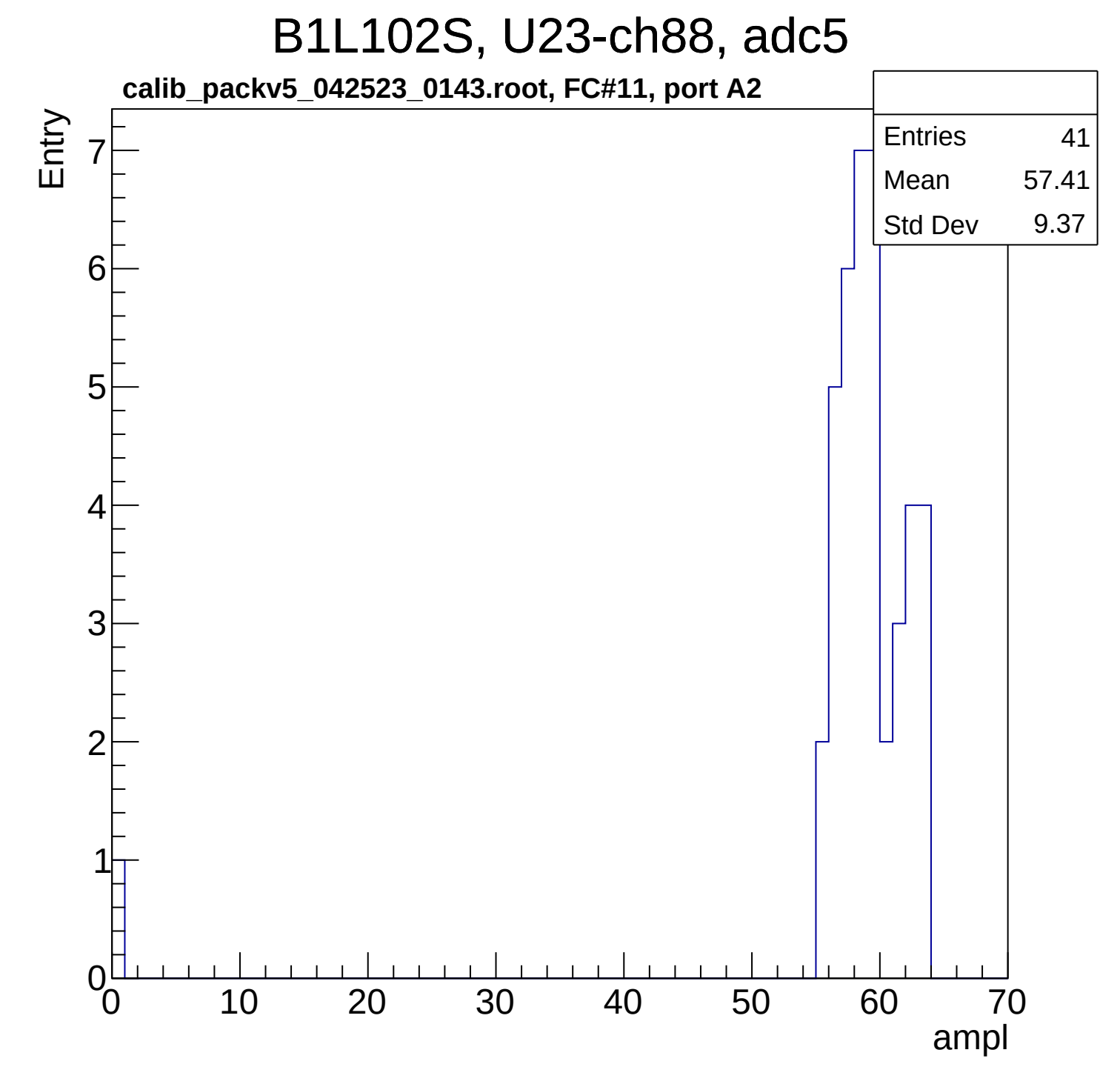

B1L102S, U23-ch88, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 28.36
Std Dev 3.889
Gaus mean : 28.1847
Gaus Width: 3.6097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

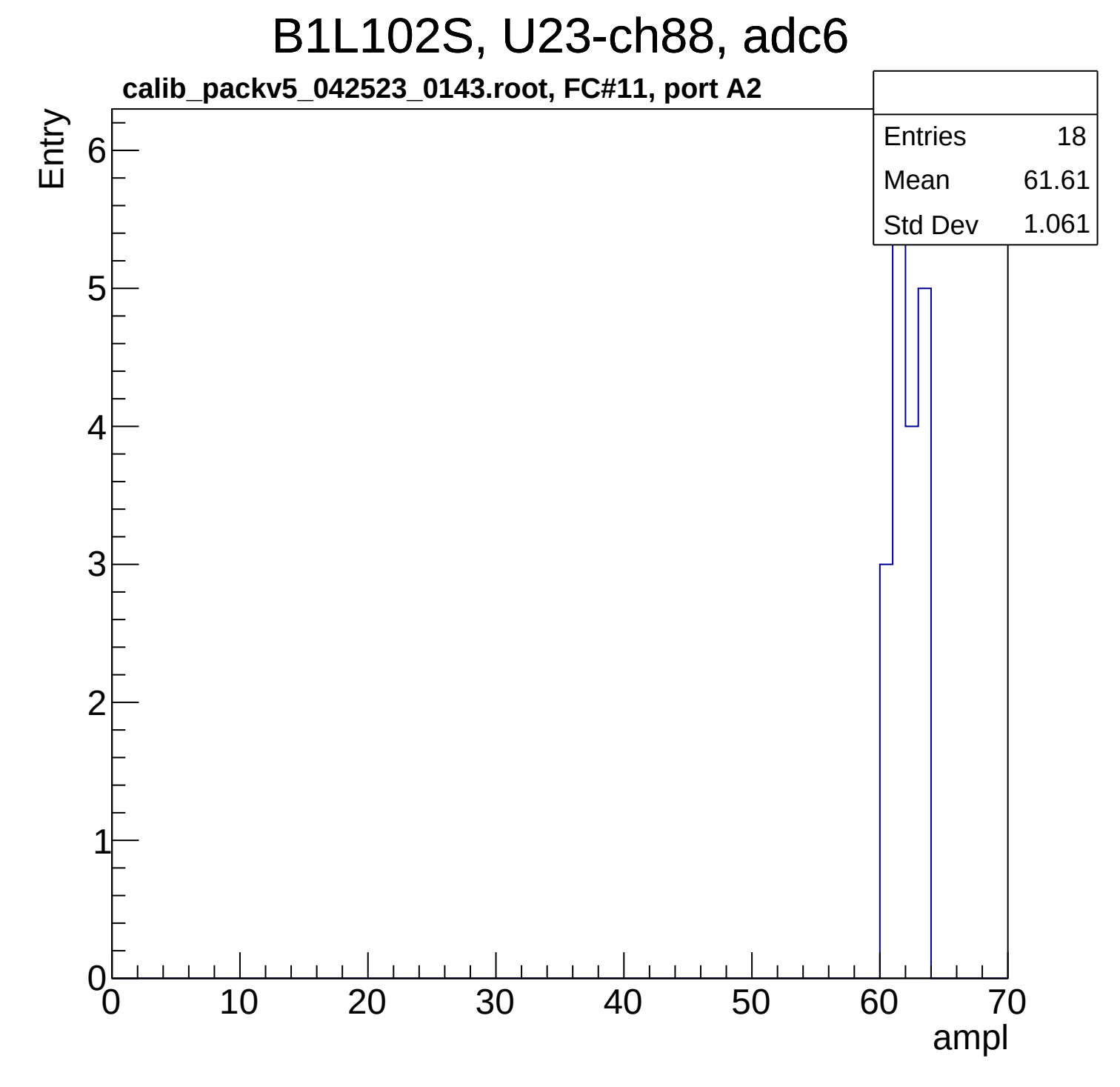

B1L102S, U23-ch88, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 35.35
Std Dev 3.207
Gaus mean : 35.7207
Gaus Width: 3.5414
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

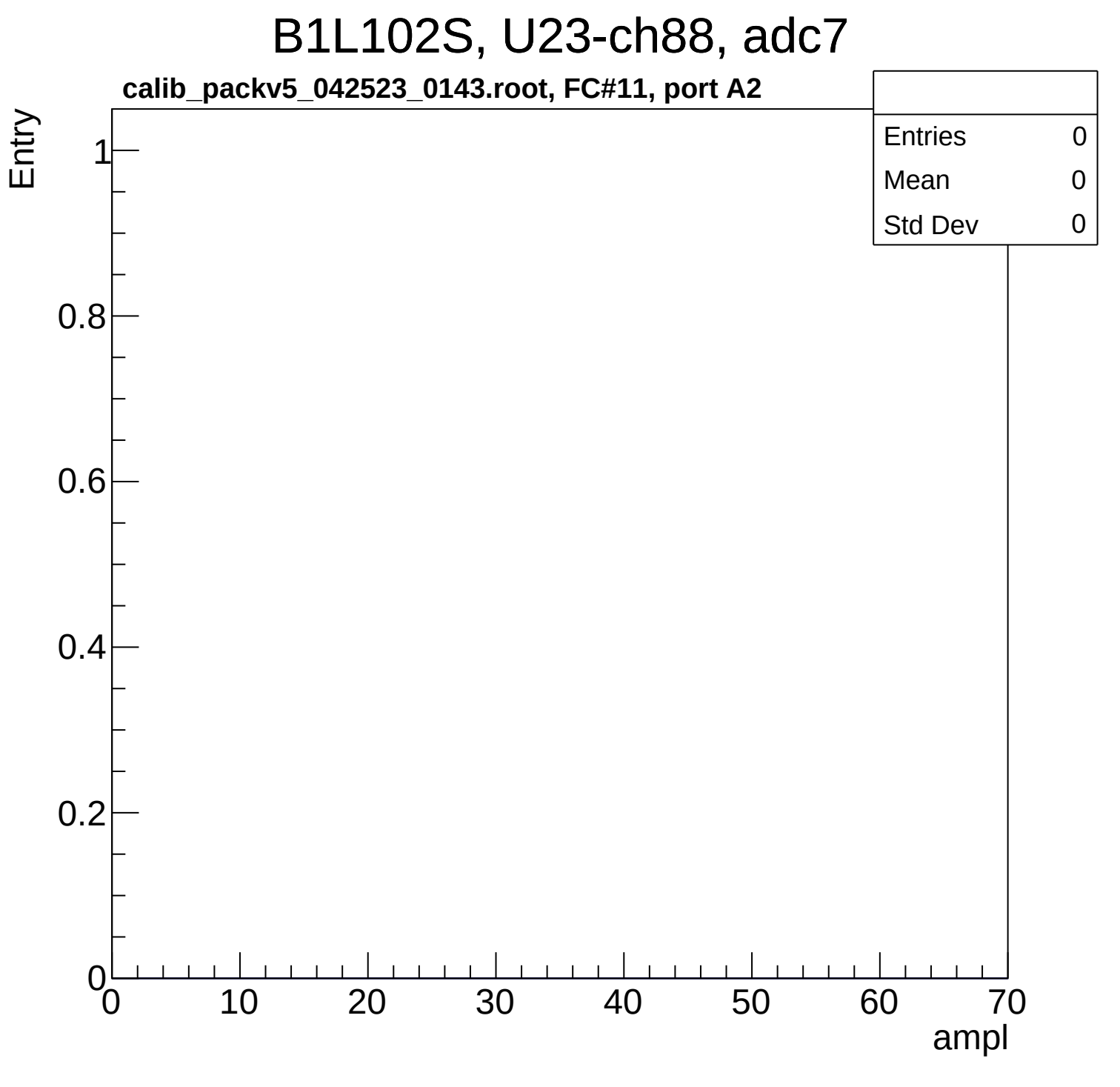

B1L102S, U23-ch88, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 42.13
Std Dev 3.762
Gaus mean : 42.5015
Gaus Width: 4.3728
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

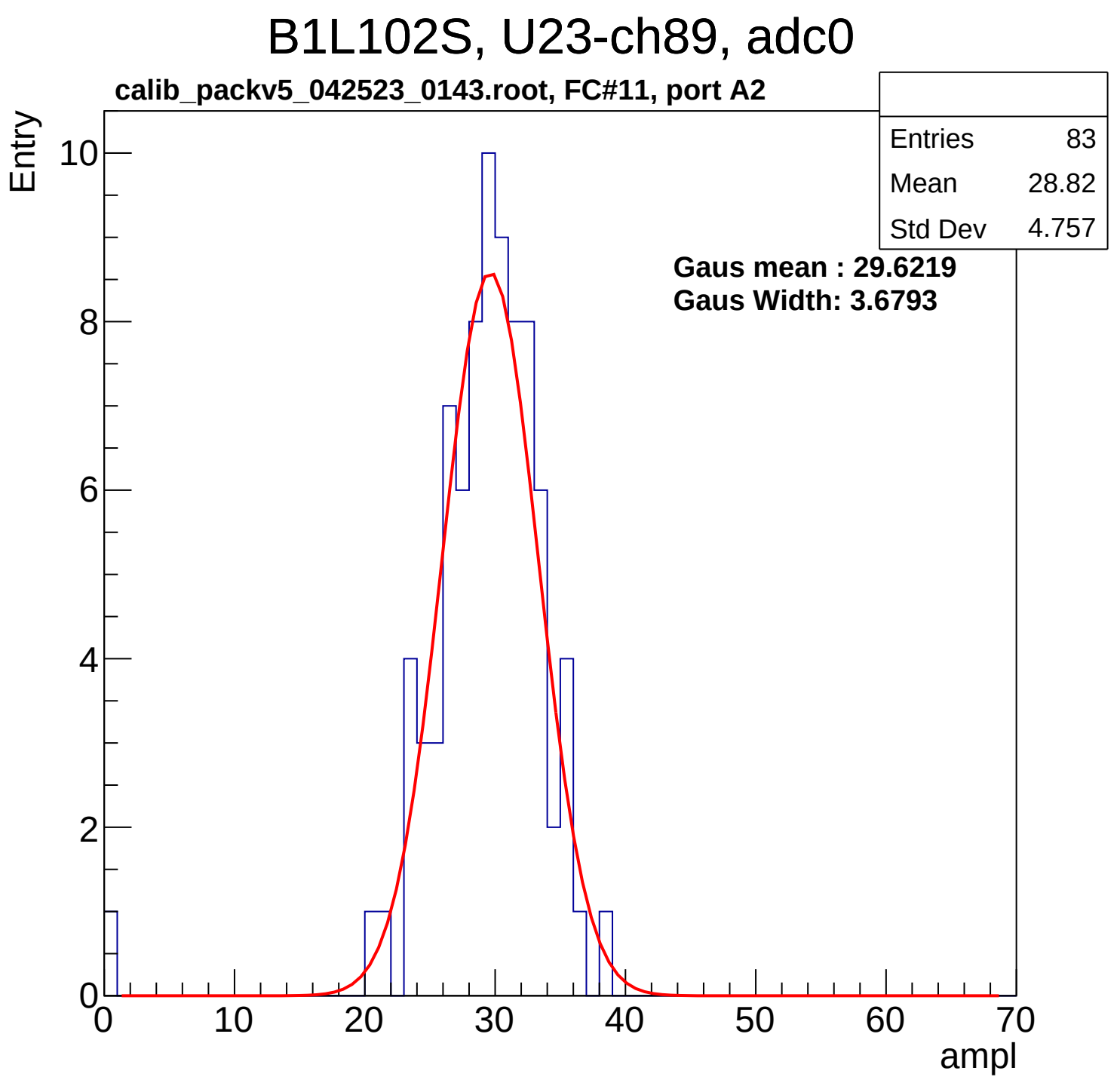

B1L102S, U23-ch88, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 47.13
Std Dev 3.73
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

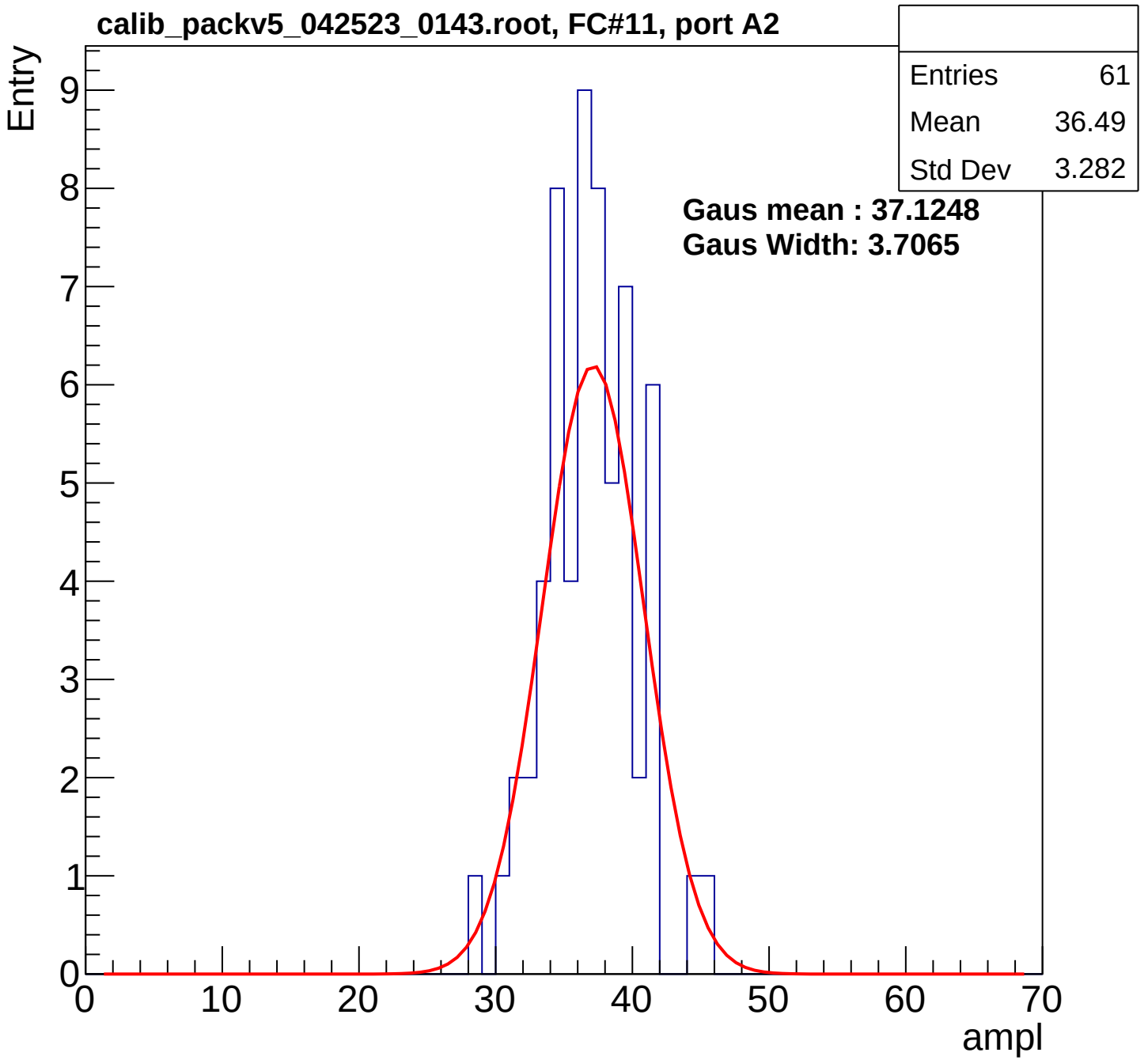

B1L102S, U23-ch88, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 54.86
Std Dev 3.473
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

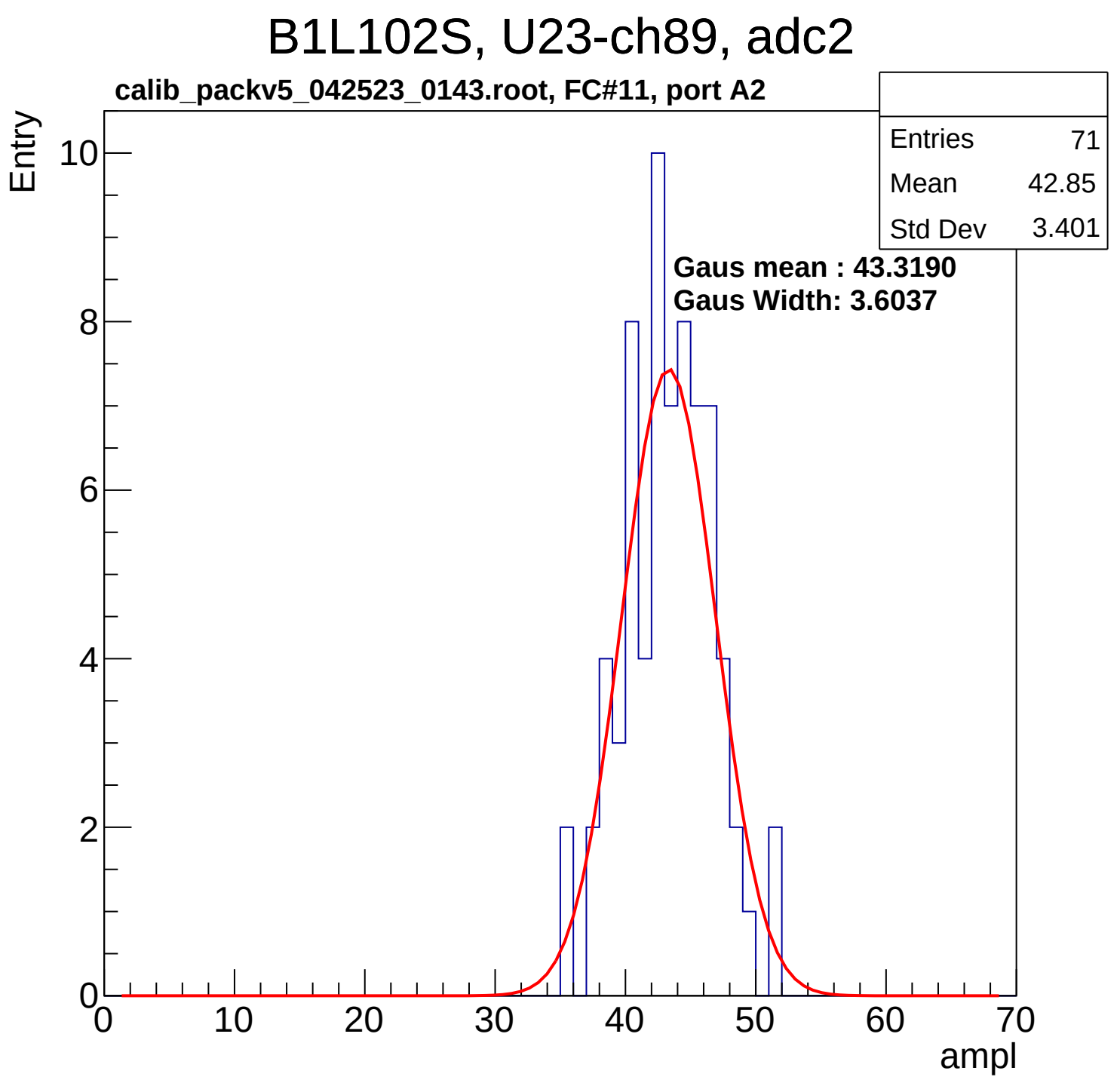

B1L102S, U23-ch88, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 57.41
Std Dev 9.37
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

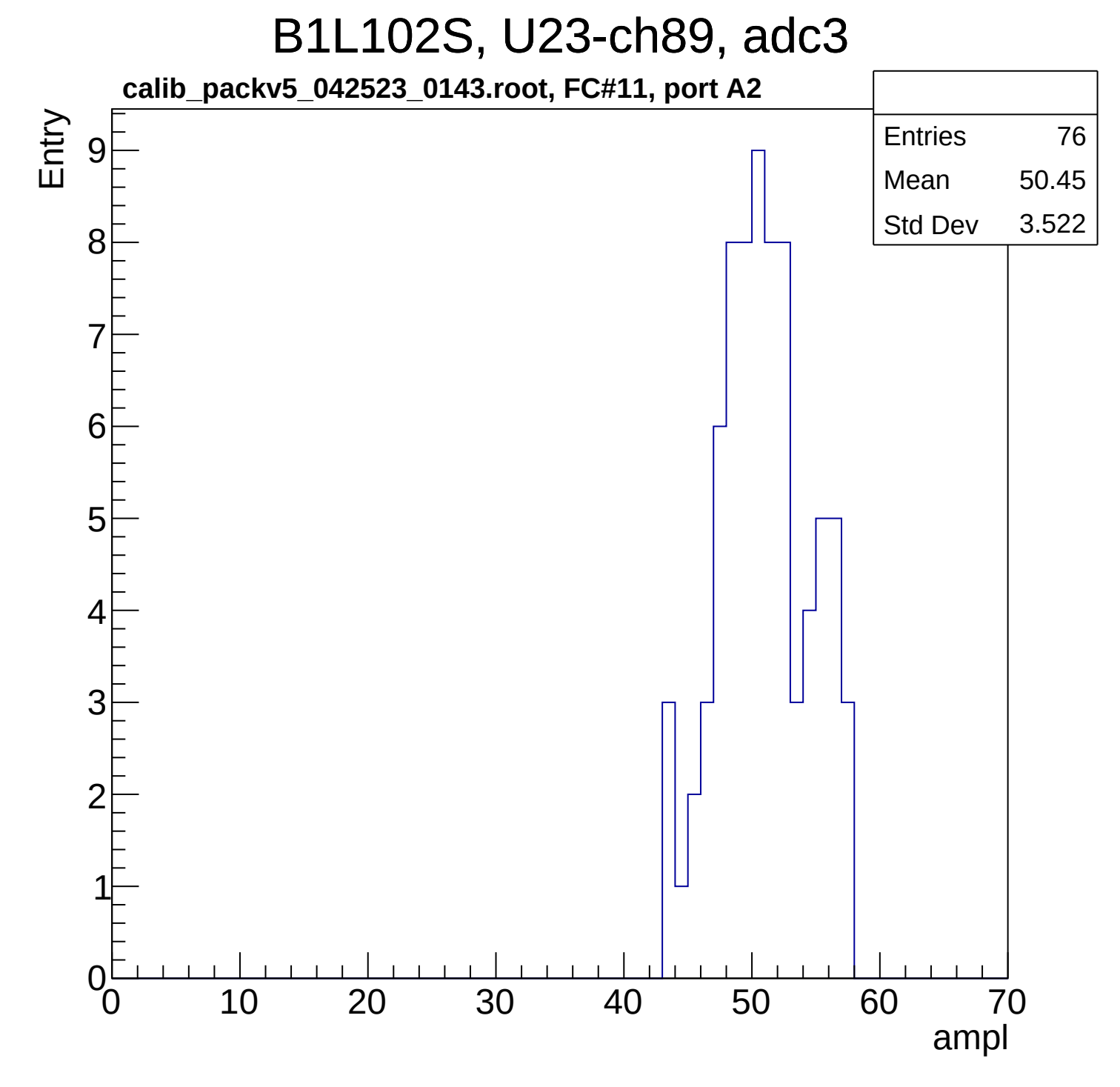

B1L102S, U23-ch88, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 61.61
Std Dev 1.061
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

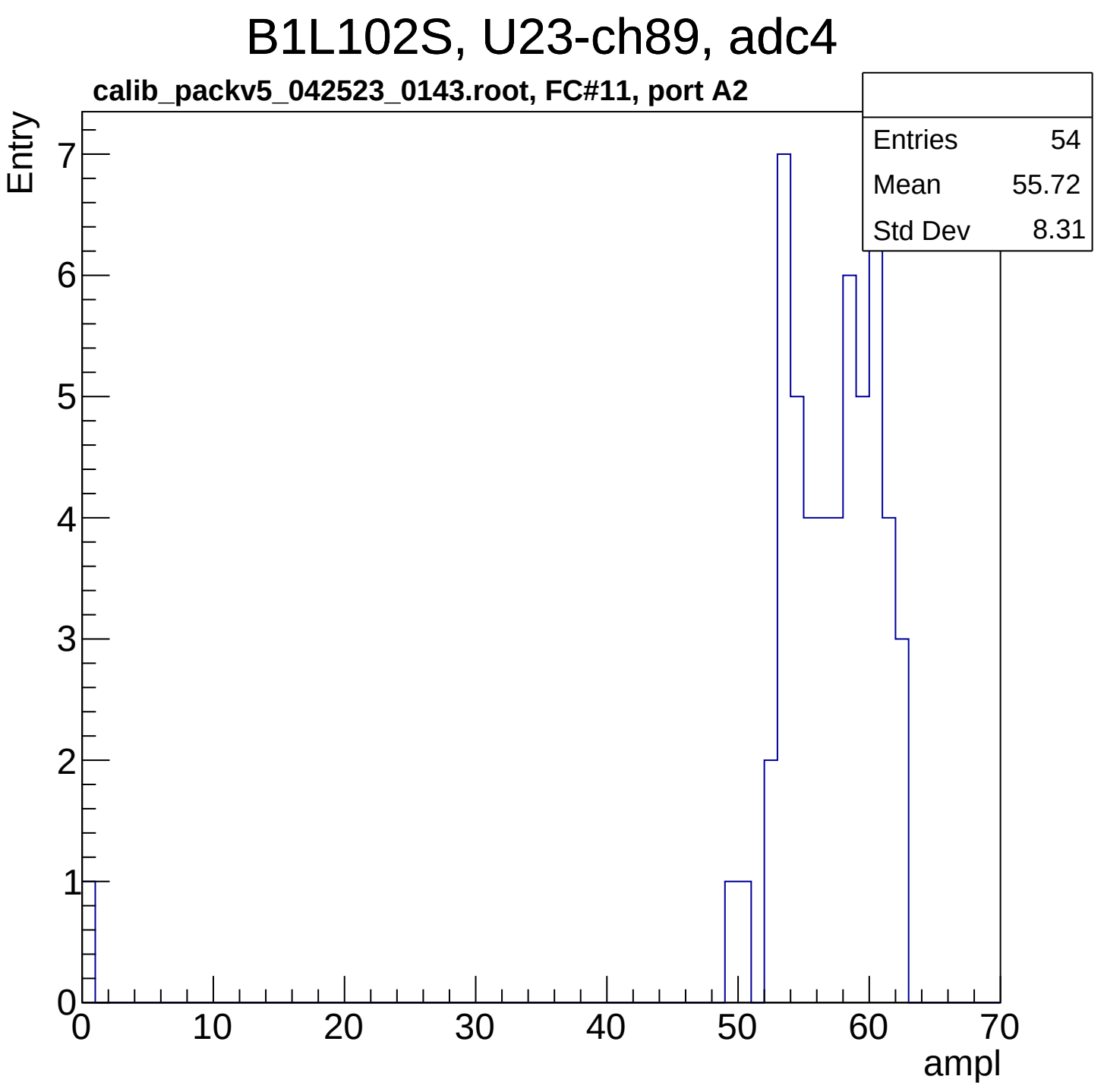

B1L102S, U23-ch88, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

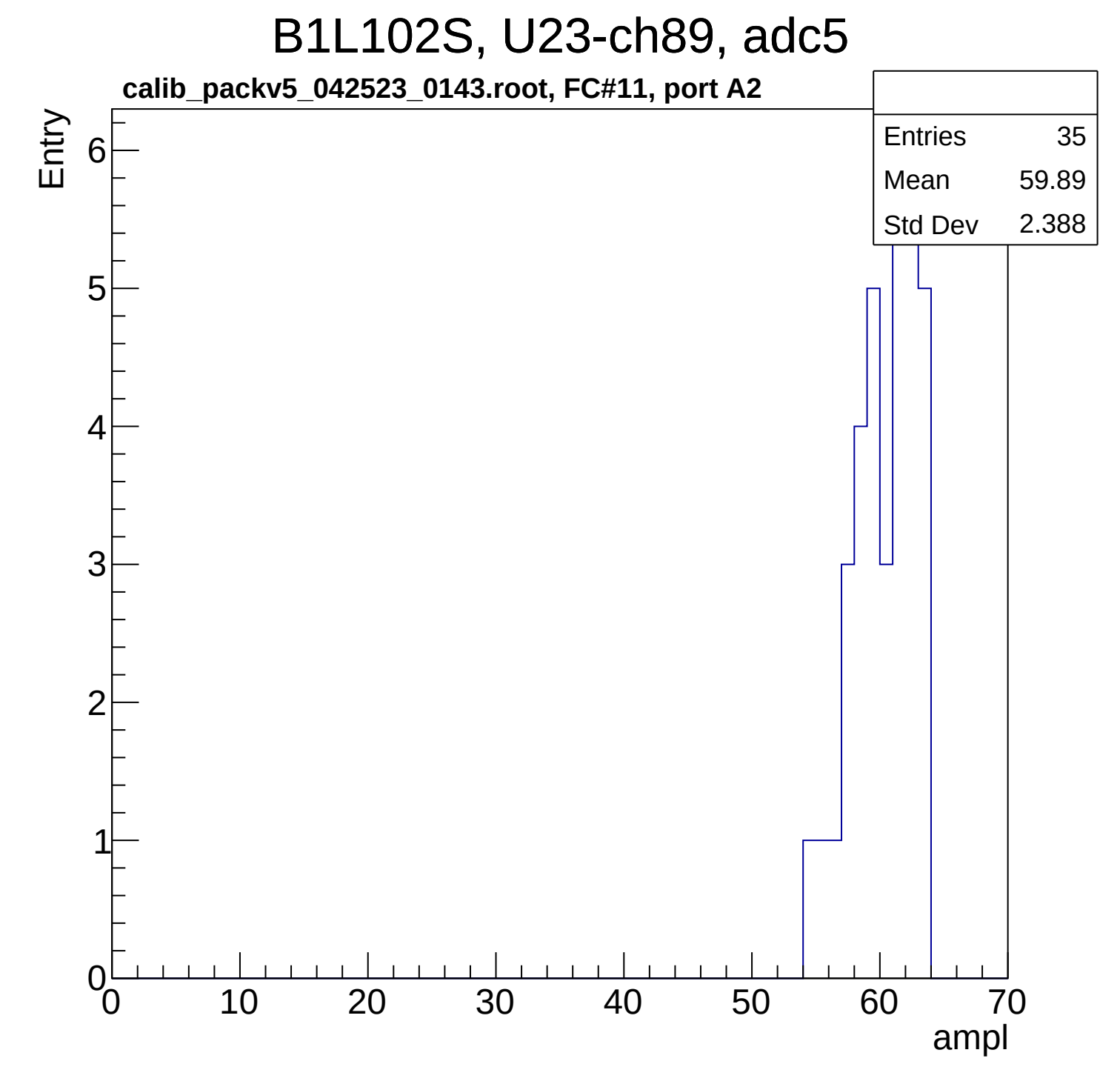

B1L102S, U23-ch89, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 28.82
Std Dev 4.757
Gaus mean : 29.6219
Gaus Width: 3.6793
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

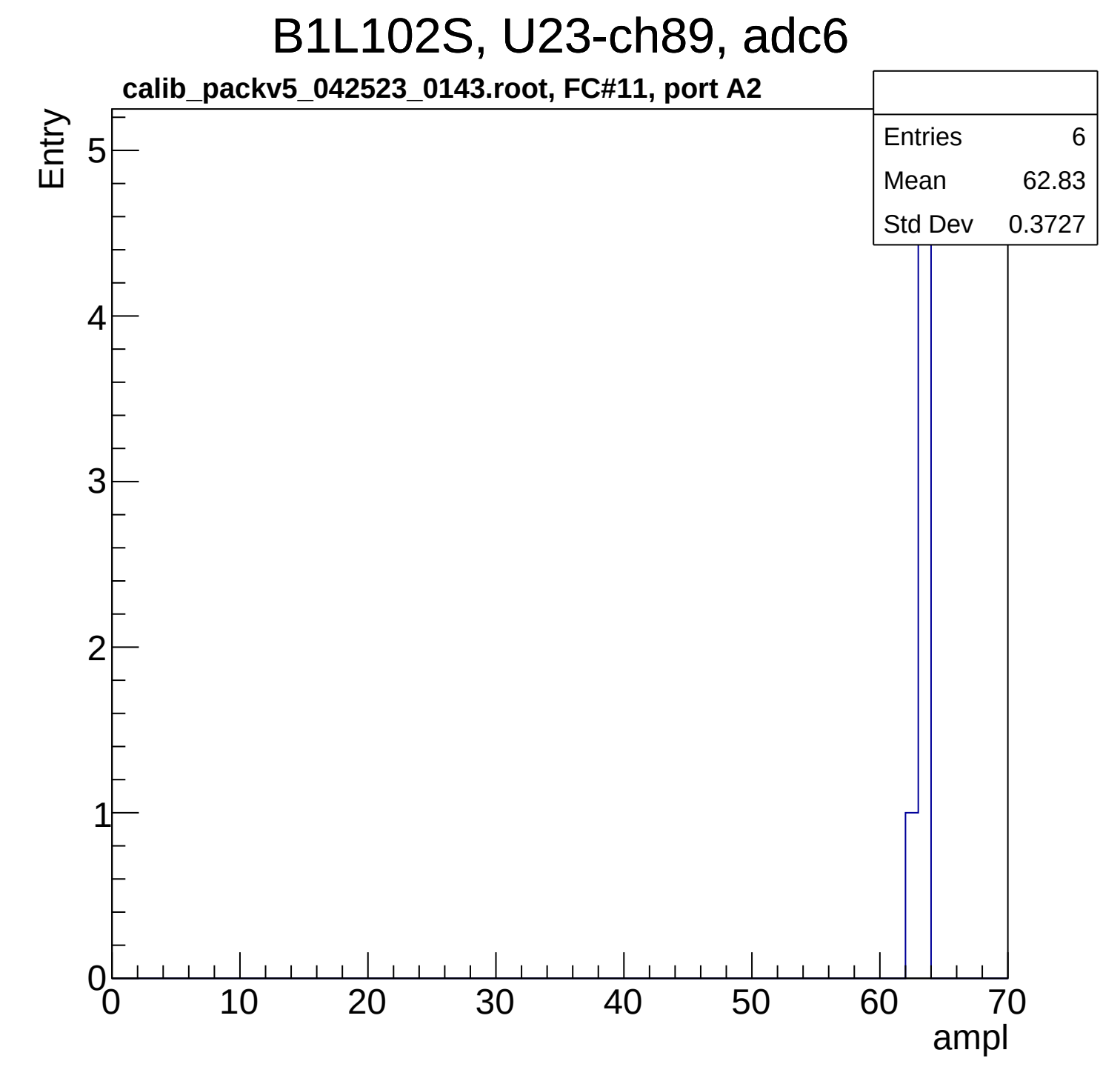

B1L102S, U23-ch89, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 36.49
Std Dev 3.282
Gaus mean : 37.1248
Gaus Width: 3.7065
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

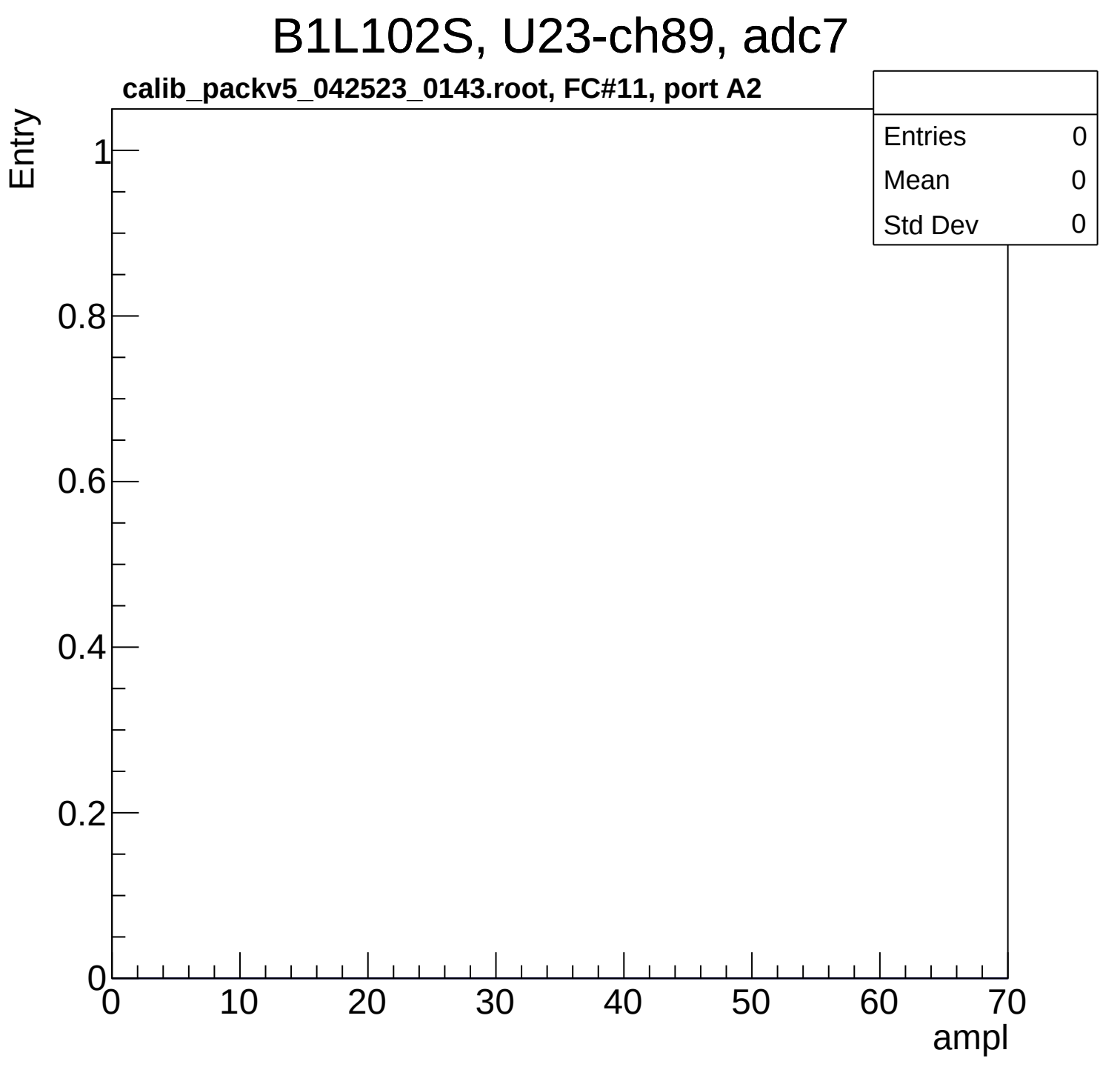

B1L102S, U23-ch89, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.85
Std Dev 3.401
Gaus mean : 43.3190
Gaus Width: 3.6037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

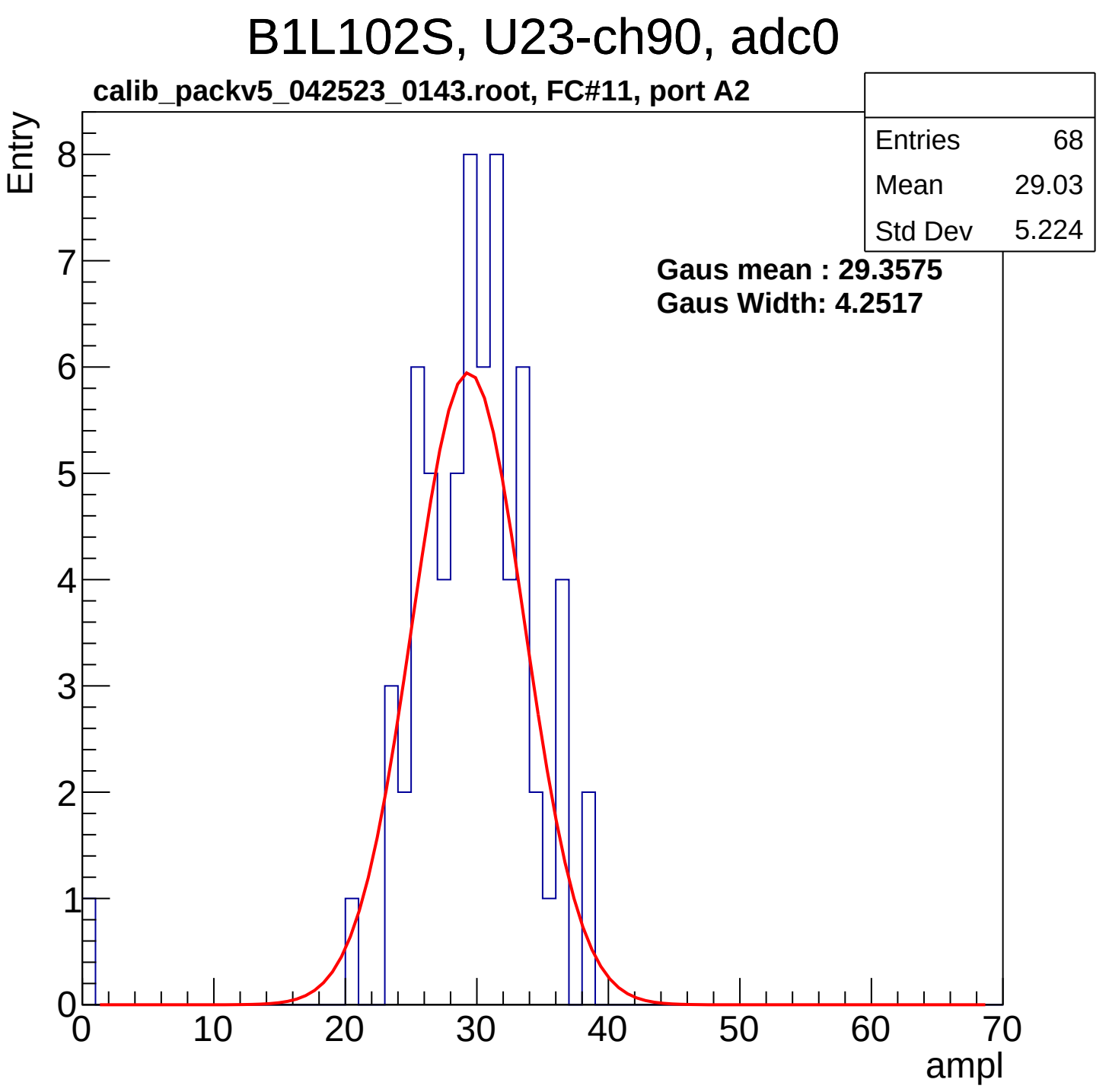

B1L102S, U23-ch89, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 50.45
Std Dev 3.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

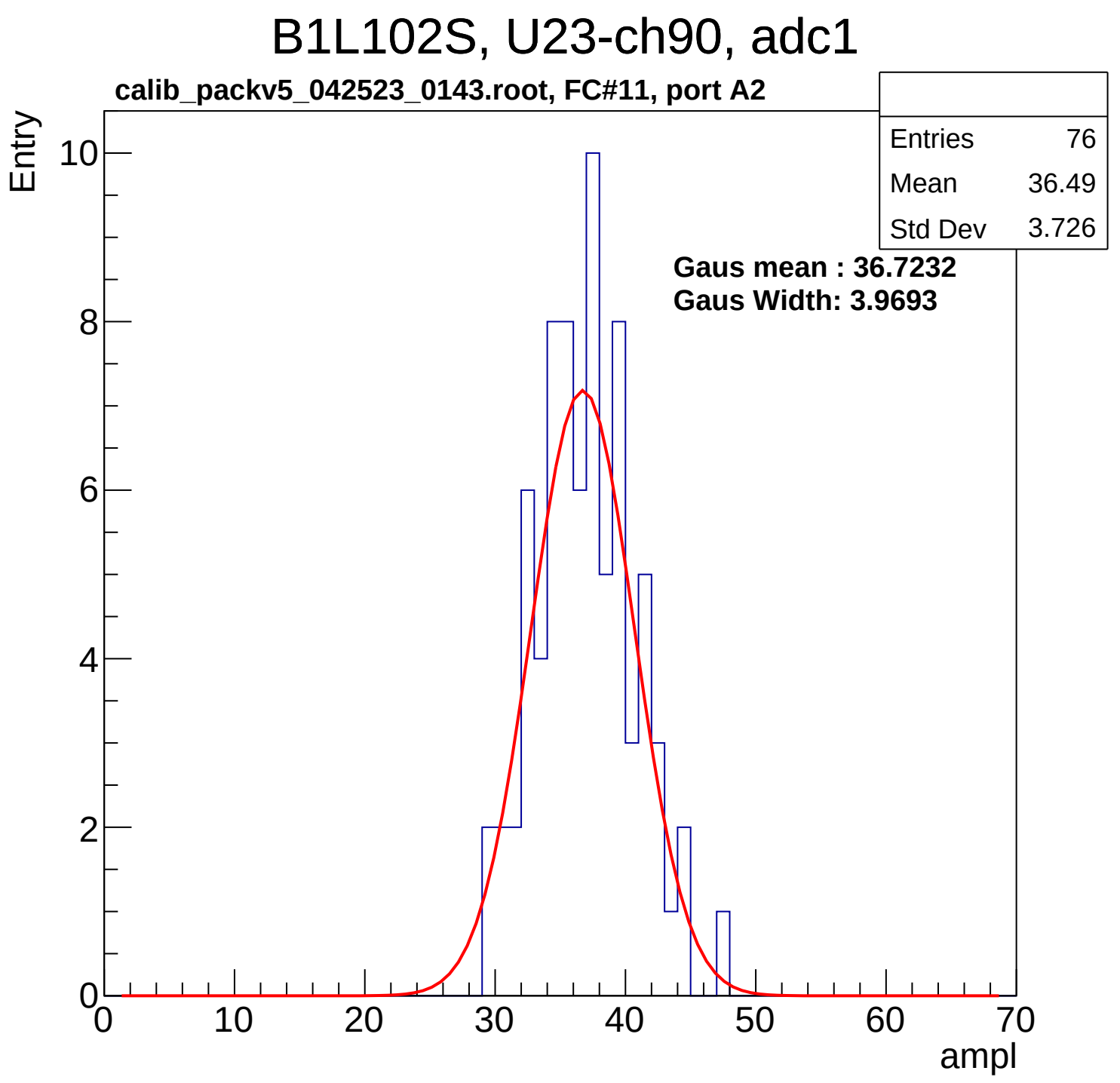

B1L102S, U23-ch89, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 55.72
Std Dev 8.31
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

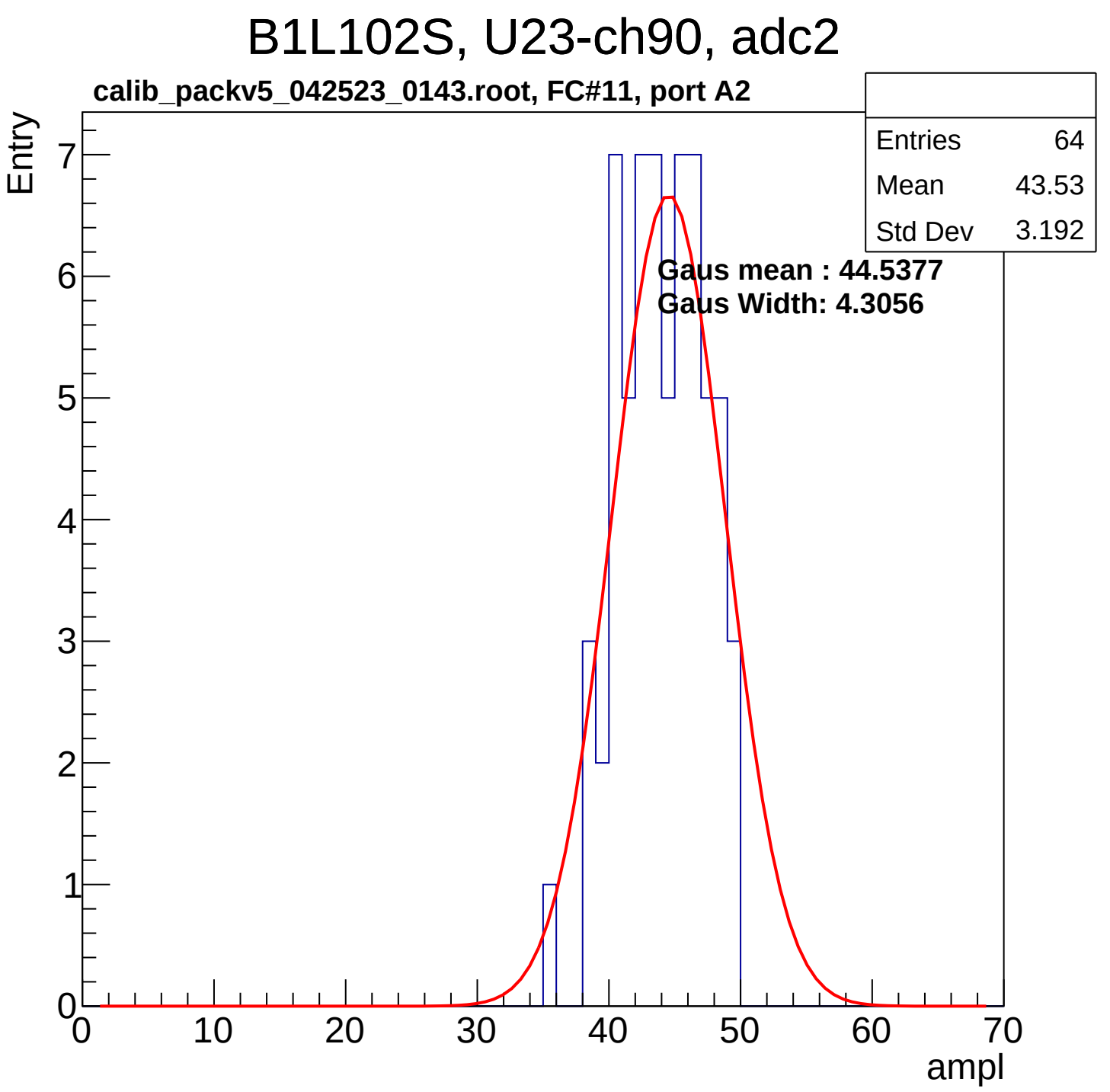

B1L102S, U23-ch89, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 59.89
Std Dev 2.388
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

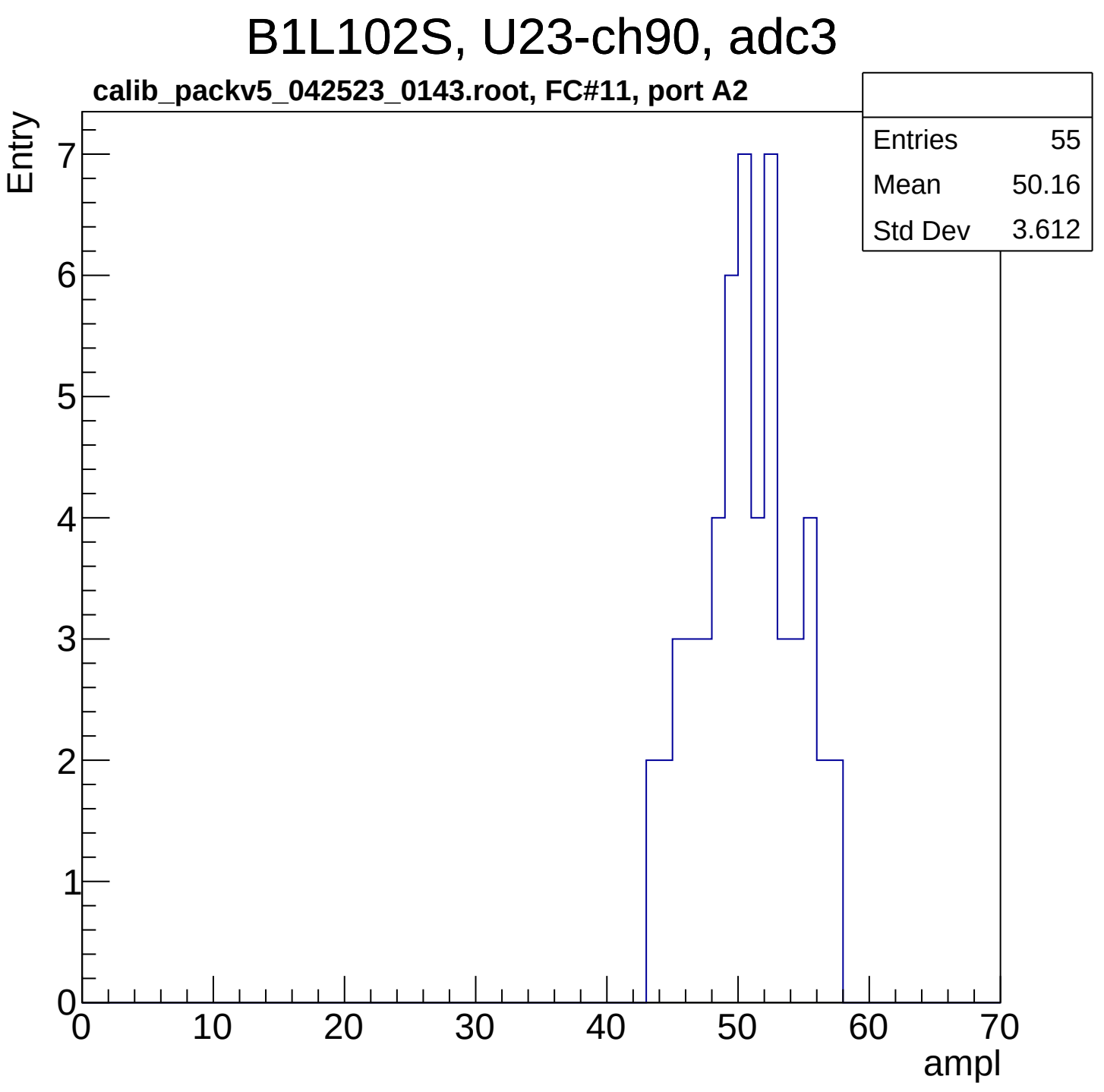

B1L102S, U23-ch89, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.83
Std Dev 0.3727
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

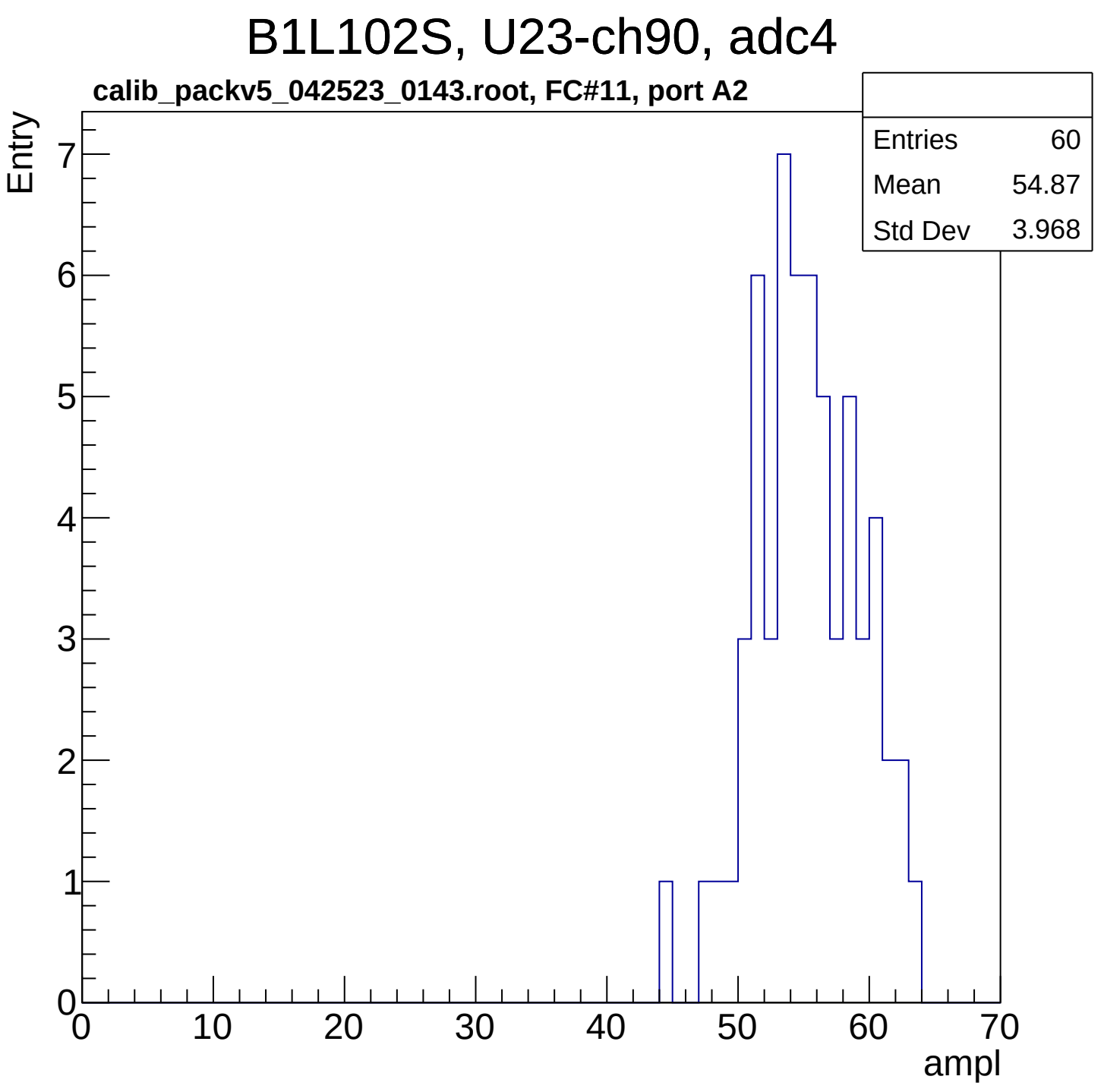

B1L102S, U23-ch89, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

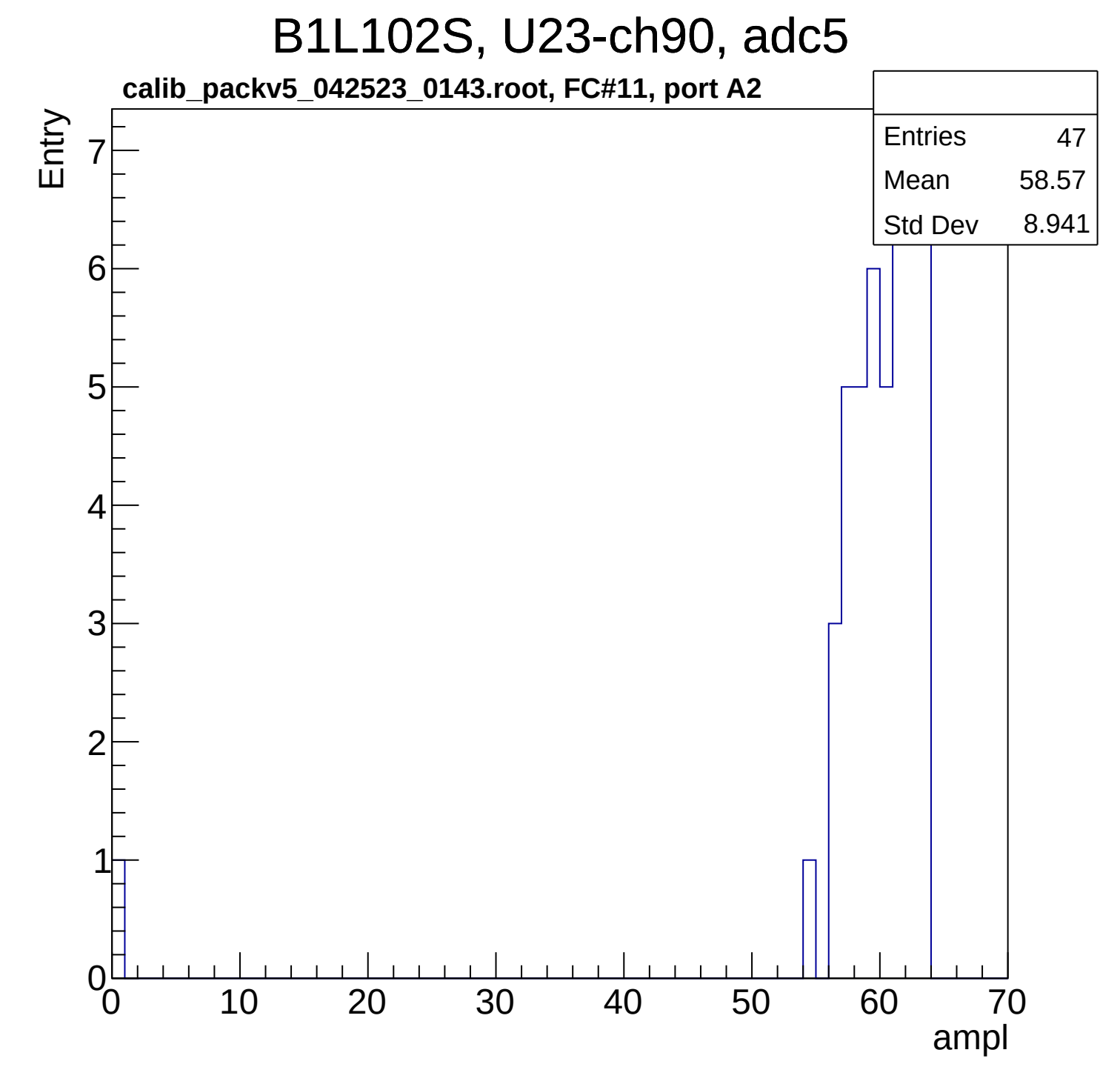

B1L102S, U23-ch90, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.03
Std Dev 5.224
Gaus mean : 29.3575
Gaus Width: 4.2517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

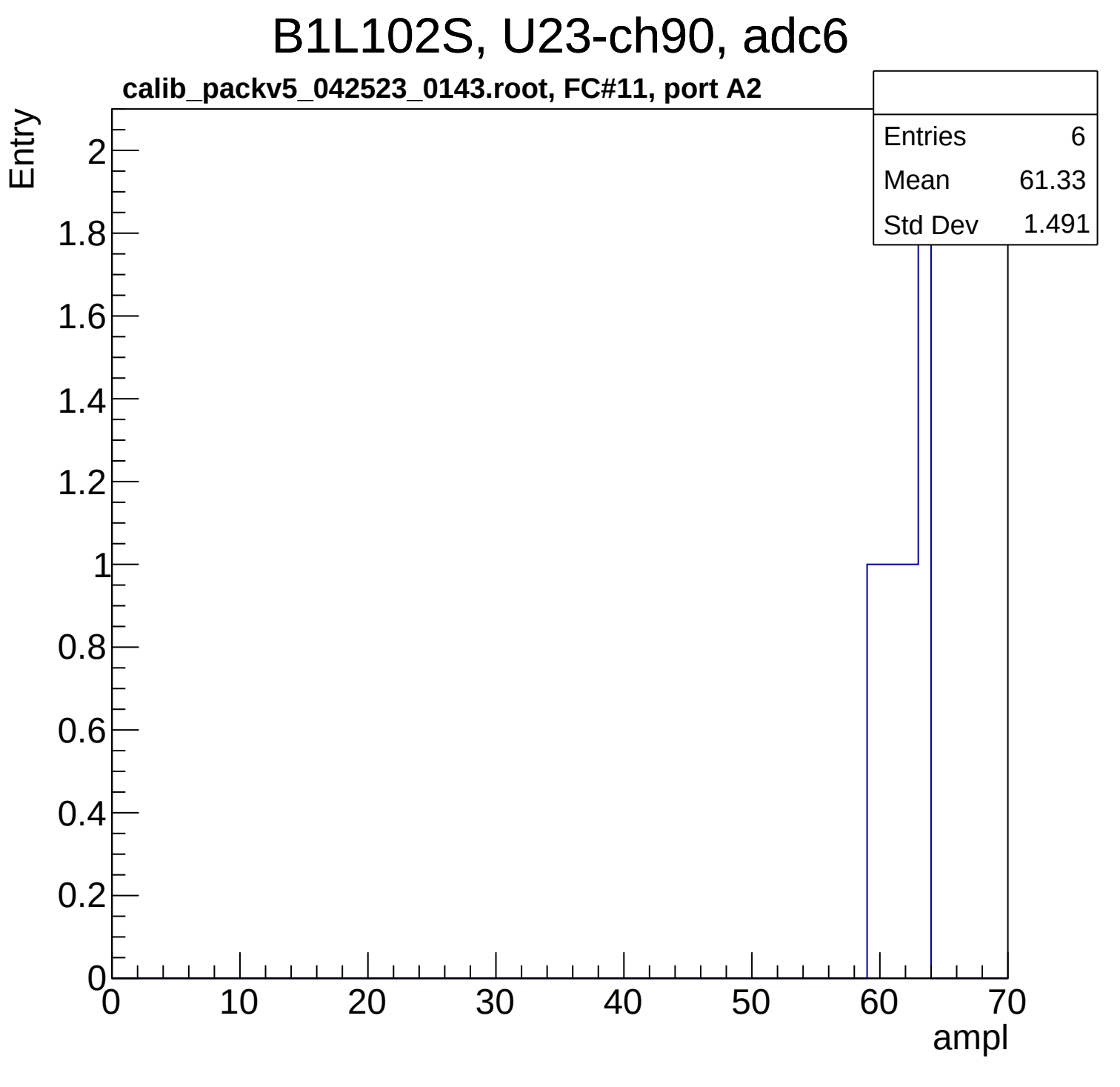

B1L102S, U23-ch90, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 36.49
Std Dev 3.726
Gaus mean : 36.7232
Gaus Width: 3.9693
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

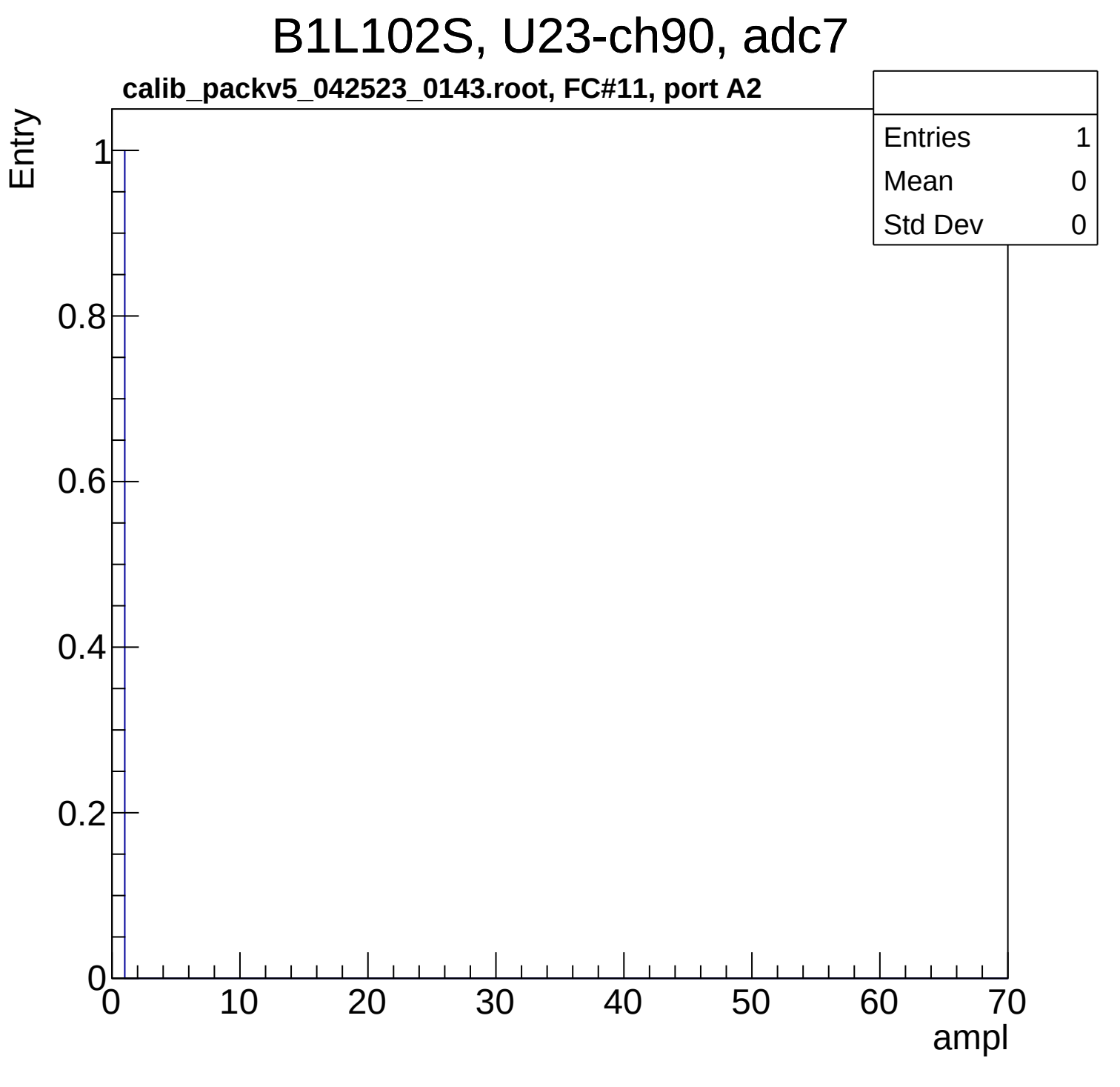

B1L102S, U23-ch90, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 43.53
Std Dev 3.192
Gaus mean : 44.5377
Gaus Width: 4.3056
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

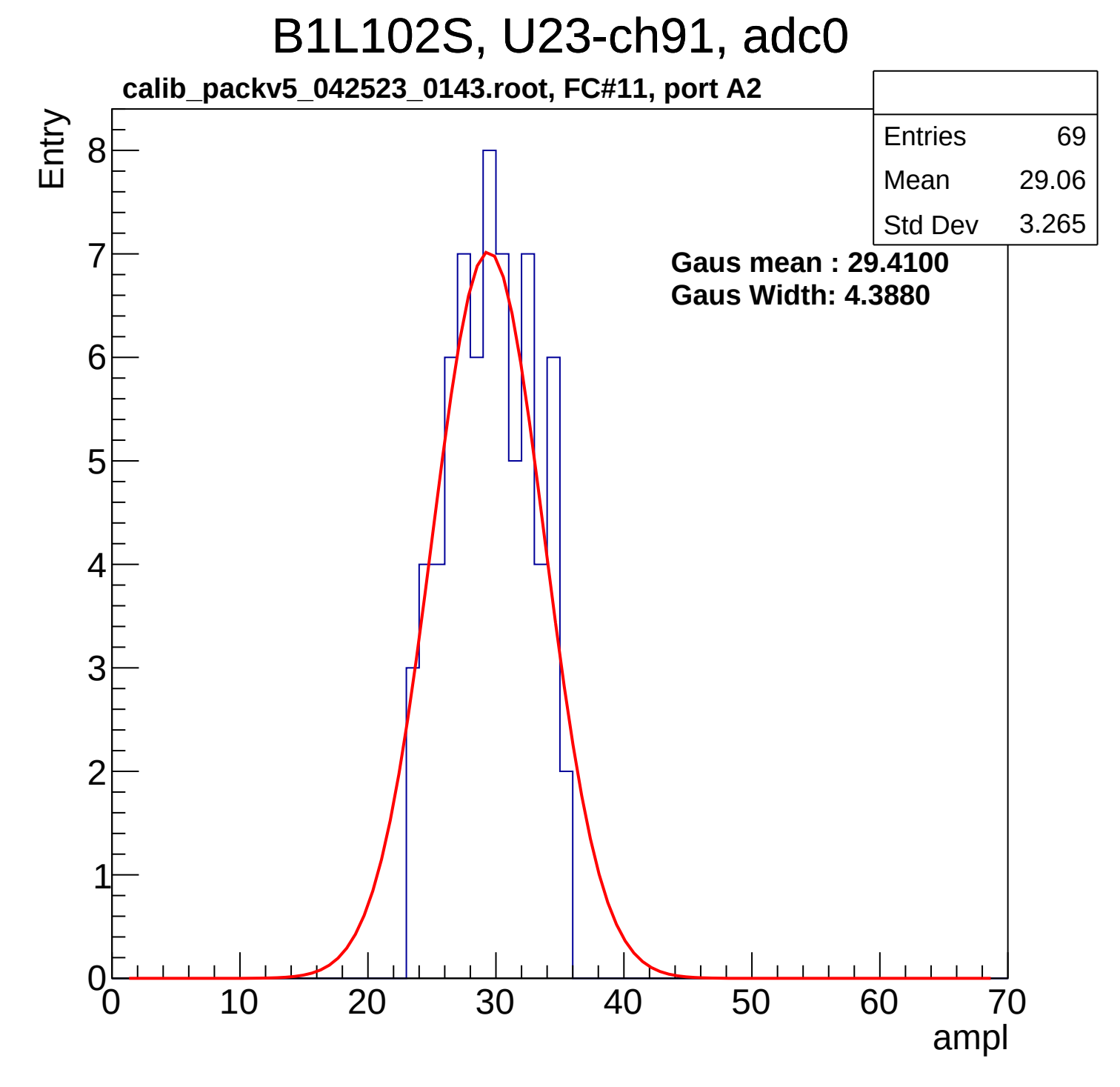

B1L102S, U23-ch90, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 50.16
Std Dev 3.612
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

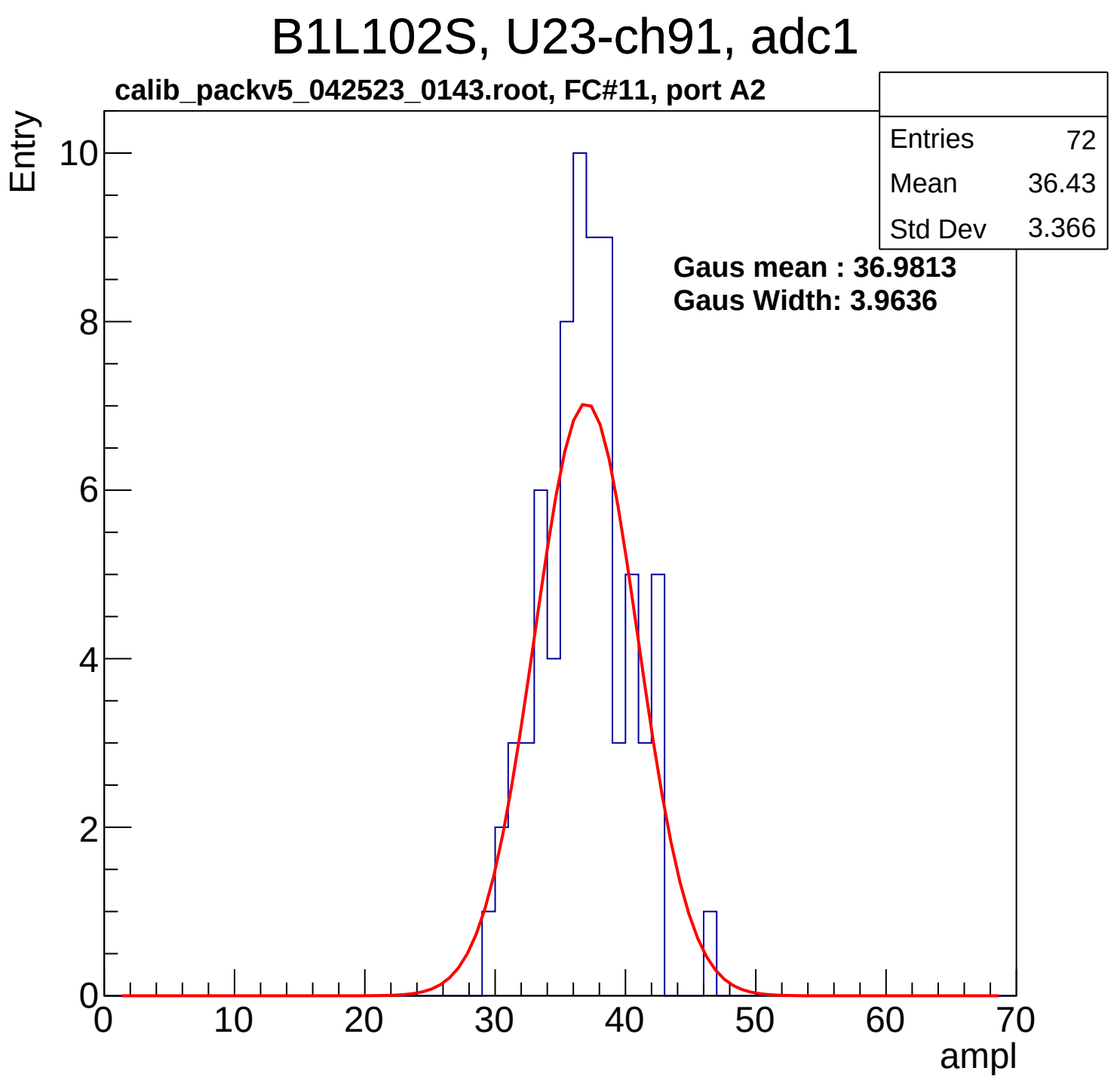

B1L102S, U23-ch90, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 54.87
Std Dev 3.968
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

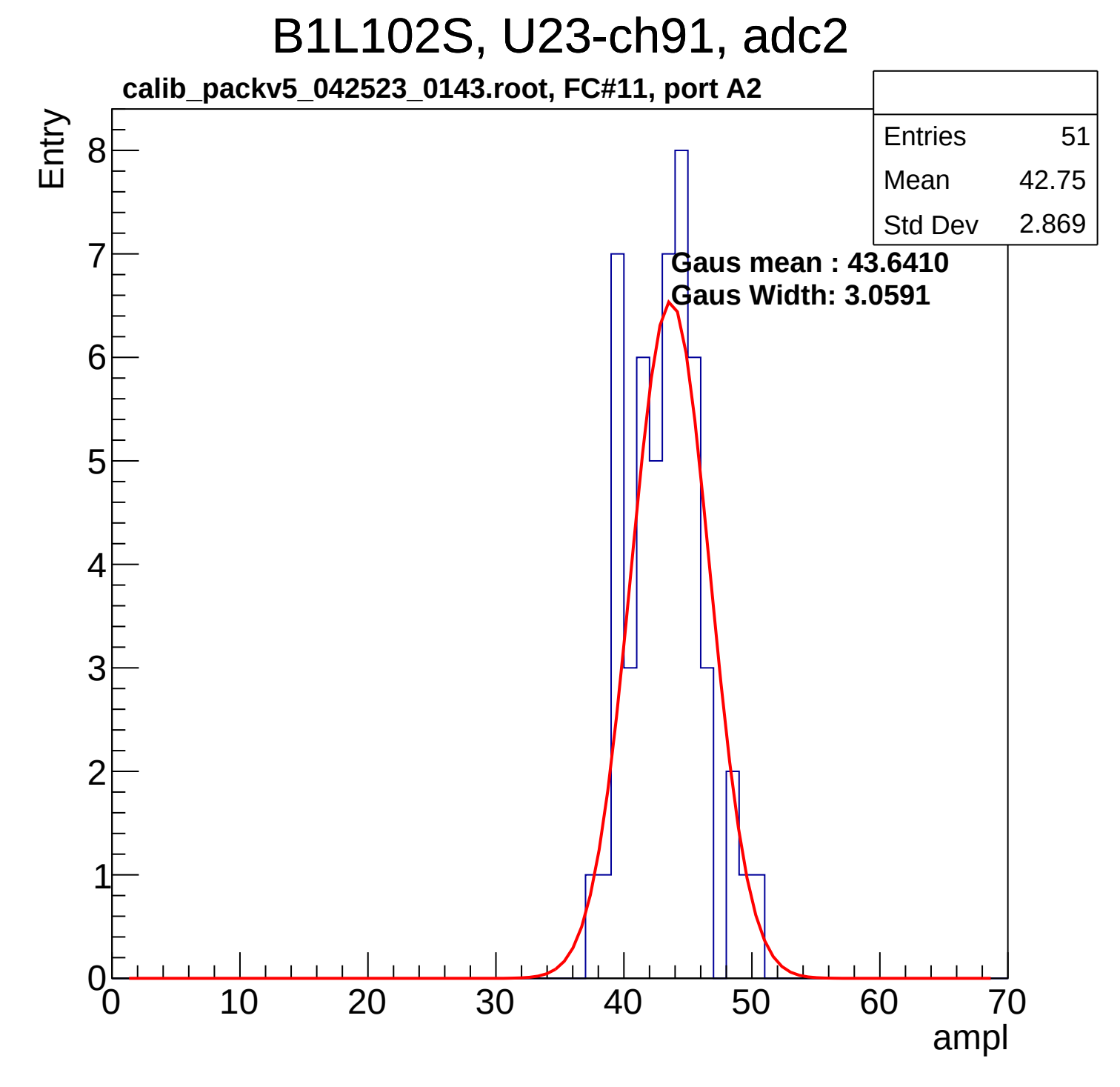

B1L102S, U23-ch90, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.57
Std Dev 8.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

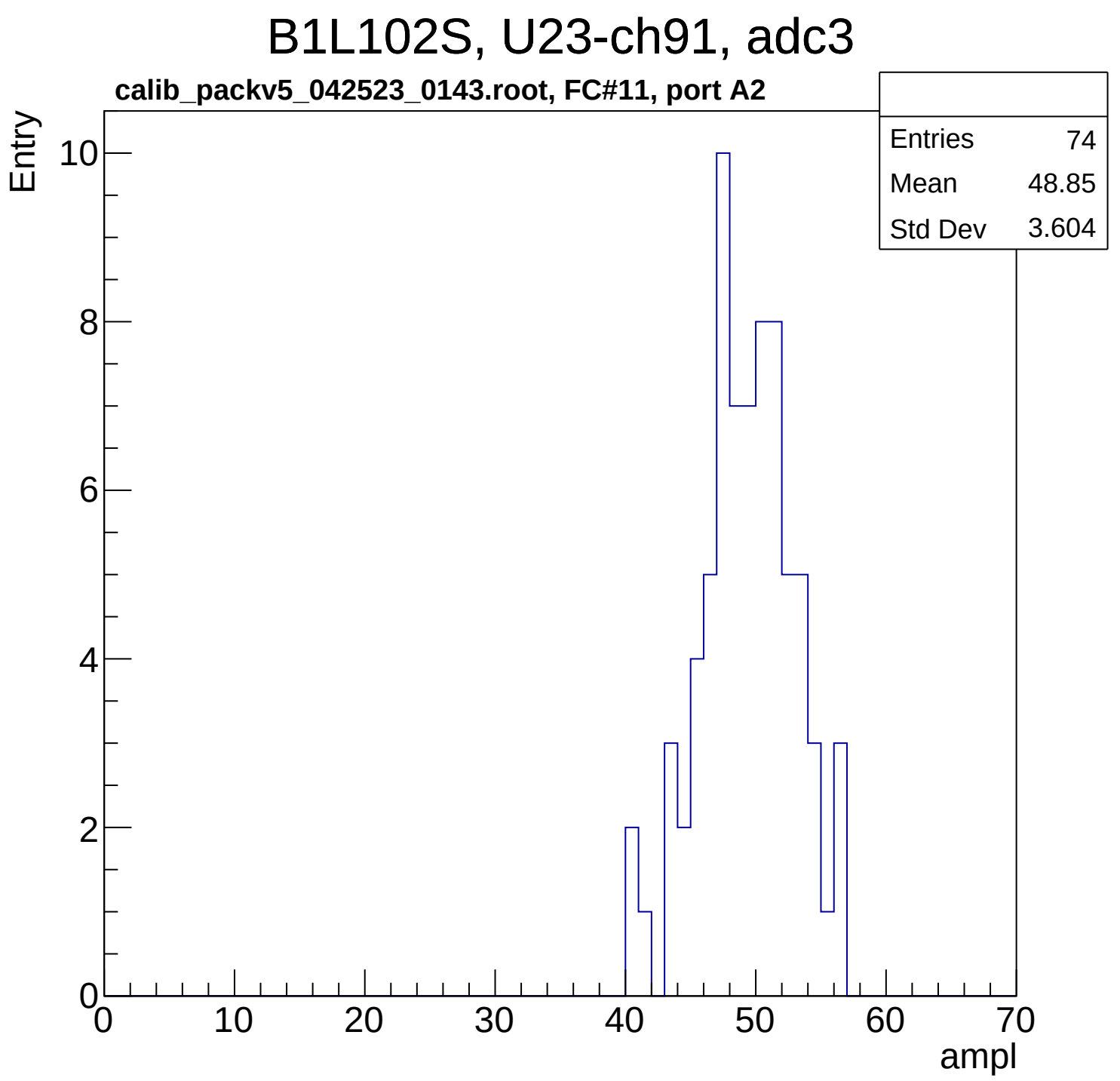

B1L102S, U23-ch90, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.33
Std Dev 1.491
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

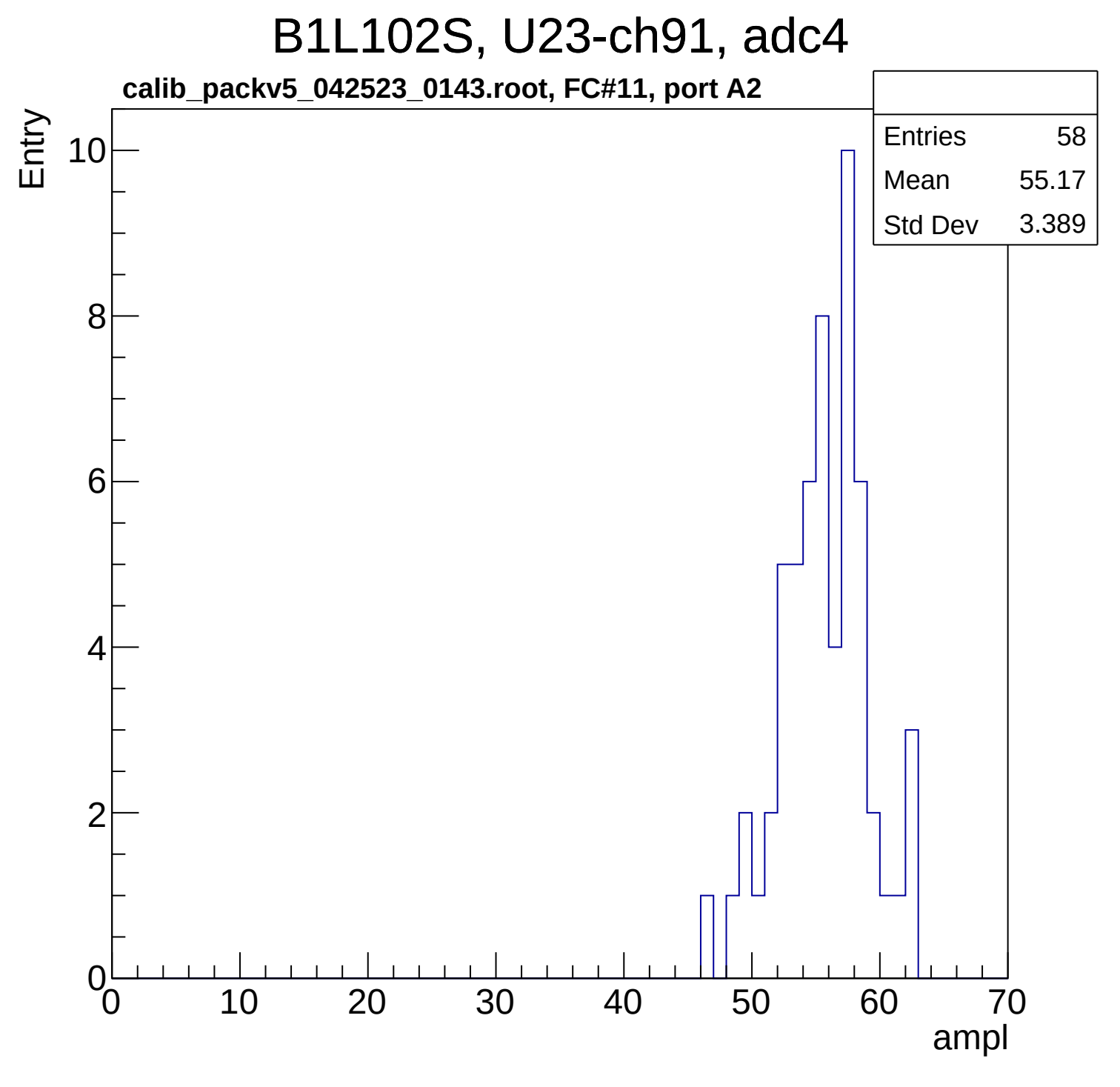

B1L102S, U23-ch90, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

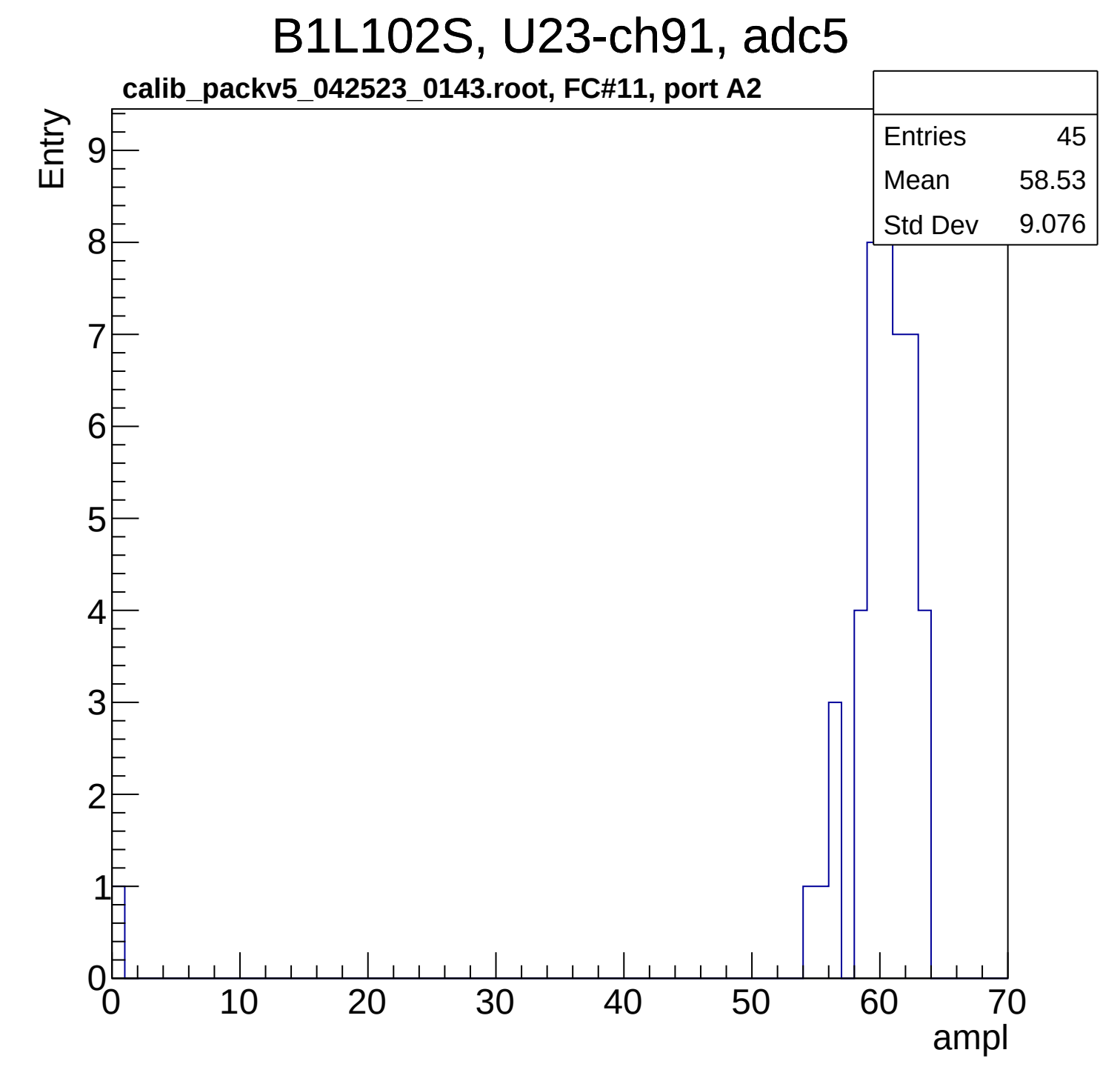

B1L102S, U23-ch91, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 29.06
Std Dev 3.265
Gaus mean : 29.4100
Gaus Width: 4.3880
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

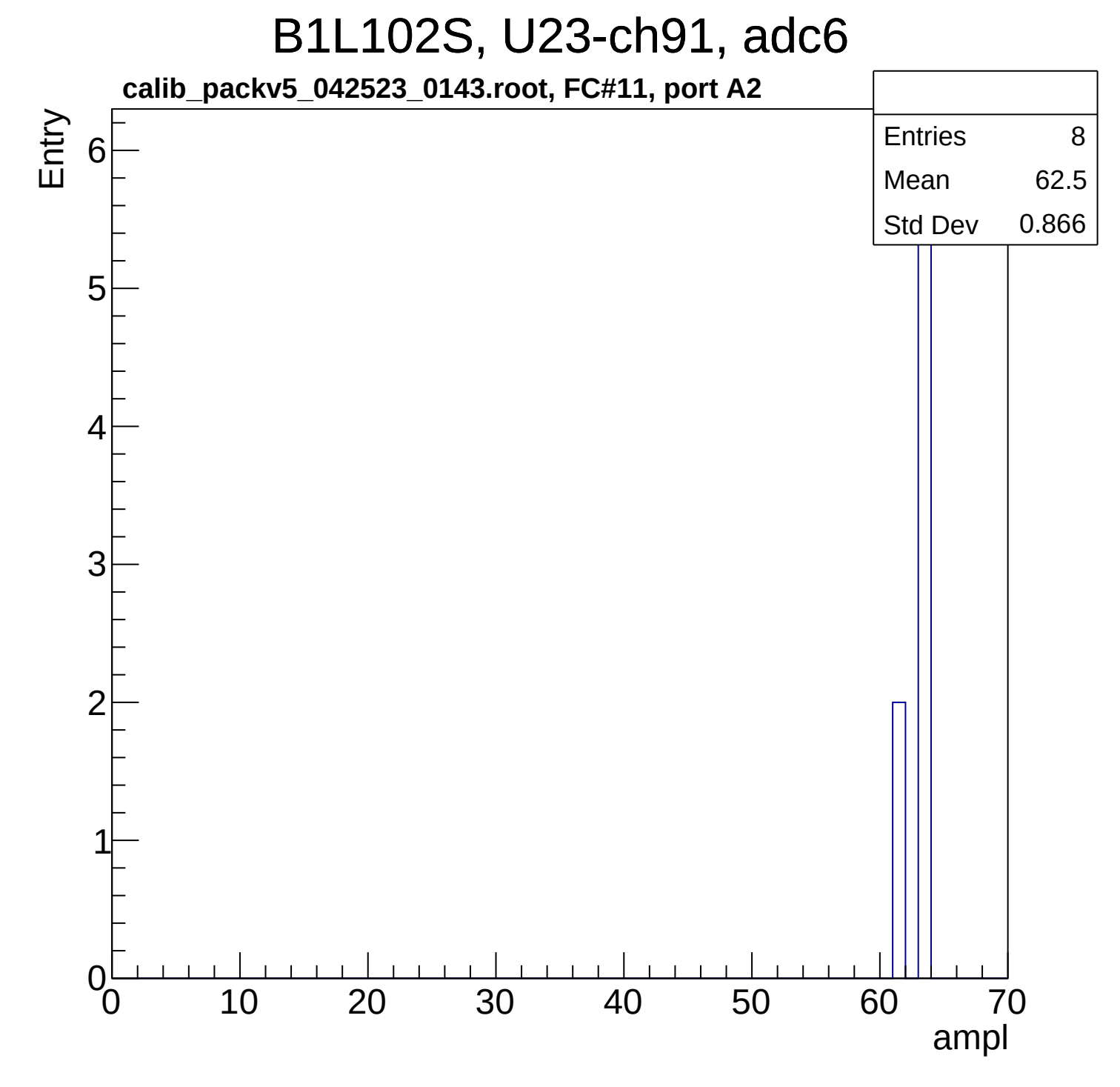

B1L102S, U23-ch91, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 36.43
Std Dev 3.366
Gaus mean : 36.9813
Gaus Width: 3.9636
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

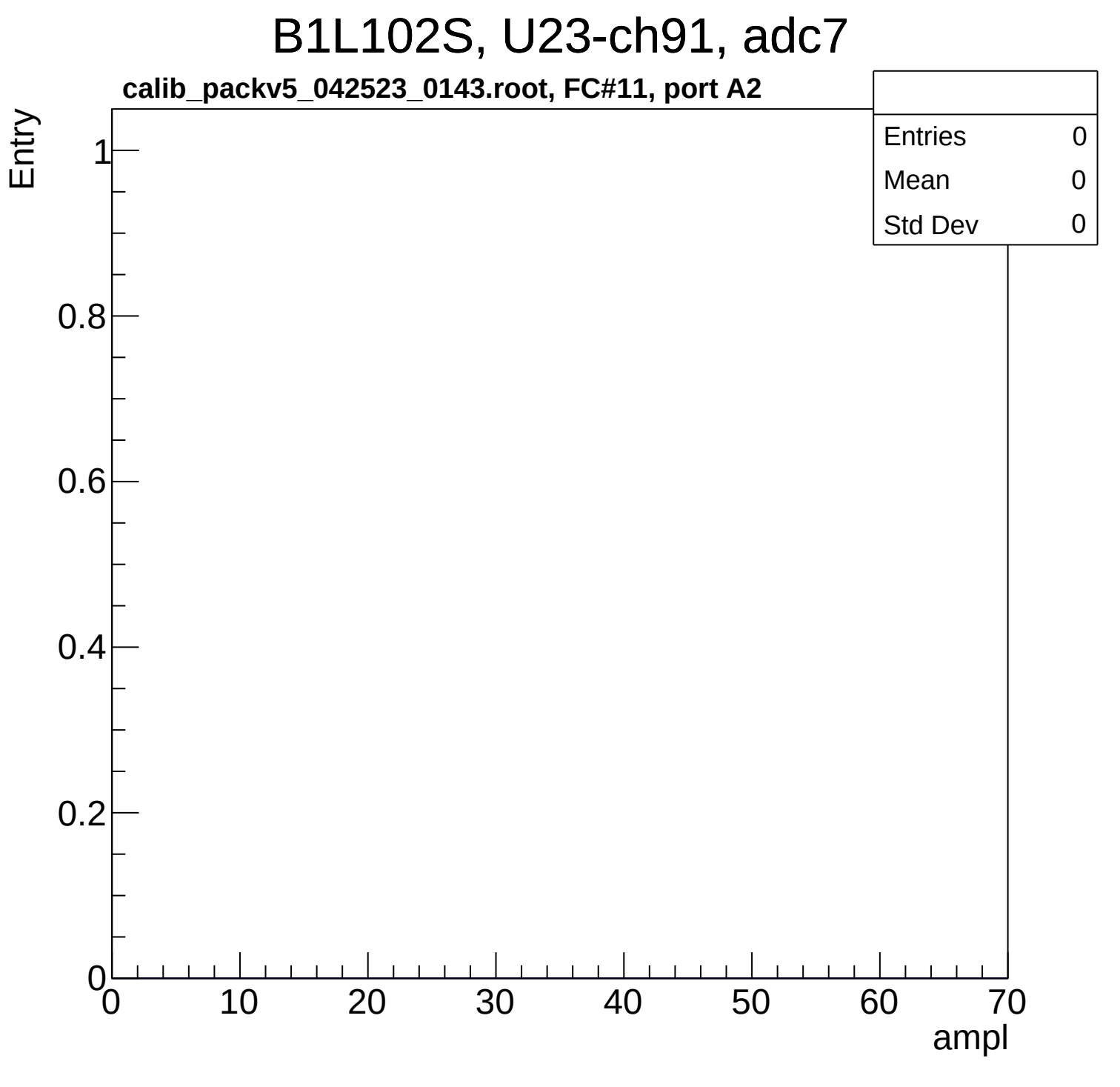

B1L102S, U23-ch91, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 42.75
Std Dev 2.869
Gaus mean : 43.6410
Gaus Width: 3.0591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

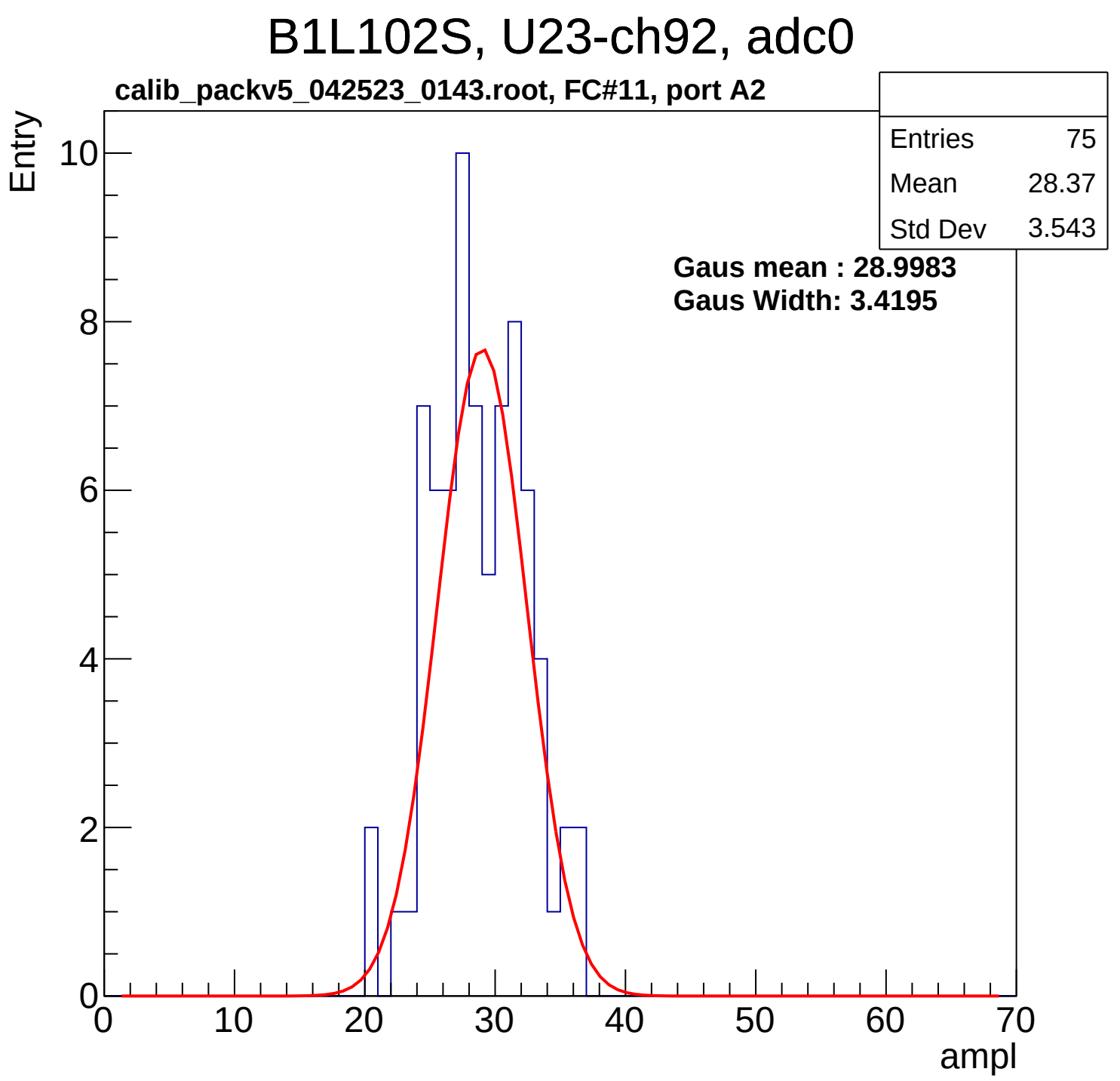

B1L102S, U23-ch91, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 48.85
Std Dev 3.604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

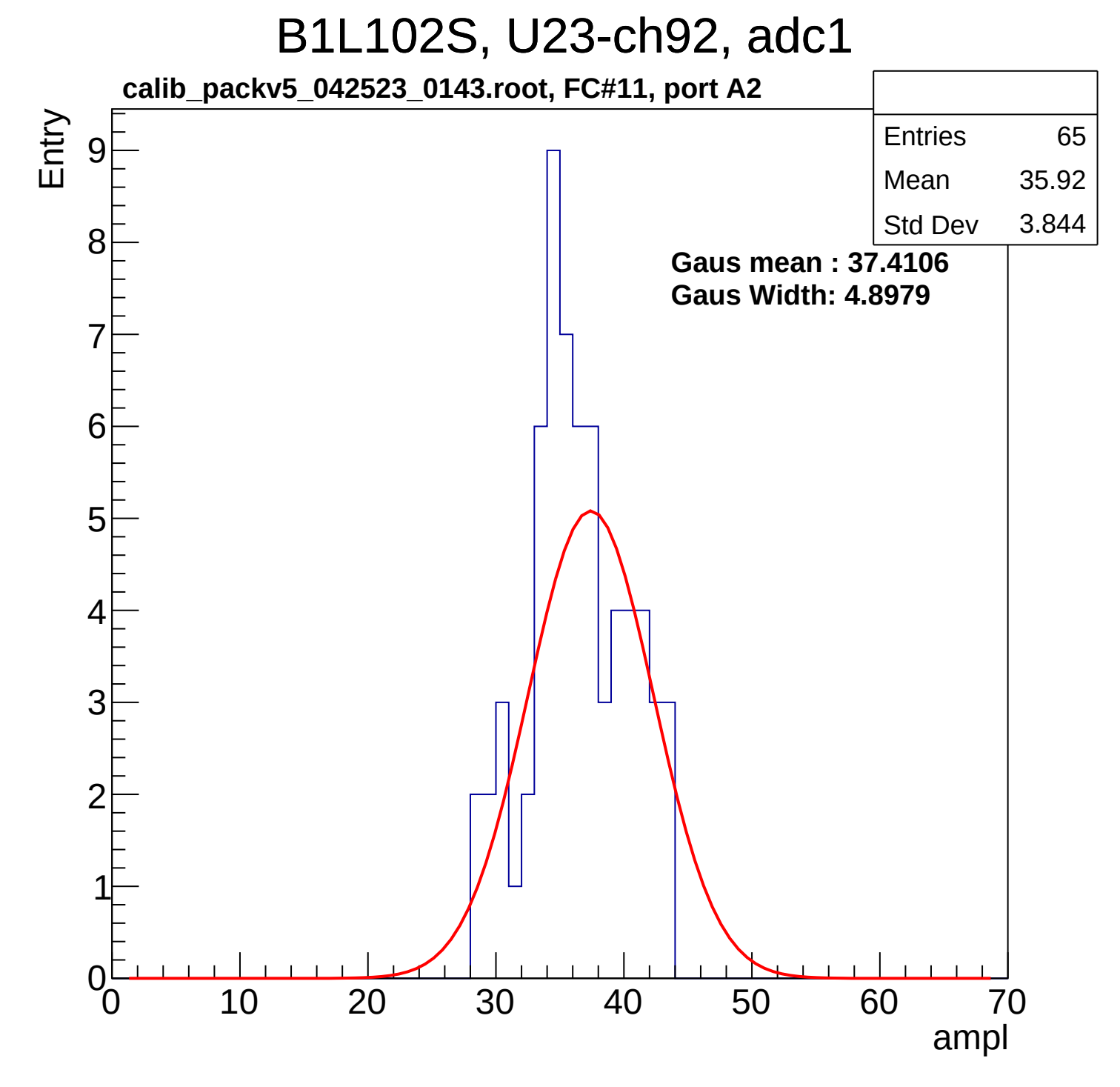

B1L102S, U23-ch91, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.17
Std Dev 3.389
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

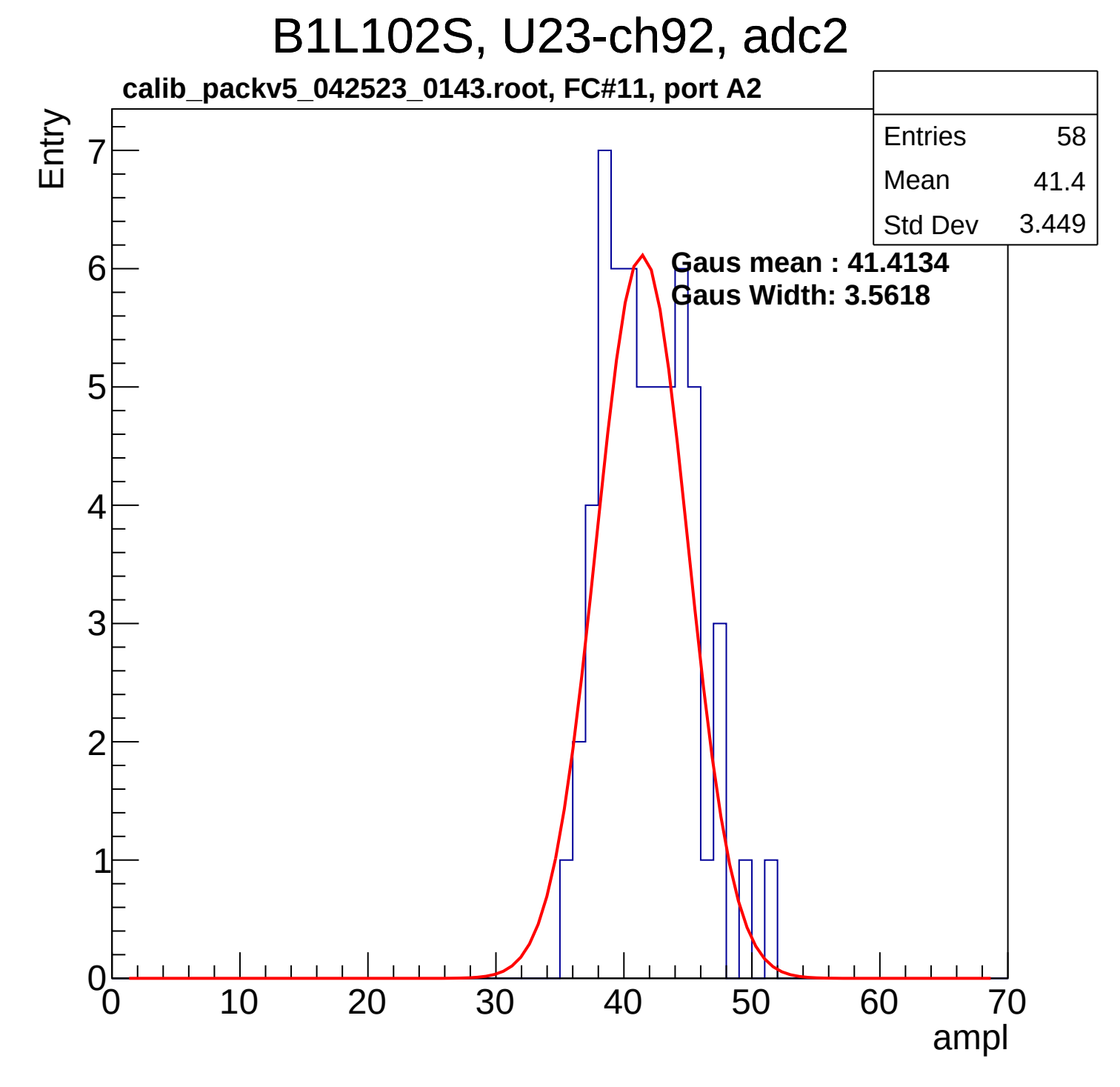

B1L102S, U23-ch91, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.53
Std Dev 9.076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

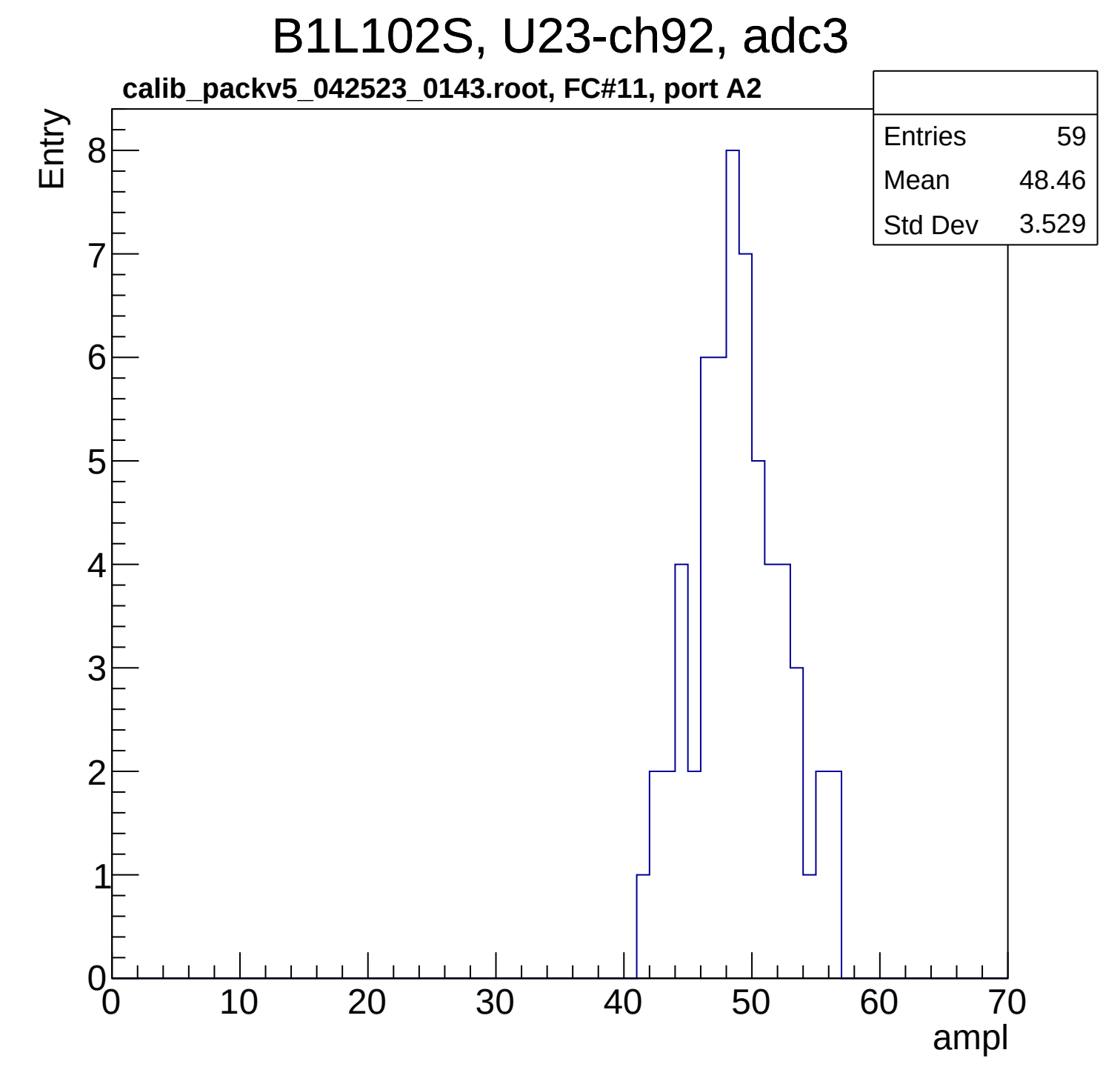

B1L102S, U23-ch91, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 62.5
Std Dev 0.866
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

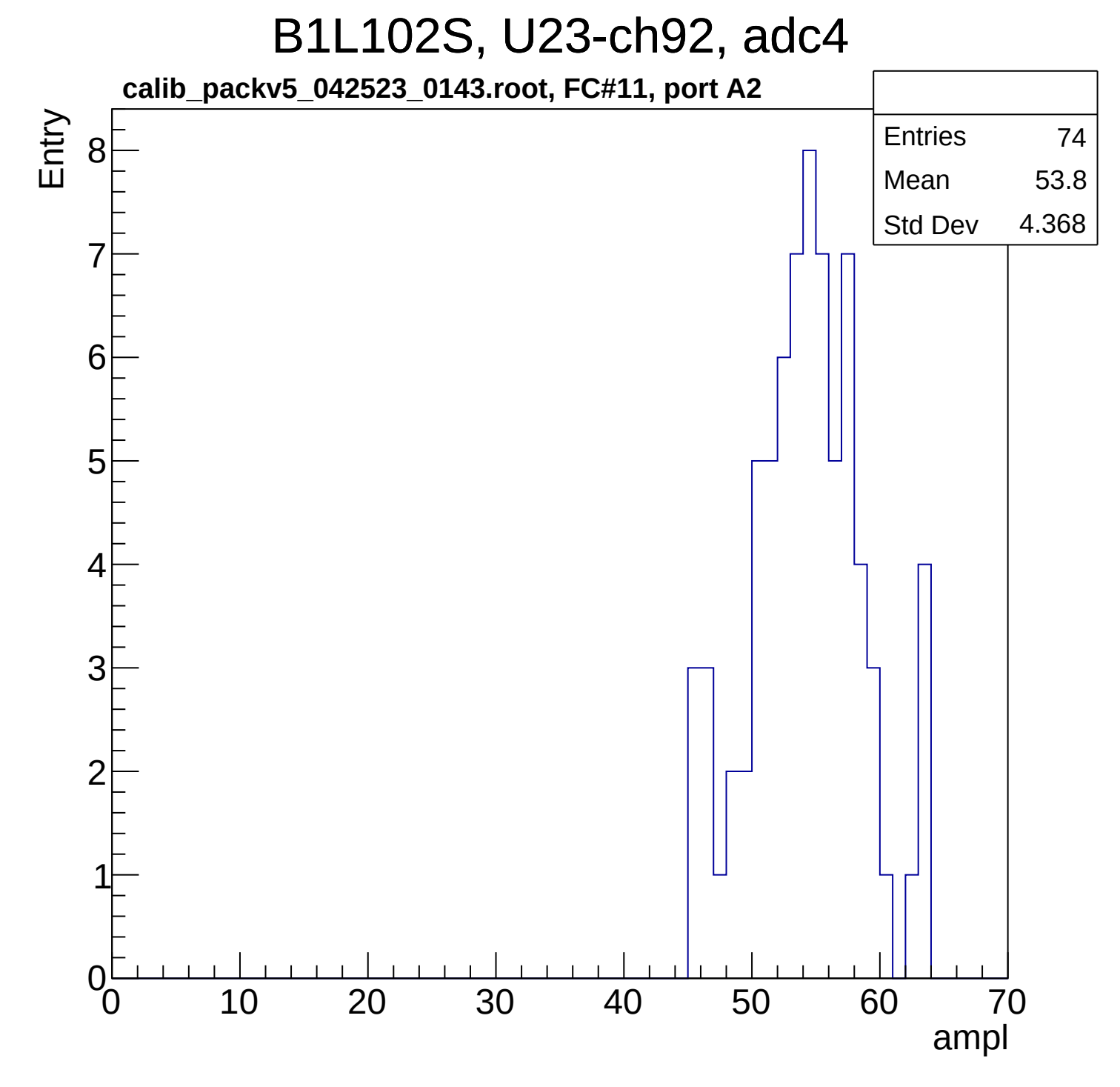

B1L102S, U23-ch91, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

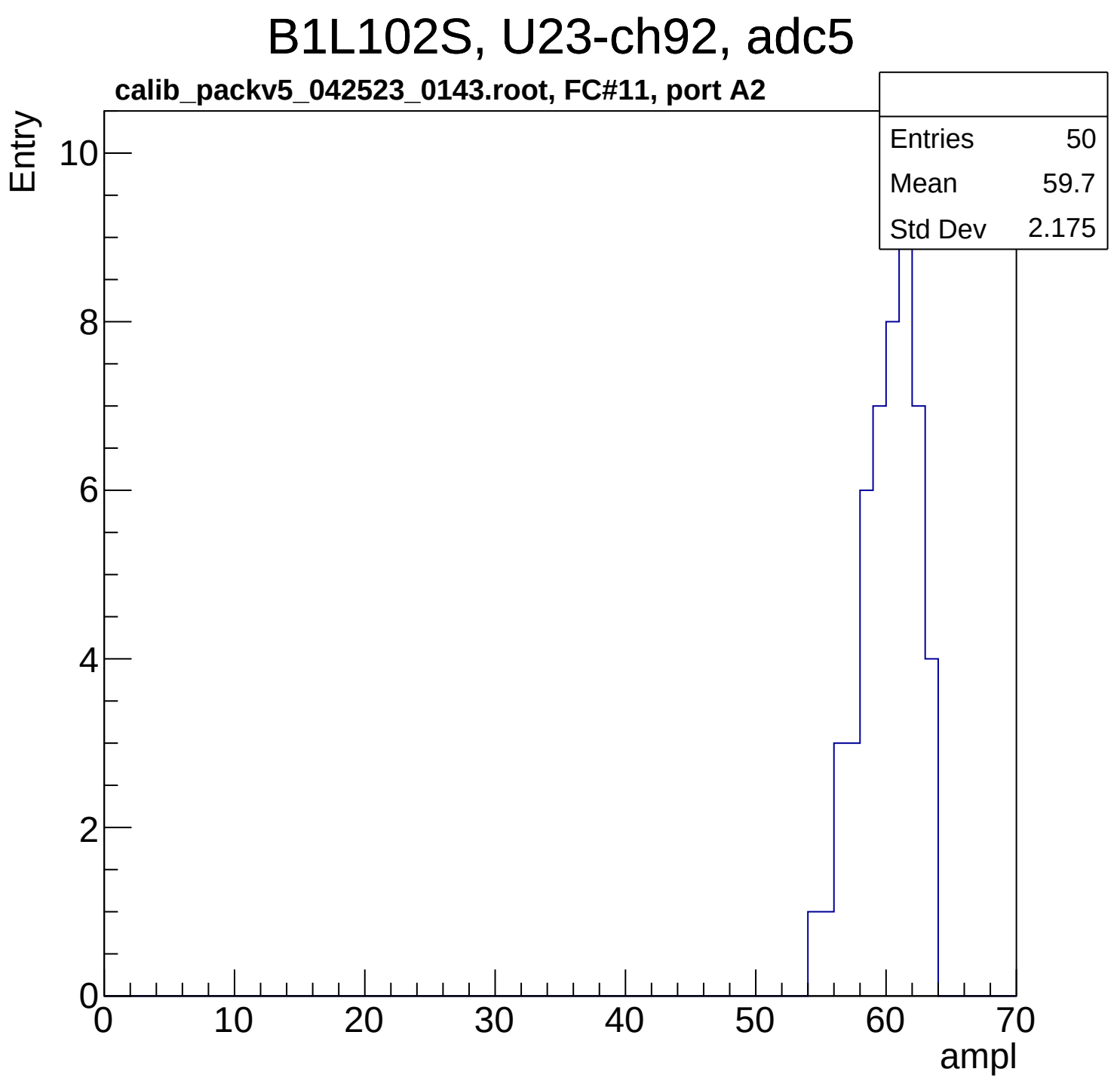

B1L102S, U23-ch92, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 28.37
Std Dev 3.543
Gaus mean : 28.9983
Gaus Width: 3.4195
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

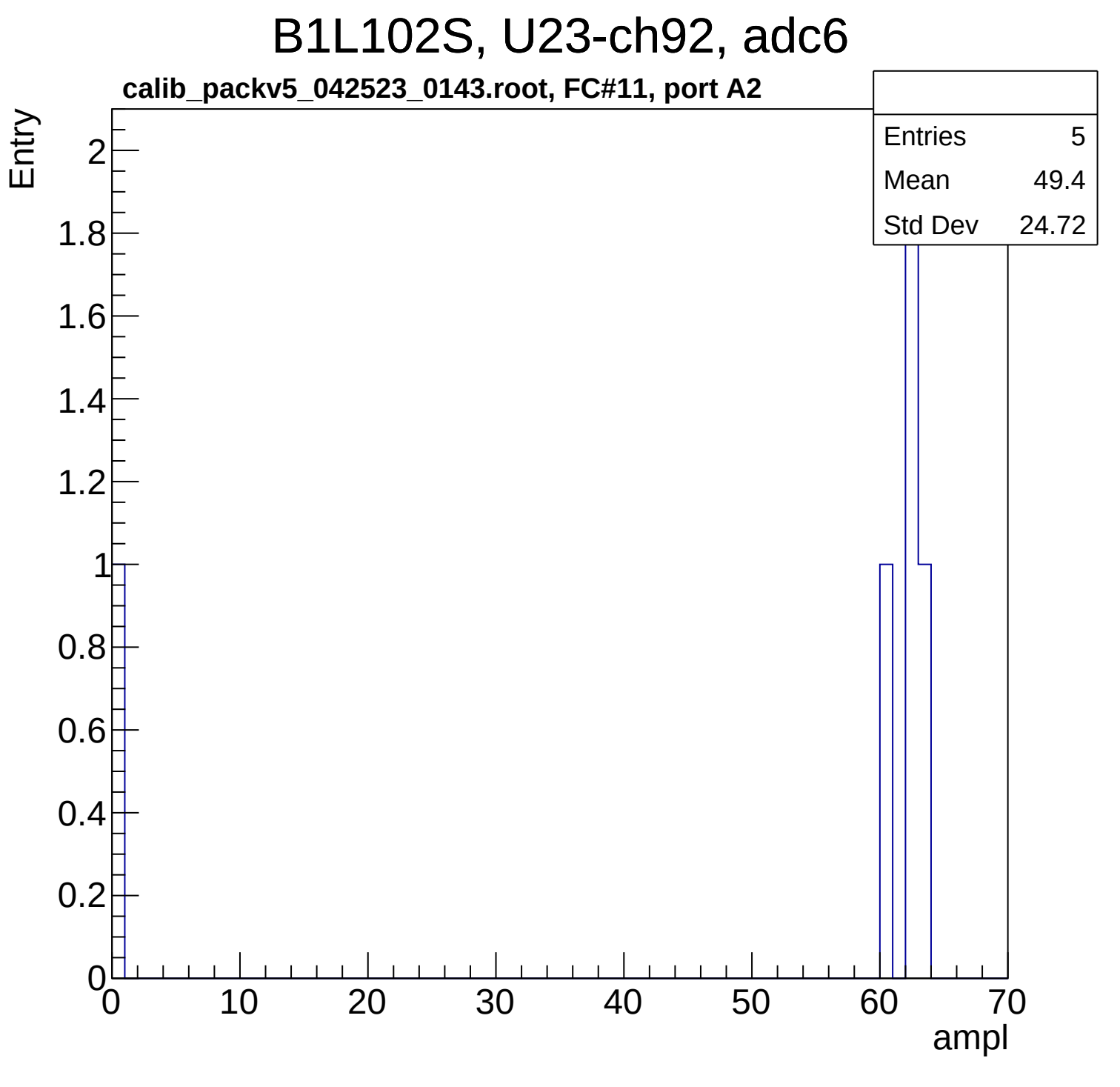

B1L102S, U23-ch92, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.92
Std Dev 3.844
Gaus mean : 37.4106
Gaus Width: 4.8979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

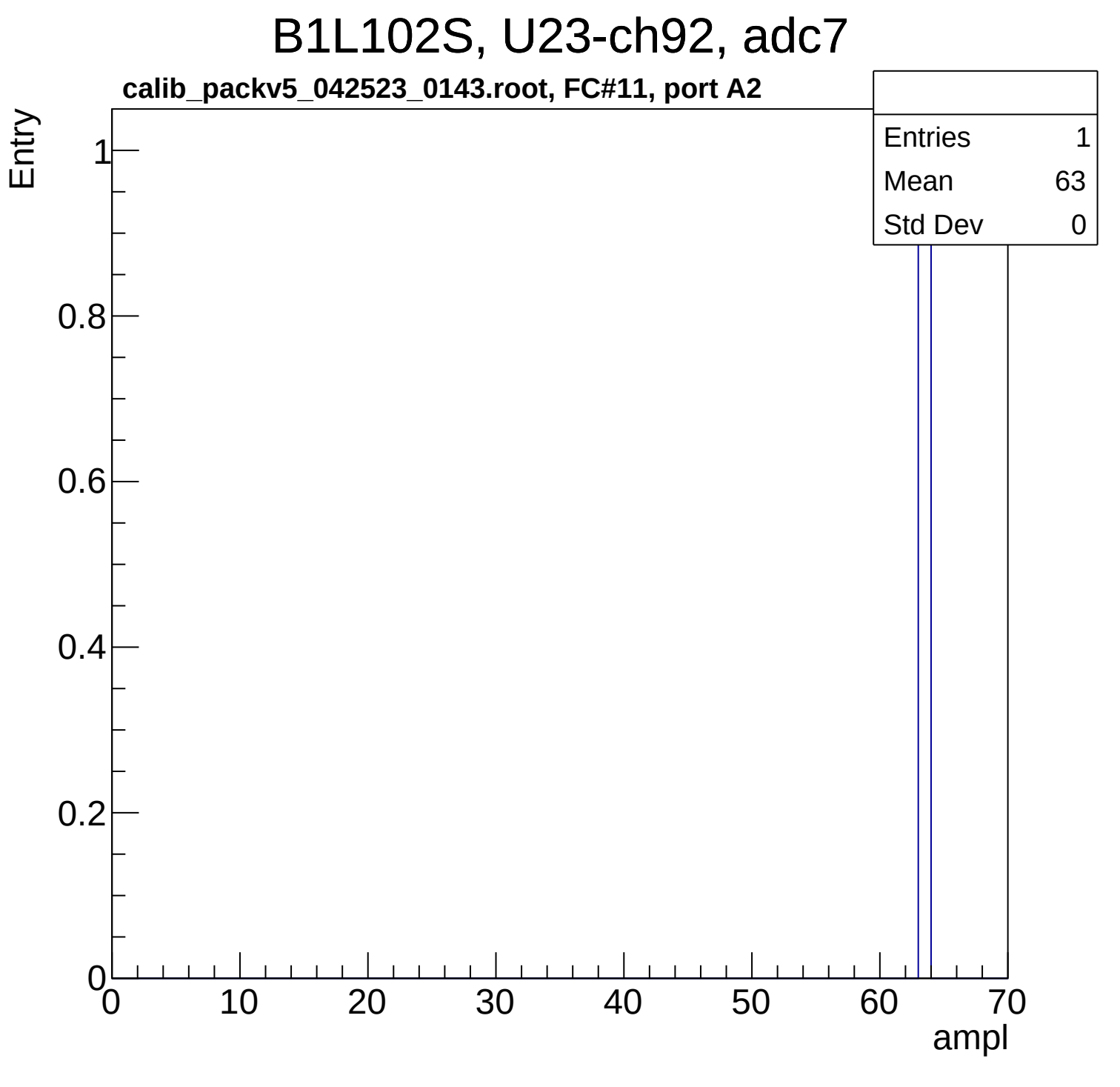

B1L102S, U23-ch92, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 41.4
Std Dev 3.449
Gaus mean : 41.4134
Gaus Width: 3.5618
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

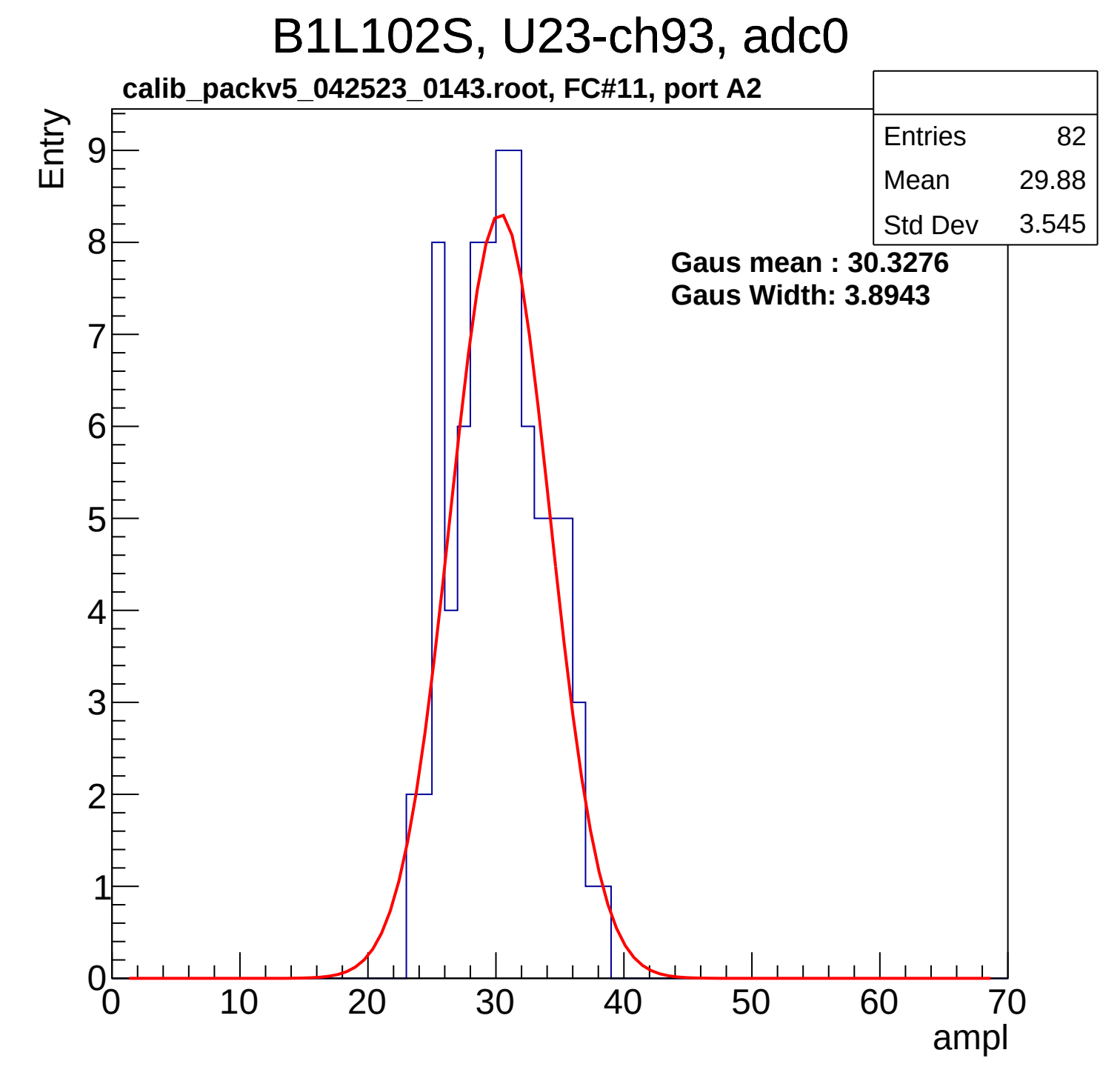

B1L102S, U23-ch92, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.46
Std Dev 3.529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

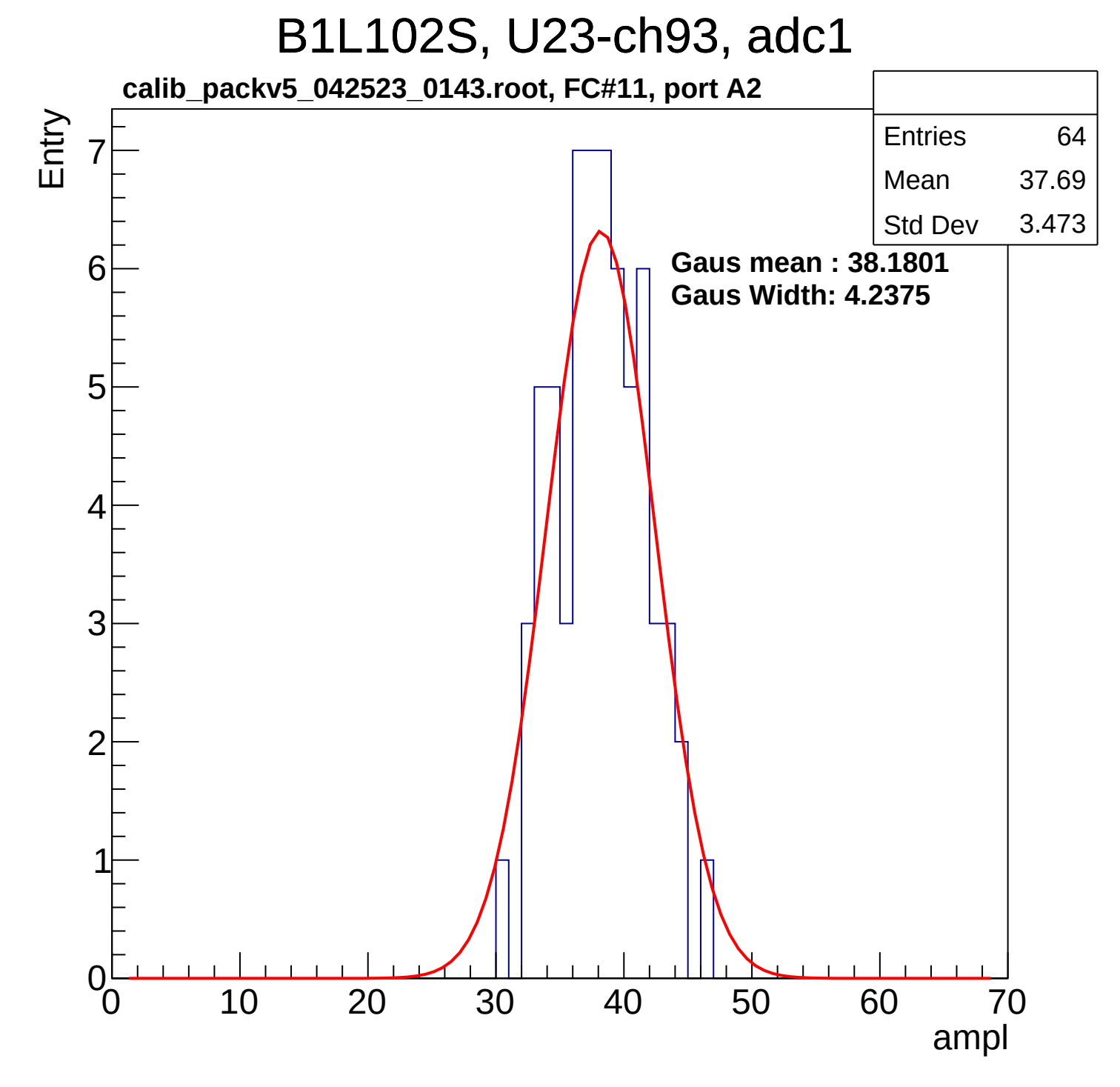

B1L102S, U23-ch92, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 53.8
Std Dev 4.368
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

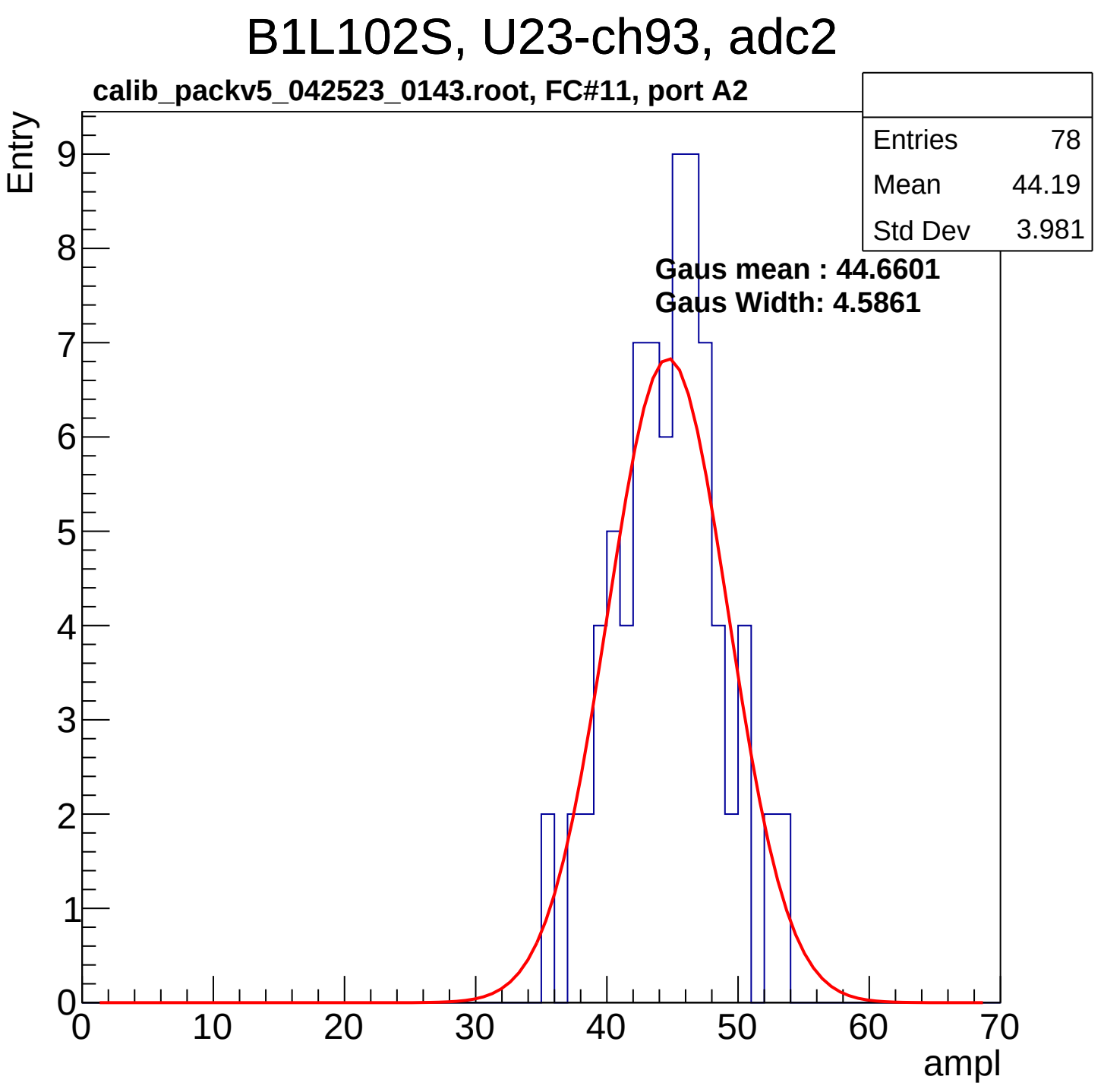

B1L102S, U23-ch92, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 59.7
Std Dev 2.175
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

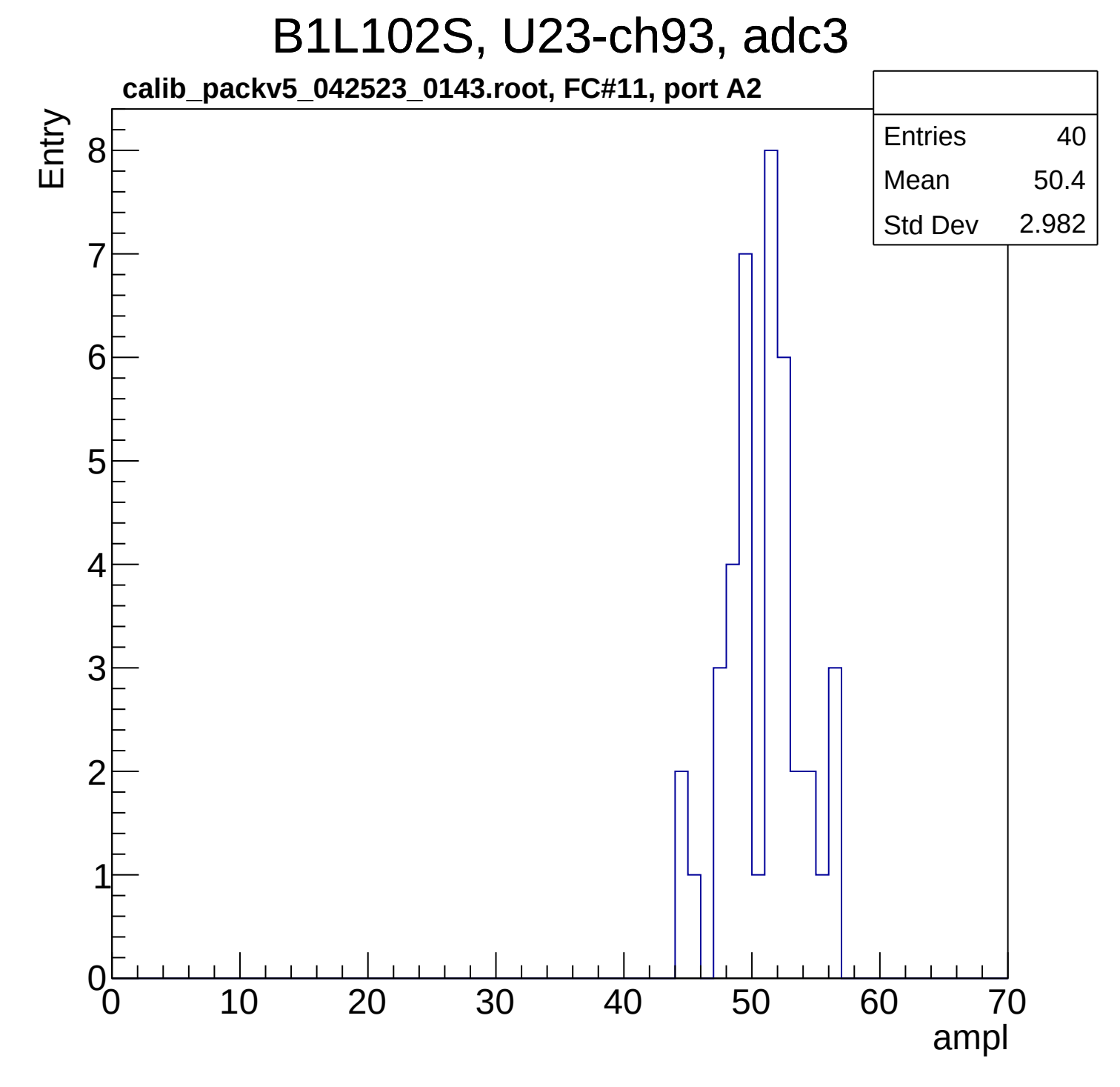

B1L102S, U23-ch92, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 49.4
Std Dev 24.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

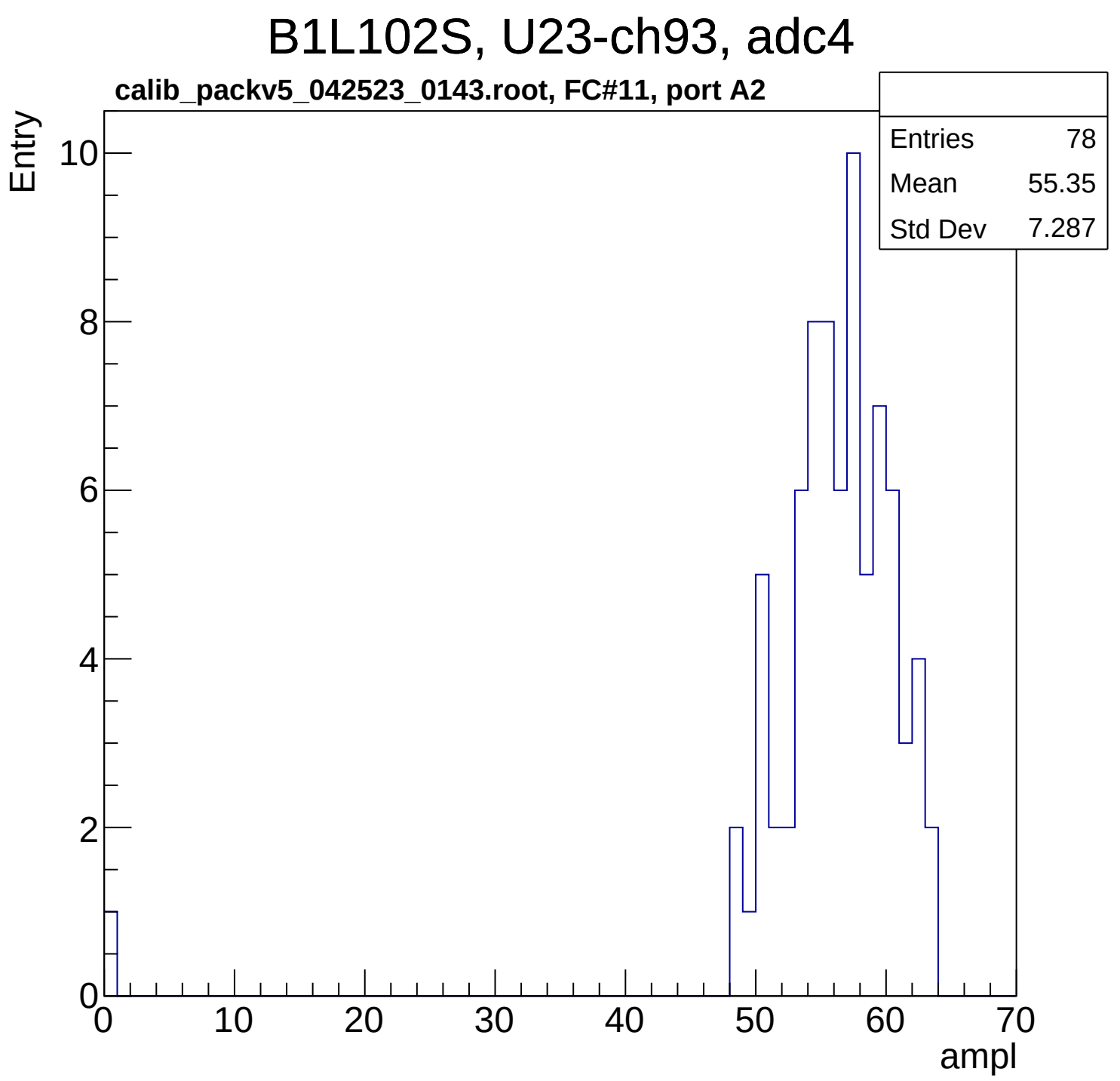

B1L102S, U23-ch92, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

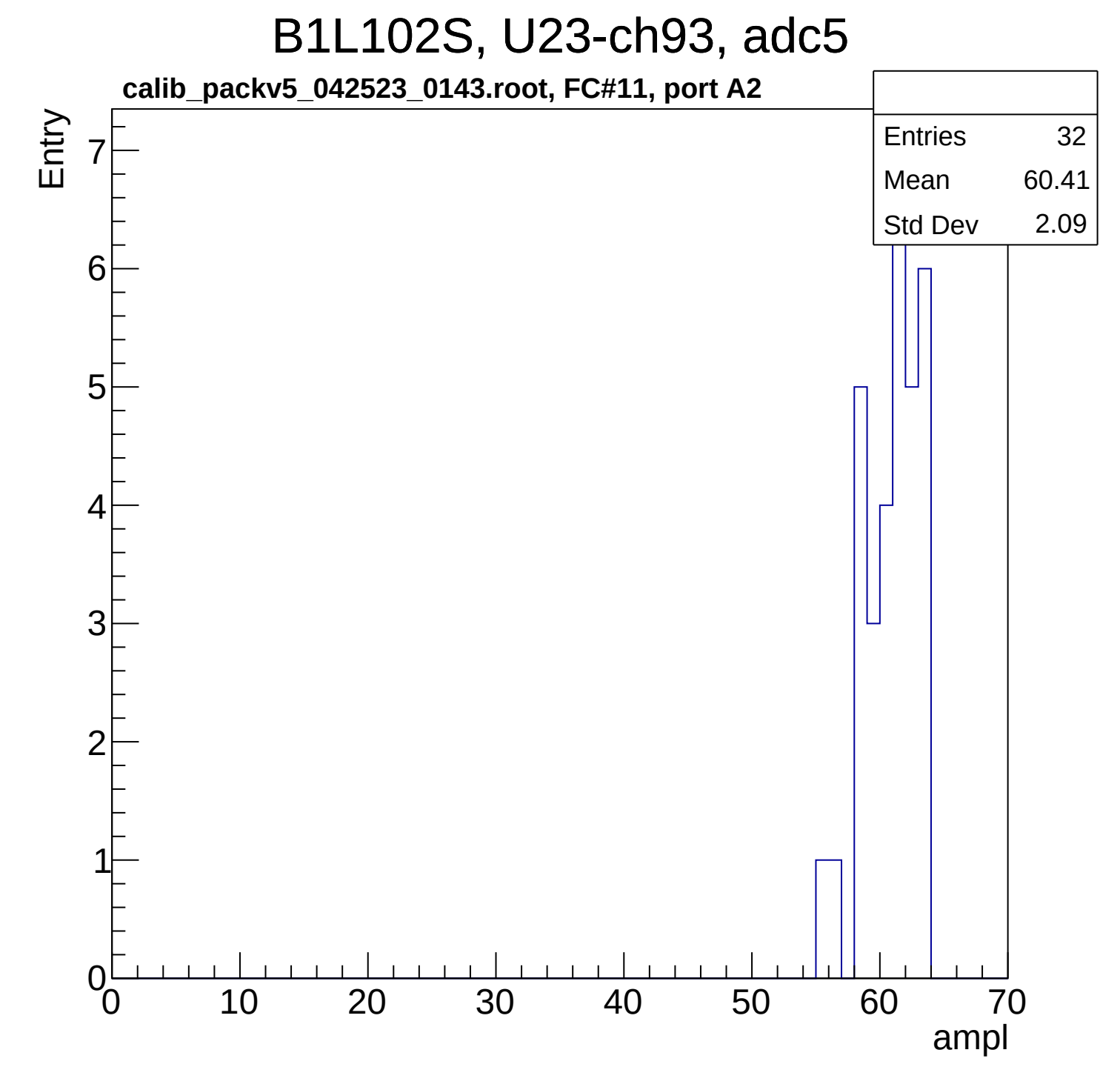

B1L102S, U23-ch93, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 29.88
Std Dev 3.545
Gaus mean : 30.3276
Gaus Width: 3.8943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

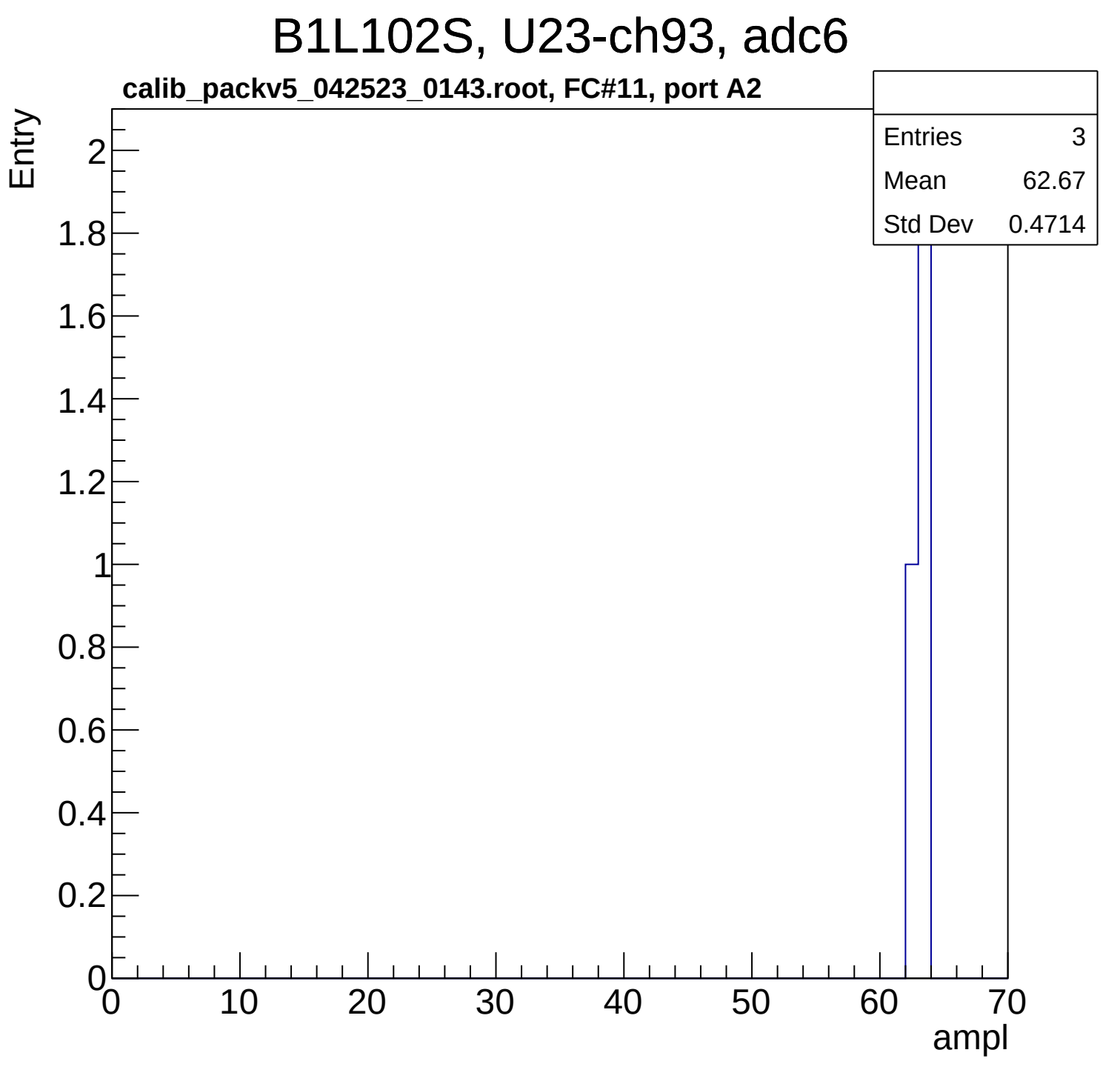

B1L102S, U23-ch93, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 37.69
Std Dev 3.473
Gaus mean : 38.1801
Gaus Width: 4.2375
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

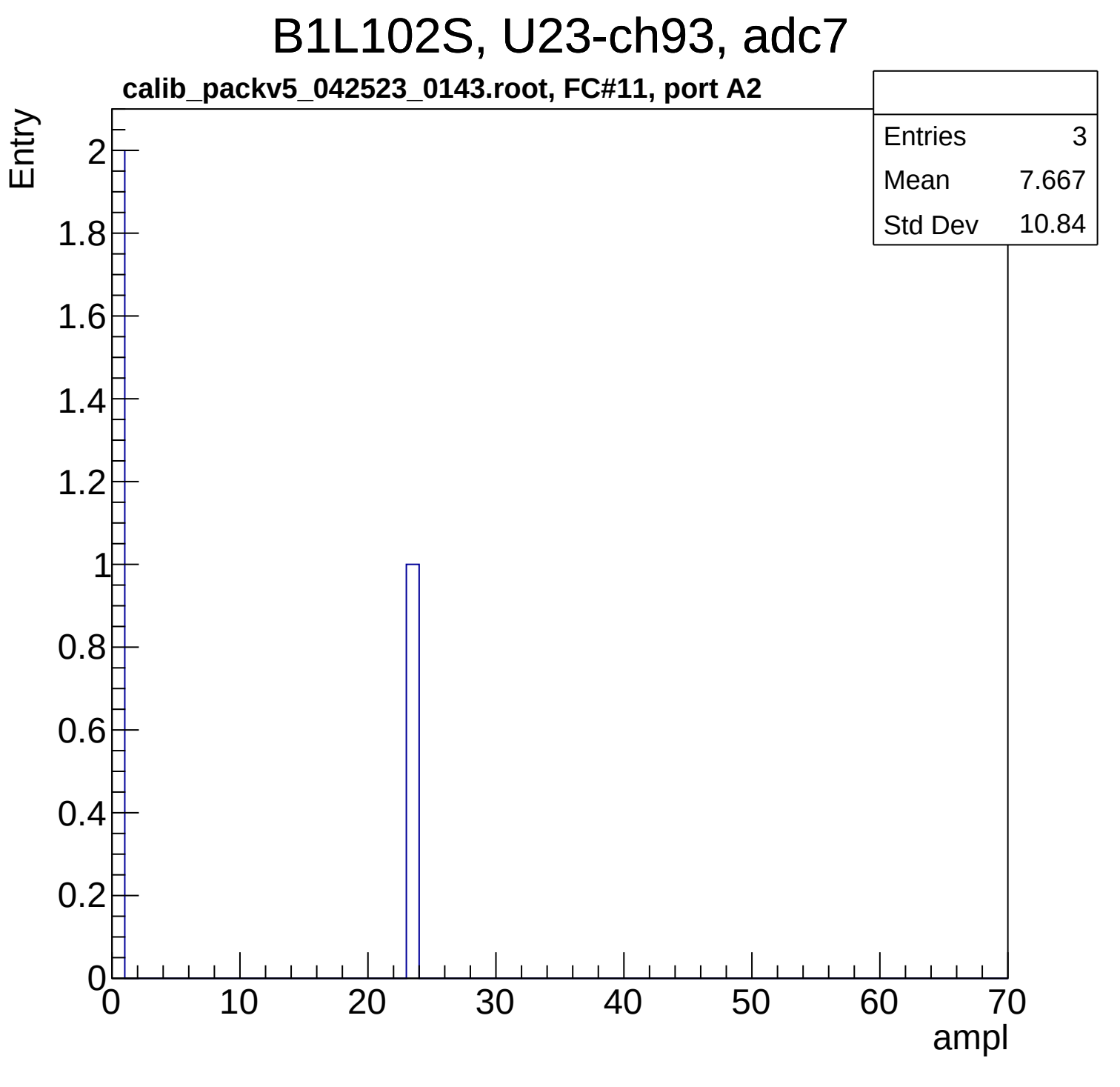

B1L102S, U23-ch93, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 44.19
Std Dev 3.981
Gaus mean : 44.6601
Gaus Width: 4.5861
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

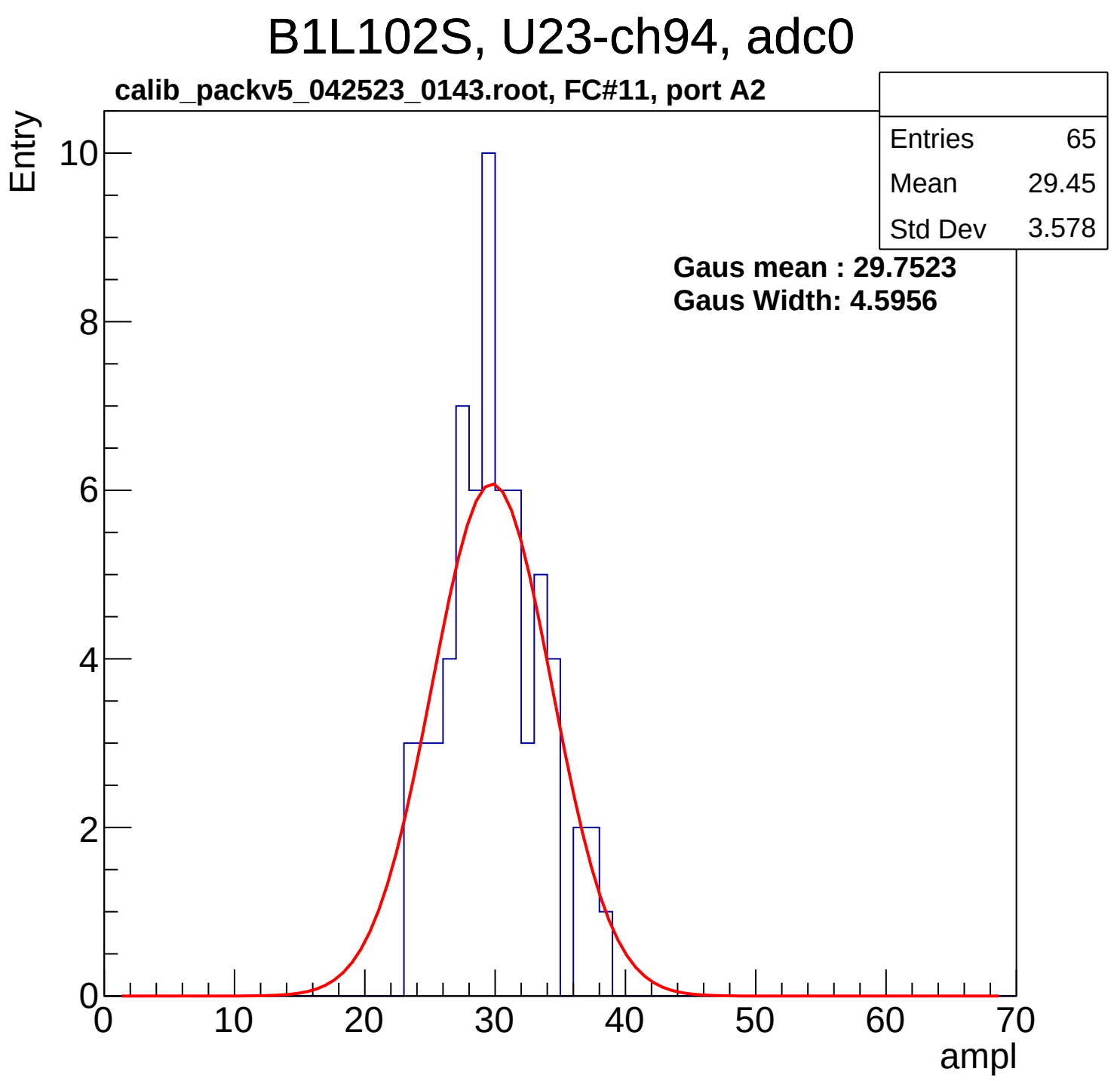

B1L102S, U23-ch93, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 50.4
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

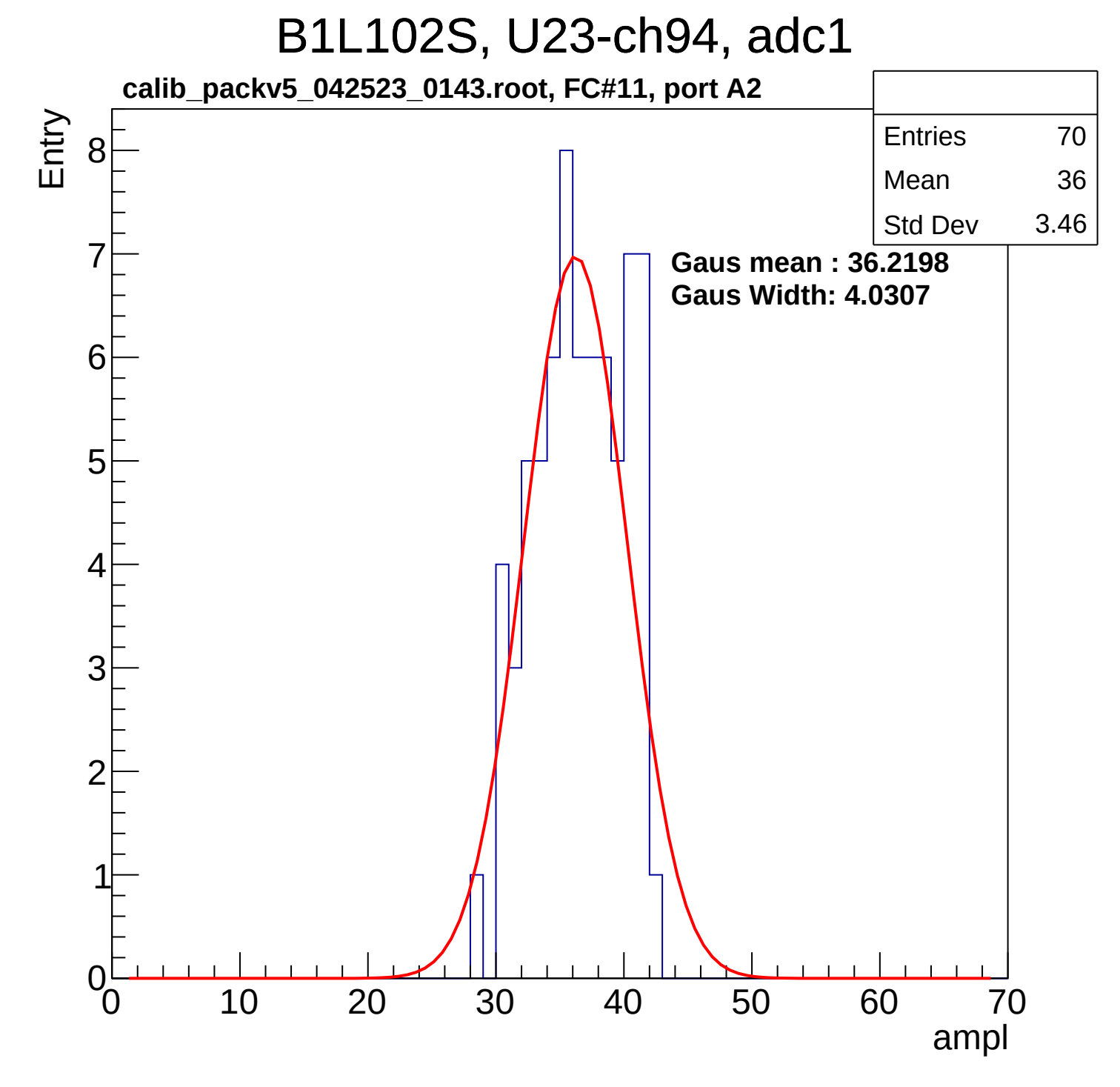

B1L102S, U23-ch93, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 55.35
Std Dev 7.287
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

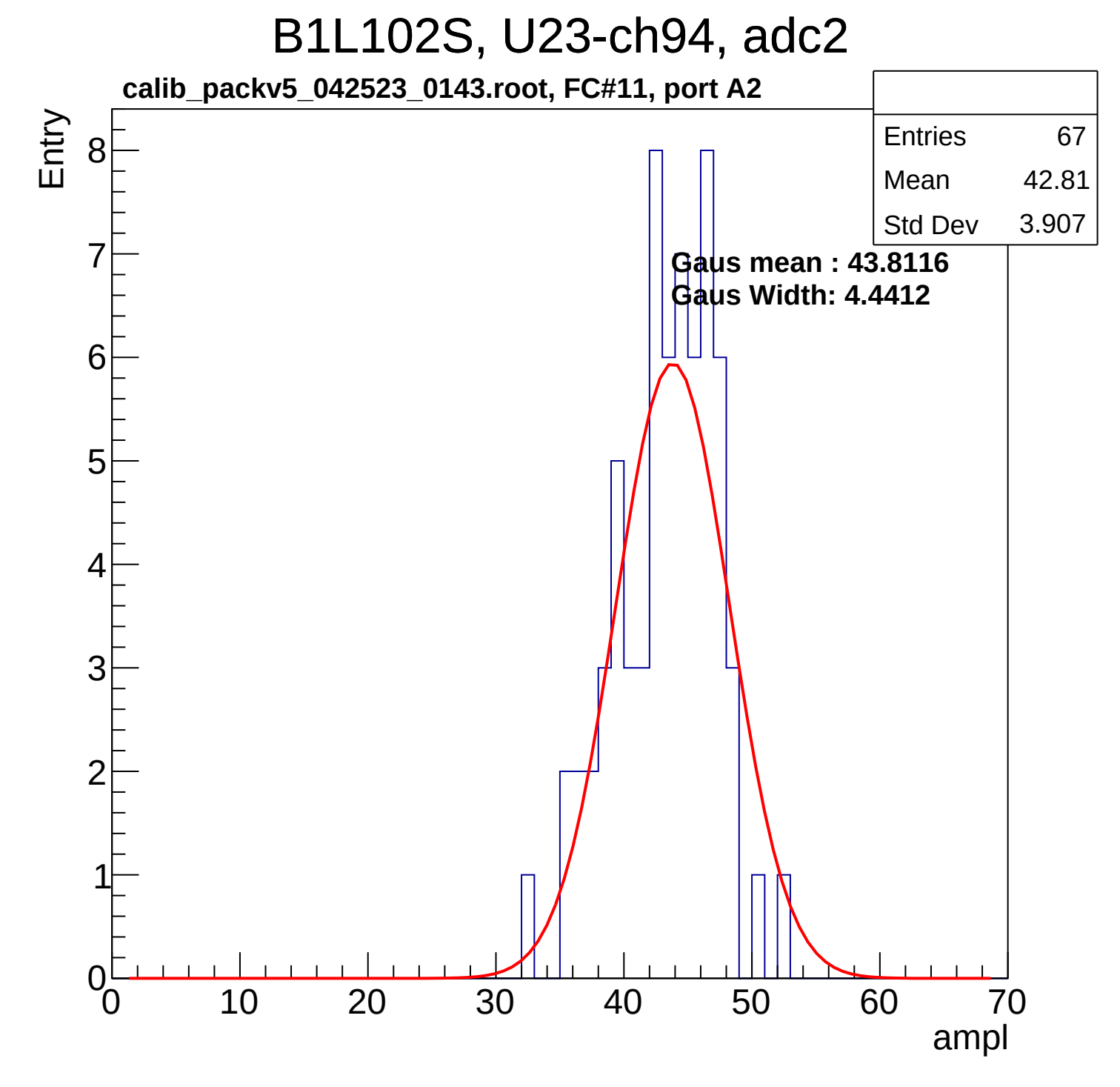

B1L102S, U23-ch93, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 32
Mean 60.41
Std Dev 2.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

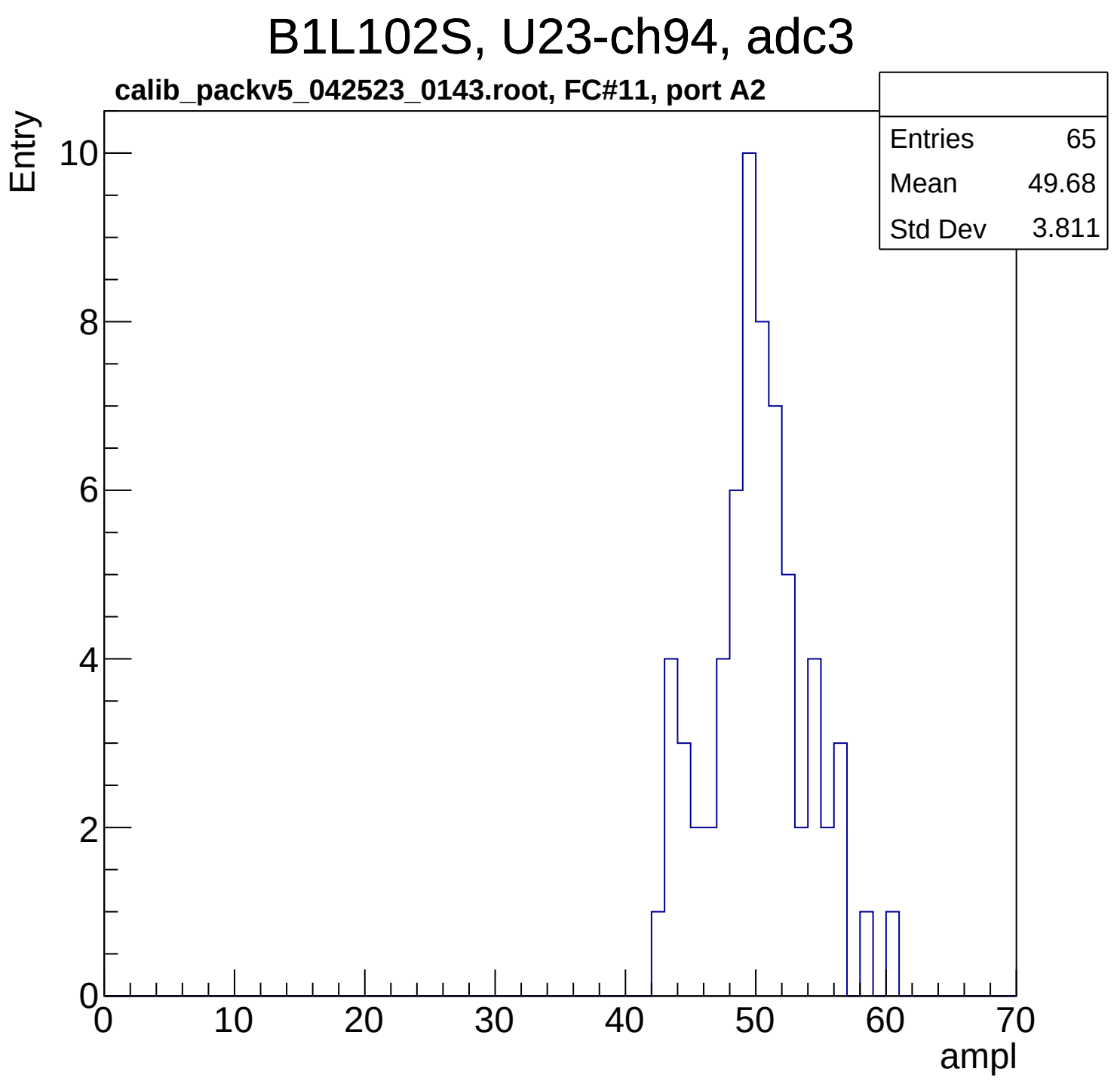

B1L102S, U23-ch93, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

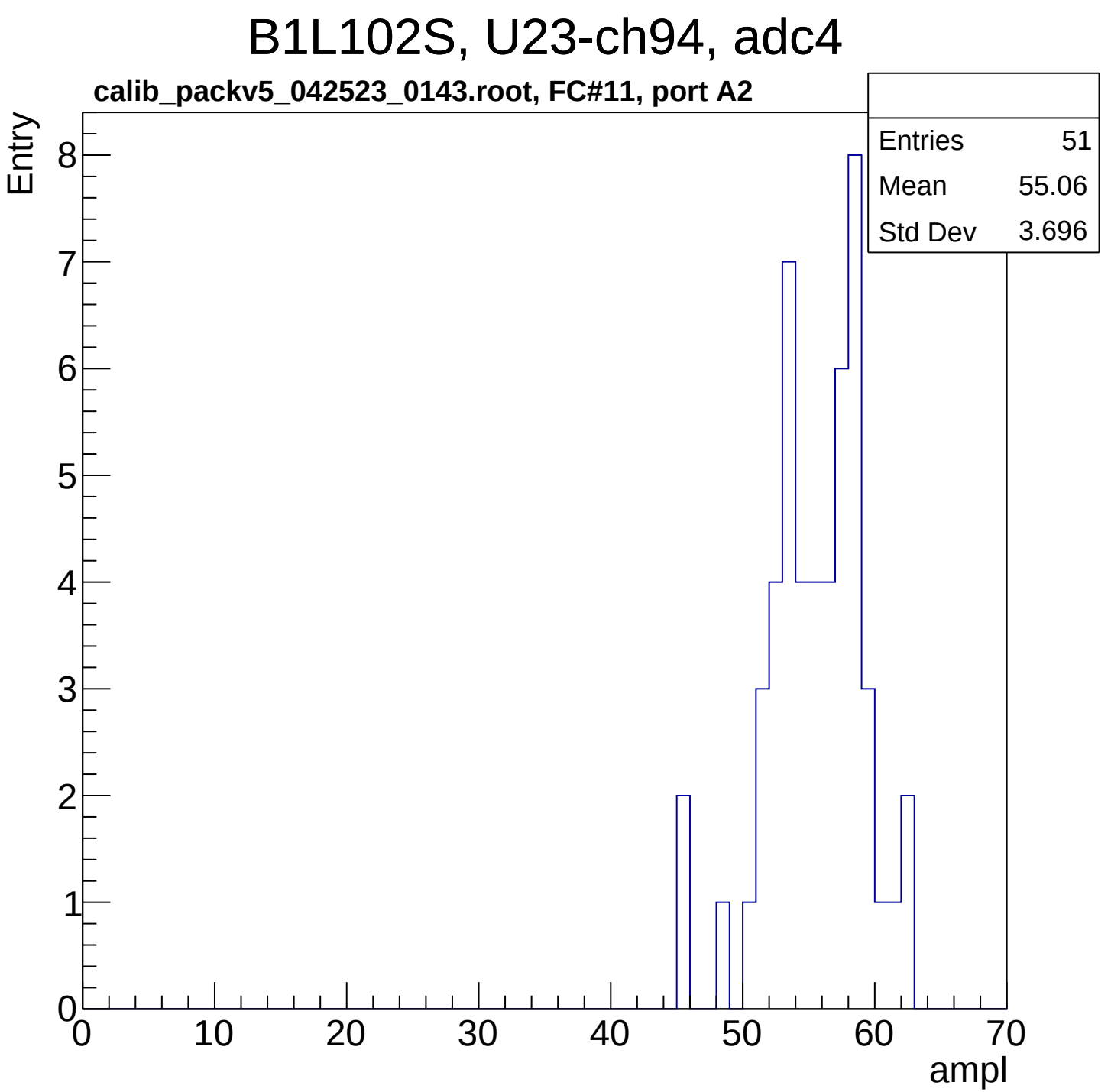

B1L102S, U23-ch93, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

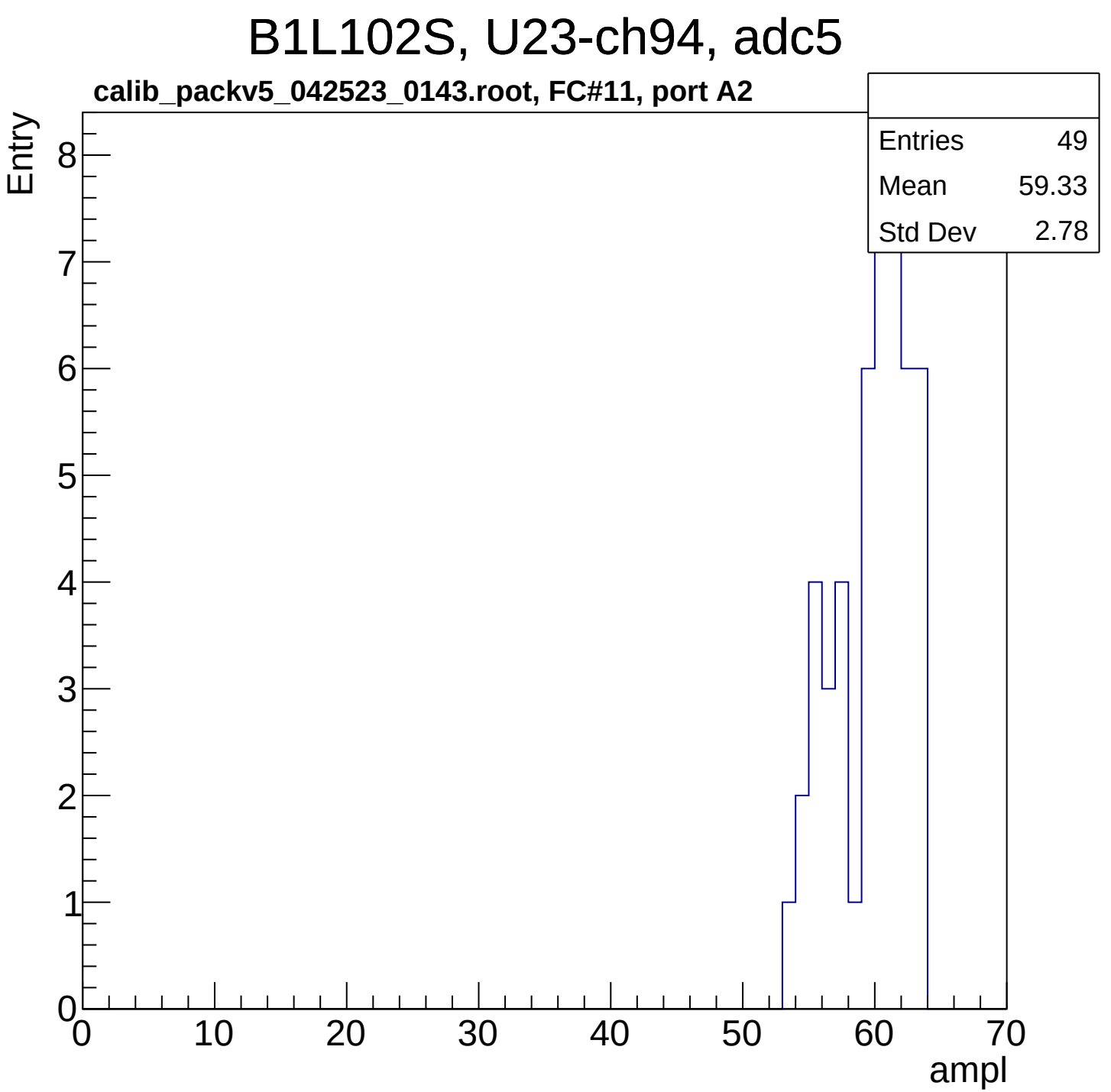

B1L102S, U23-ch94, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 29.45
Std Dev 3.578
Gaus mean : 29.7523
Gaus Width: 4.5956
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

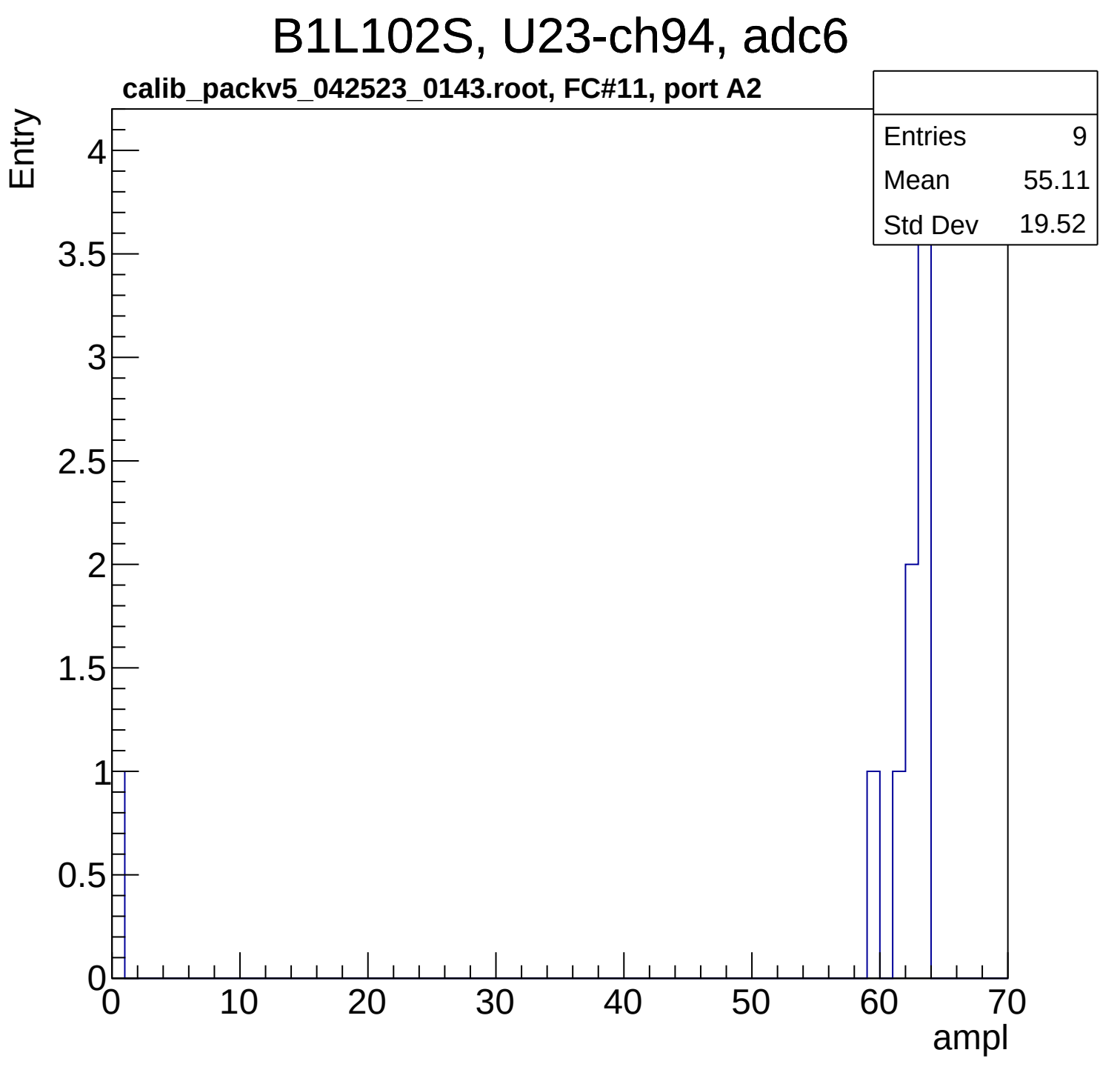

B1L102S, U23-ch94, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 36
Std Dev 3.46
Gaus mean : 36.2198
Gaus Width: 4.0307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

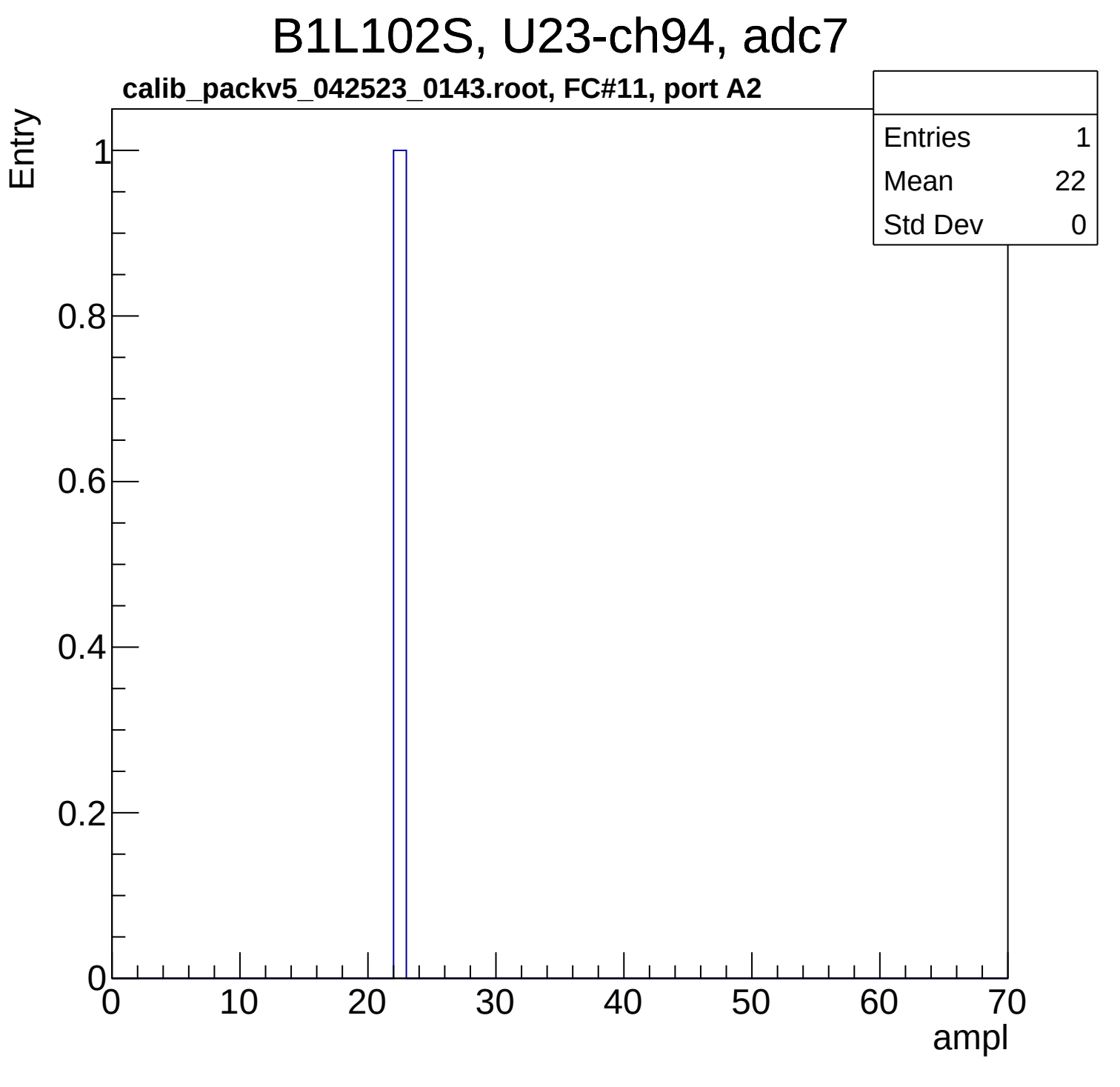

B1L102S, U23-ch94, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 42.81
Std Dev 3.907
Gaus mean : 43.8116
Gaus Width: 4.4412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

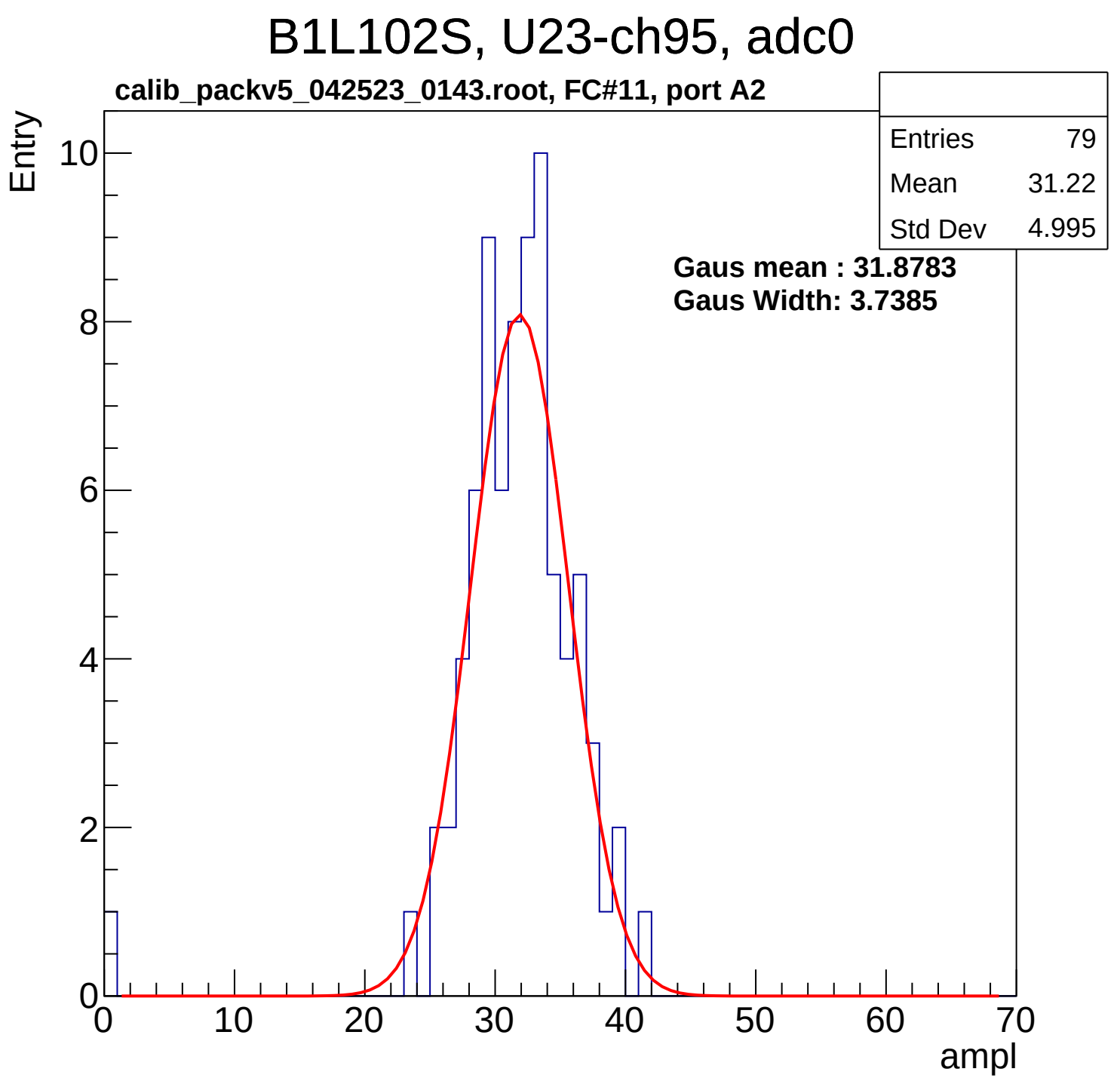

B1L102S, U23-ch94, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 49.68
Std Dev 3.811
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

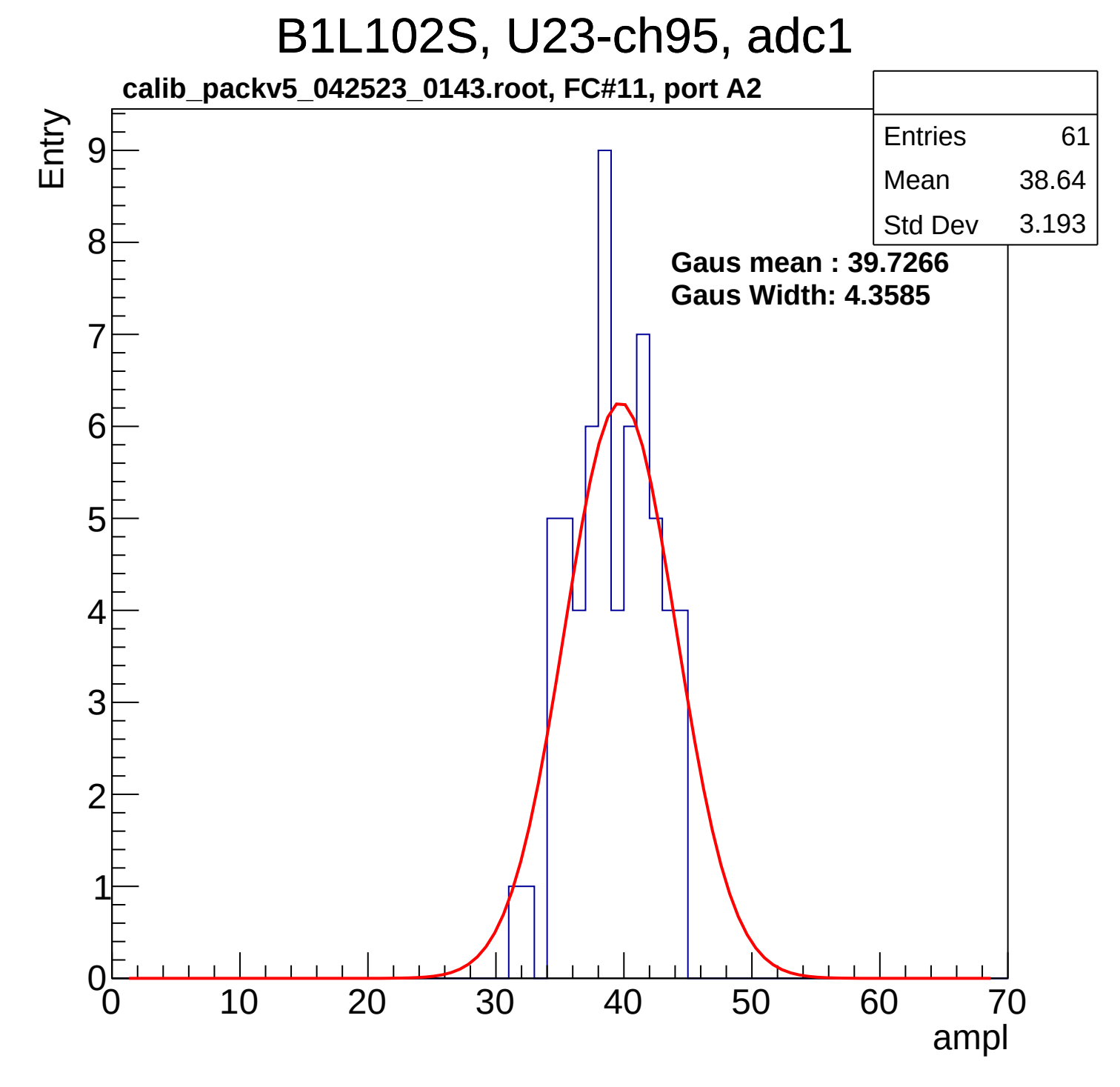

B1L102S, U23-ch94, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 55.06
Std Dev 3.696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

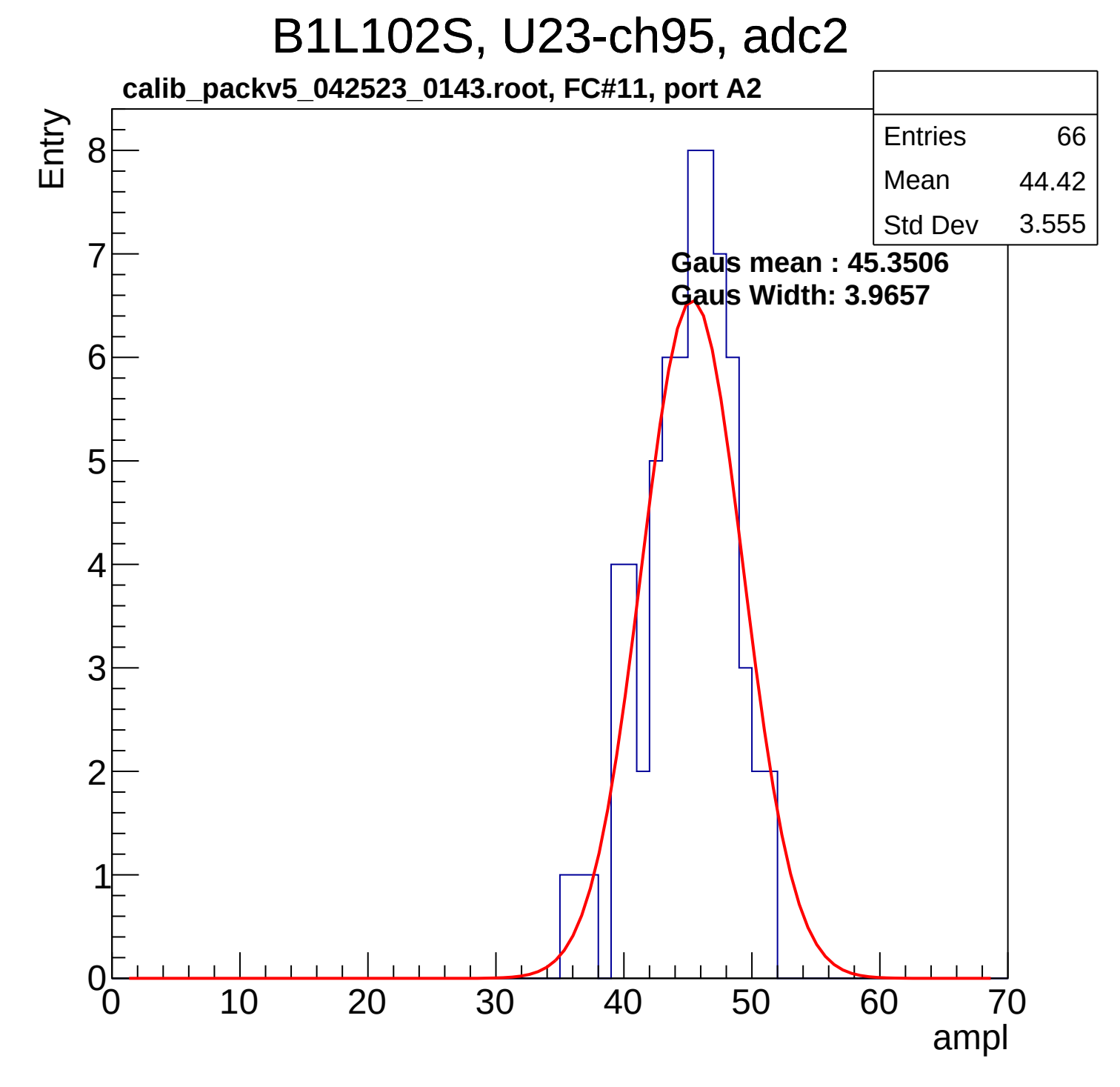

B1L102S, U23-ch94, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.33
Std Dev 2.78
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

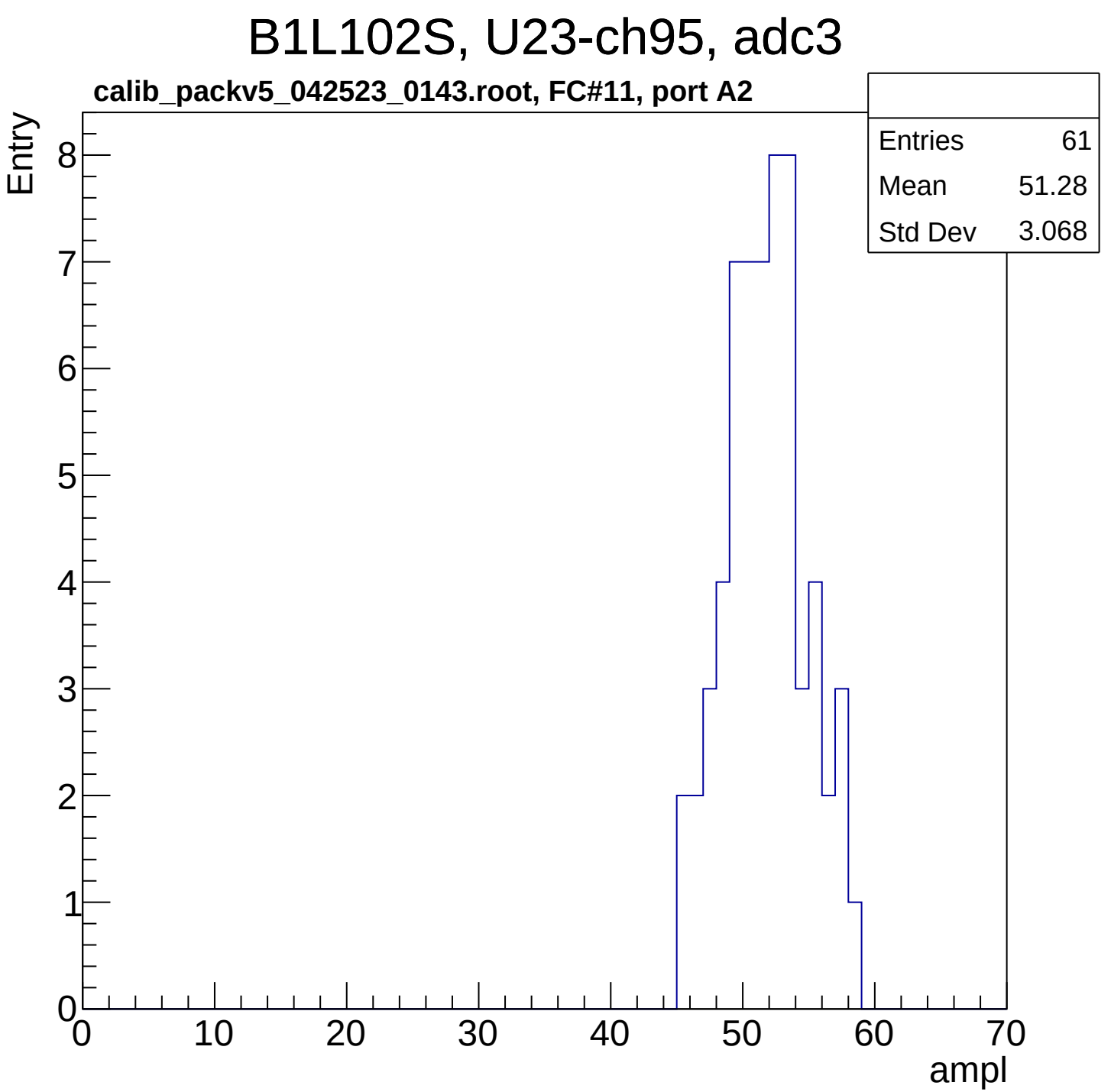

B1L102S, U23-ch94, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 55.11
Std Dev 19.52
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

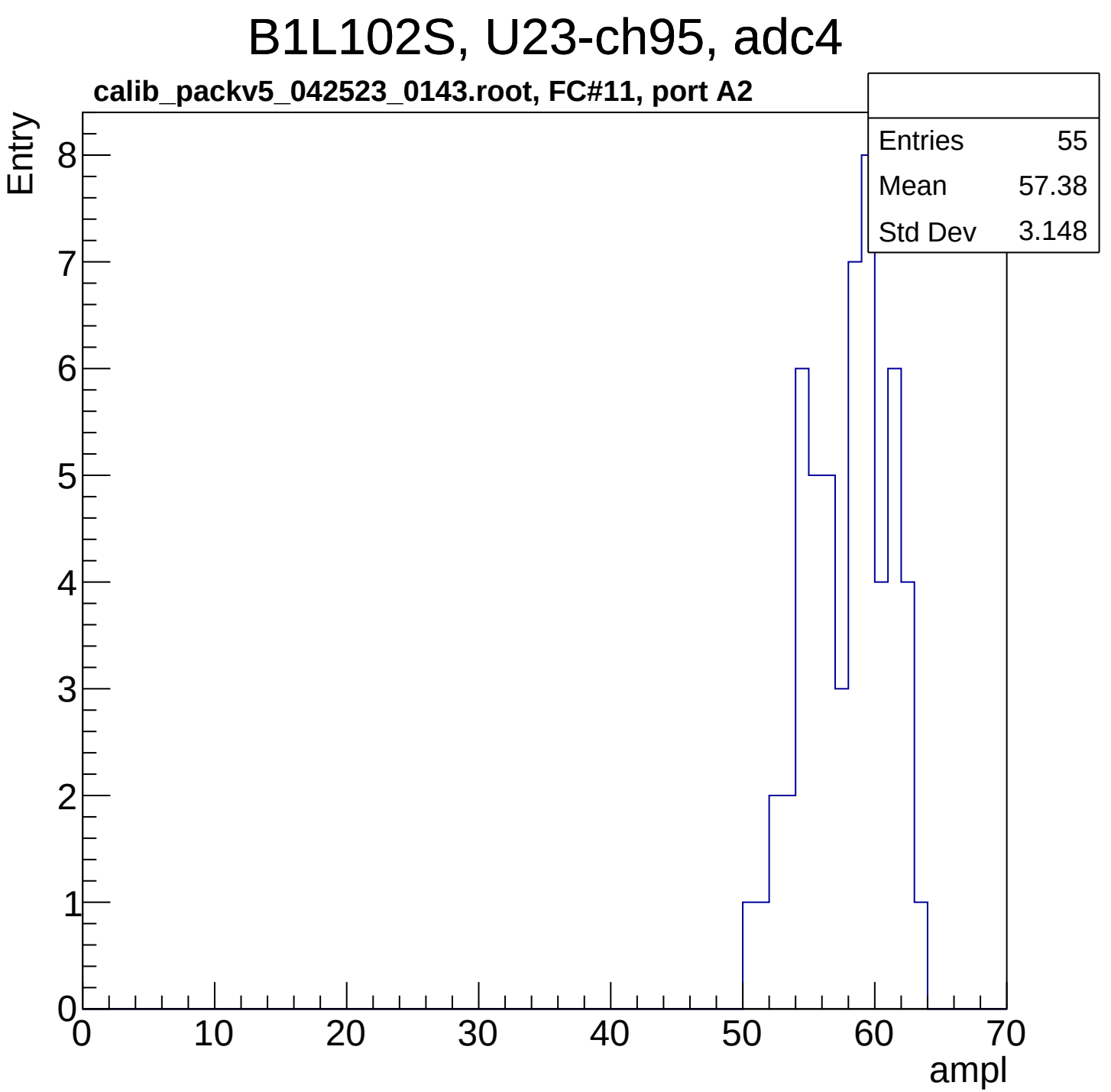

B1L102S, U23-ch94, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

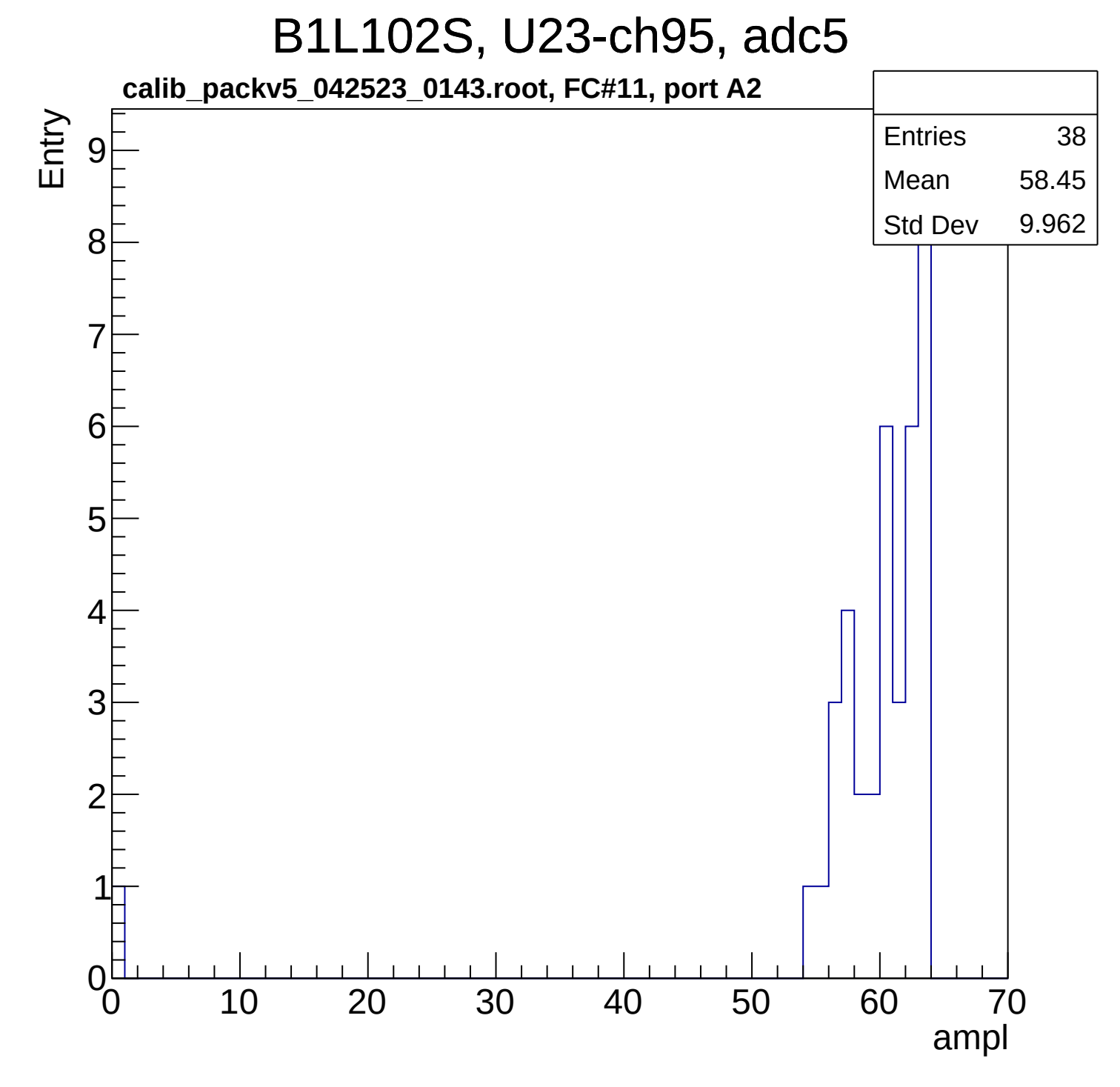

B1L102S, U23-ch95, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 31.22
Std Dev 4.995
Gaus mean : 31.8783
Gaus Width: 3.7385
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

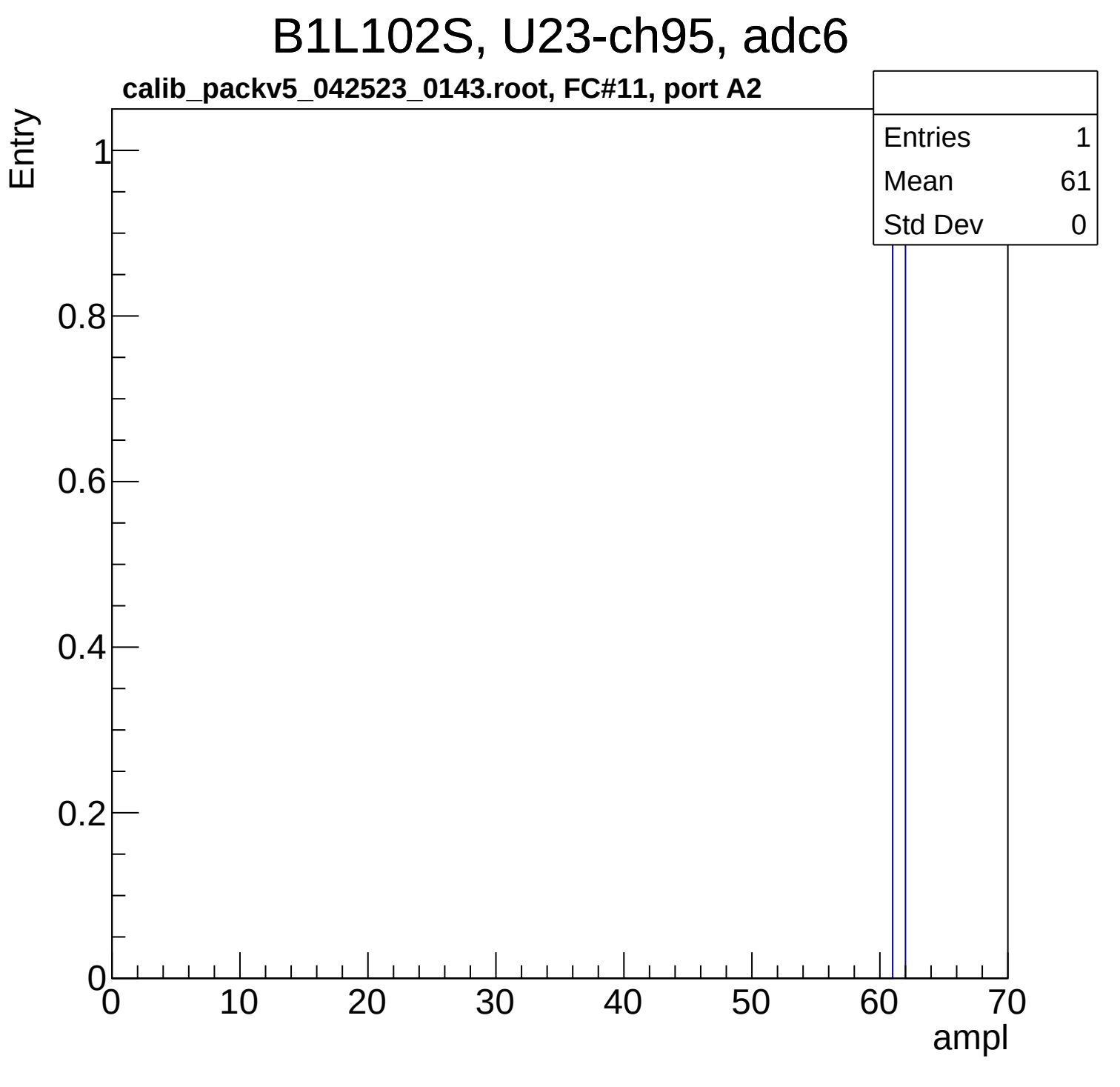

B1L102S, U23-ch95, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 38.64
Std Dev 3.193
Gaus mean : 39.7266
Gaus Width: 4.3585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

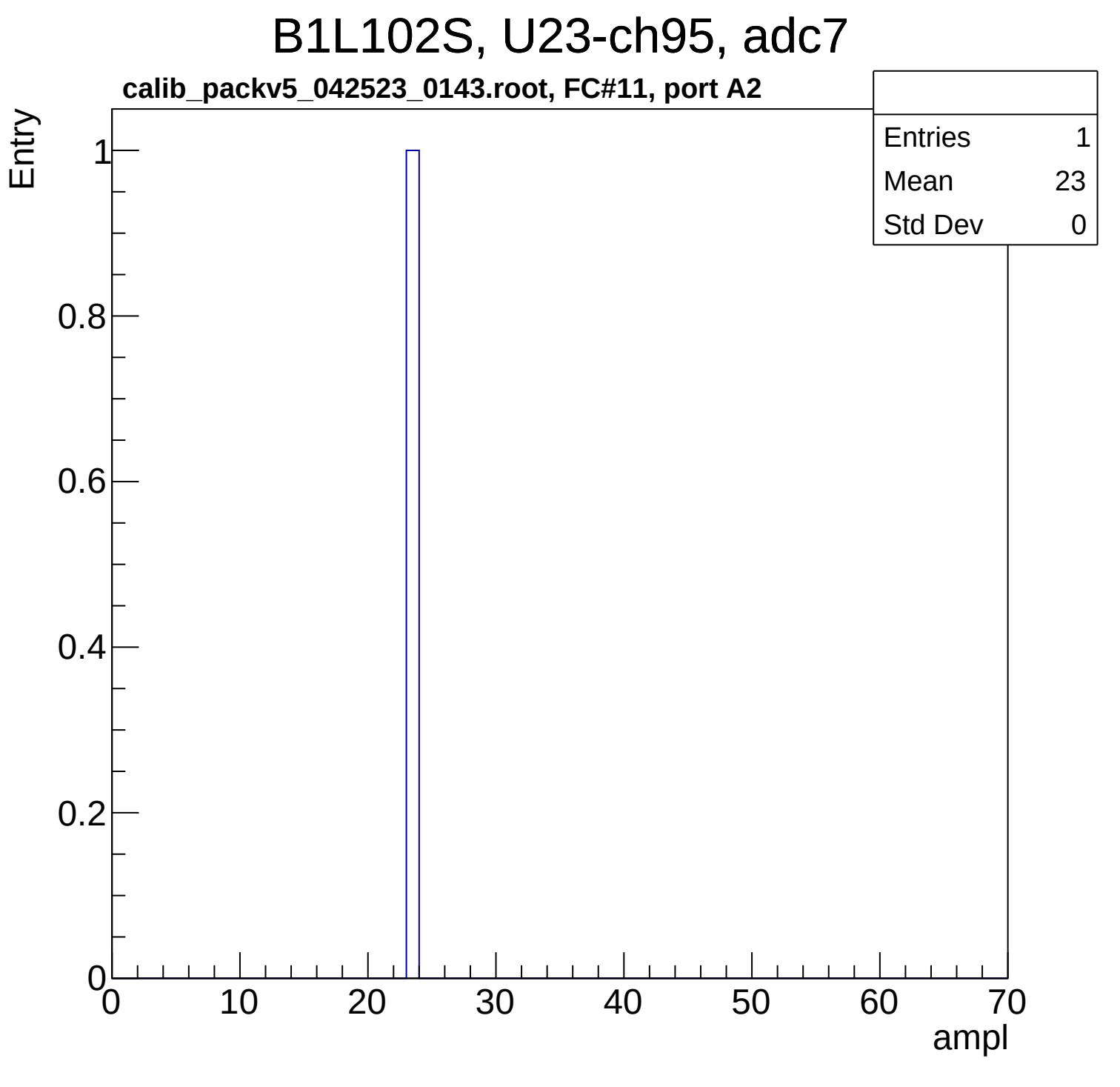

B1L102S, U23-ch95, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 44.42
Std Dev 3.555
Gaus mean : 45.3506
Gaus Width: 3.9657
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

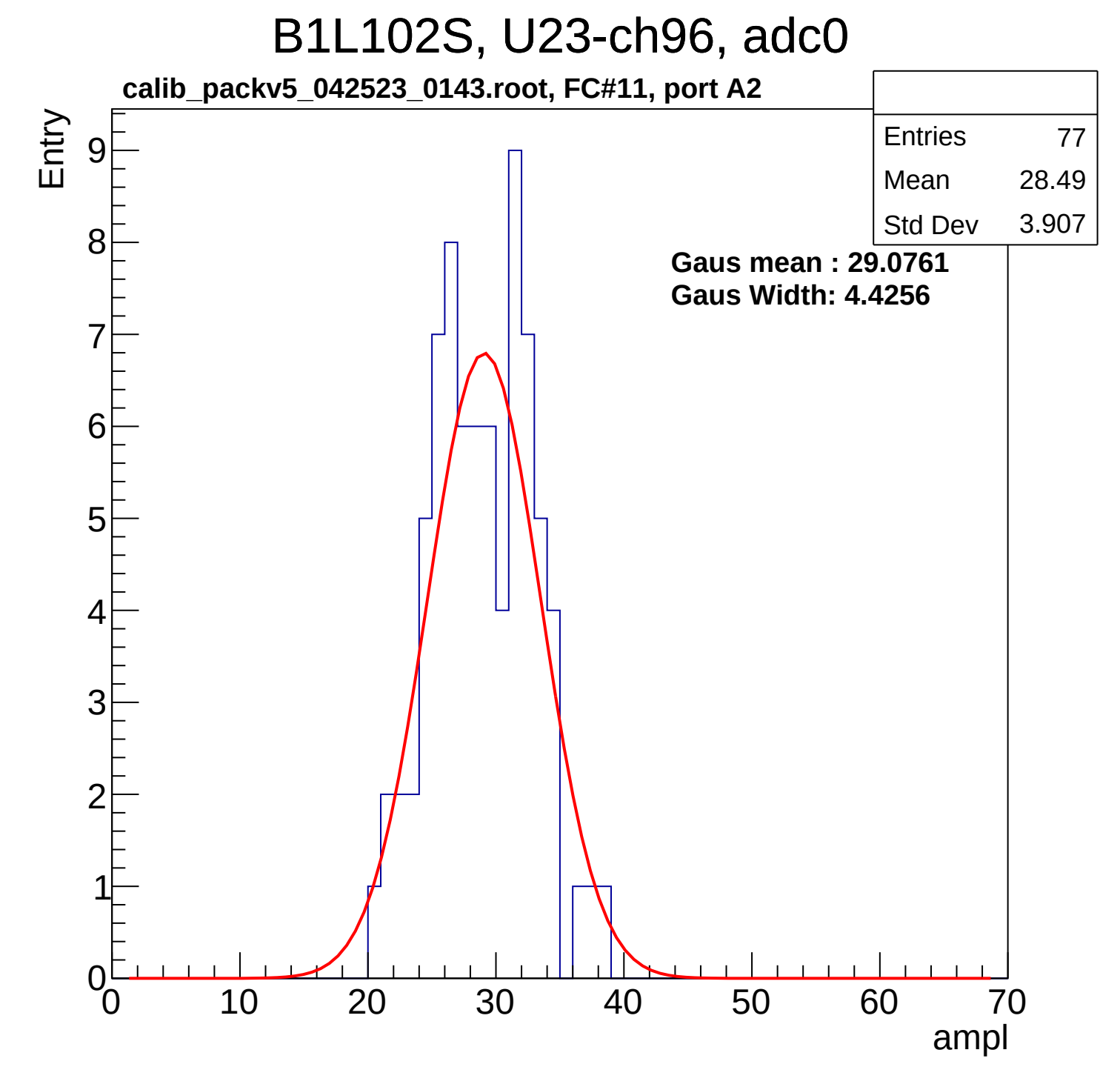

B1L102S, U23-ch95, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 51.28
Std Dev 3.068
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

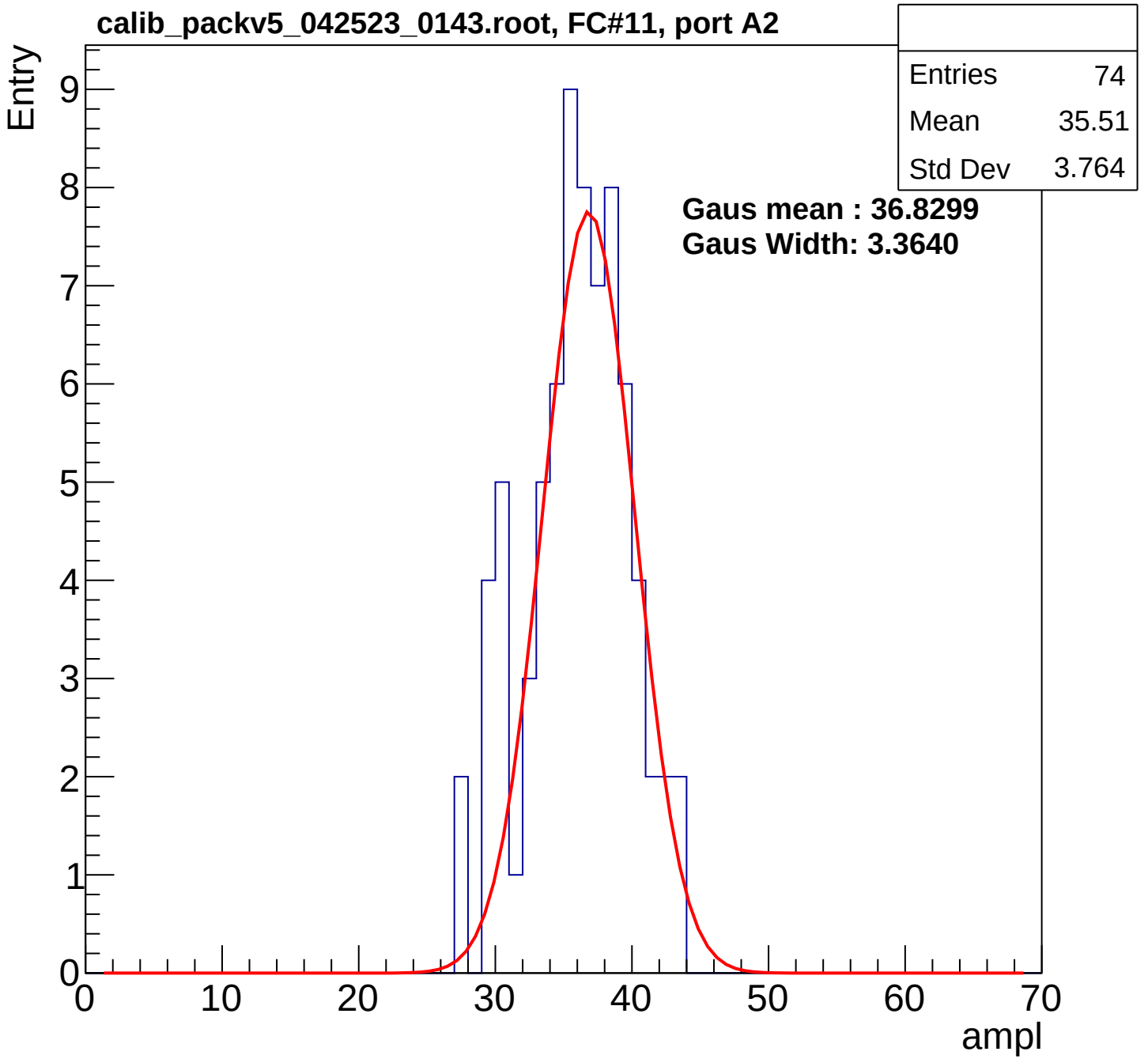

B1L102S, U23-ch95, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 57.38
Std Dev 3.148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

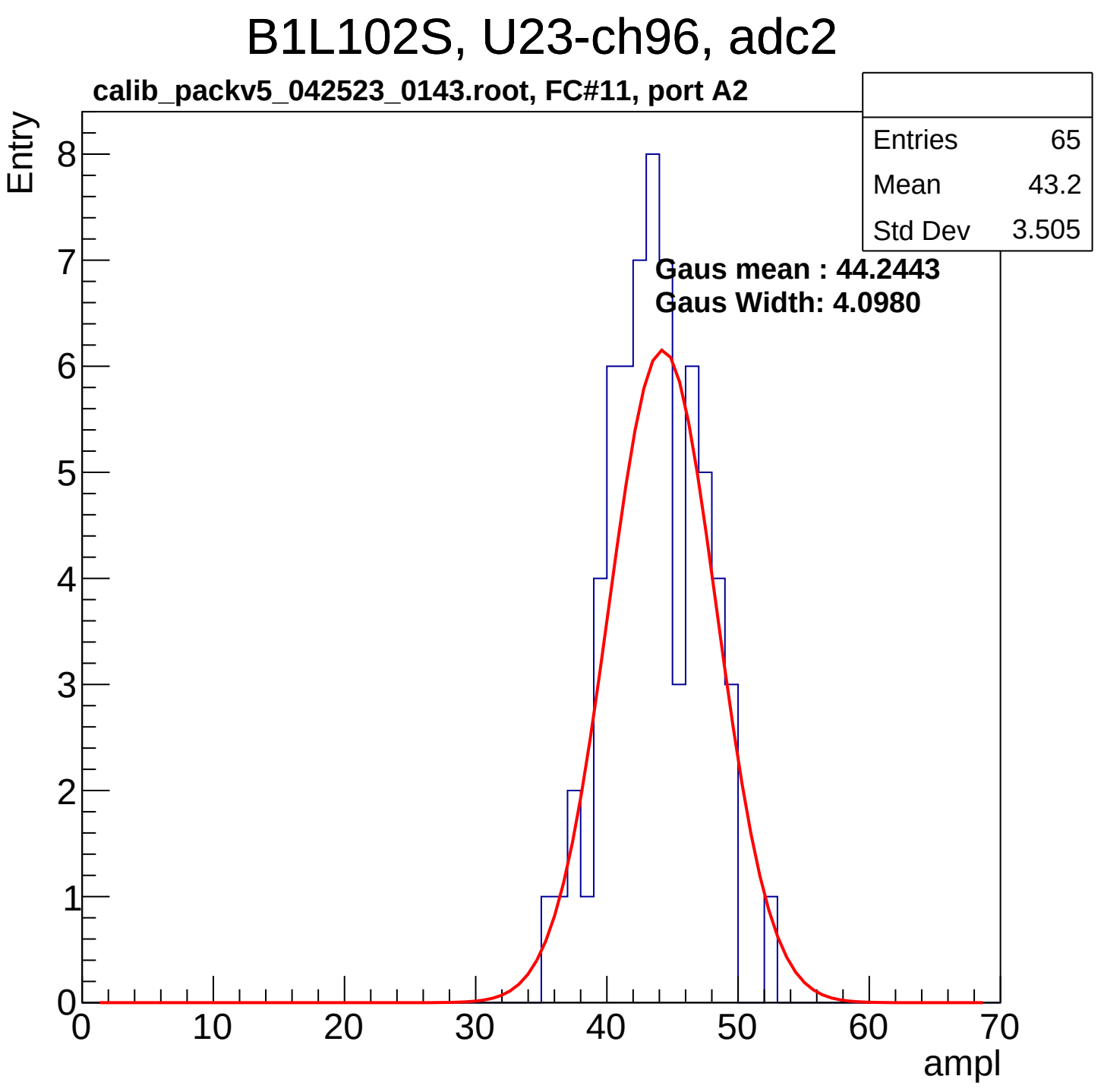

B1L102S, U23-ch95, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.45
Std Dev 9.962
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

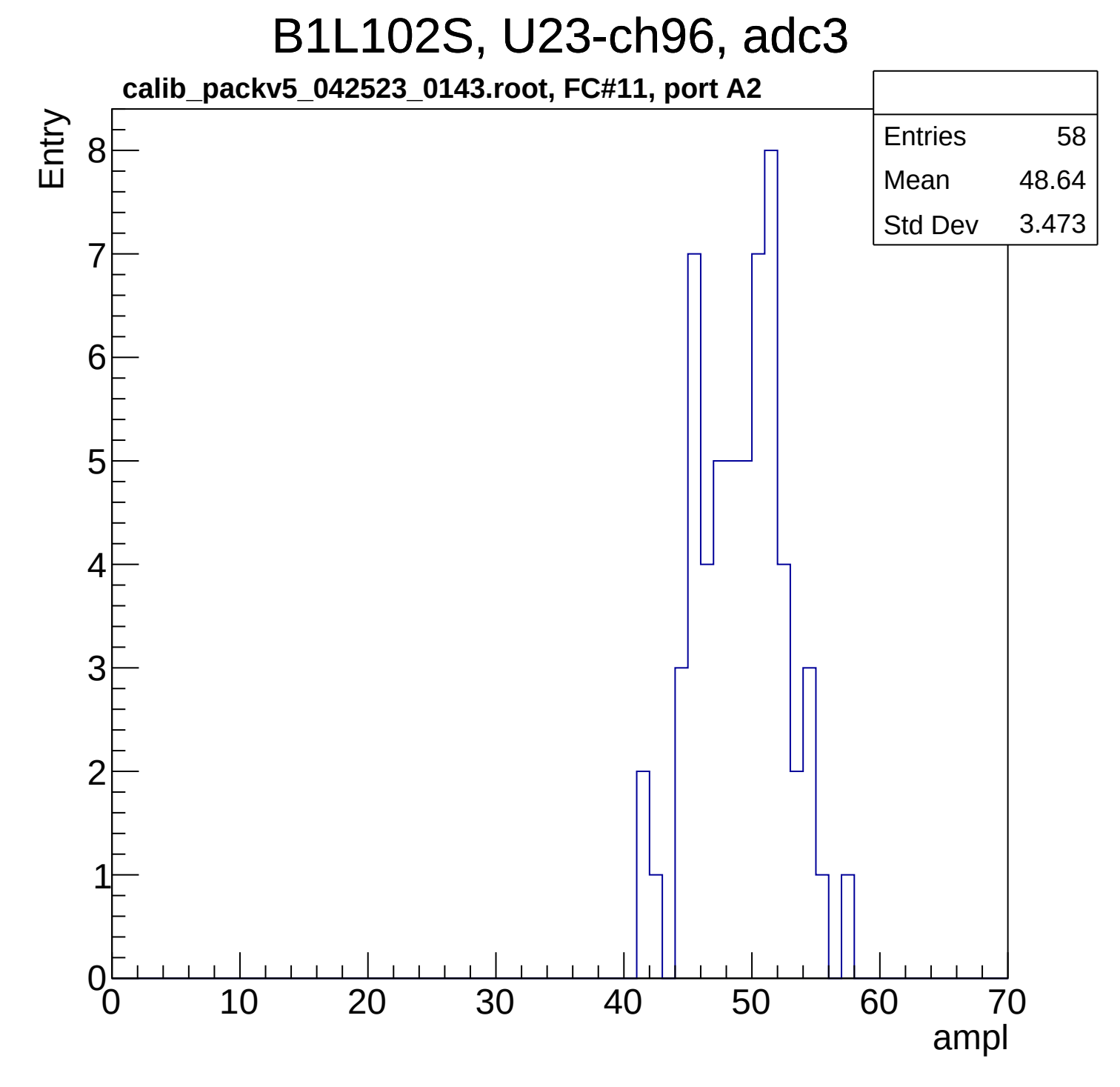

B1L102S, U23-ch95, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

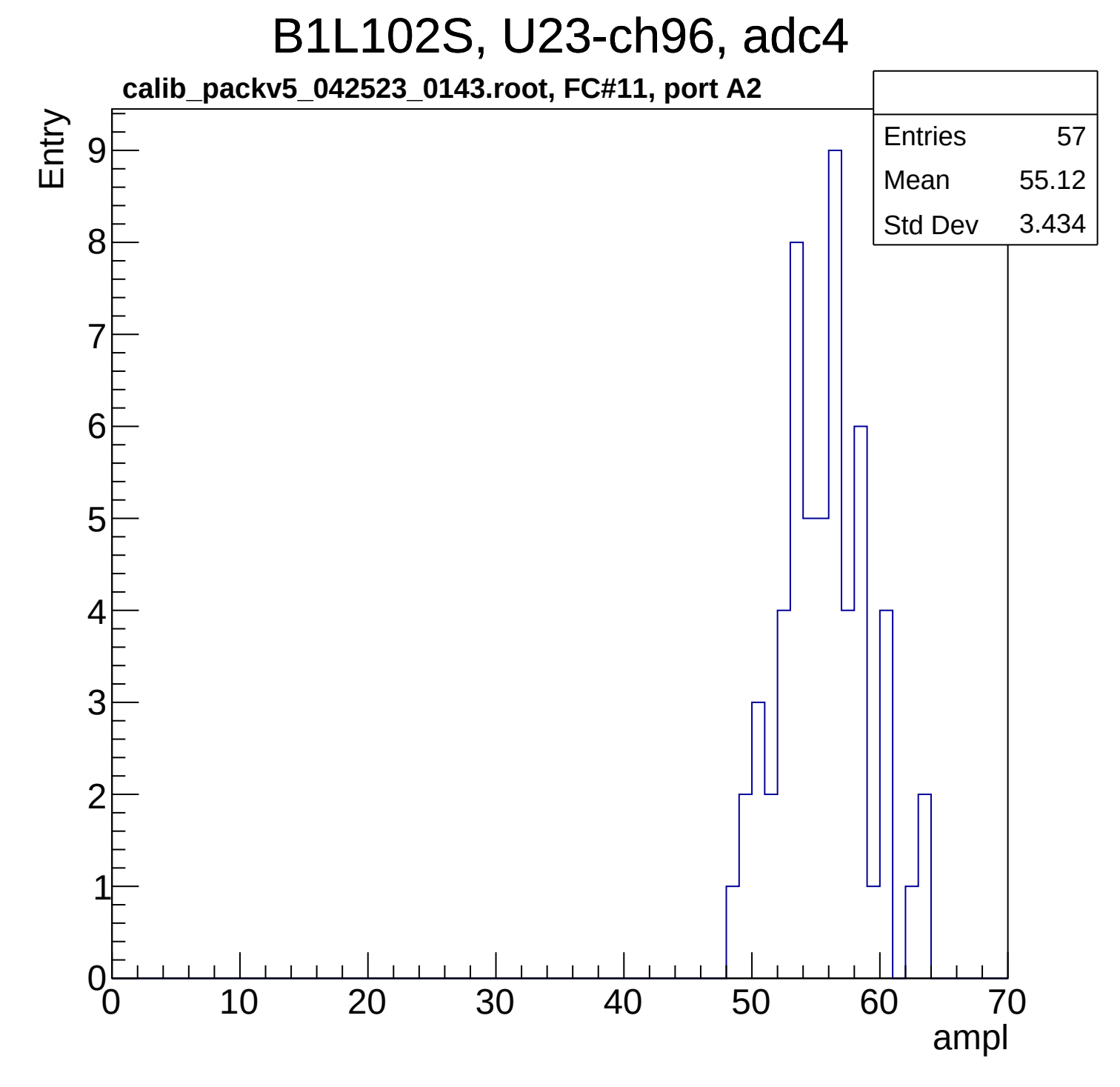

B1L102S, U23-ch95, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

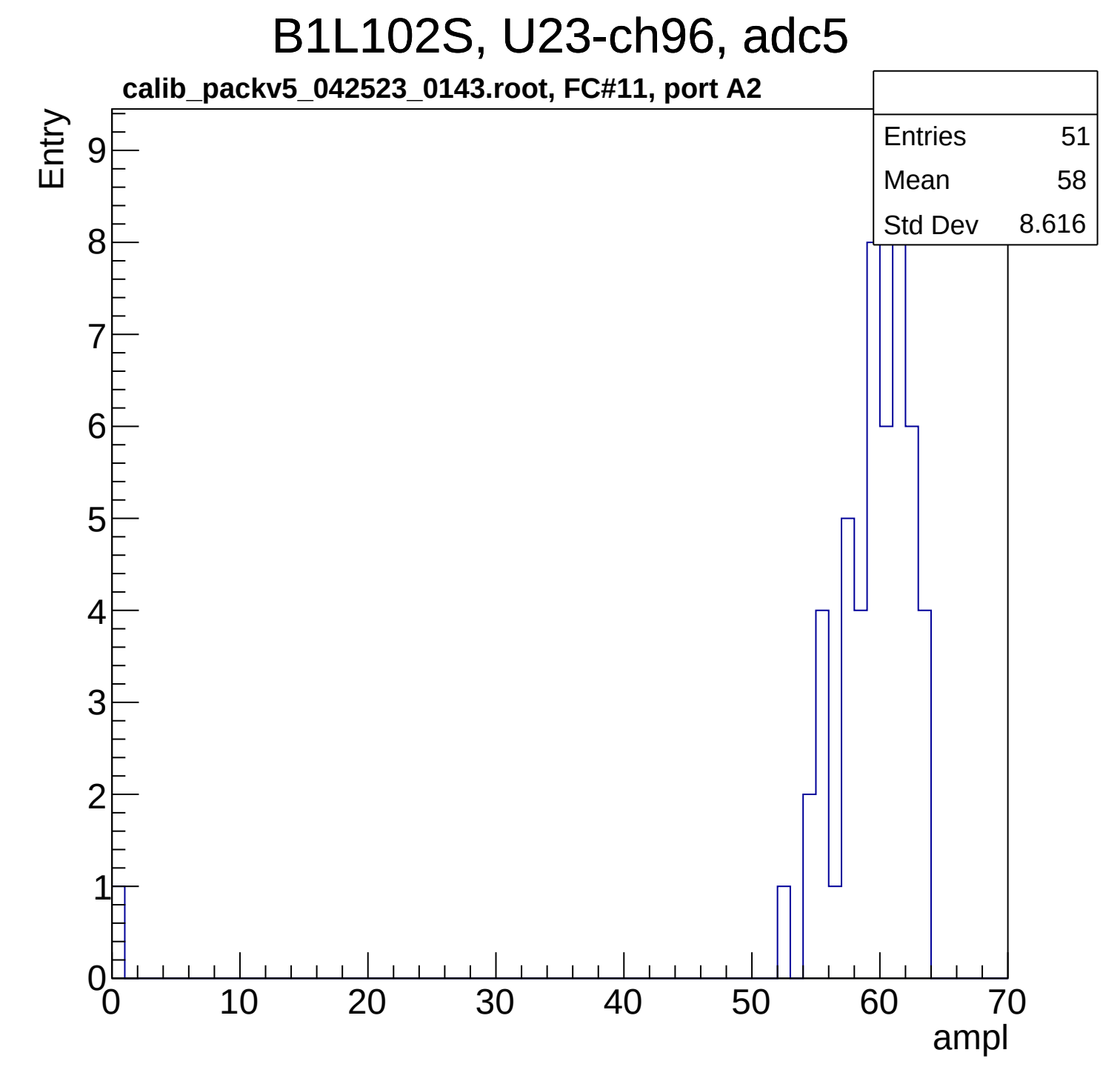

B1L102S, U23-ch96, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 28.49
Std Dev 3.907
Gaus mean : 29.0761
Gaus Width: 4.4256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

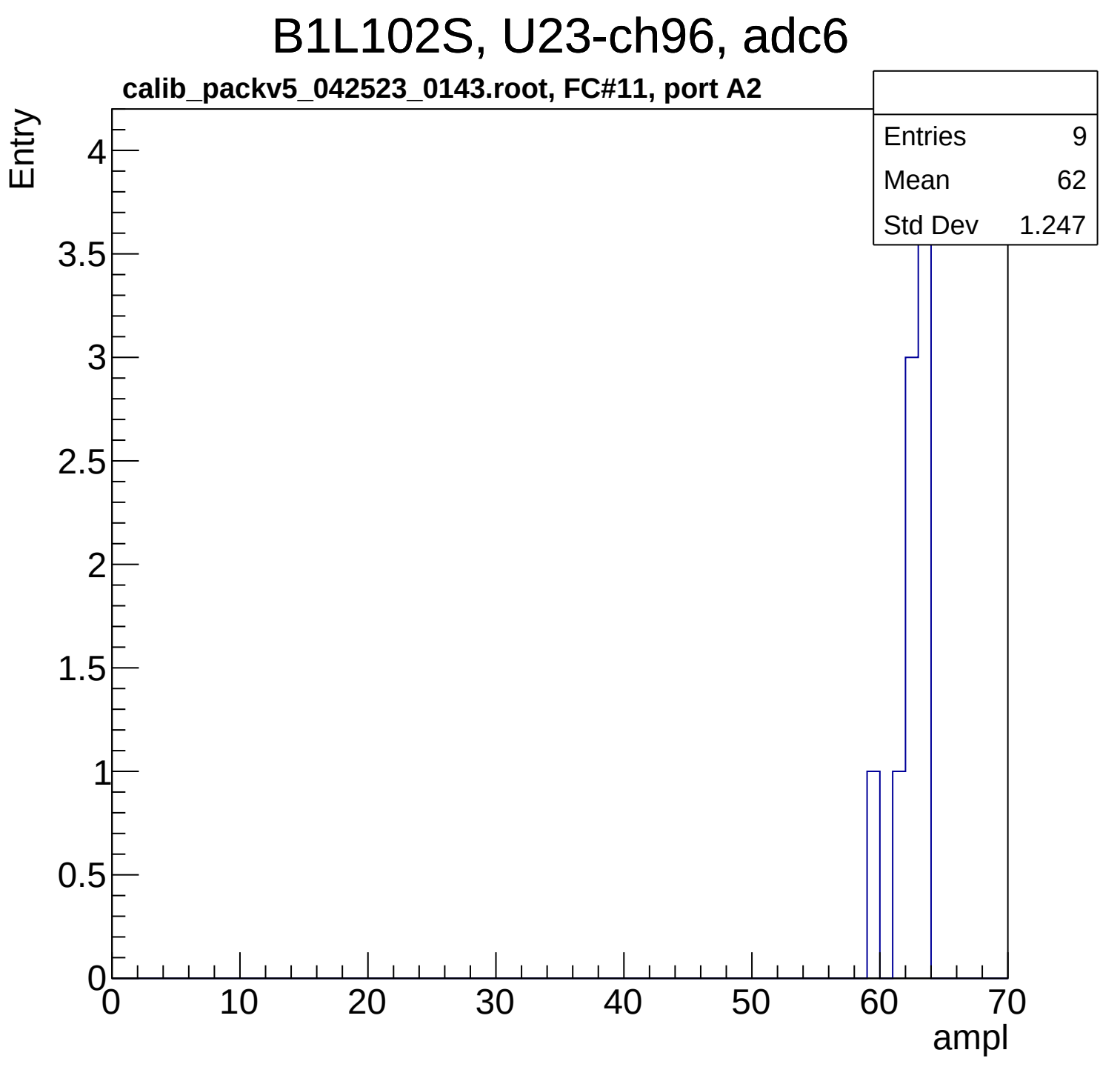

B1L102S, U23-ch96, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 35.51
Std Dev 3.764
Gaus mean : 36.8299
Gaus Width: 3.3640
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

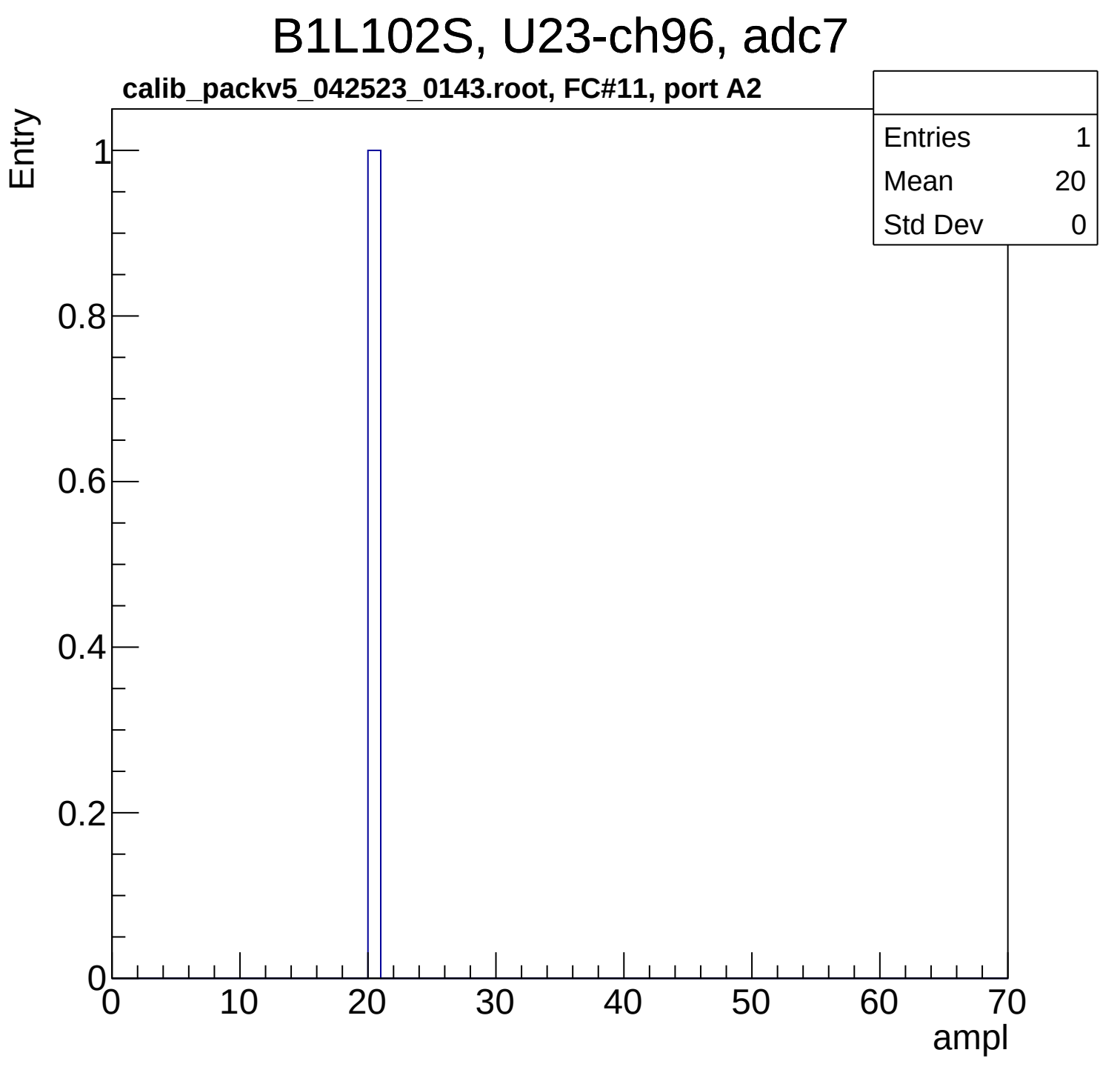

B1L102S, U23-ch96, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 43.2
Std Dev 3.505
Gaus mean : 44.2443
Gaus Width: 4.0980
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

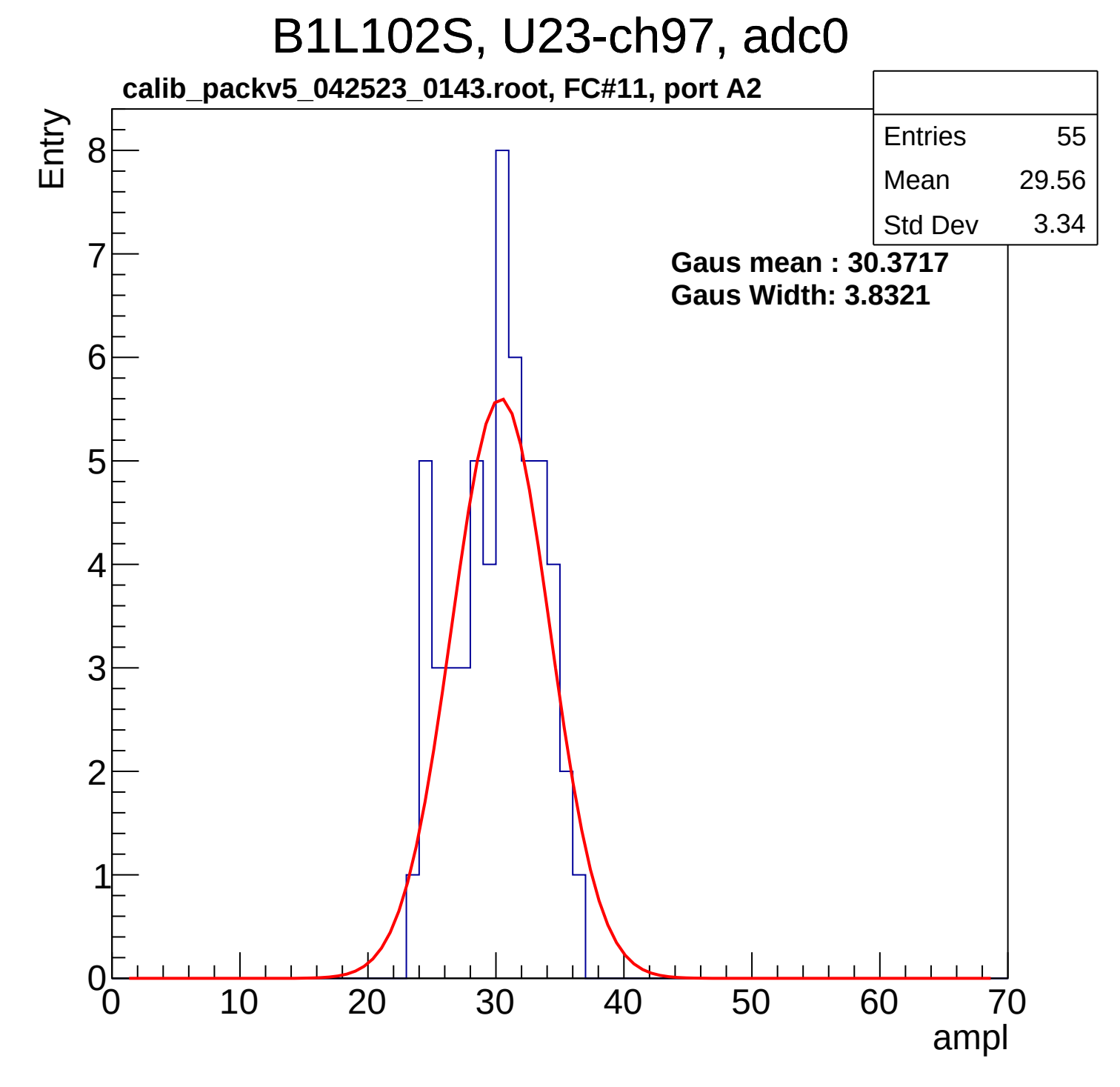

B1L102S, U23-ch96, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 48.64
Std Dev 3.473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

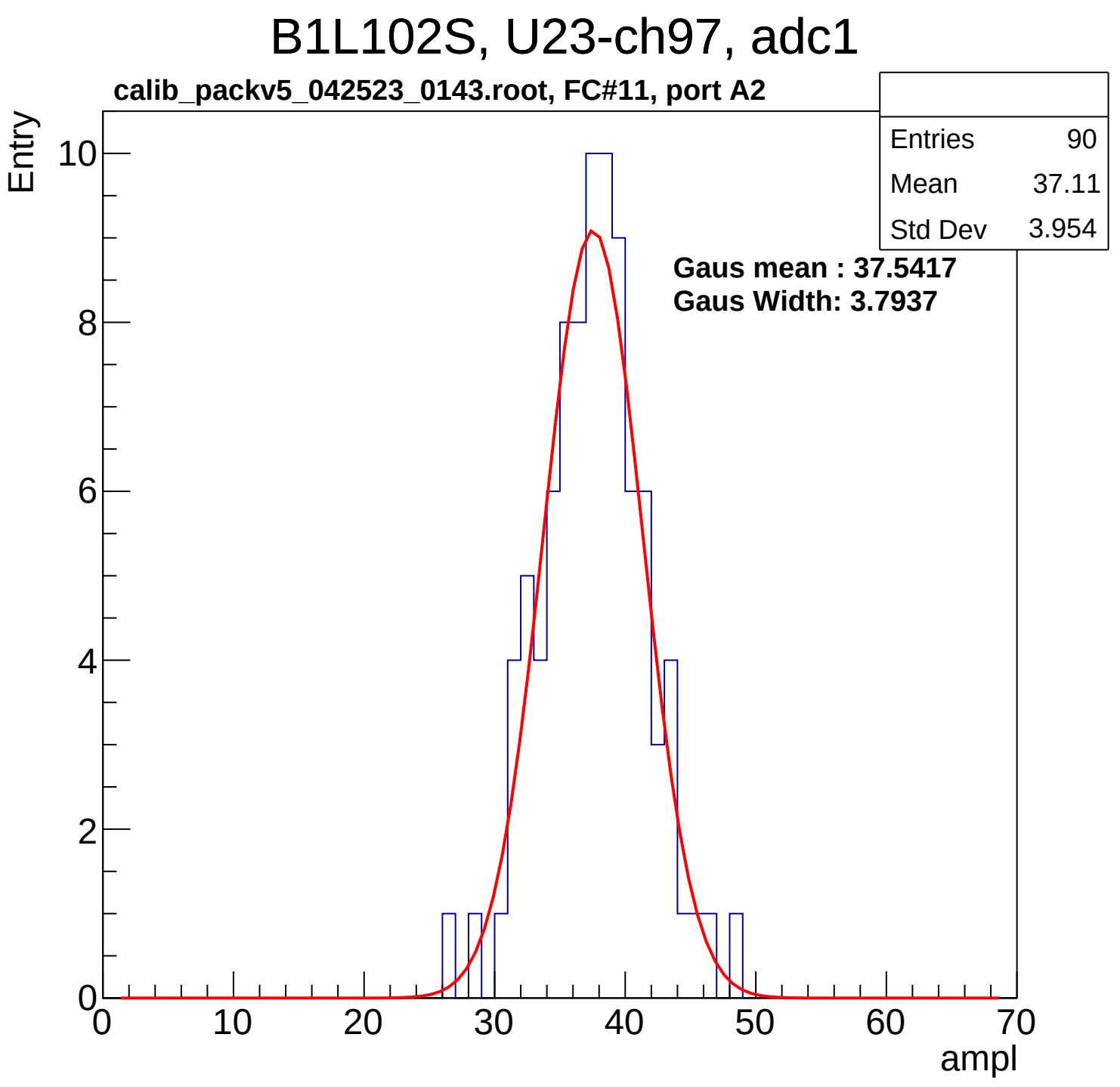

B1L102S, U23-ch96, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.12
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

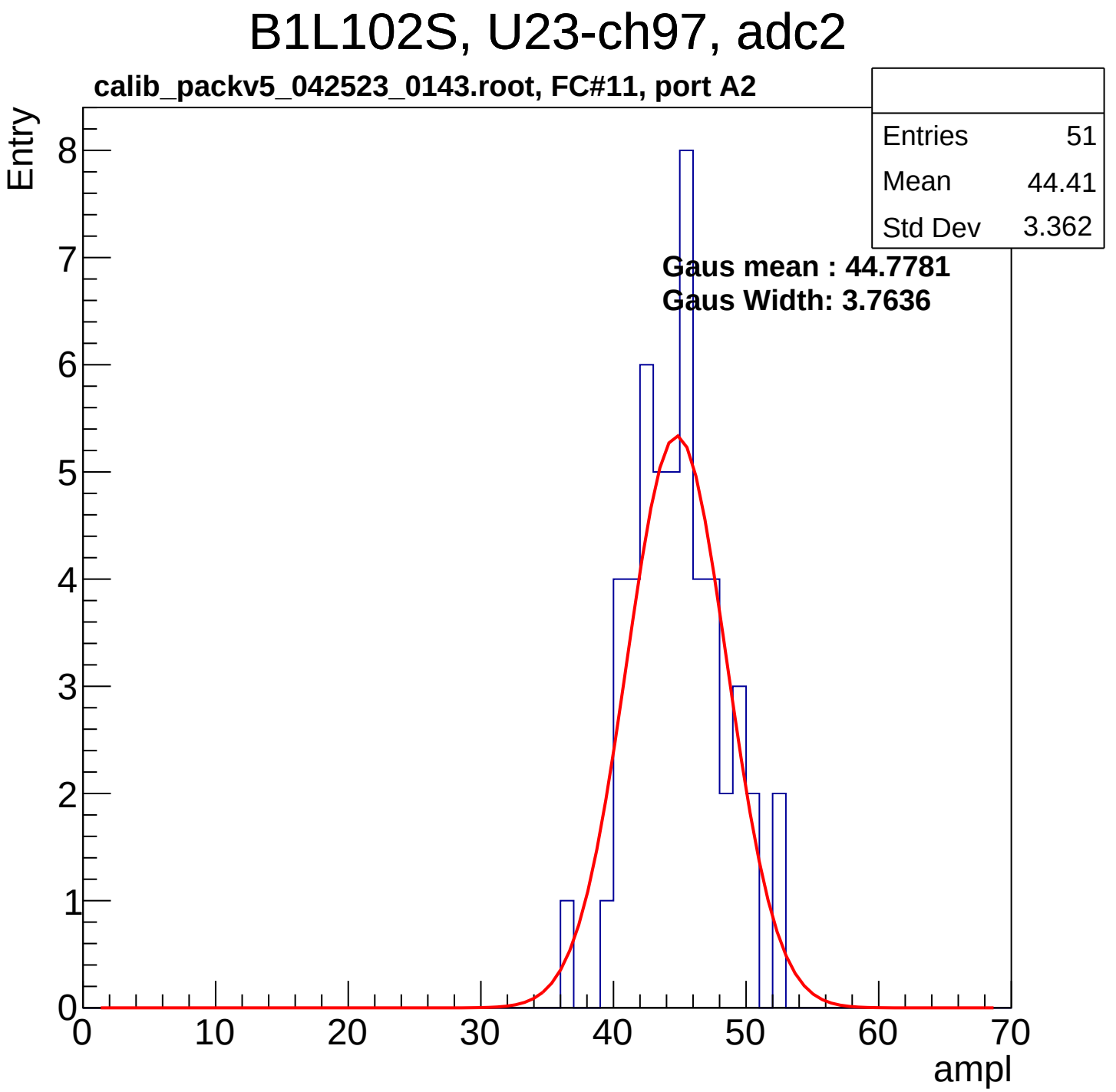

B1L102S, U23-ch96, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58
Std Dev 8.616
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

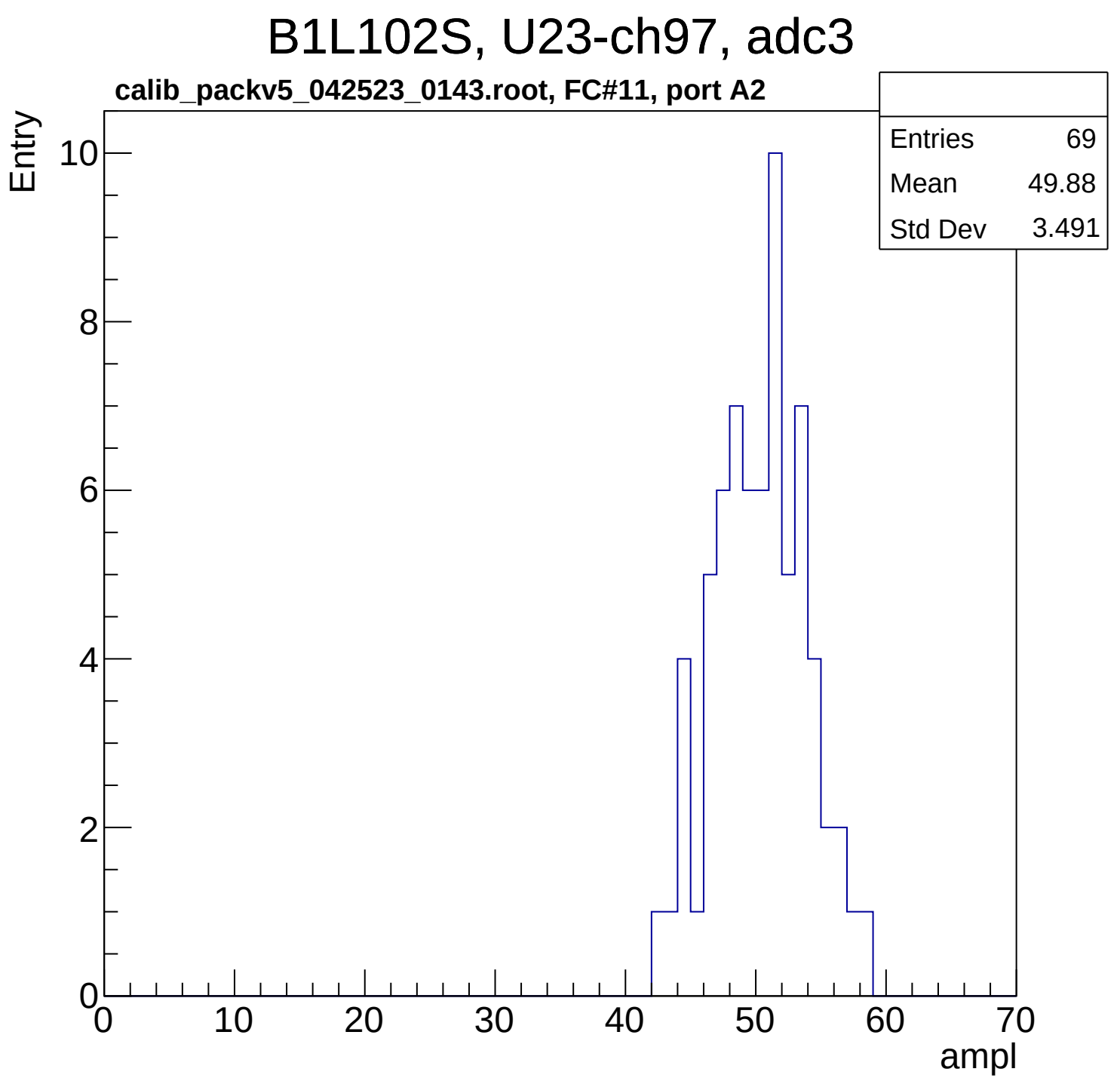

B1L102S, U23-ch96, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62
Std Dev 1.247
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

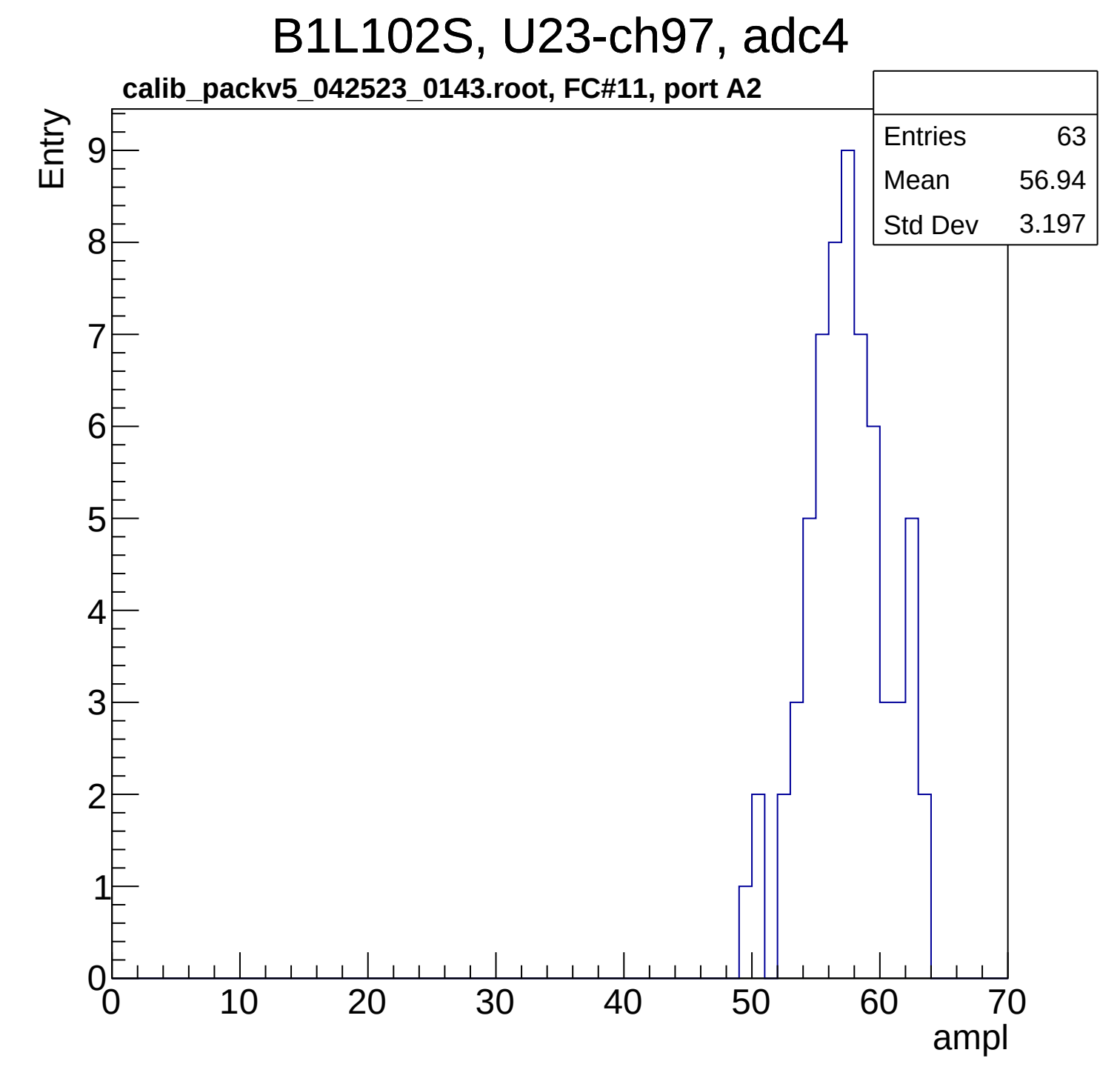

B1L102S, U23-ch96, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

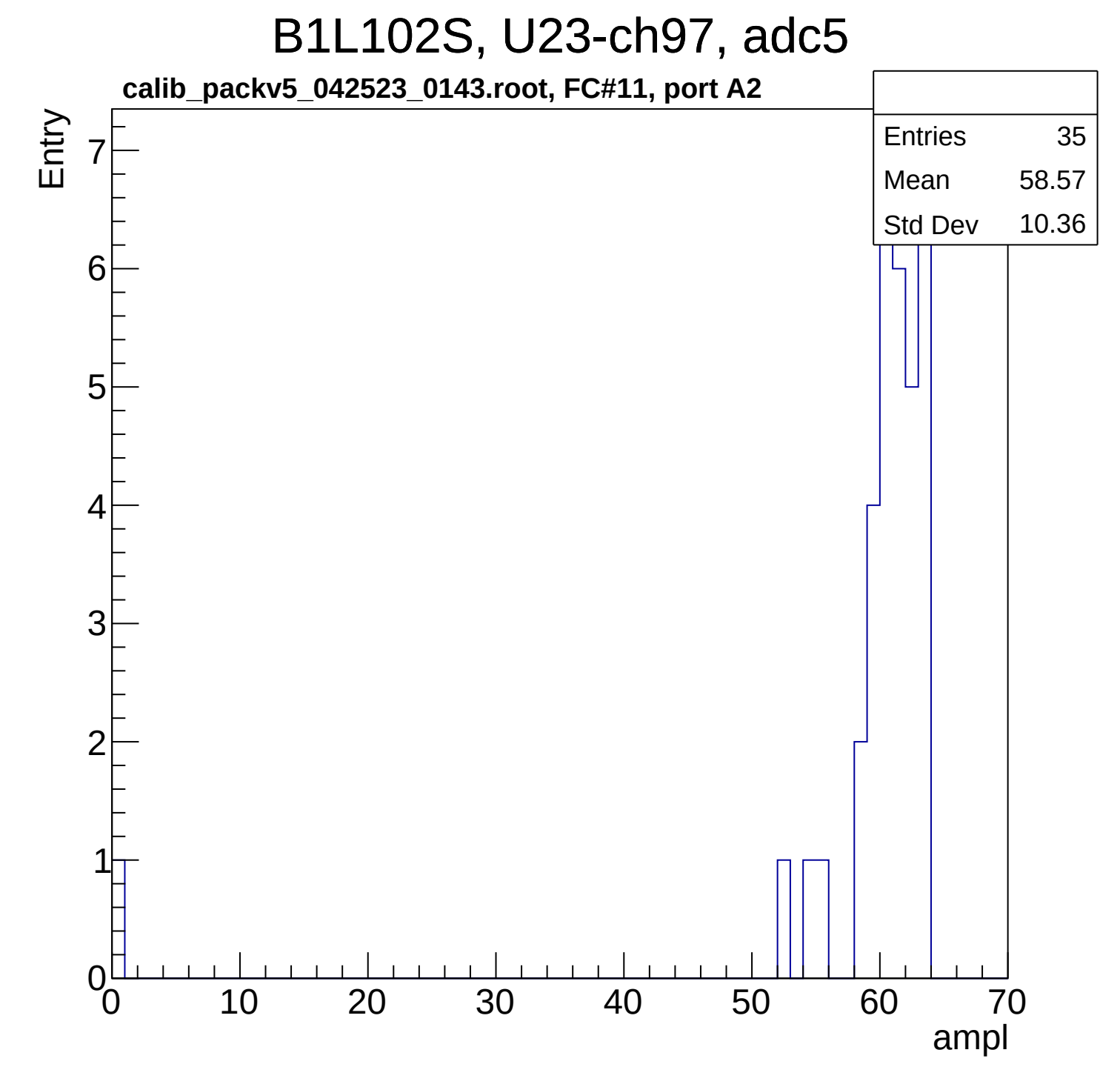

B1L102S, U23-ch97, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 29.56
Std Dev 3.34
Gaus mean : 30.3717
Gaus Width: 3.8321
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

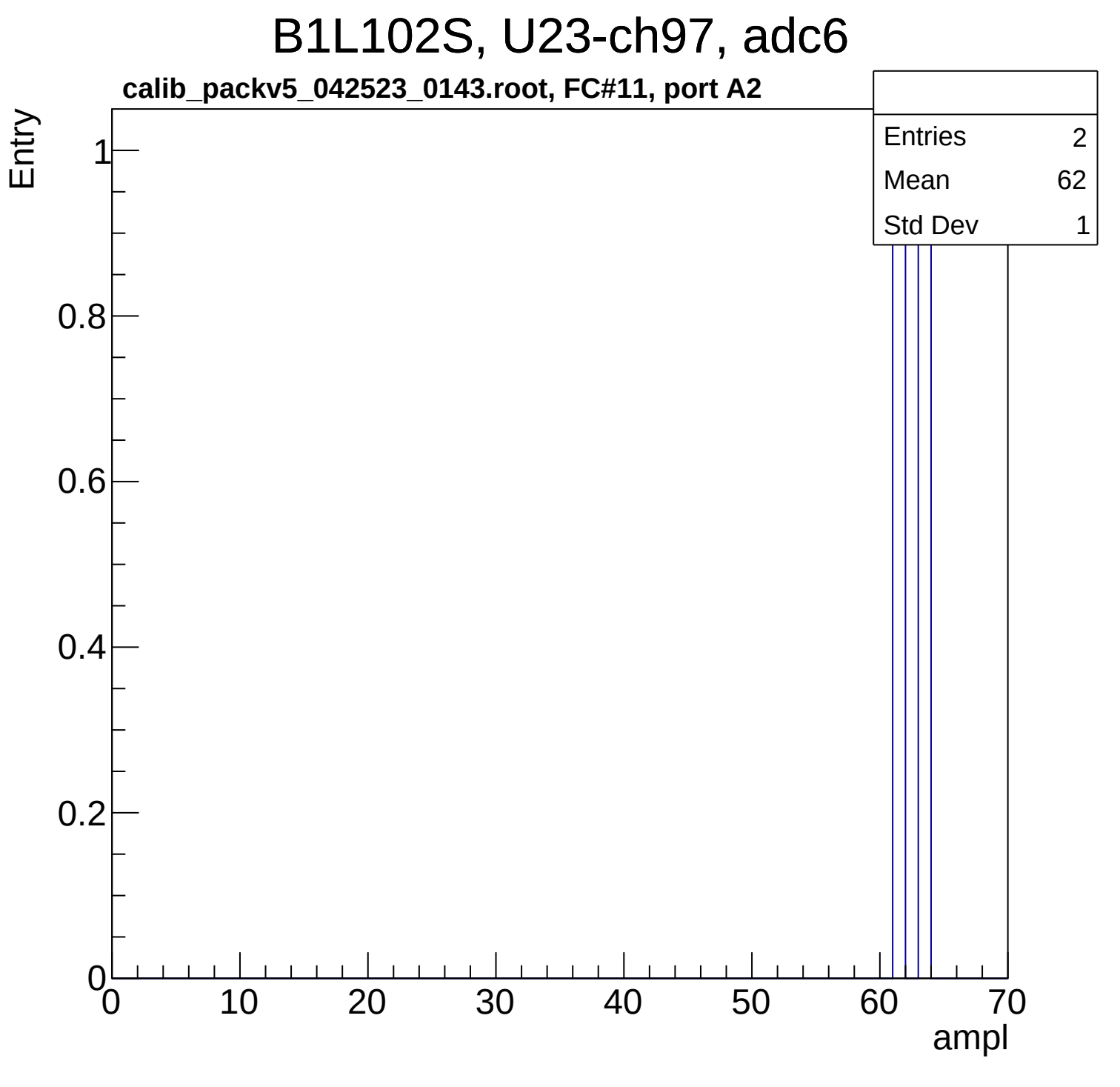

B1L102S, U23-ch97, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 90
Mean 37.11
Std Dev 3.954
Gaus mean : 37.5417
Gaus Width: 3.7937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

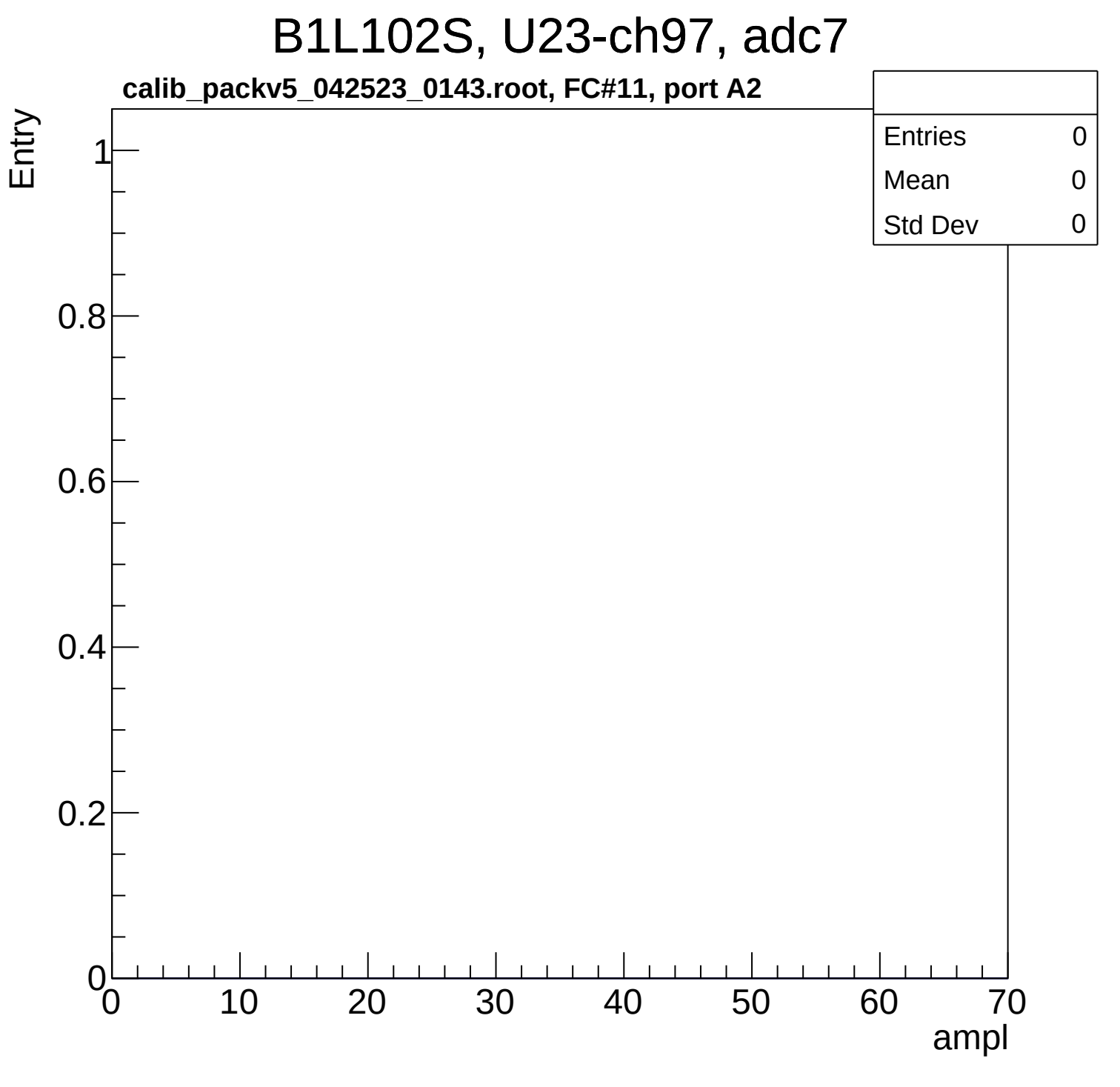

B1L102S, U23-ch97, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 44.41
Std Dev 3.362
Gaus mean : 44.7781
Gaus Width: 3.7636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

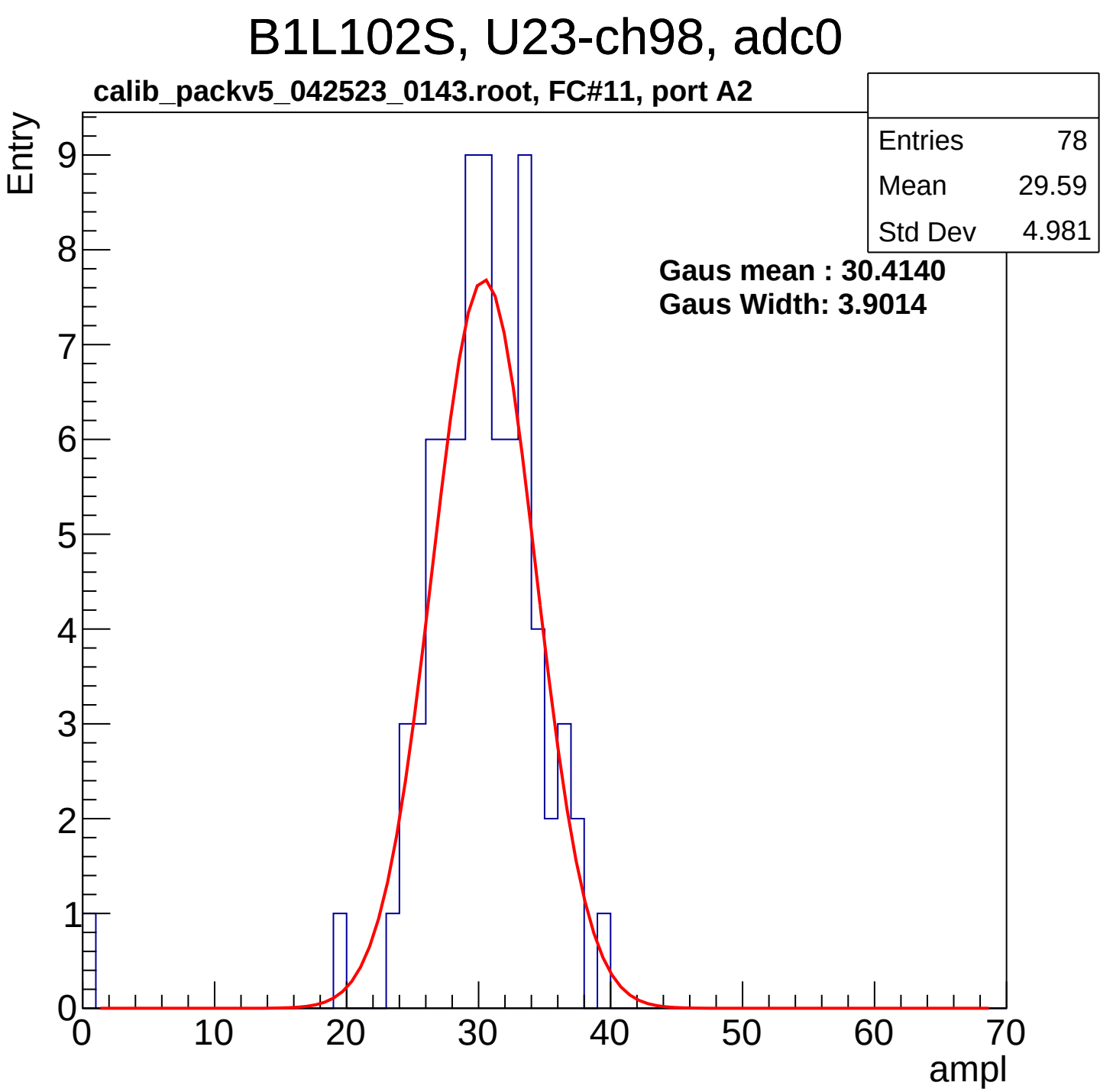

B1L102S, U23-ch97, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 49.88
Std Dev 3.491
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

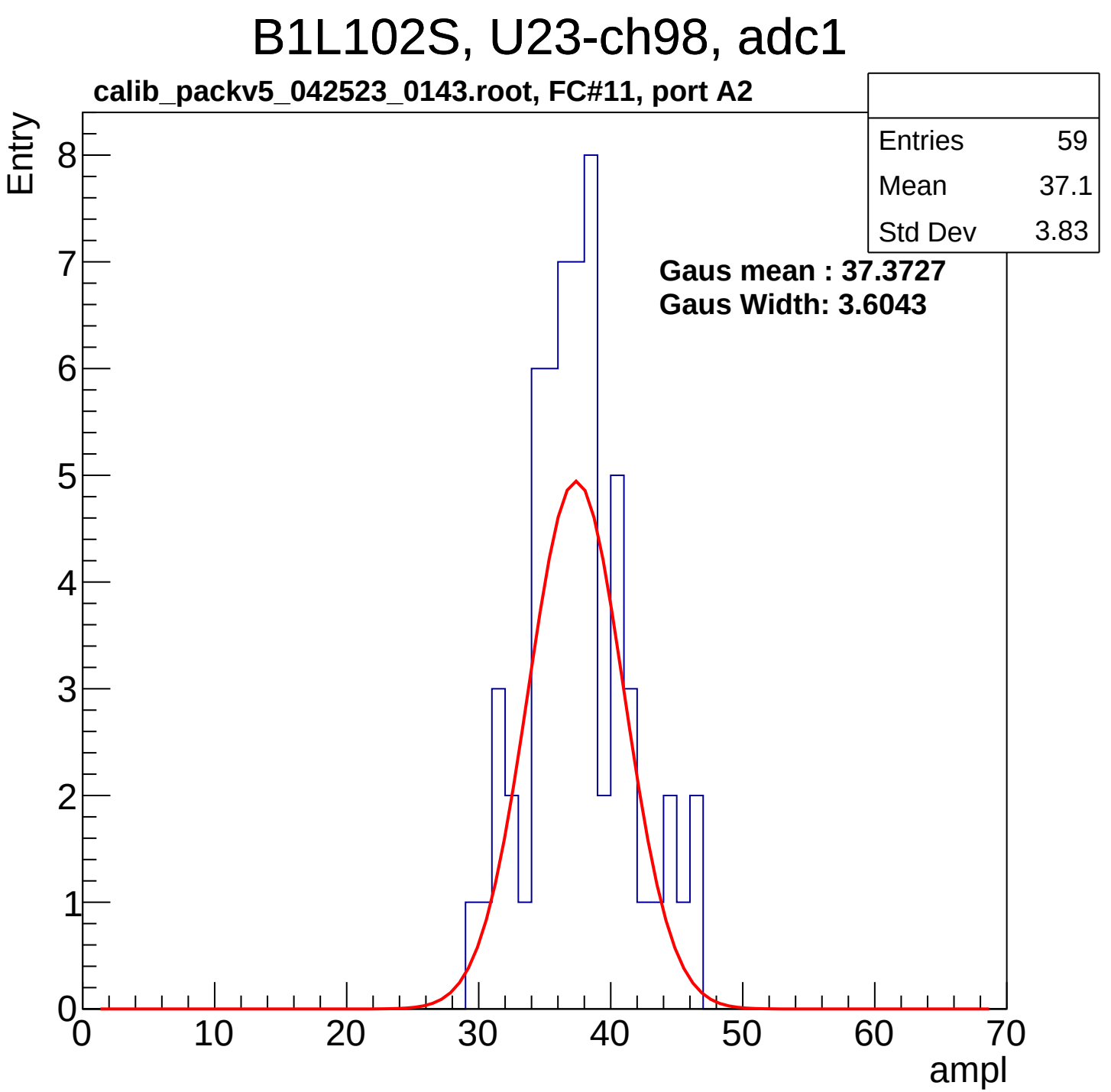

B1L102S, U23-ch97, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 56.94
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

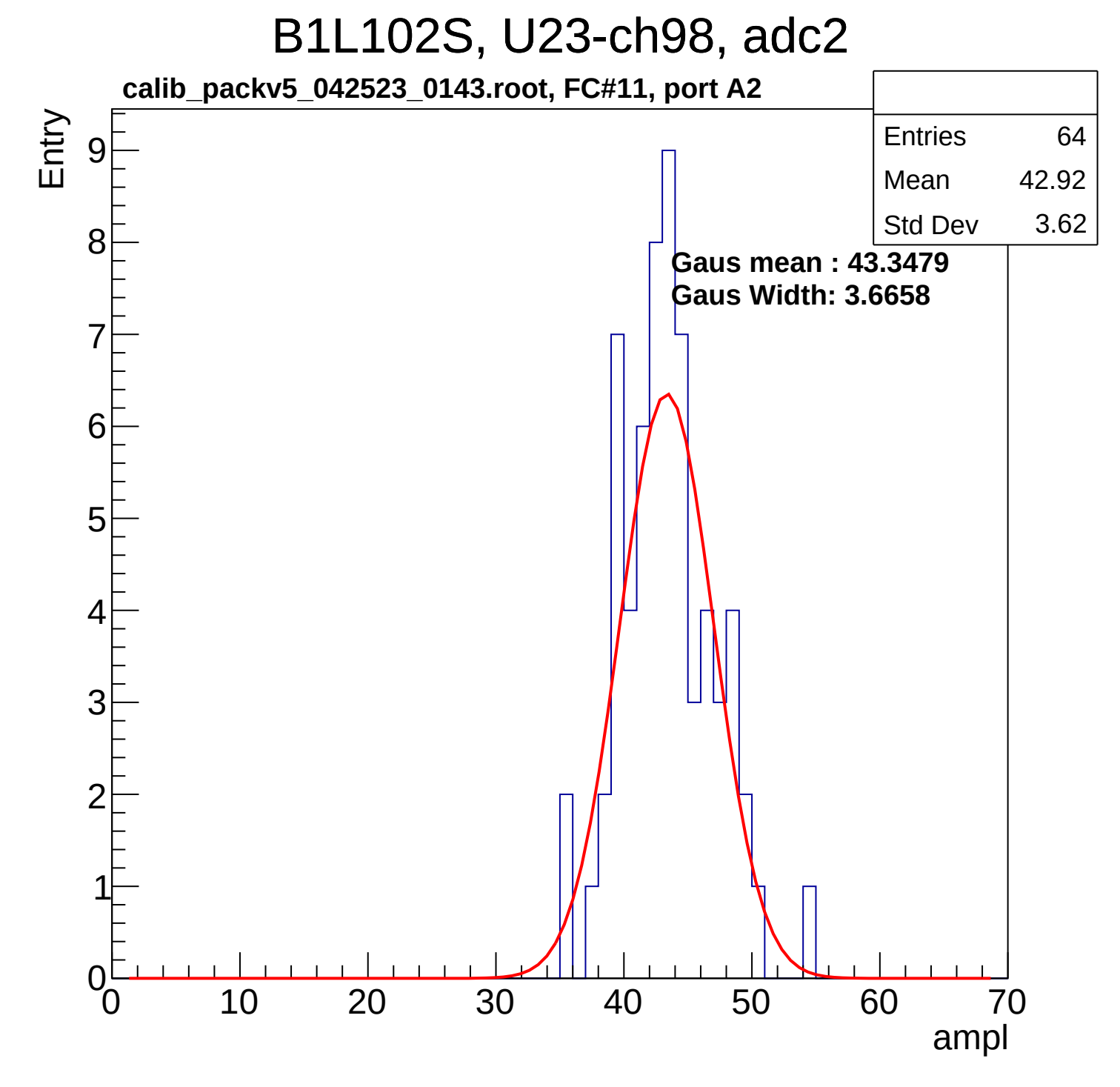

B1L102S, U23-ch97, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 58.57
Std Dev 10.36
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

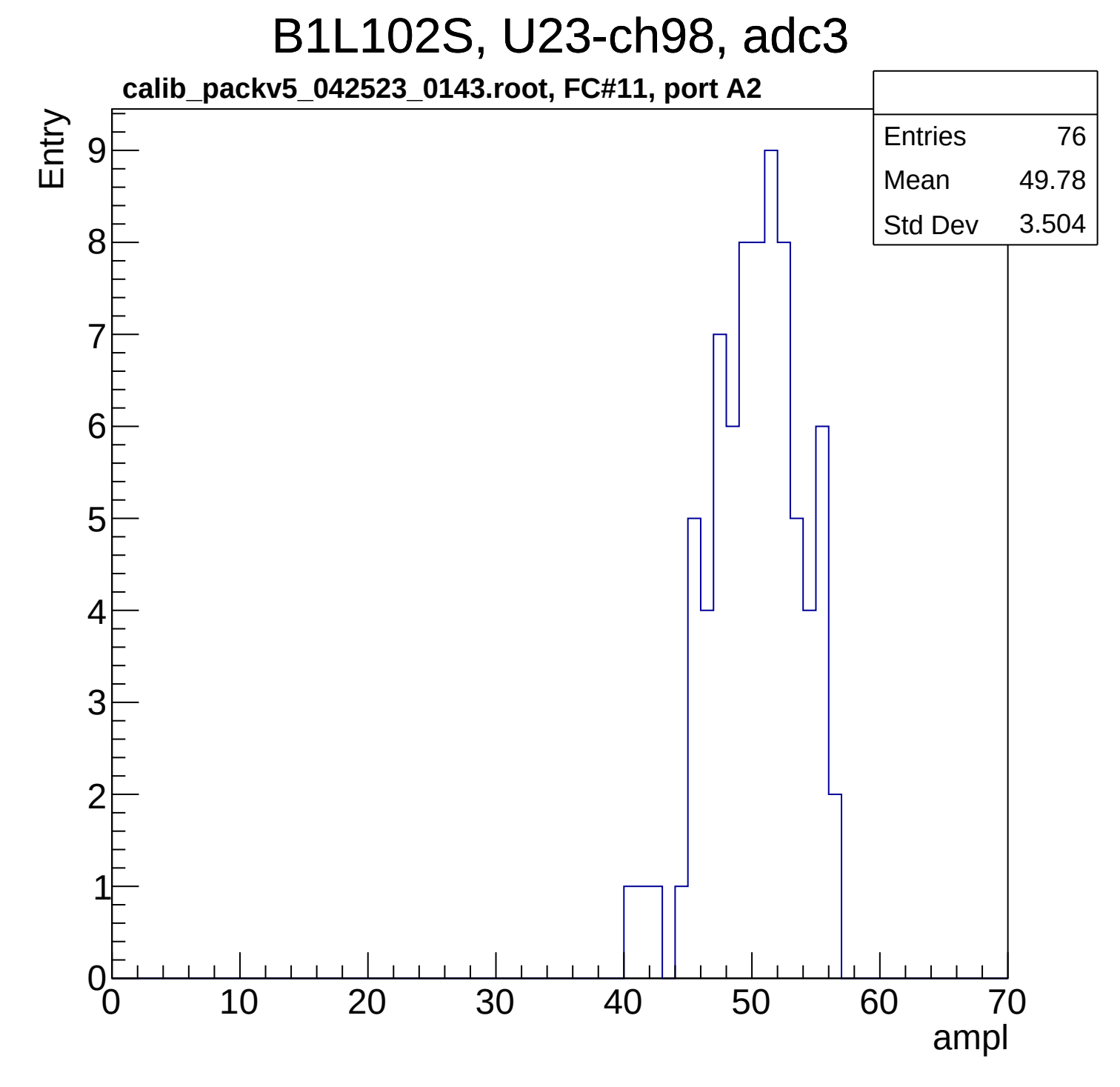

B1L102S, U23-ch97, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

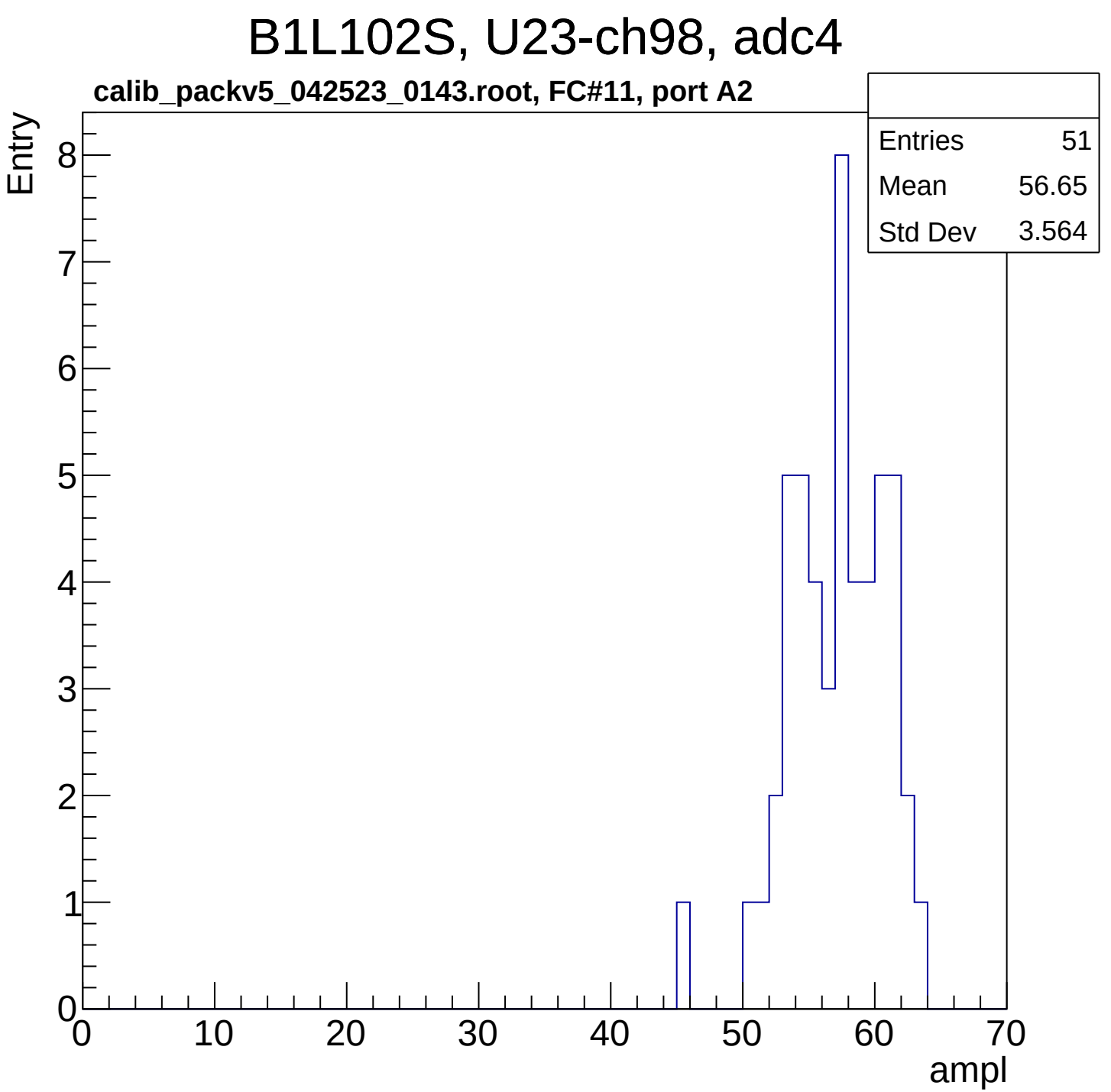

B1L102S, U23-ch97, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

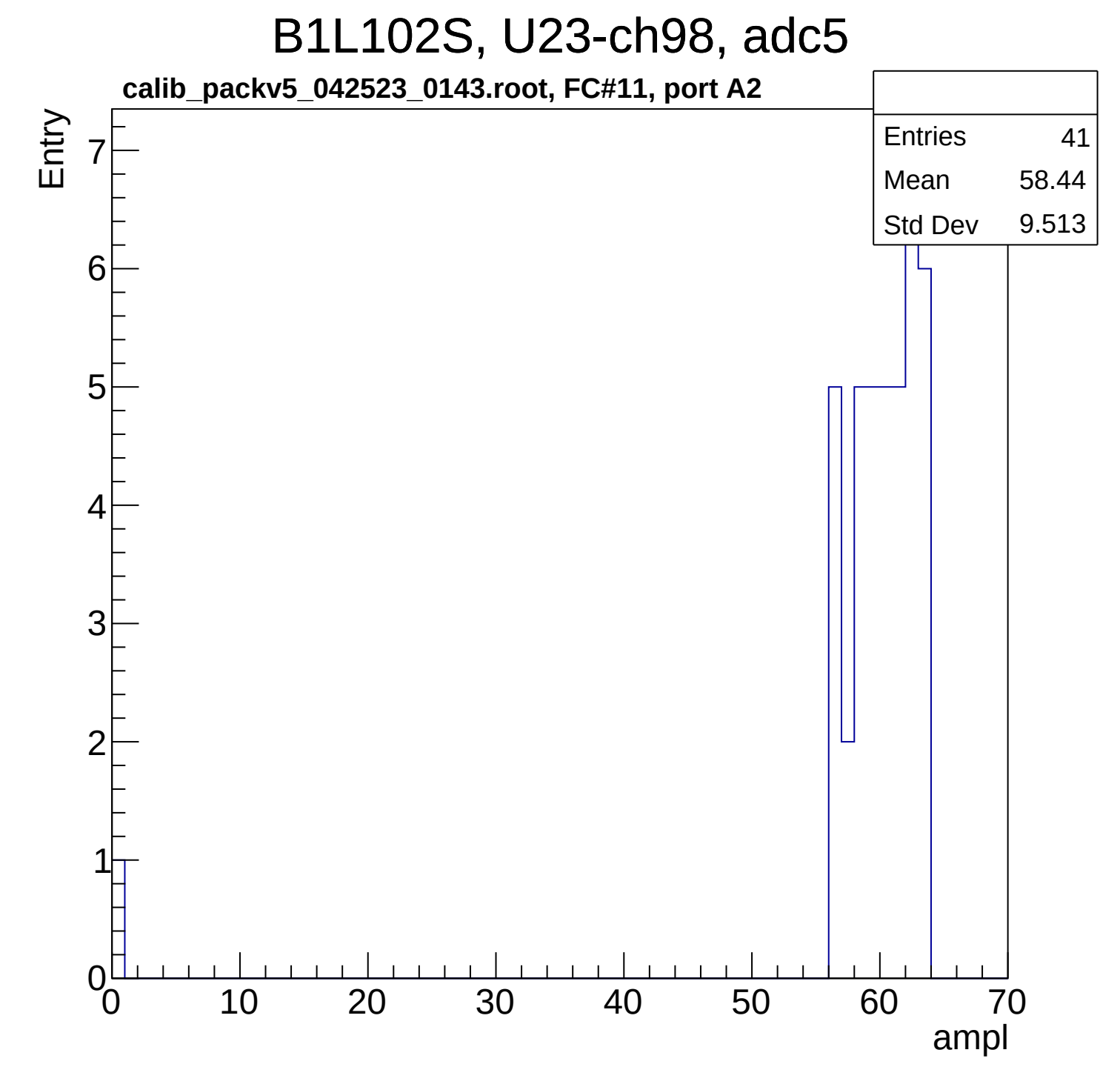

B1L102S, U23-ch98, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 29.59
Std Dev 4.981
Gaus mean : 30.4140
Gaus Width: 3.9014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

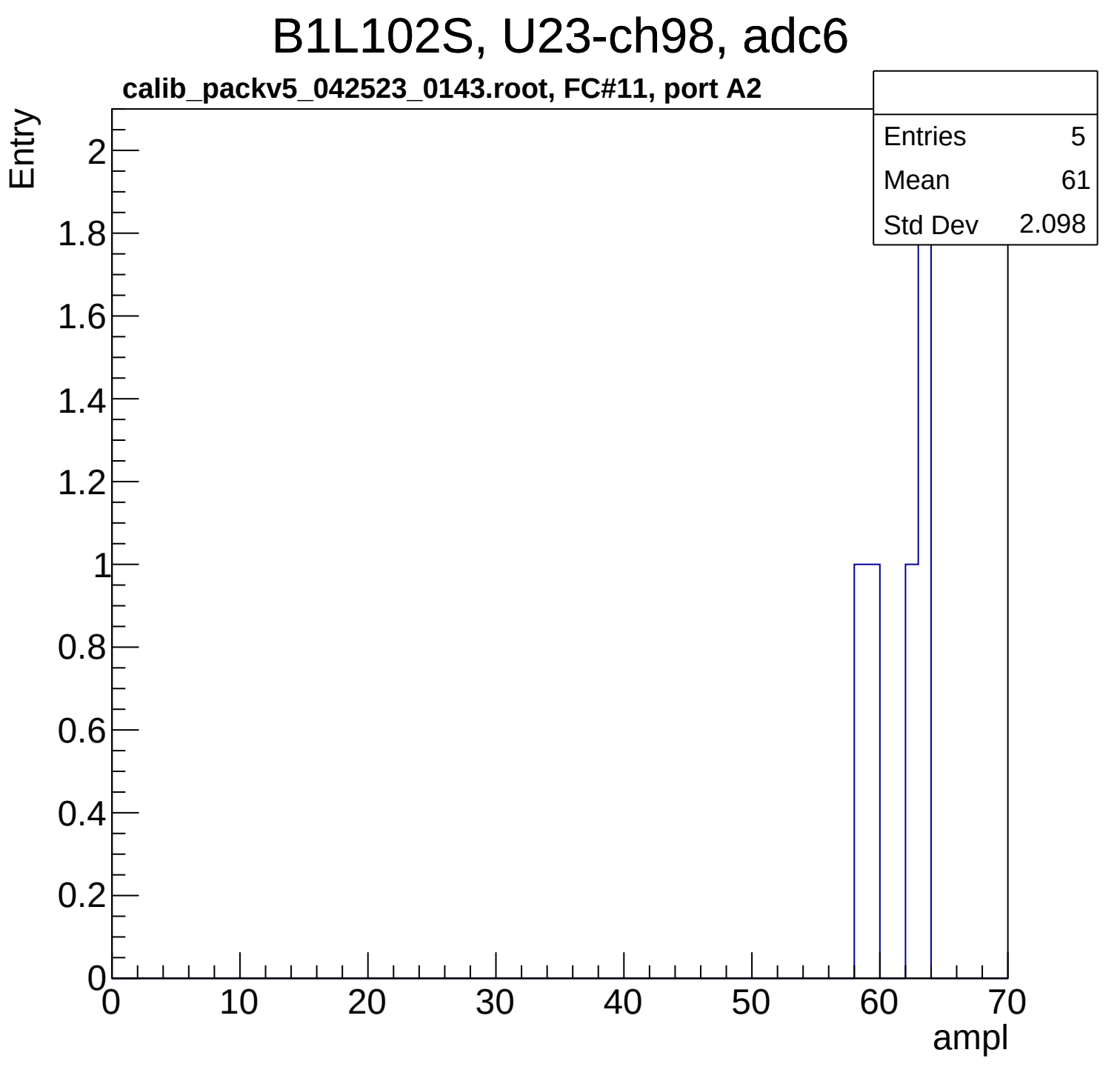

B1L102S, U23-ch98, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 37.1
Std Dev 3.83
Gaus mean : 37.3727
Gaus Width: 3.6043
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

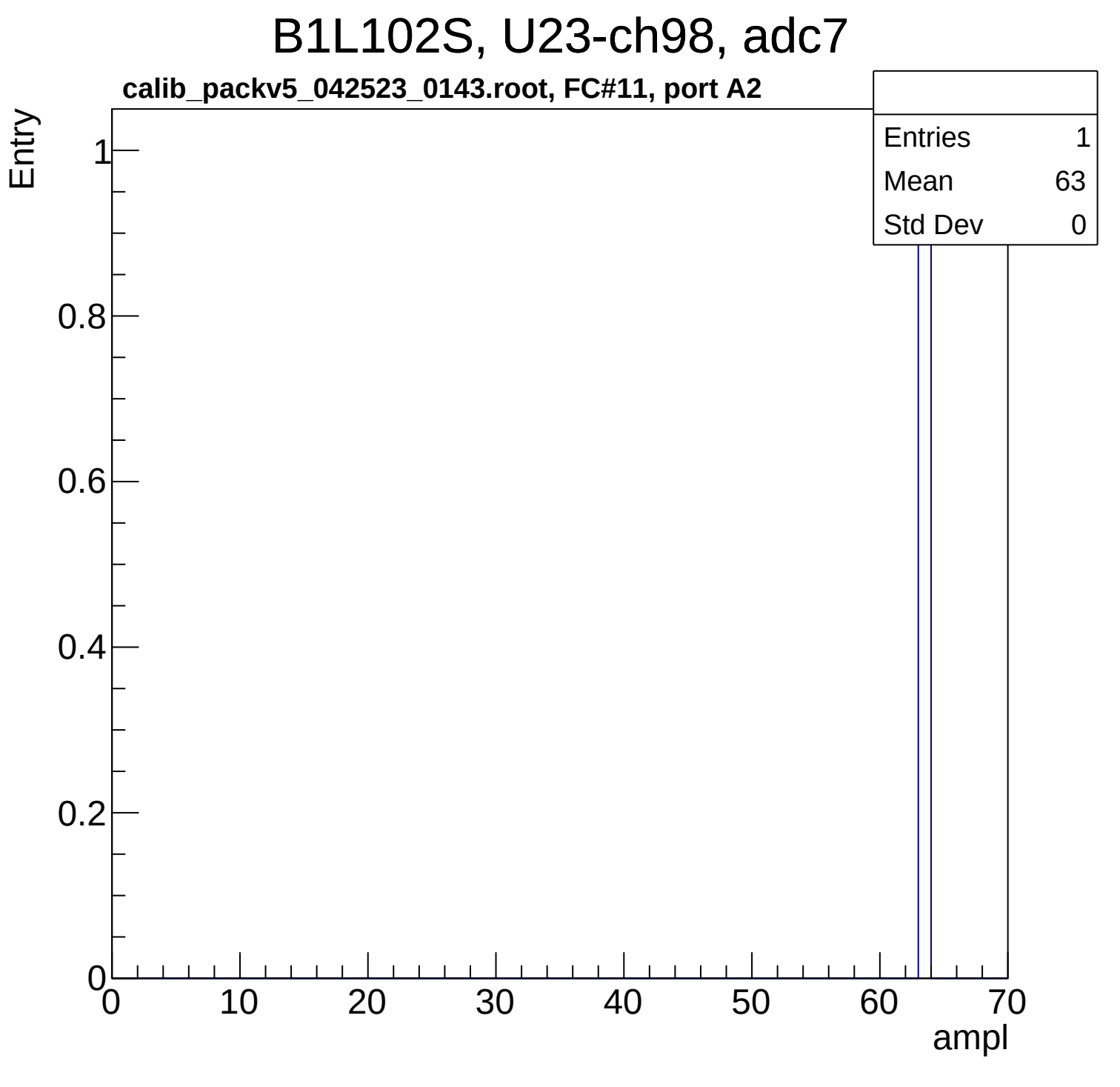

B1L102S, U23-ch98, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 42.92
Std Dev 3.62
Gaus mean : 43.3479
Gaus Width: 3.6658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

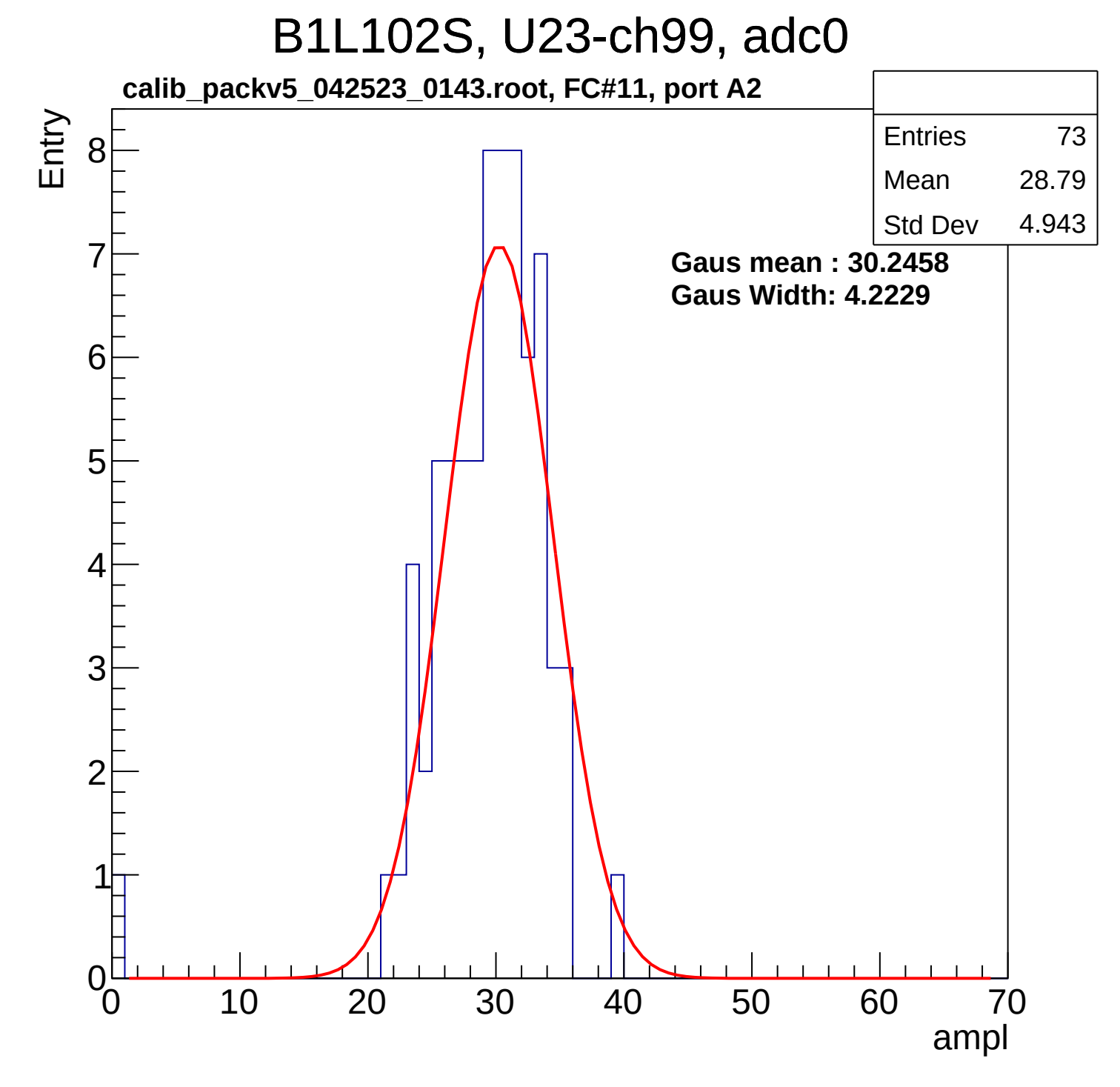

B1L102S, U23-ch98, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 49.78
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

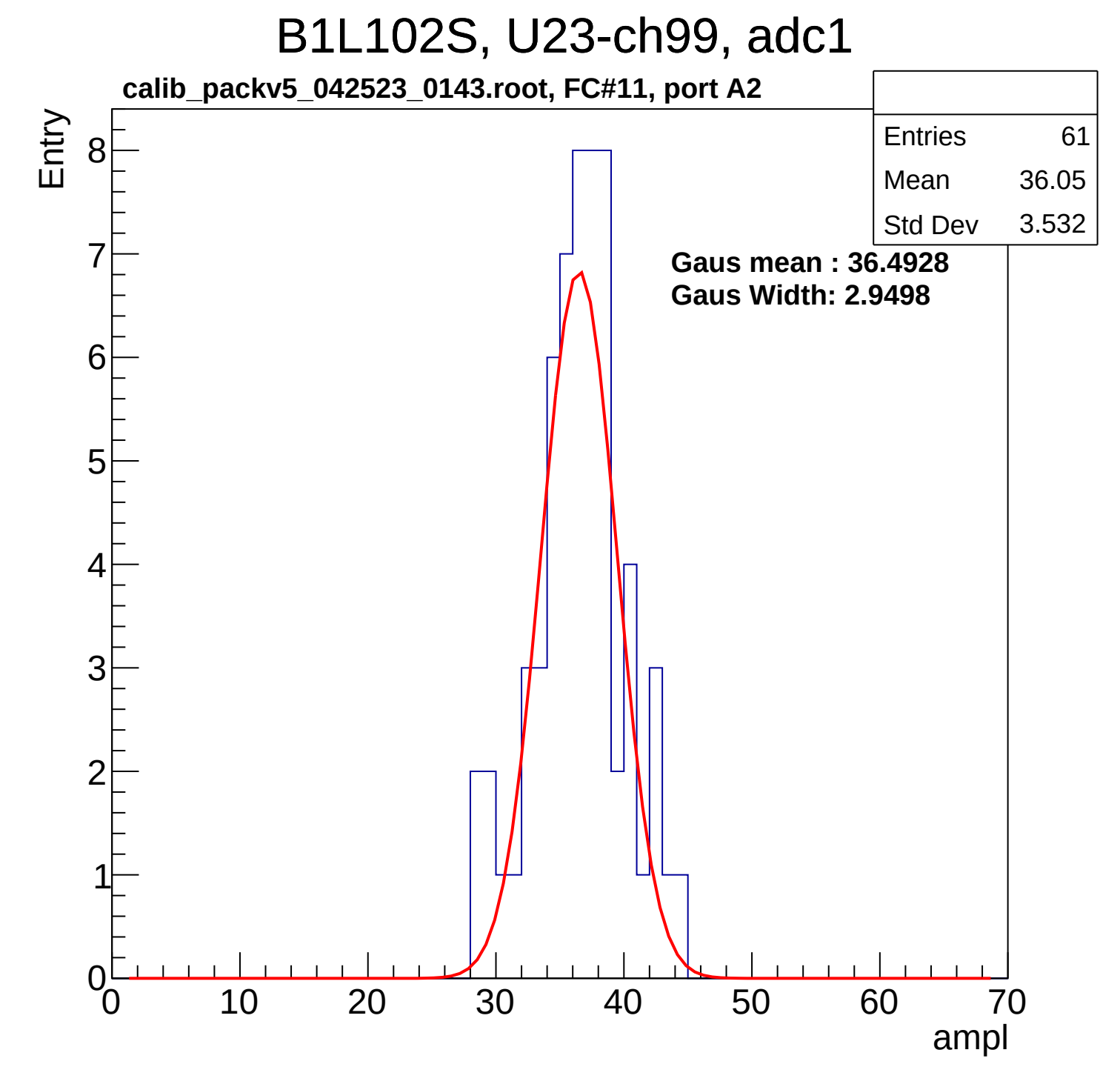

B1L102S, U23-ch98, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 56.65
Std Dev 3.564
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

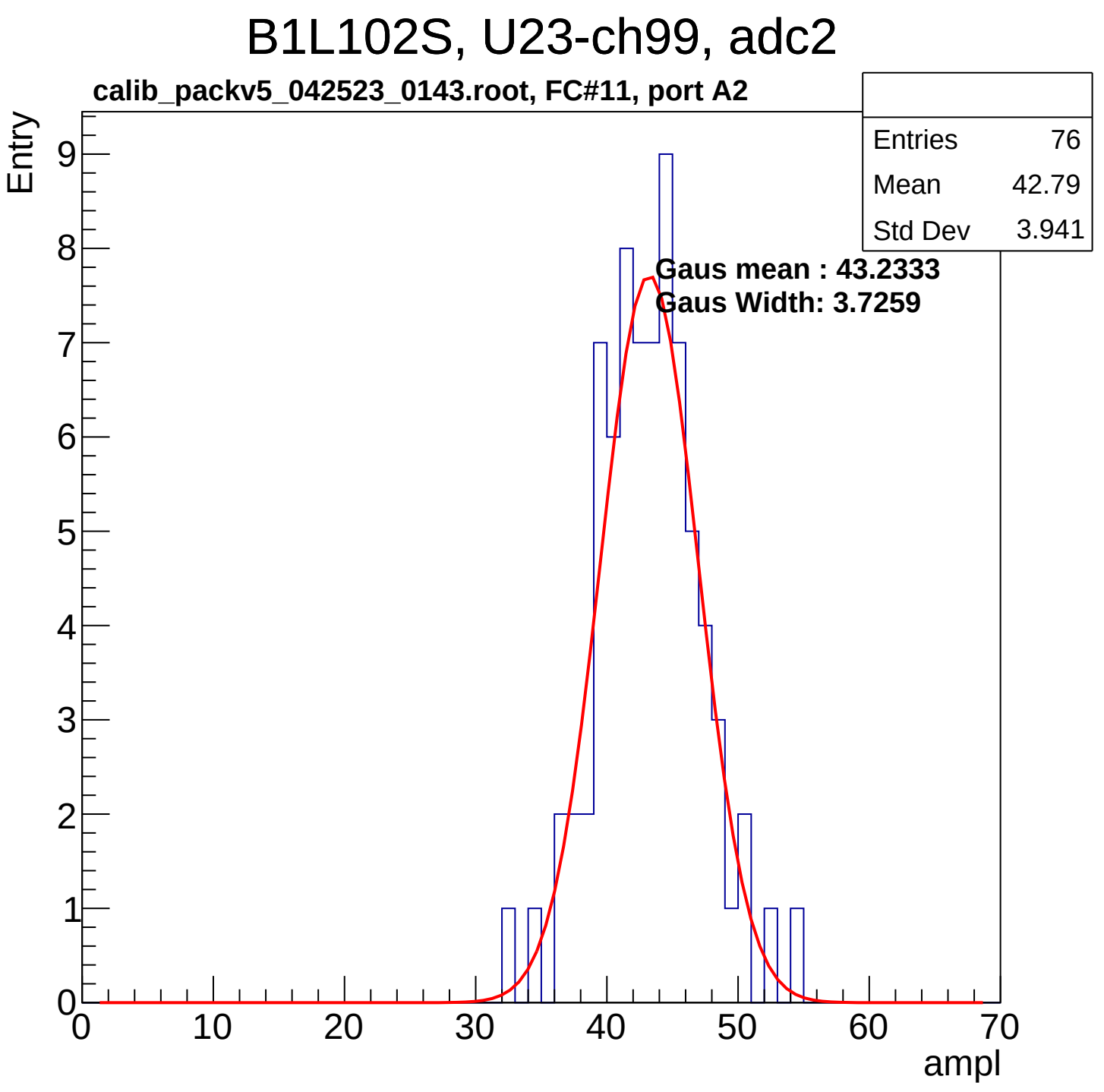

B1L102S, U23-ch98, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 58.44
Std Dev 9.513
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

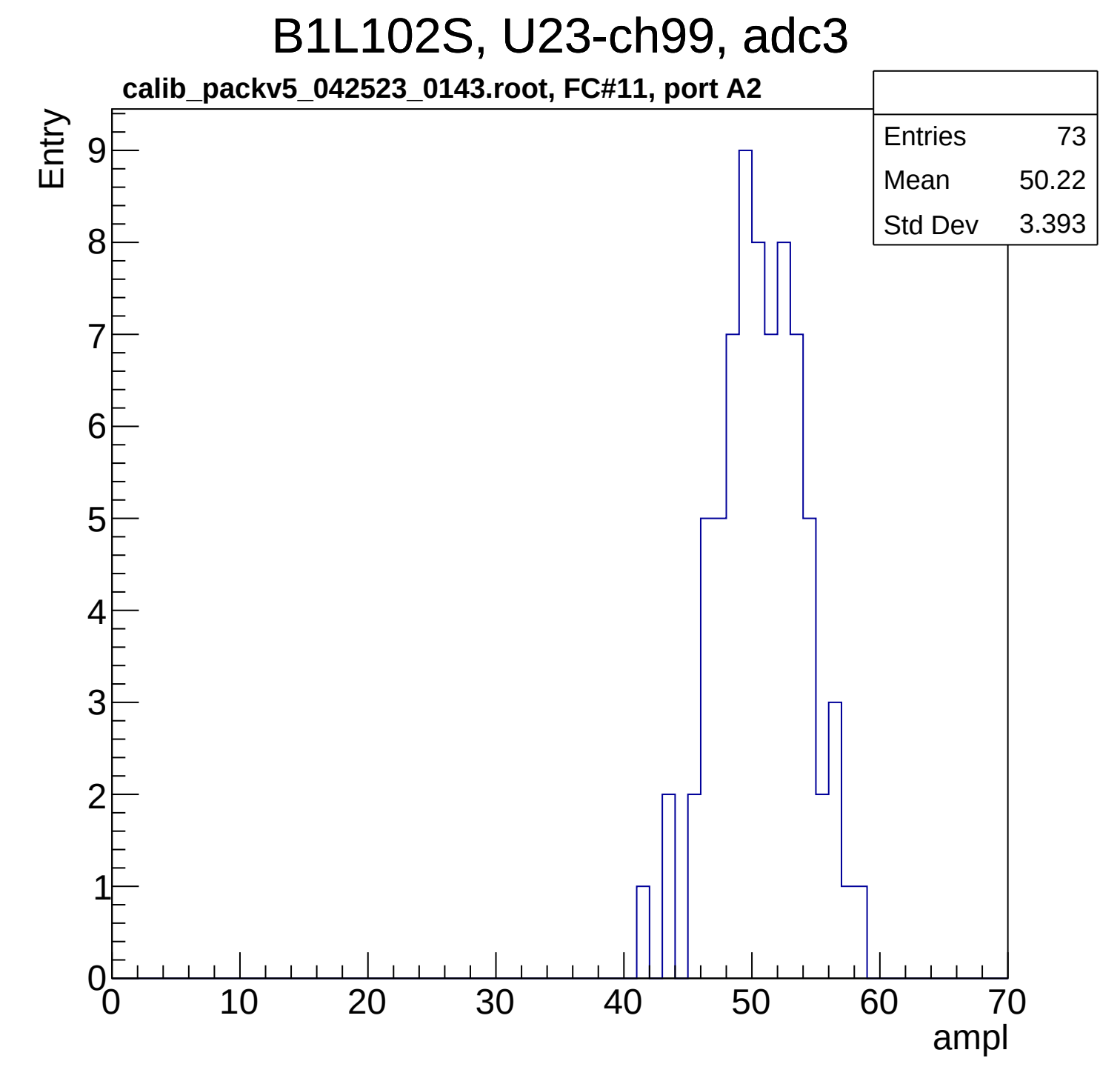

B1L102S, U23-ch98, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61
Std Dev 2.098
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

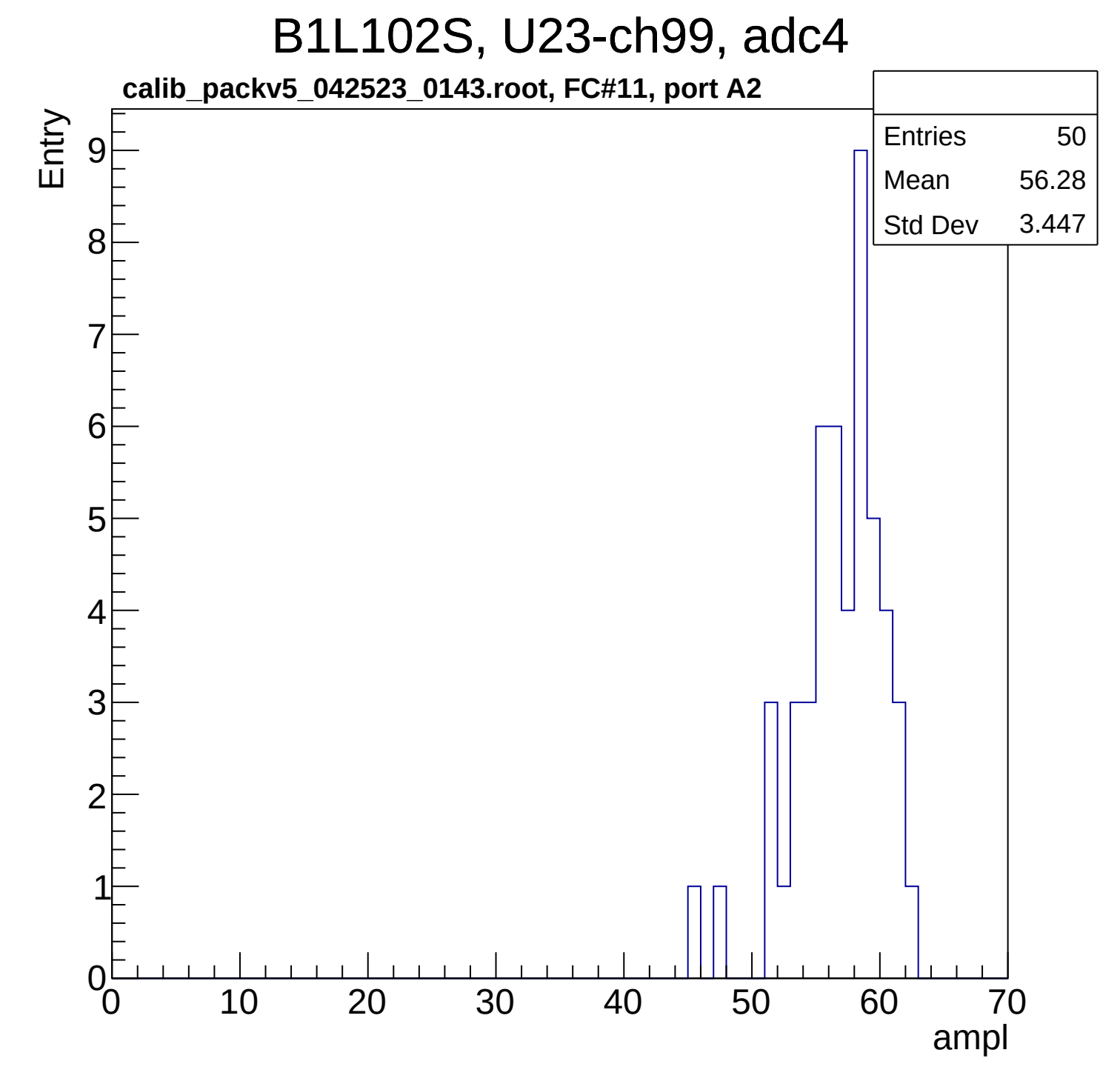

B1L102S, U23-ch98, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

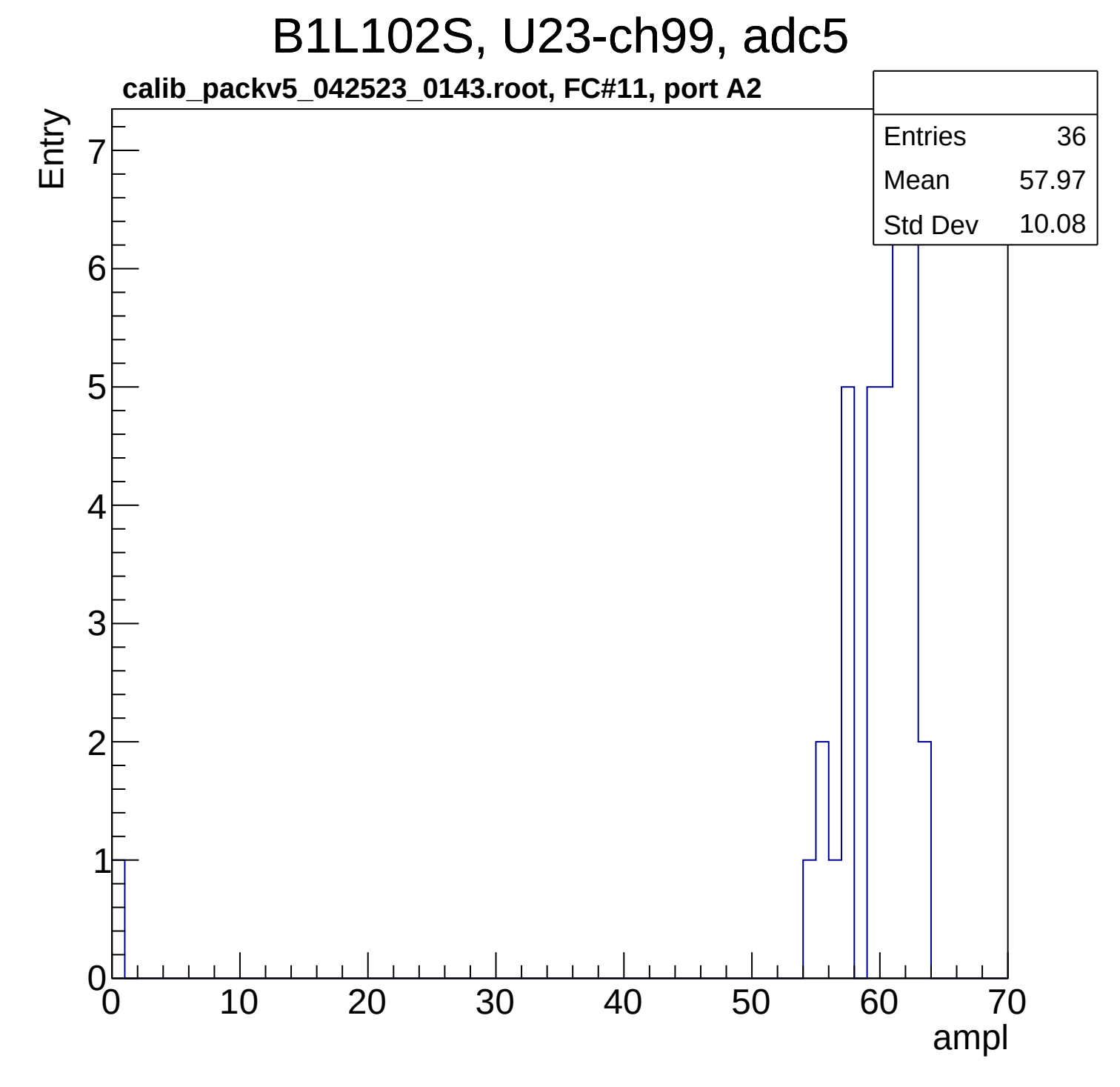

B1L102S, U23-ch99, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 28.79
Std Dev 4.943
Gaus mean : 30.2458
Gaus Width: 4.2229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

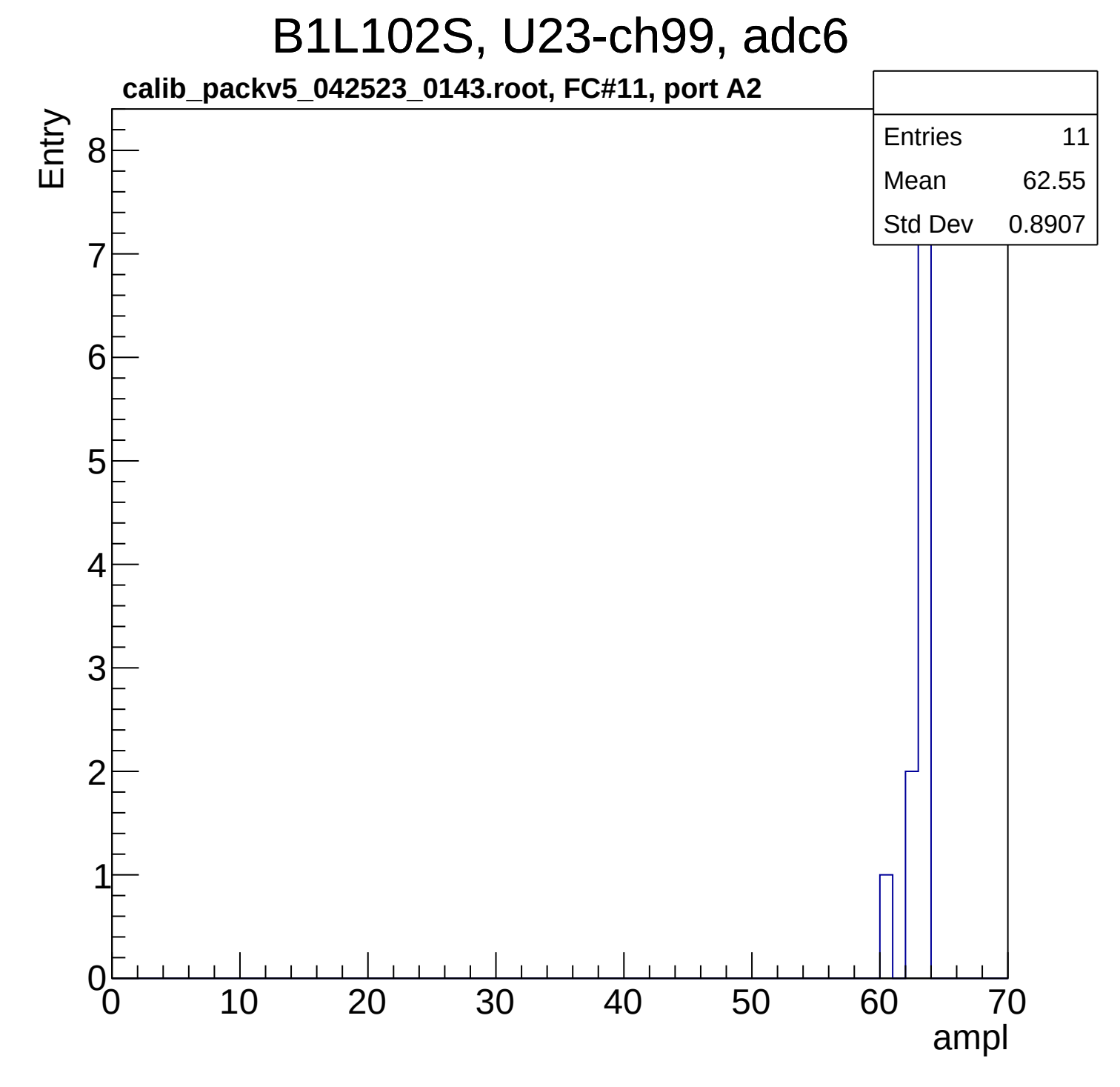

B1L102S, U23-ch99, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 36.05
Std Dev 3.532
Gaus mean : 36.4928
Gaus Width: 2.9498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

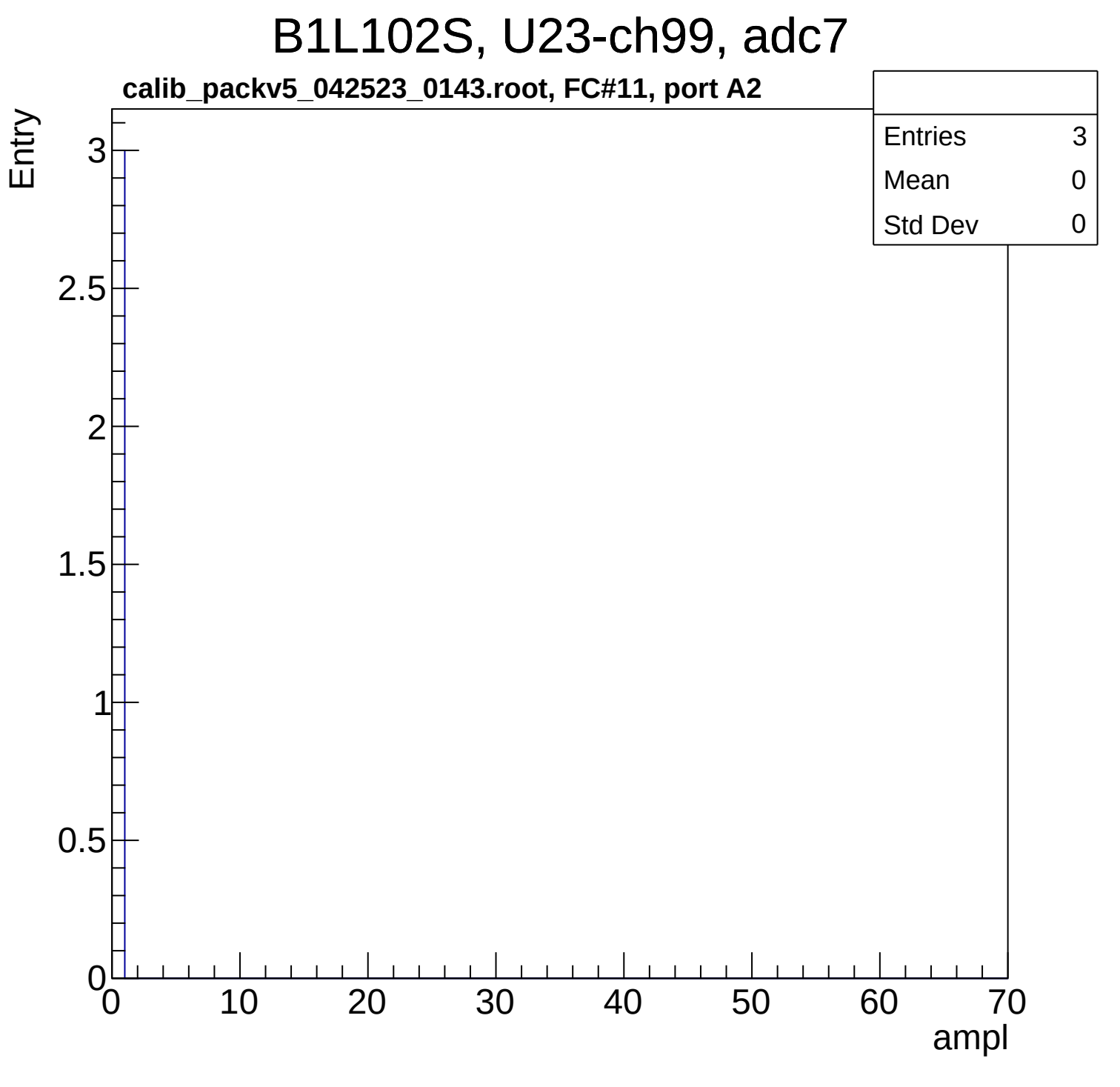

B1L102S, U23-ch99, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 42.79
Std Dev 3.941
Gaus mean : 43.2333
Gaus Width: 3.7259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

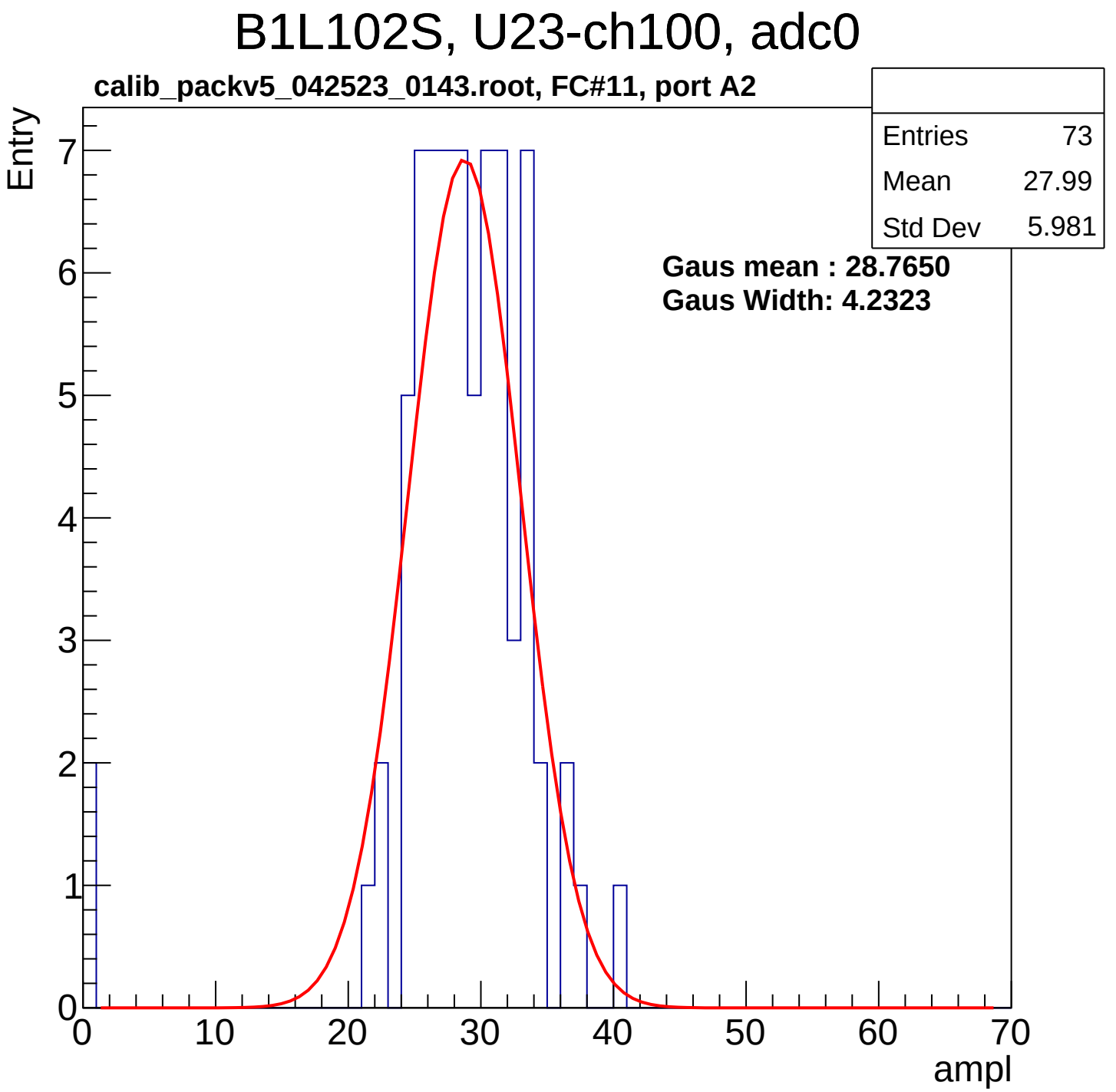

B1L102S, U23-ch99, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 50.22
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

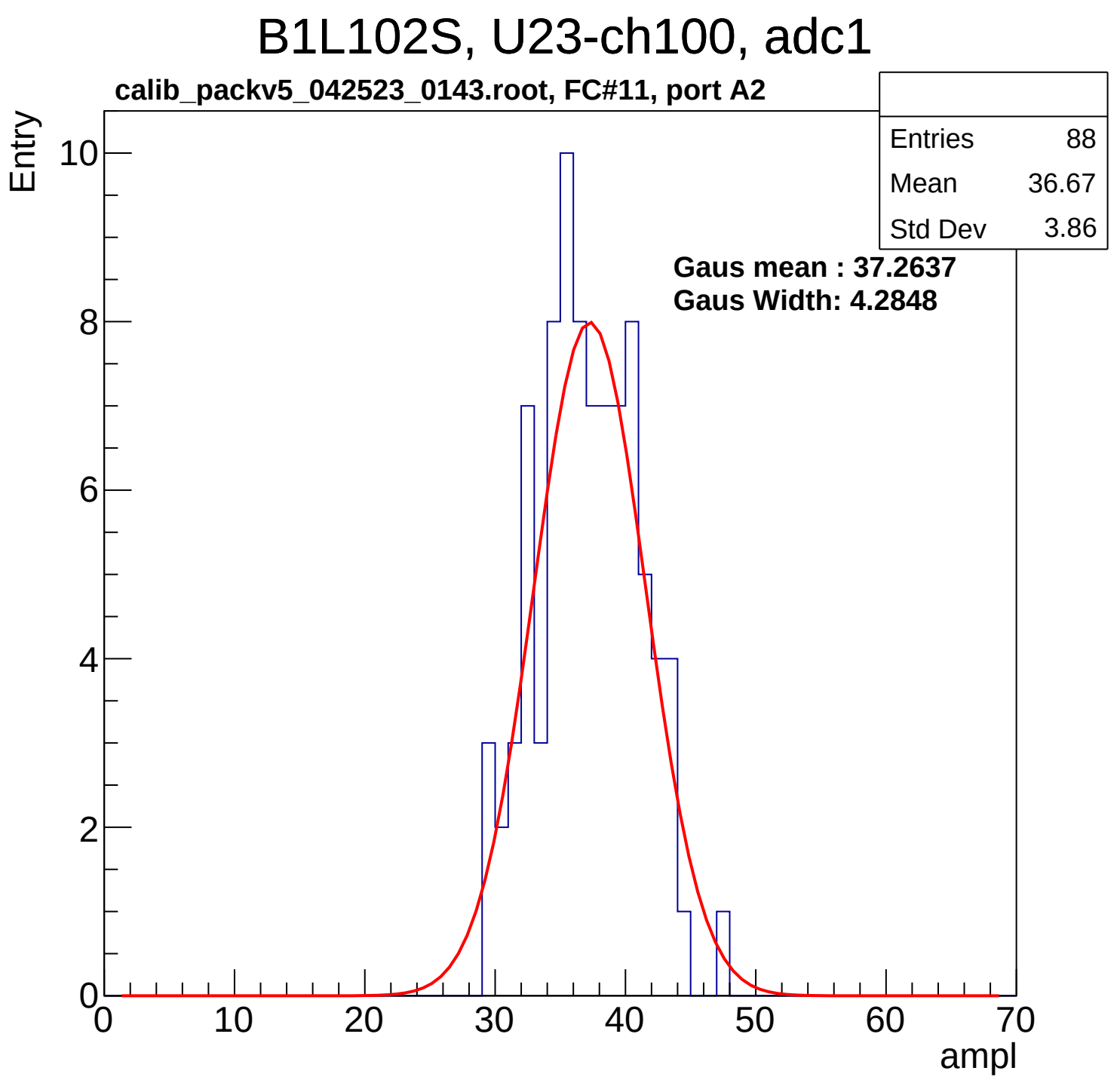

B1L102S, U23-ch99, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 56.28
Std Dev 3.447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

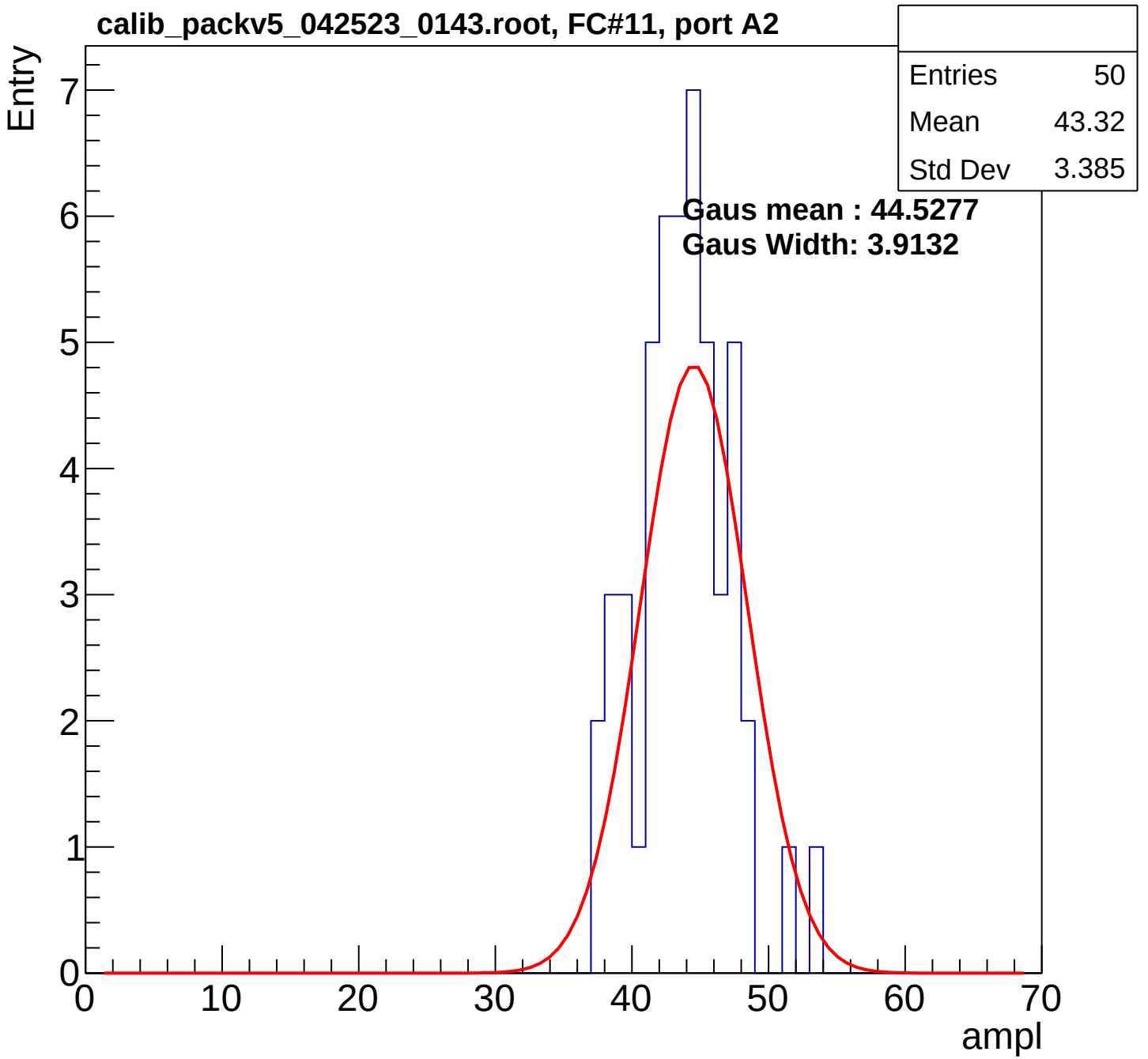

B1L102S, U23-ch99, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 36
Mean 57.97
Std Dev 10.08
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

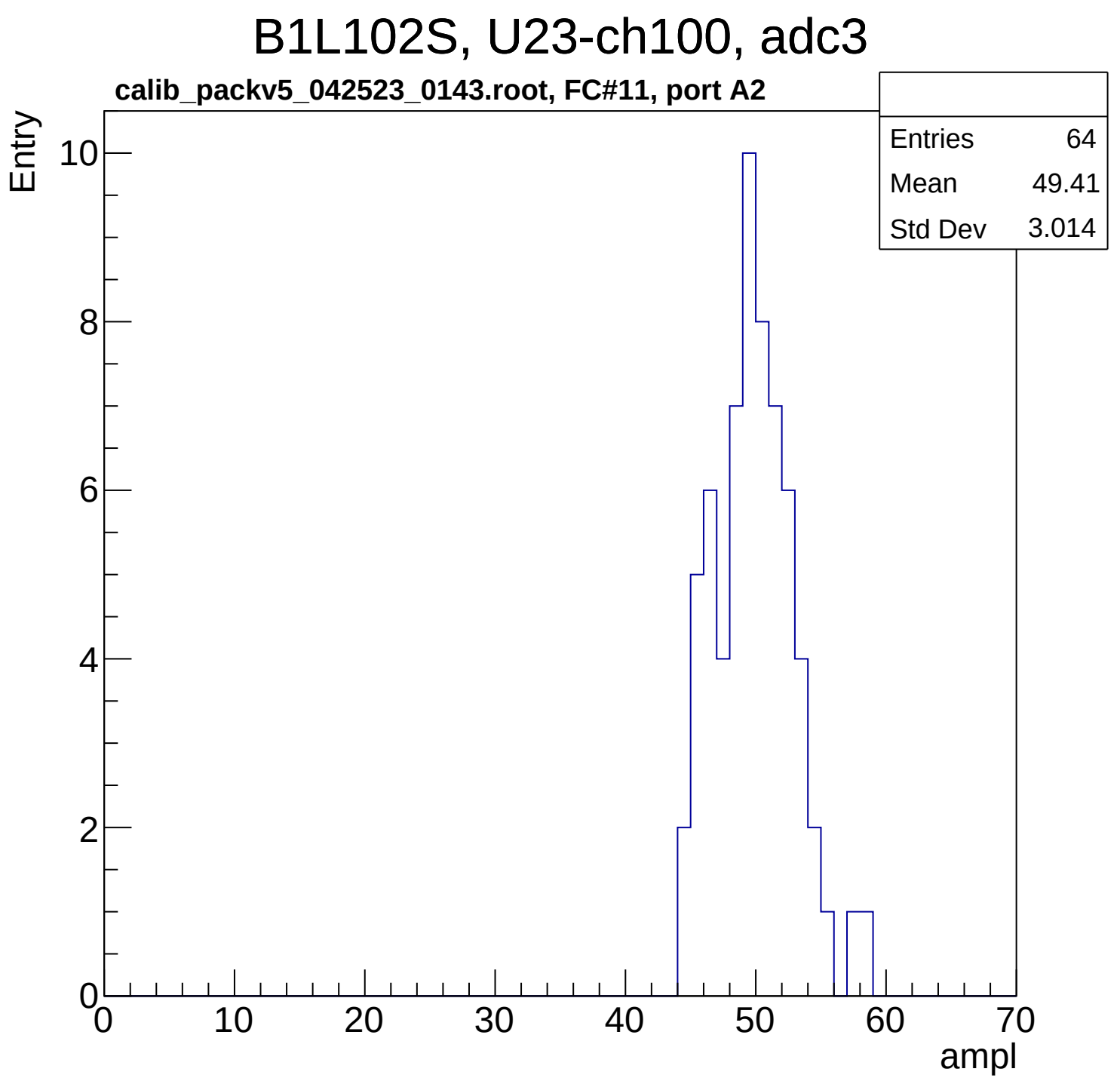

B1L102S, U23-ch99, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 62.55
Std Dev 0.8907
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

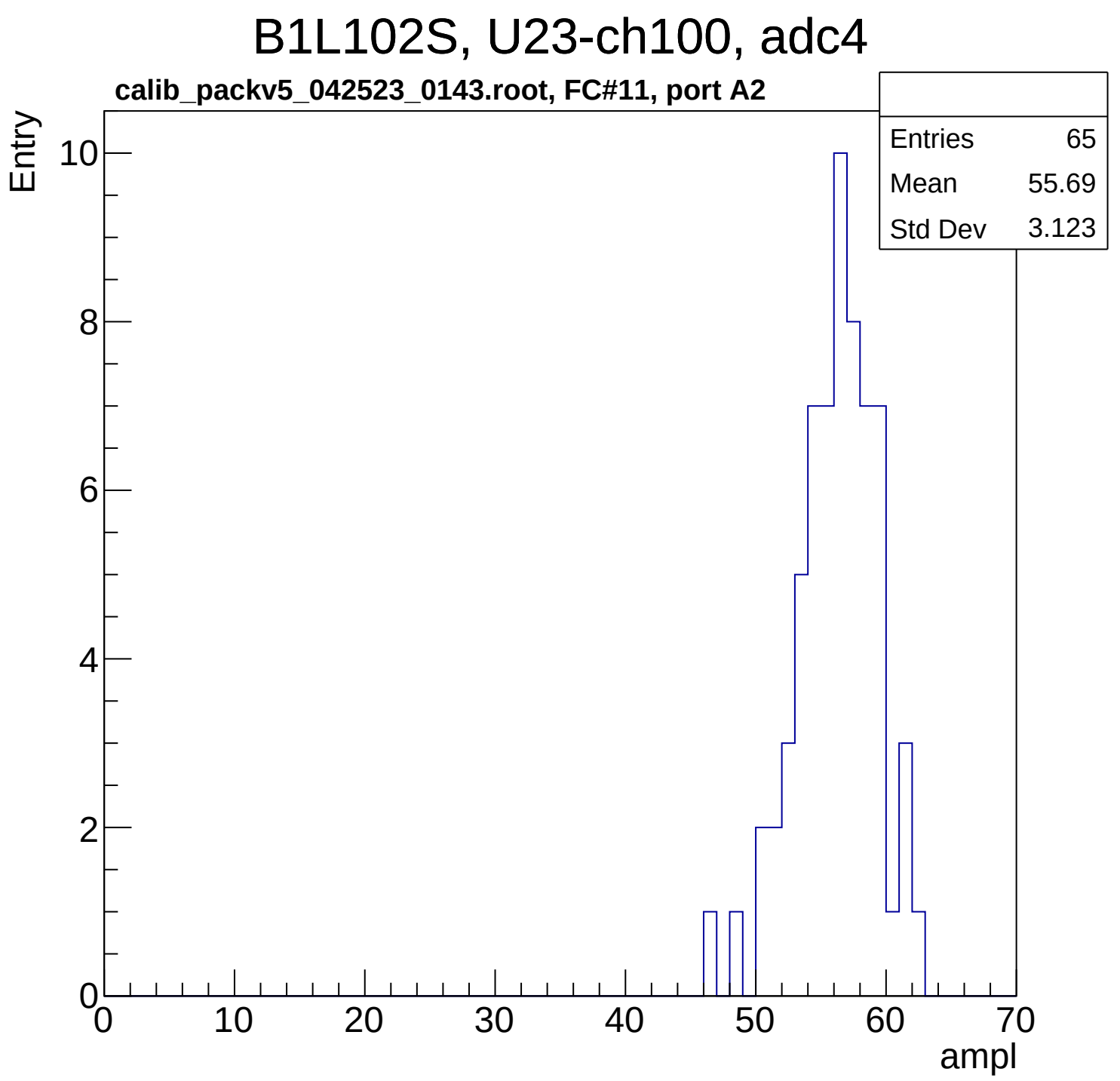

B1L102S, U23-ch99, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

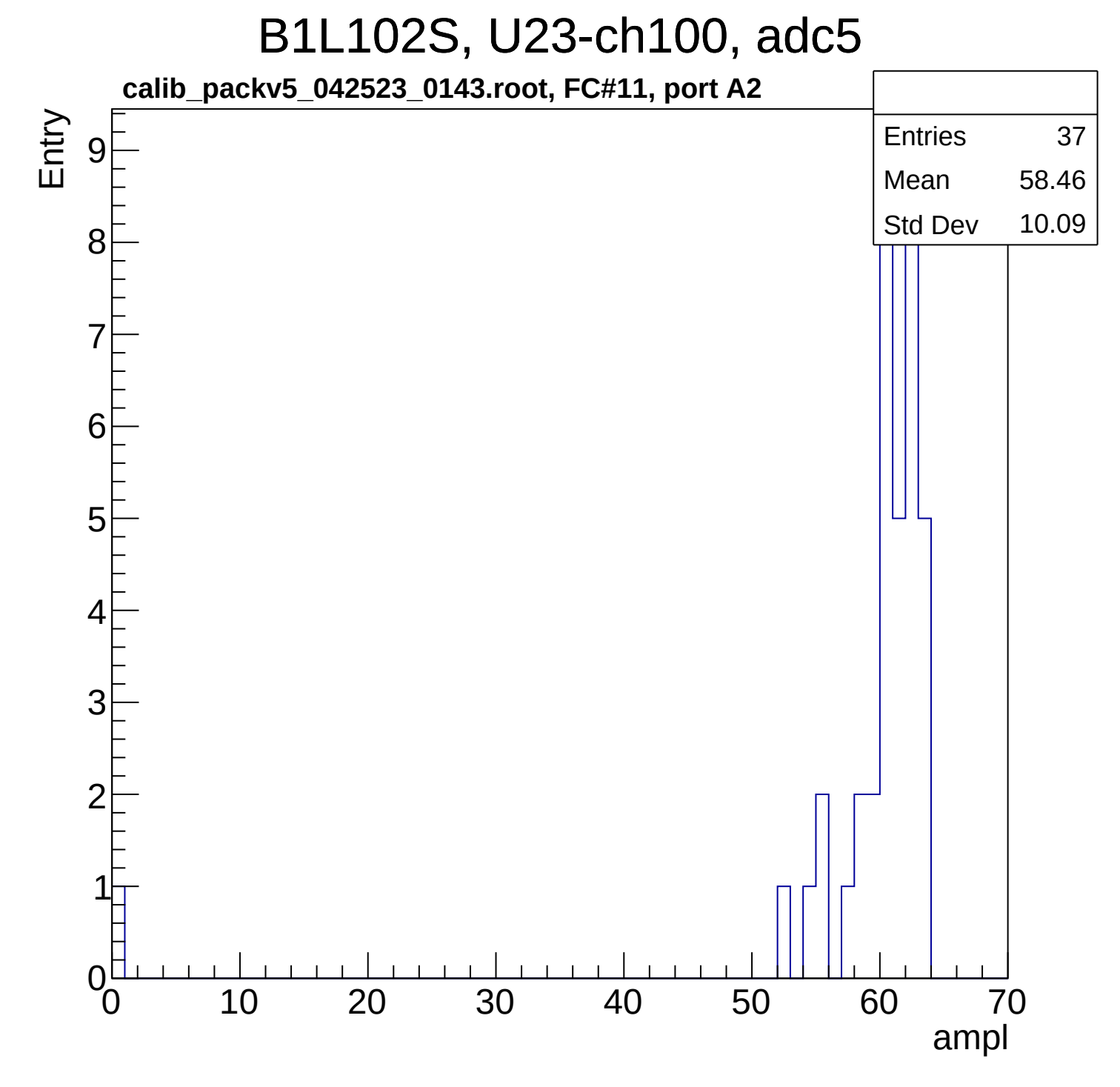

B1L102S, U23-ch100, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 27.99
Std Dev 5.981
Gaus mean : 28.7650
Gaus Width: 4.2323
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

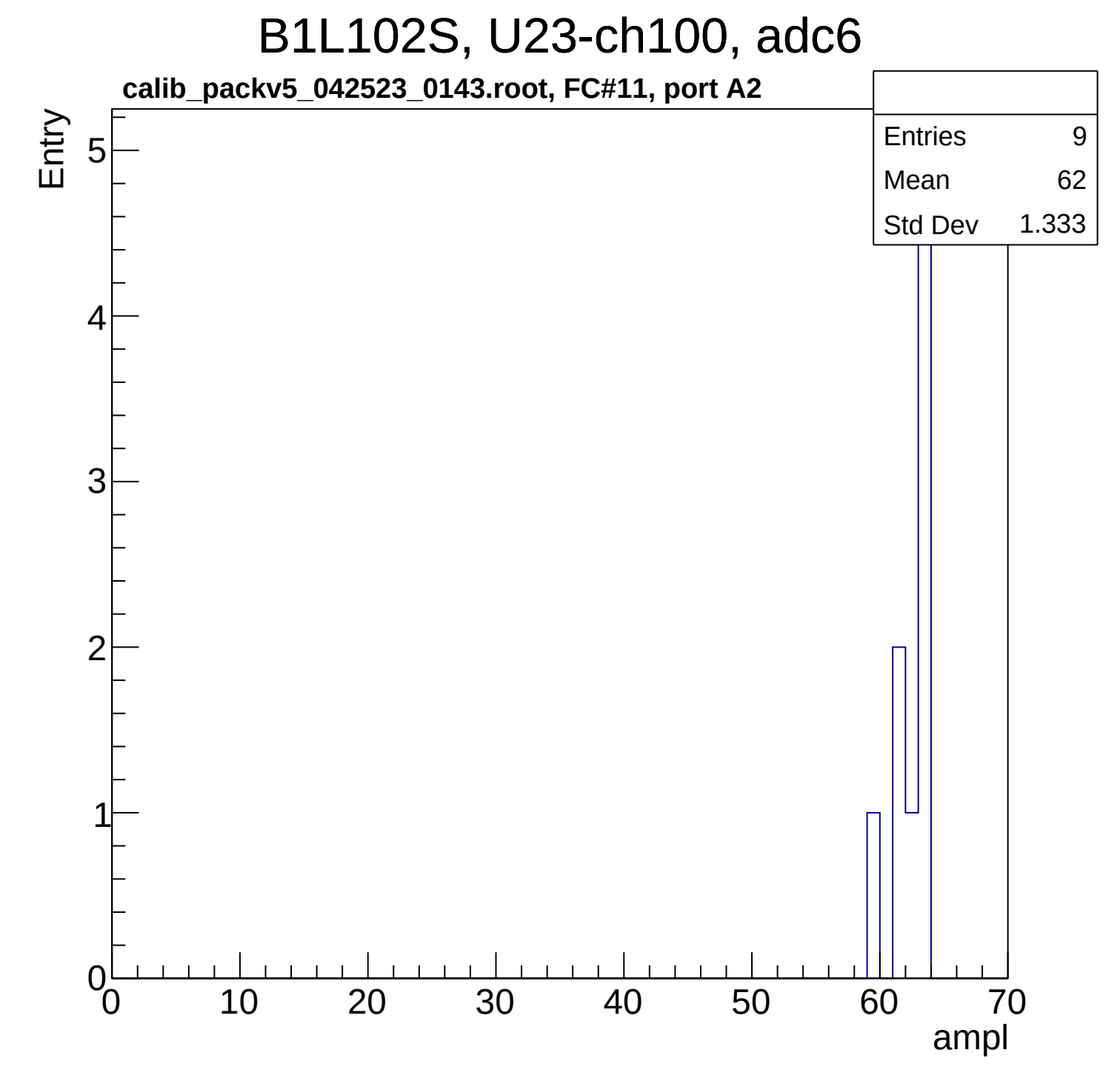

B1L102S, U23-ch100, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 36.67
Std Dev 3.86
Gaus mean : 37.2637
Gaus Width: 4.2848
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

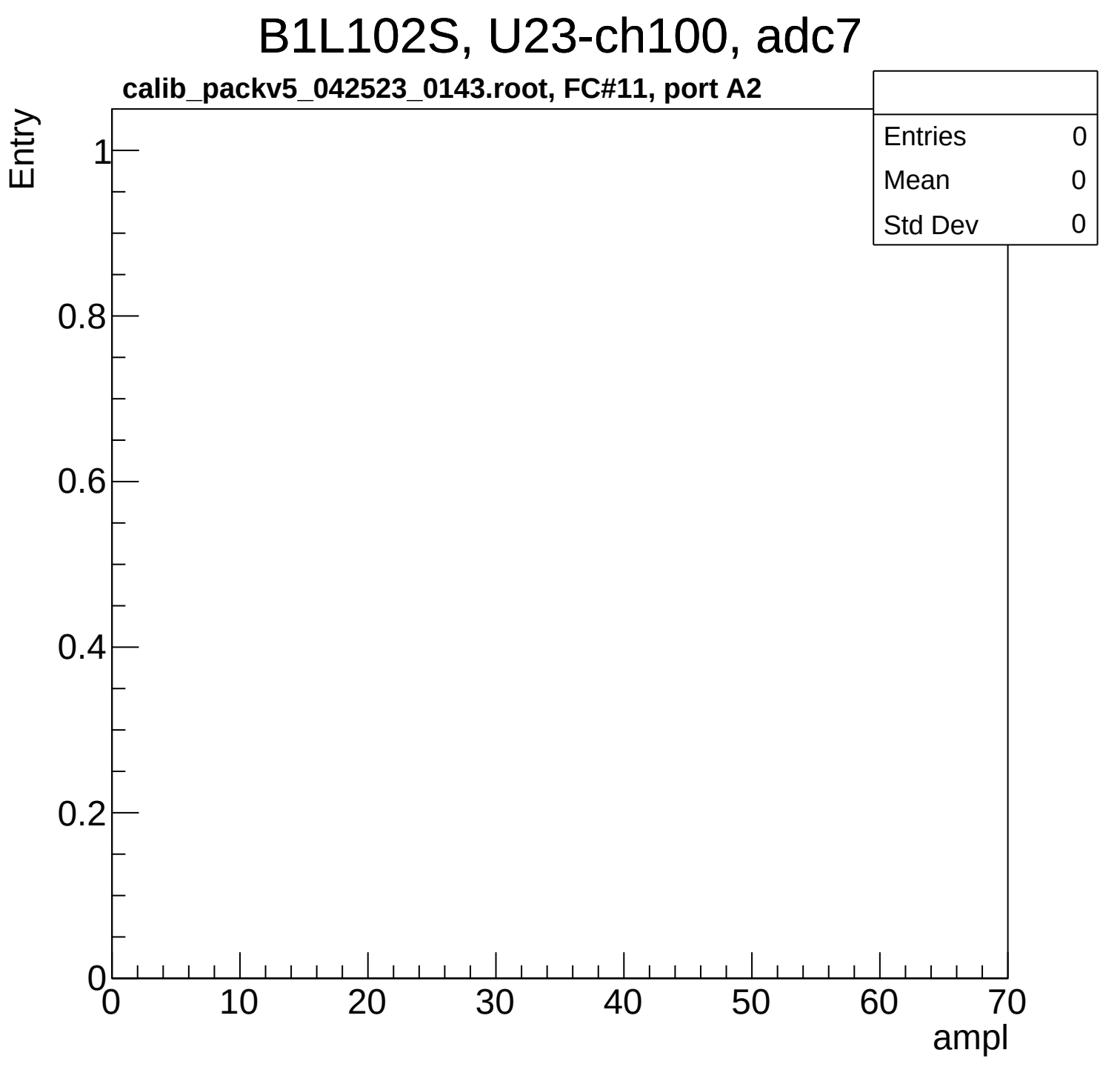

B1L102S, U23-ch100, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 43.32
Std Dev 3.385
Gaus mean : 44.5277
Gaus Width: 3.9132
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

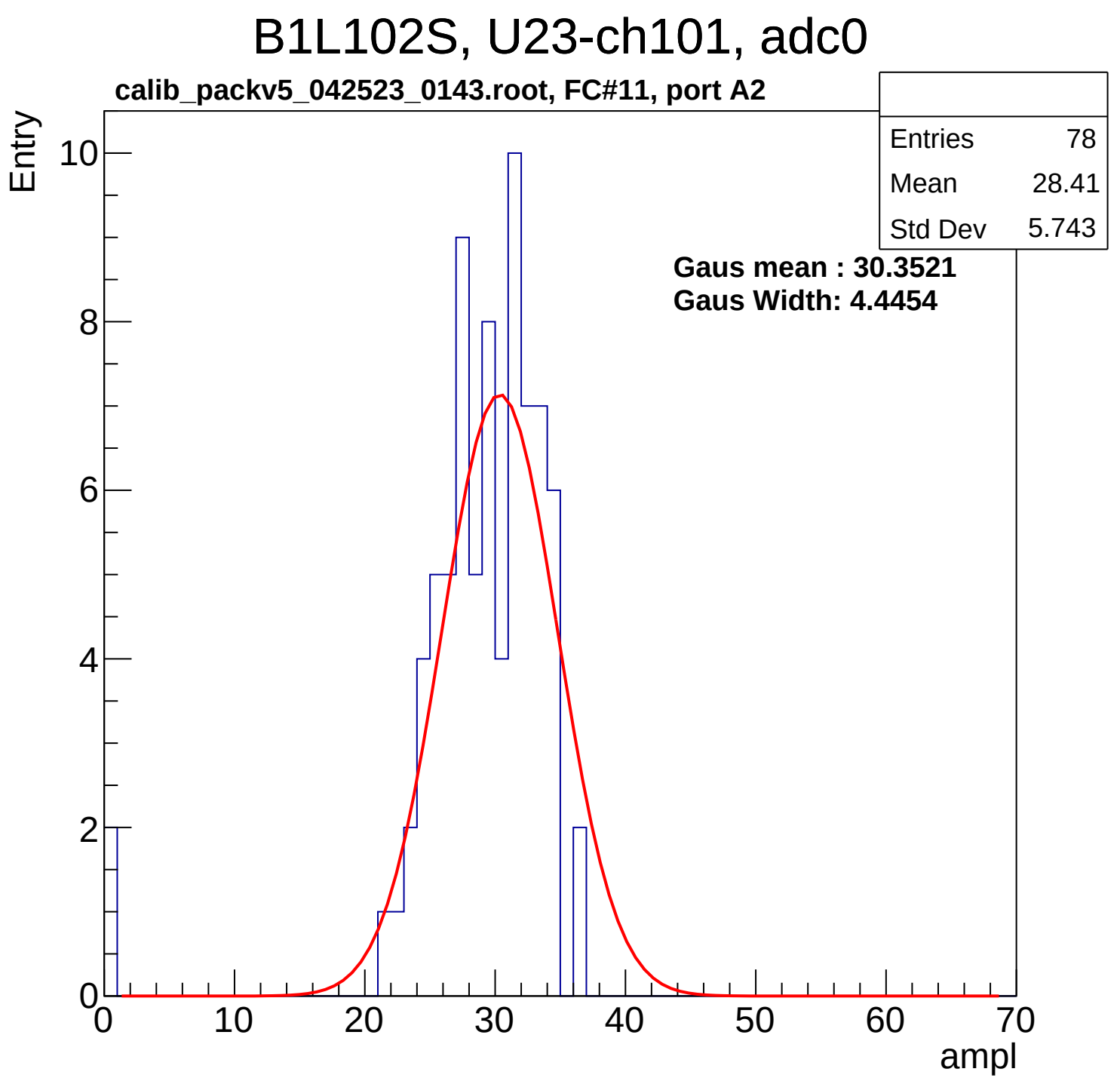

B1L102S, U23-ch100, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 49.41
Std Dev 3.014
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

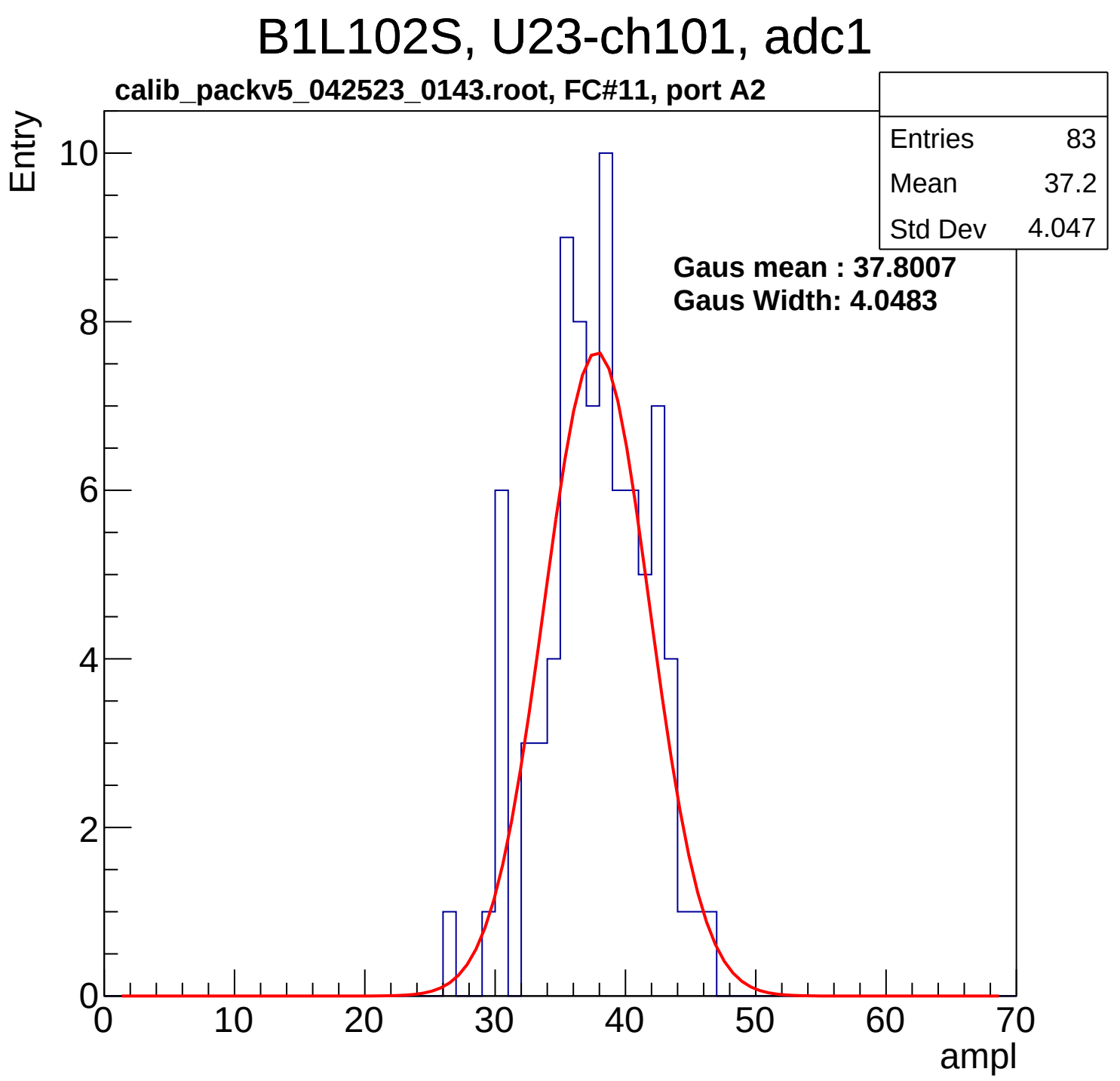

B1L102S, U23-ch100, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 55.69
Std Dev 3.123
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

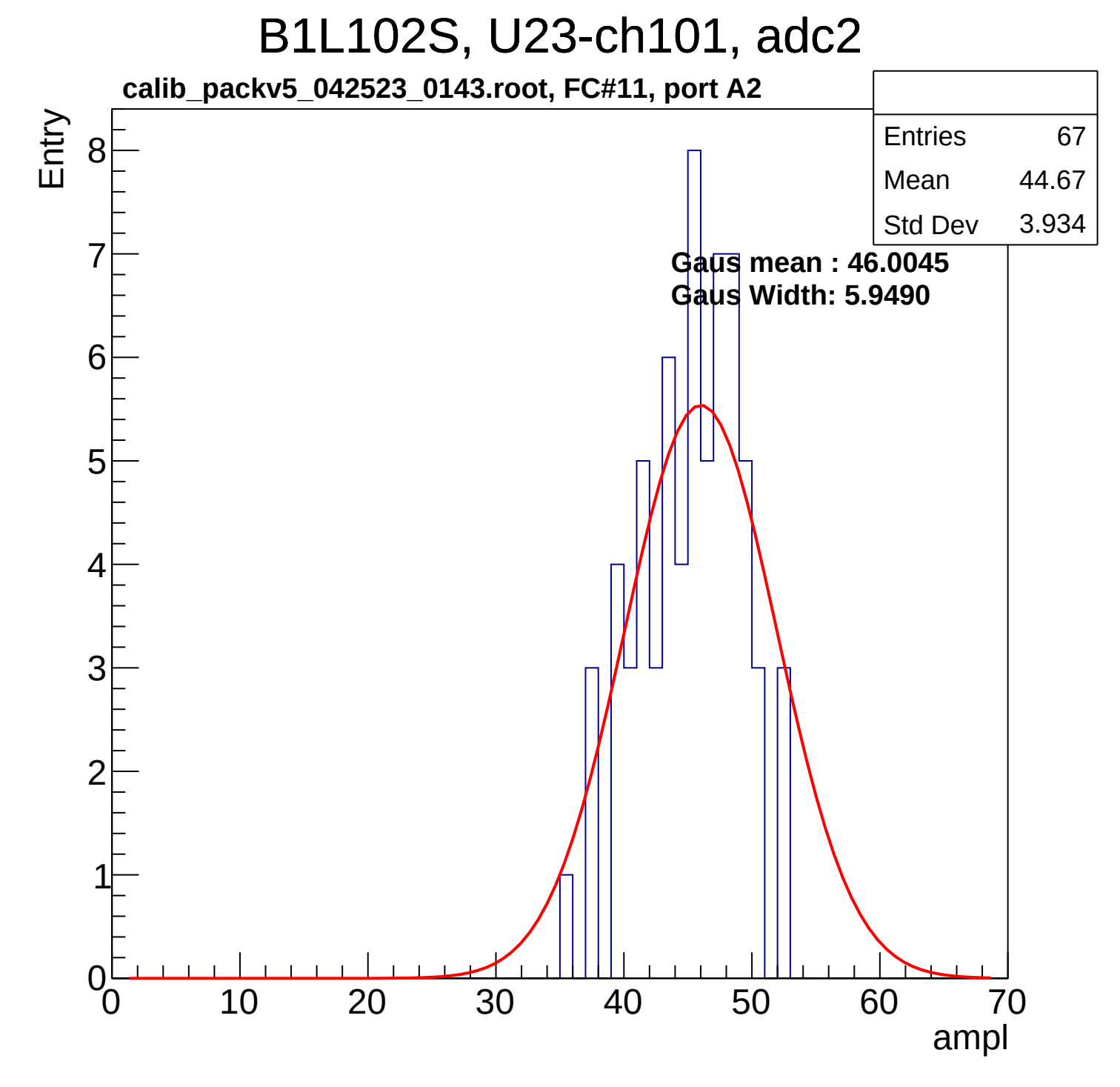

B1L102S, U23-ch100, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 58.46
Std Dev 10.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

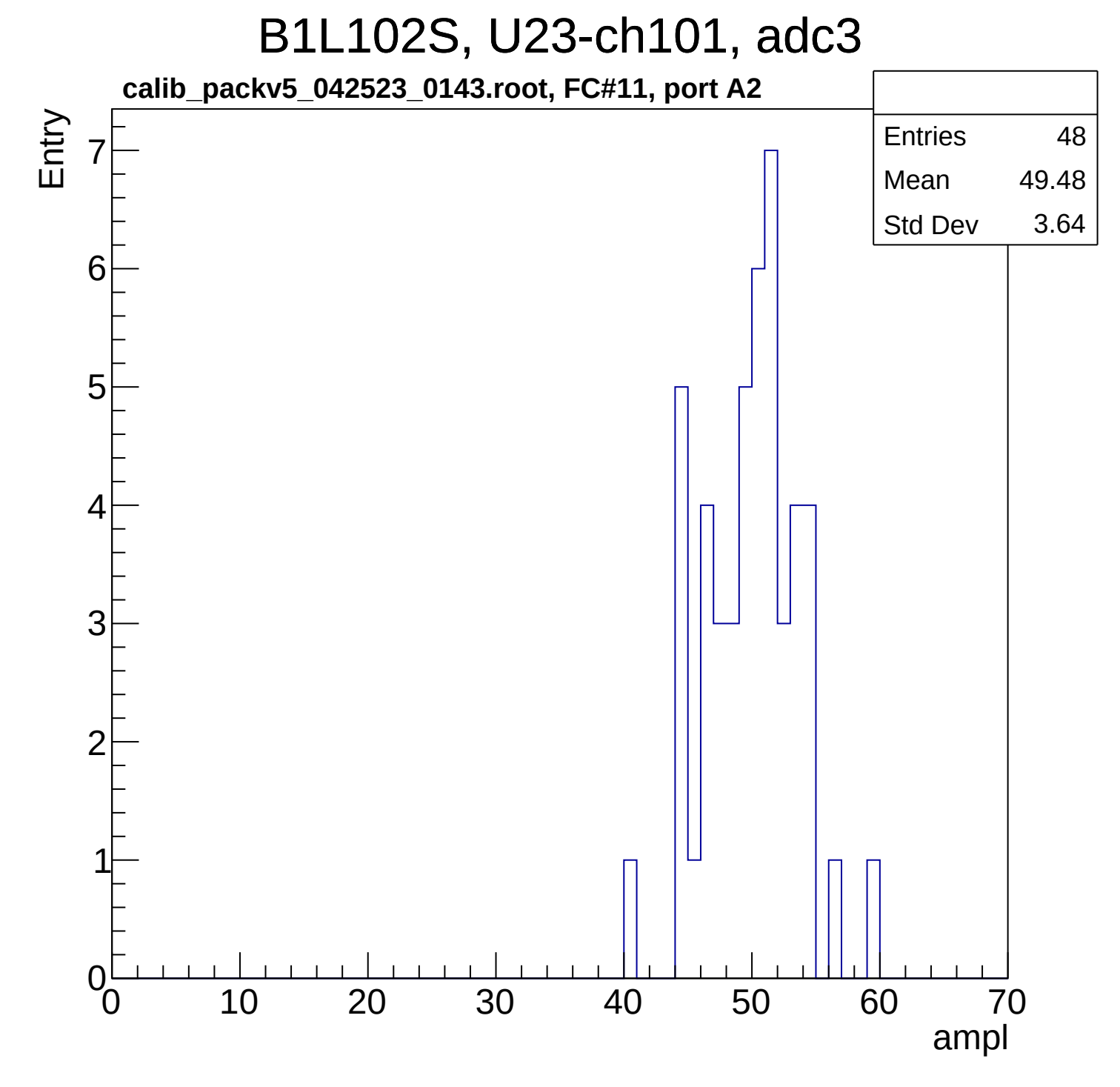

B1L102S, U23-ch100, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62
Std Dev 1.333
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

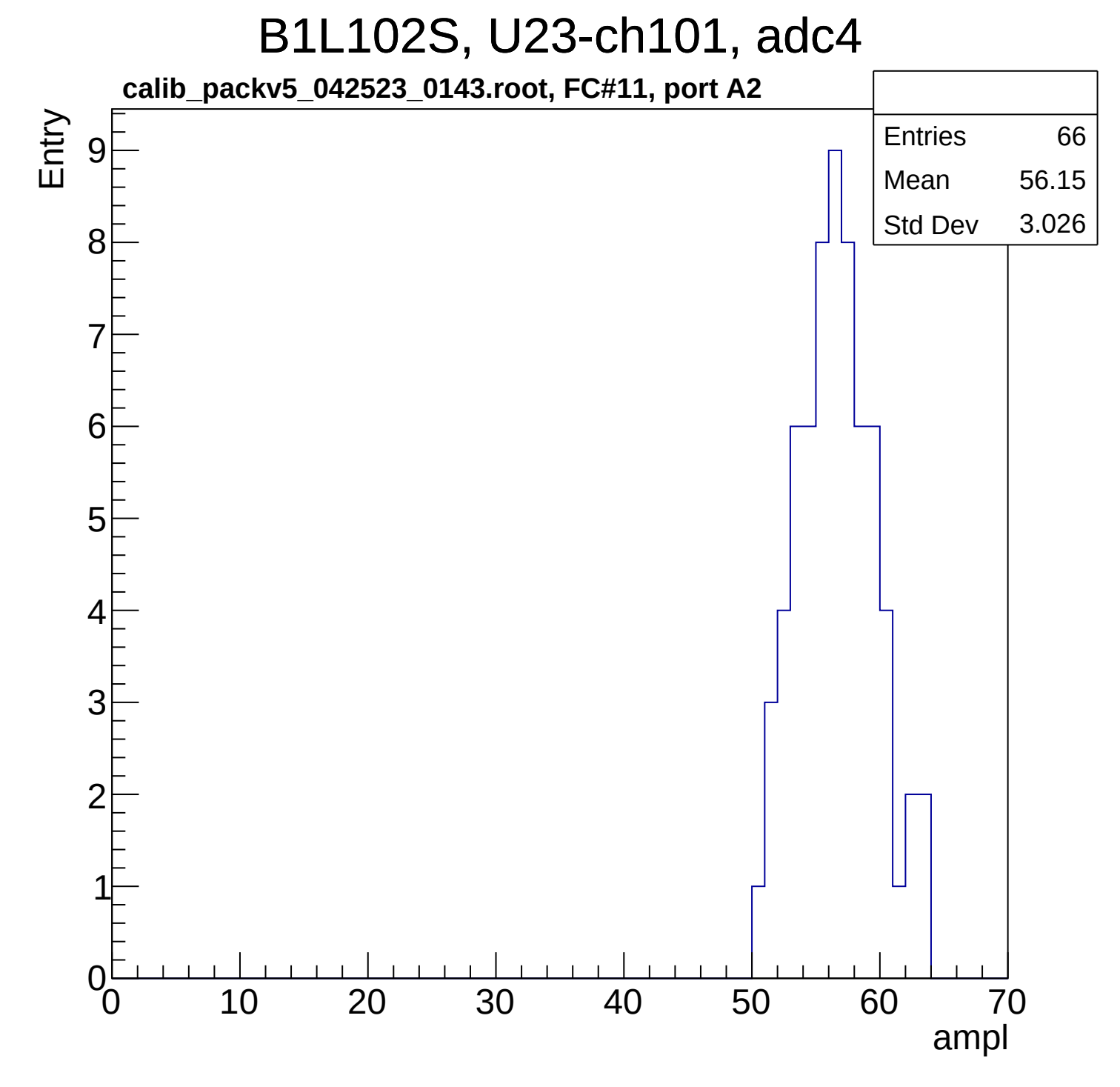

B1L102S, U23-ch100, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

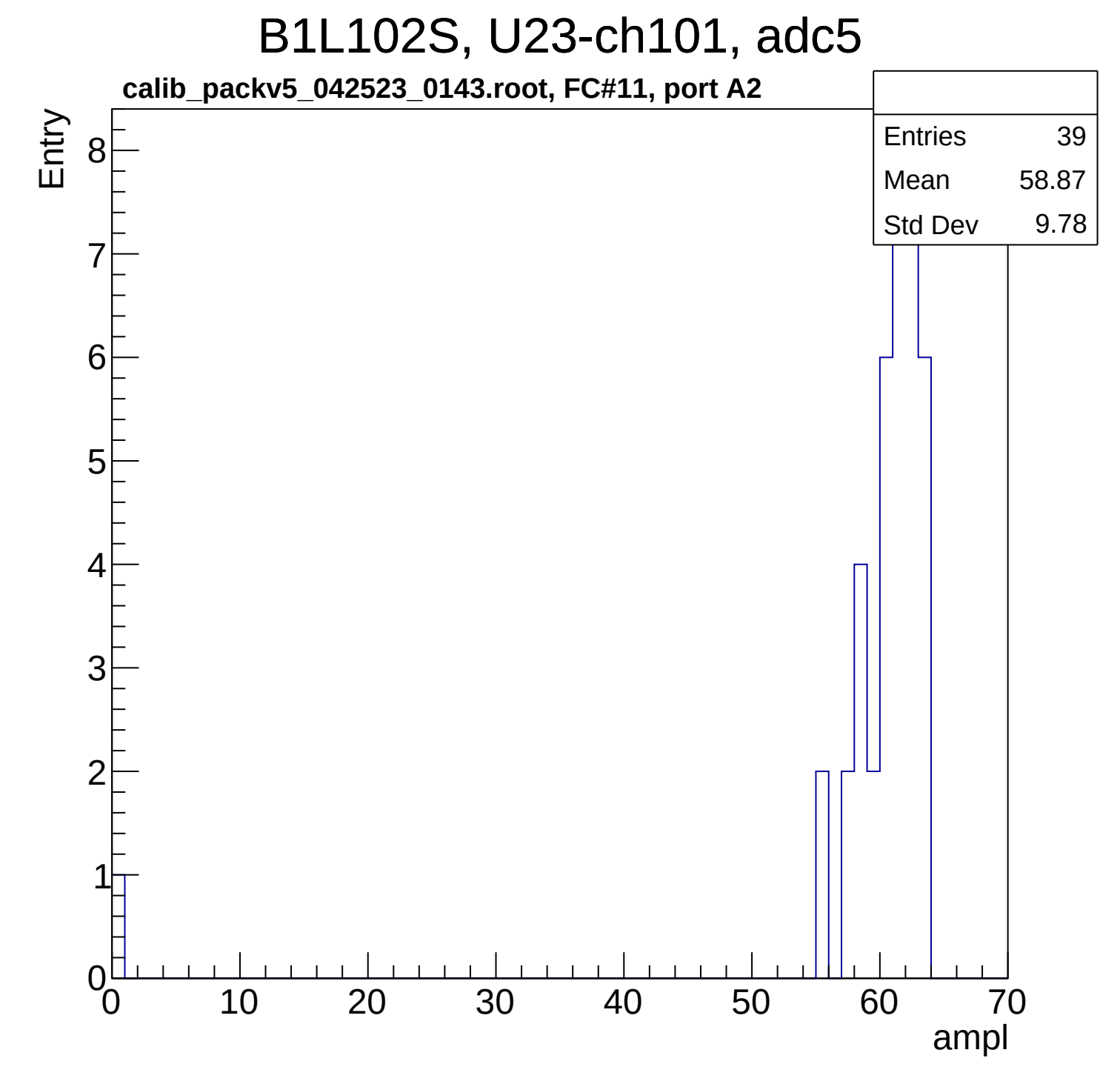

B1L102S, U23-ch101, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 28.41
Std Dev 5.743
Gaus mean : 30.3521
Gaus Width: 4.4454
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

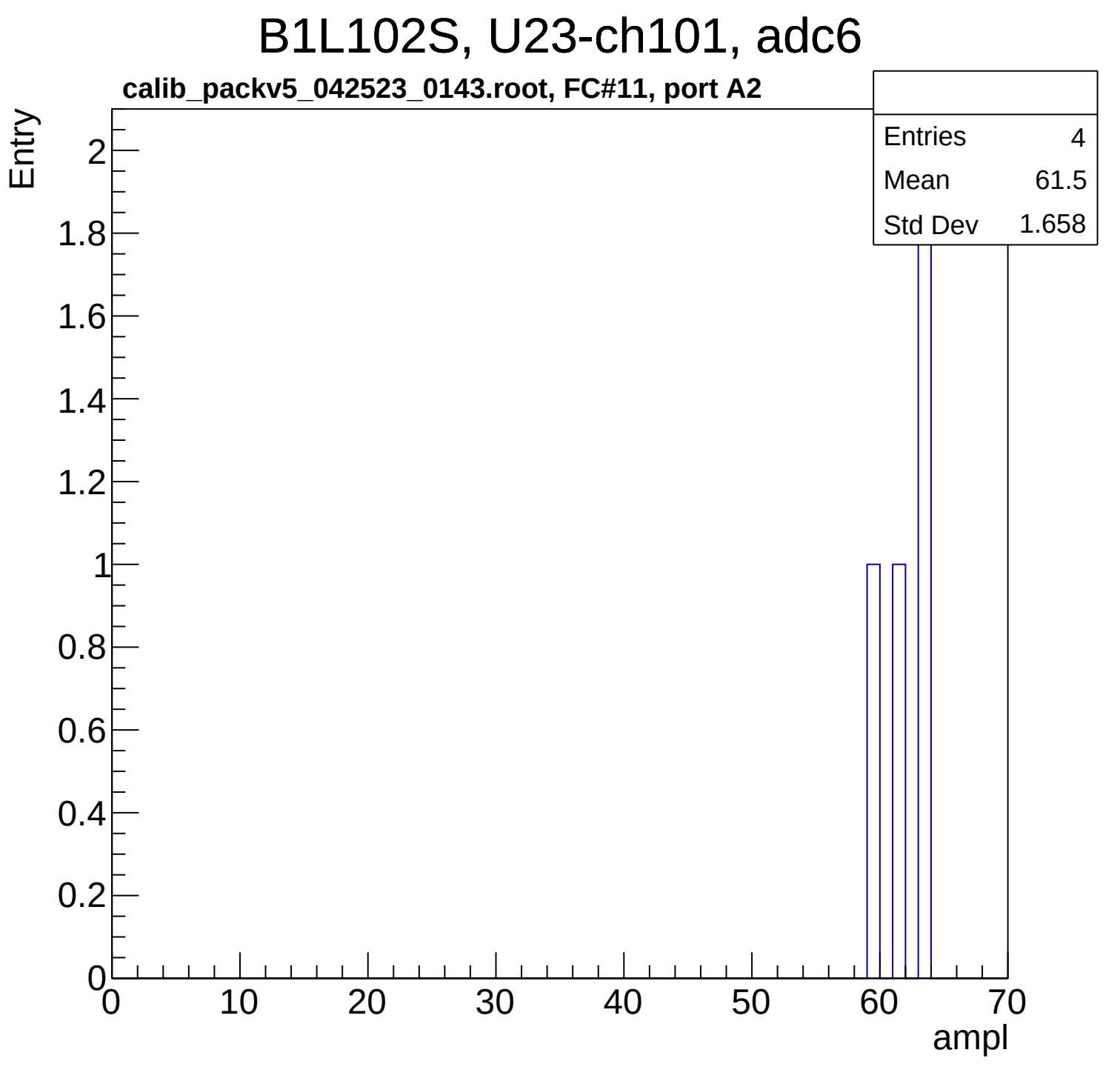

B1L102S, U23-ch101, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 37.2
Std Dev 4.047
Gaus mean : 37.8007
Gaus Width: 4.0483
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

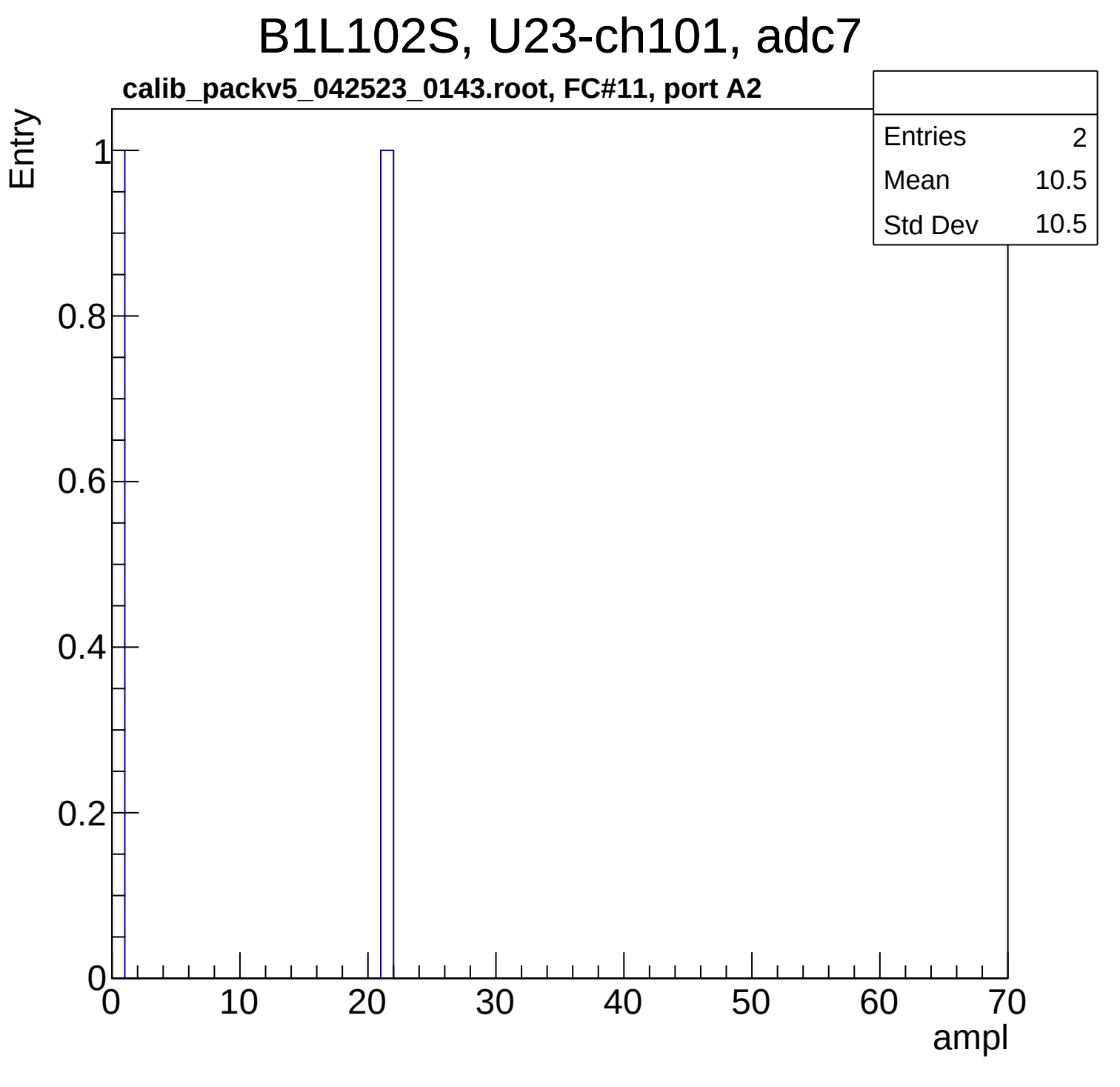

B1L102S, U23-ch101, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 44.67
Std Dev 3.934
Gaus mean : 46.0045
Gaus Width: 5.9490
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

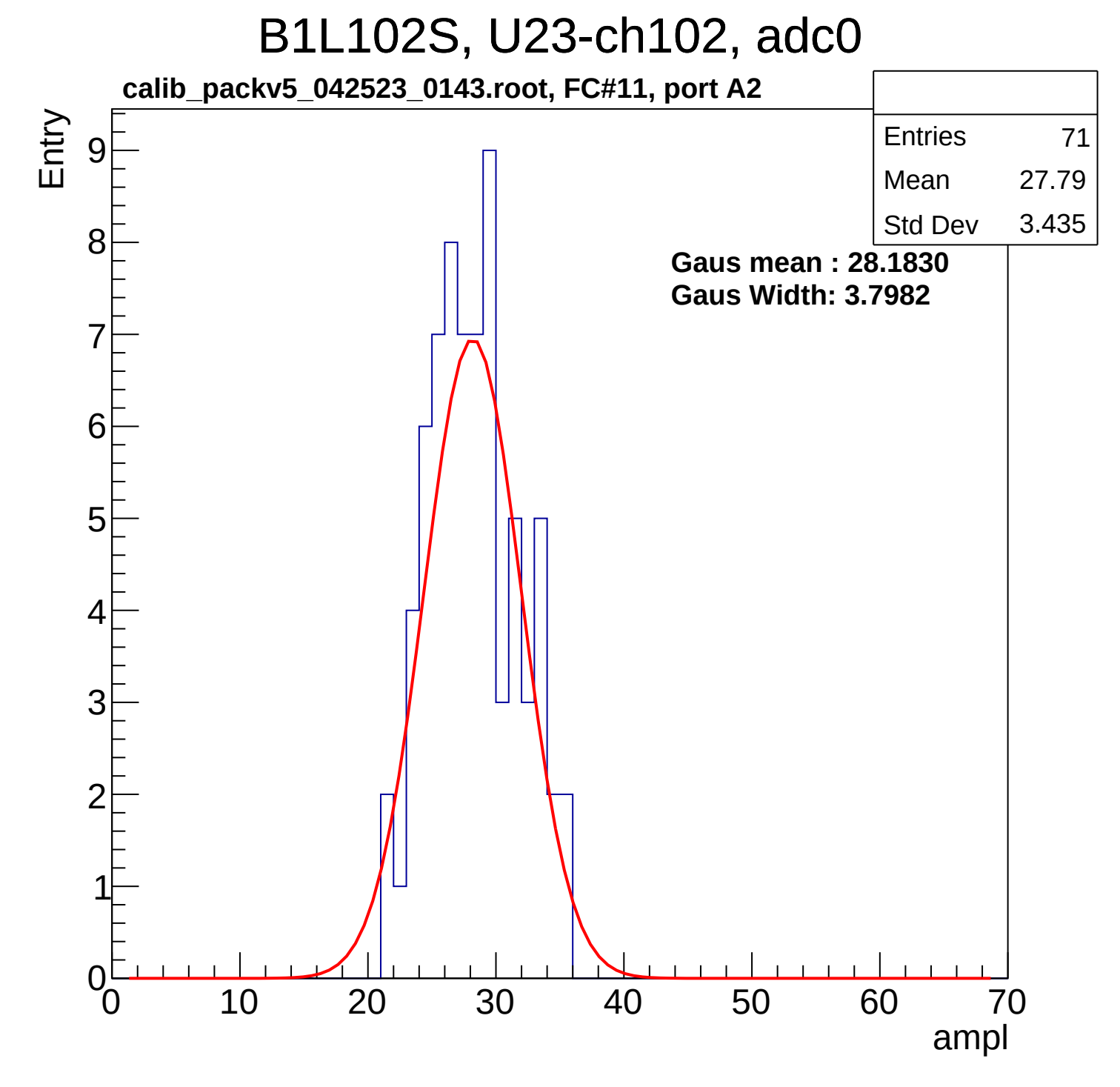

B1L102S, U23-ch101, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 49.48
Std Dev 3.64
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

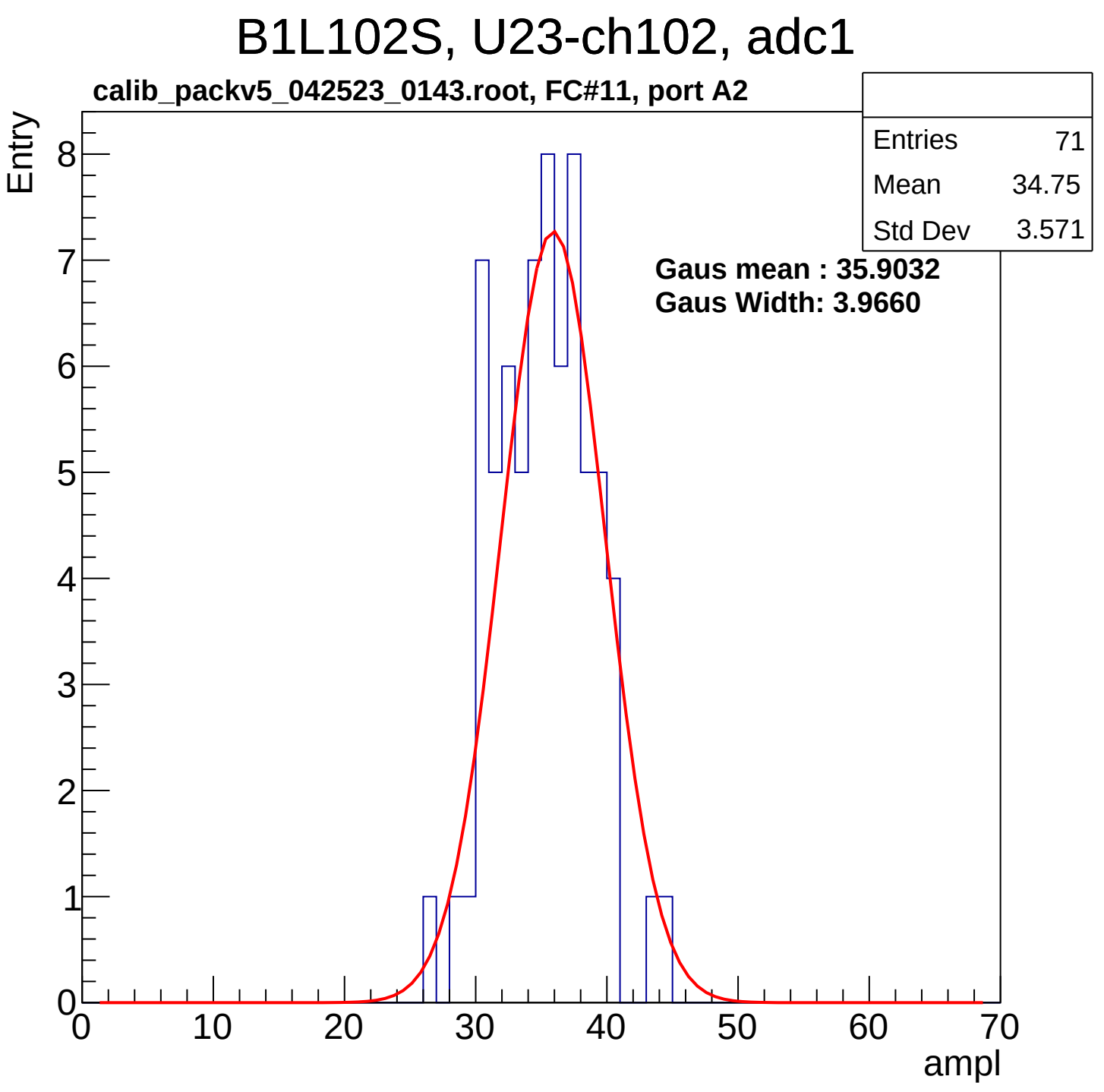

B1L102S, U23-ch101, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 56.15
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

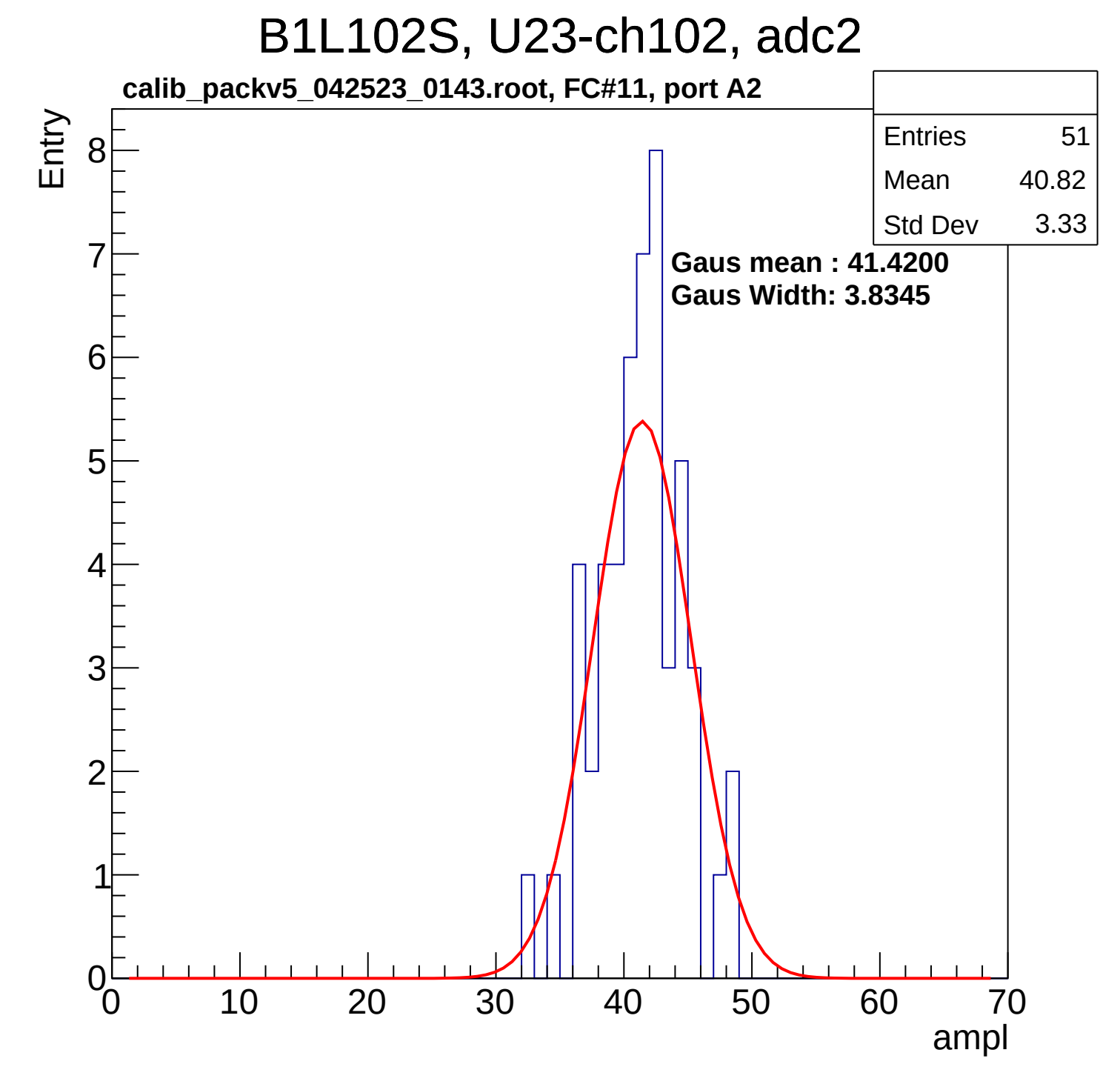

B1L102S, U23-ch101, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 58.87
Std Dev 9.78
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

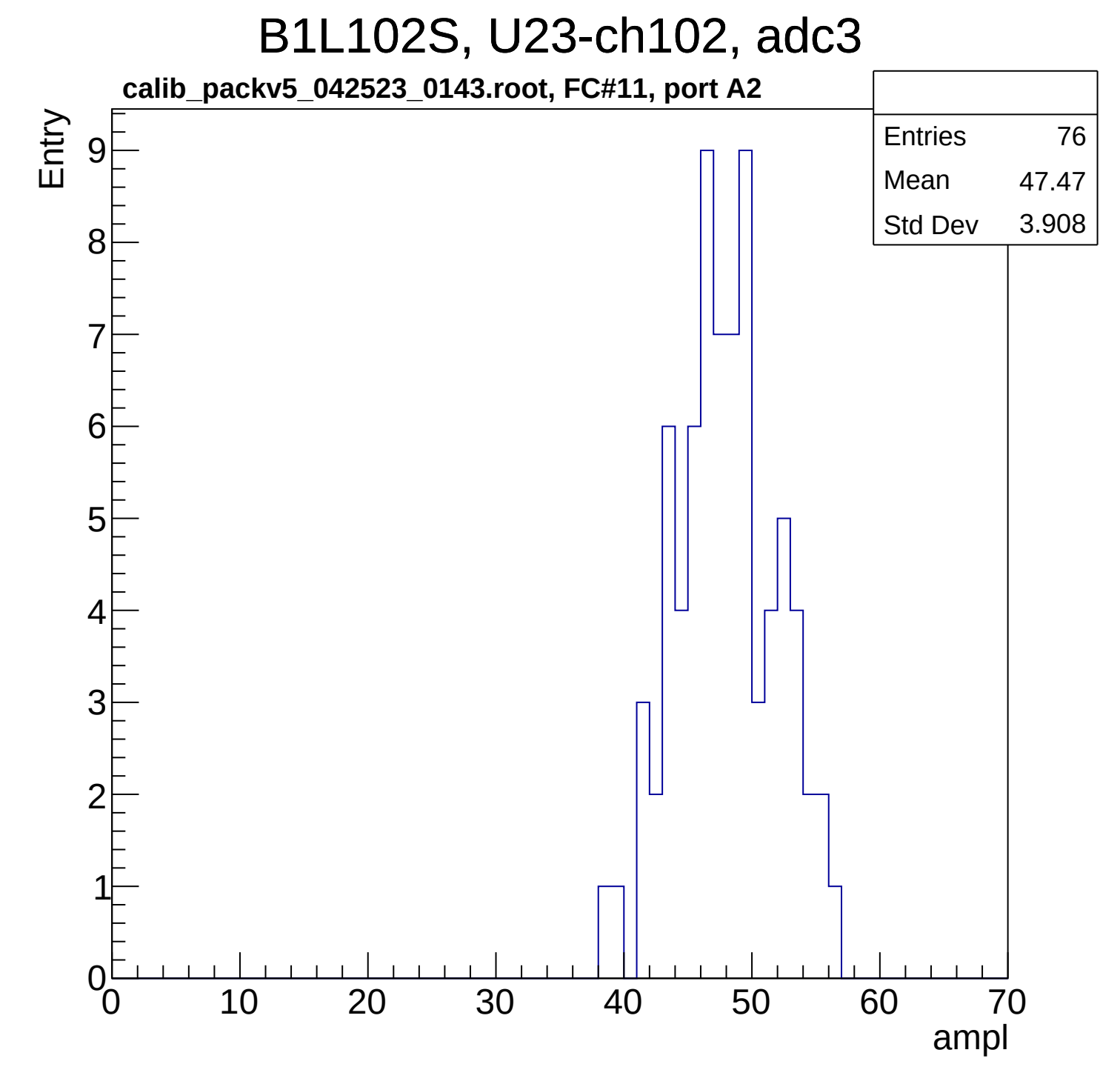

B1L102S, U23-ch101, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

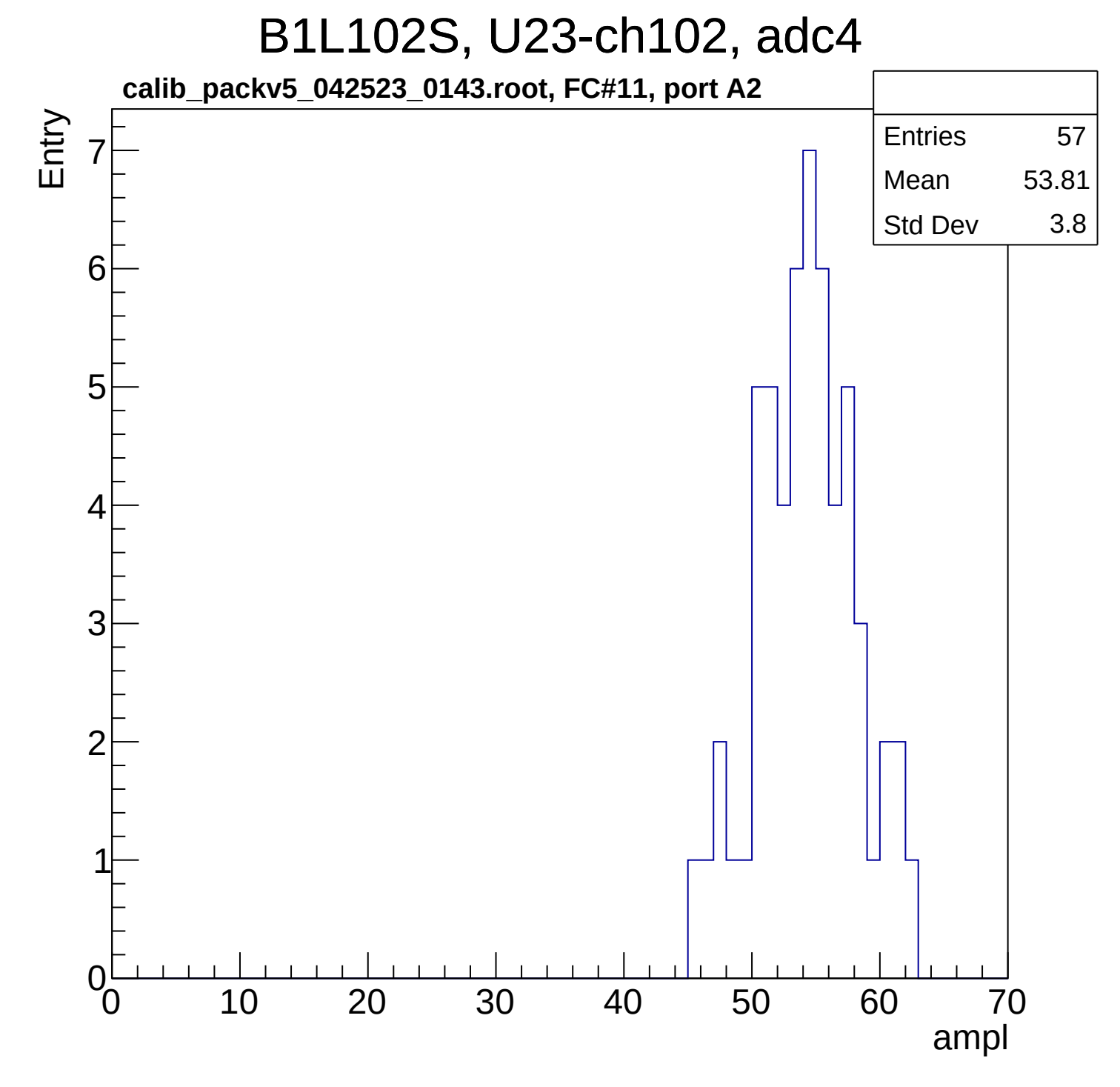

B1L102S, U23-ch101, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

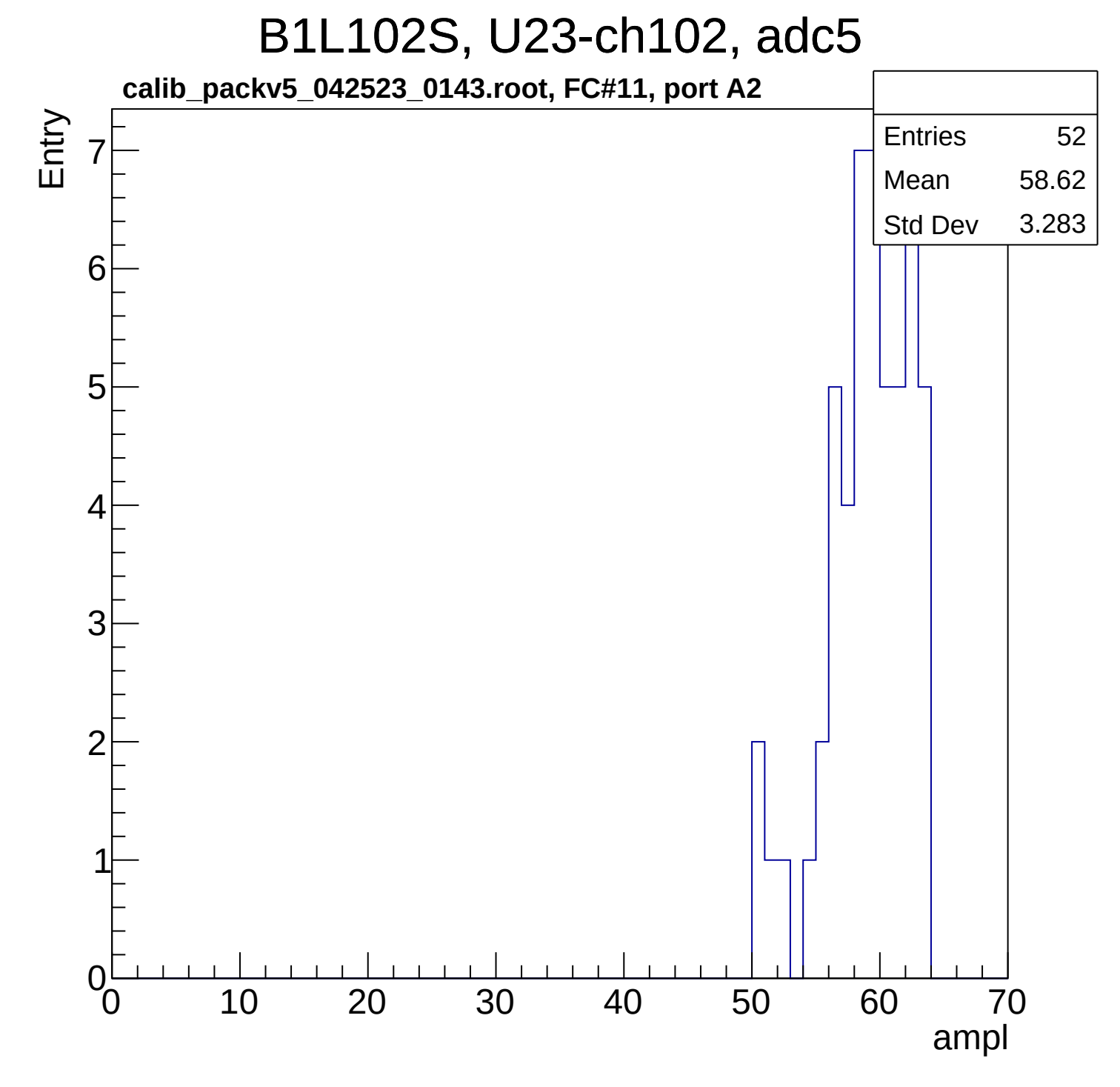

B1L102S, U23-ch102, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.79
Std Dev 3.435
Gaus mean : 28.1830
Gaus Width: 3.7982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

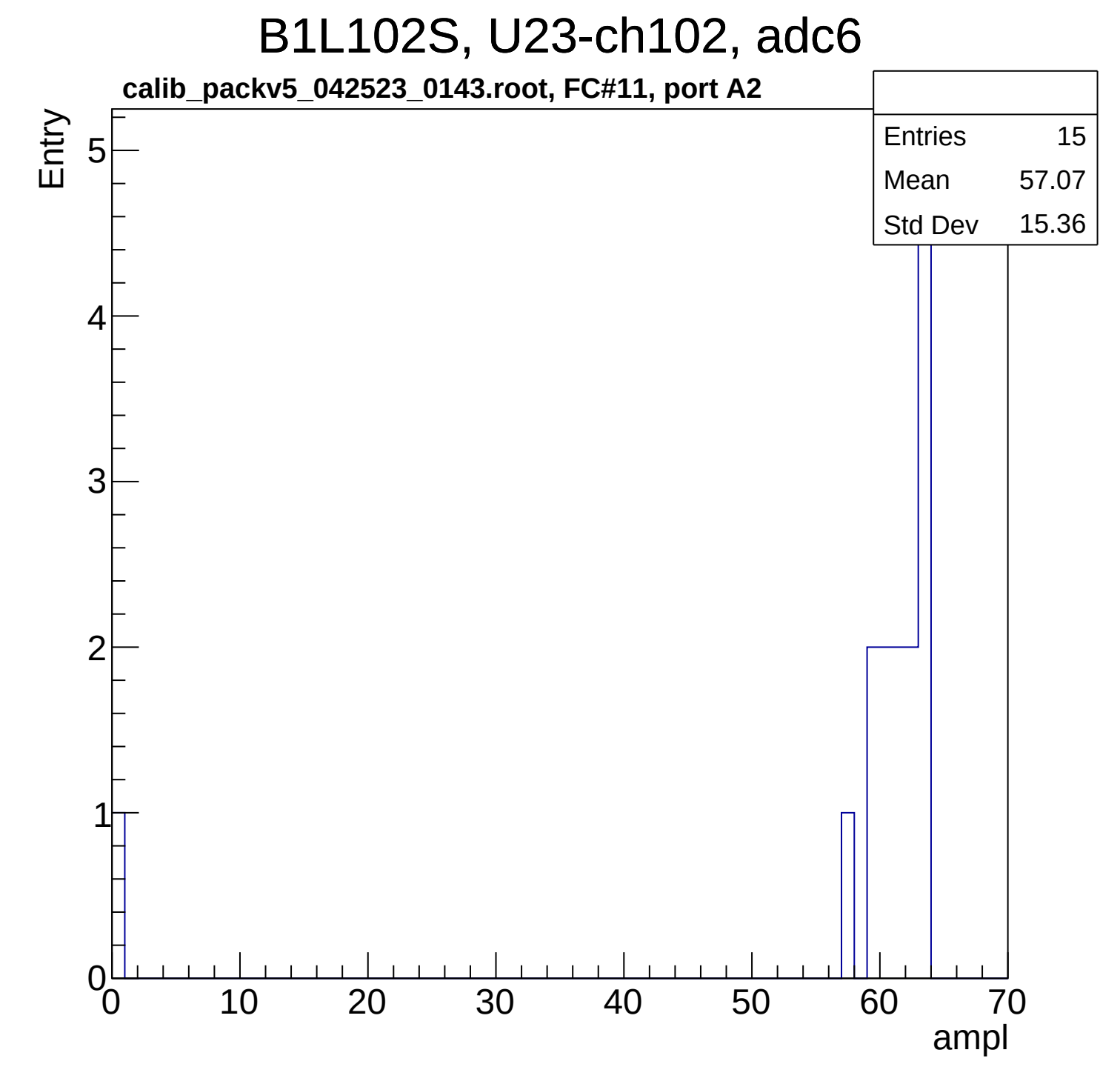

B1L102S, U23-ch102, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 34.75
Std Dev 3.571
Gaus mean : 35.9032
Gaus Width: 3.9660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

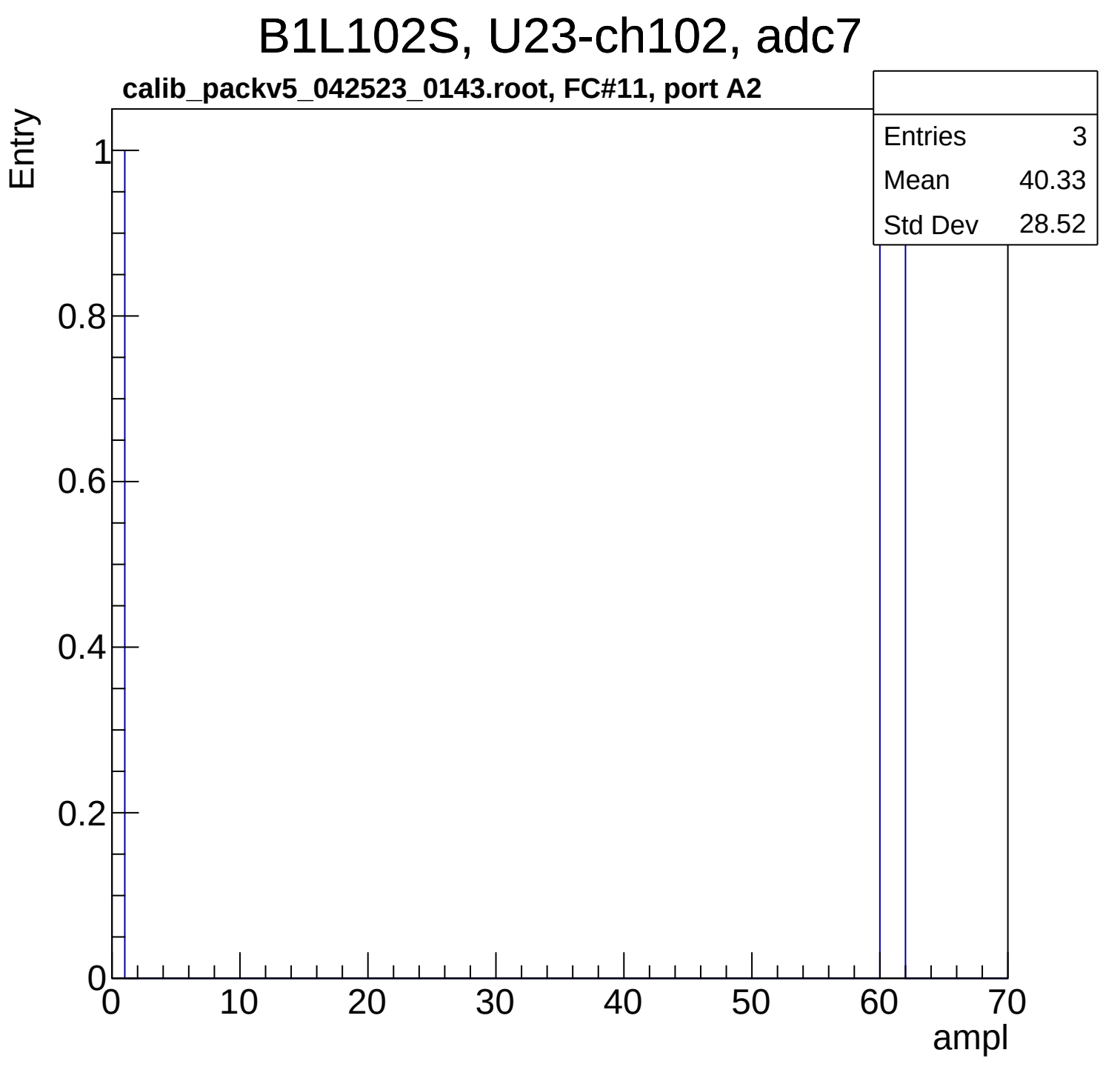

B1L102S, U23-ch102, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 40.82
Std Dev 3.33
Gaus mean : 41.4200
Gaus Width: 3.8345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

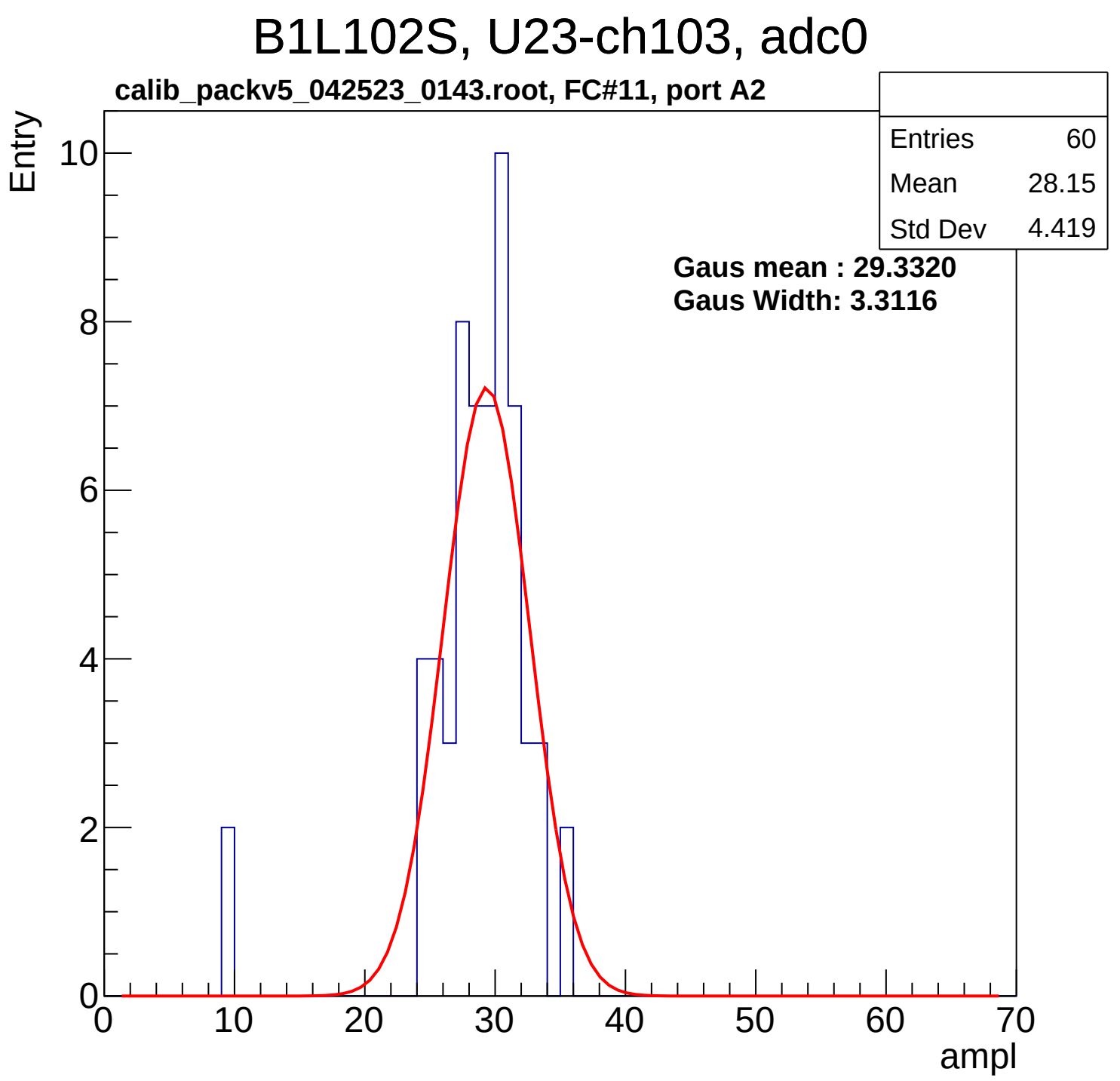

B1L102S, U23-ch102, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 47.47
Std Dev 3.908
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

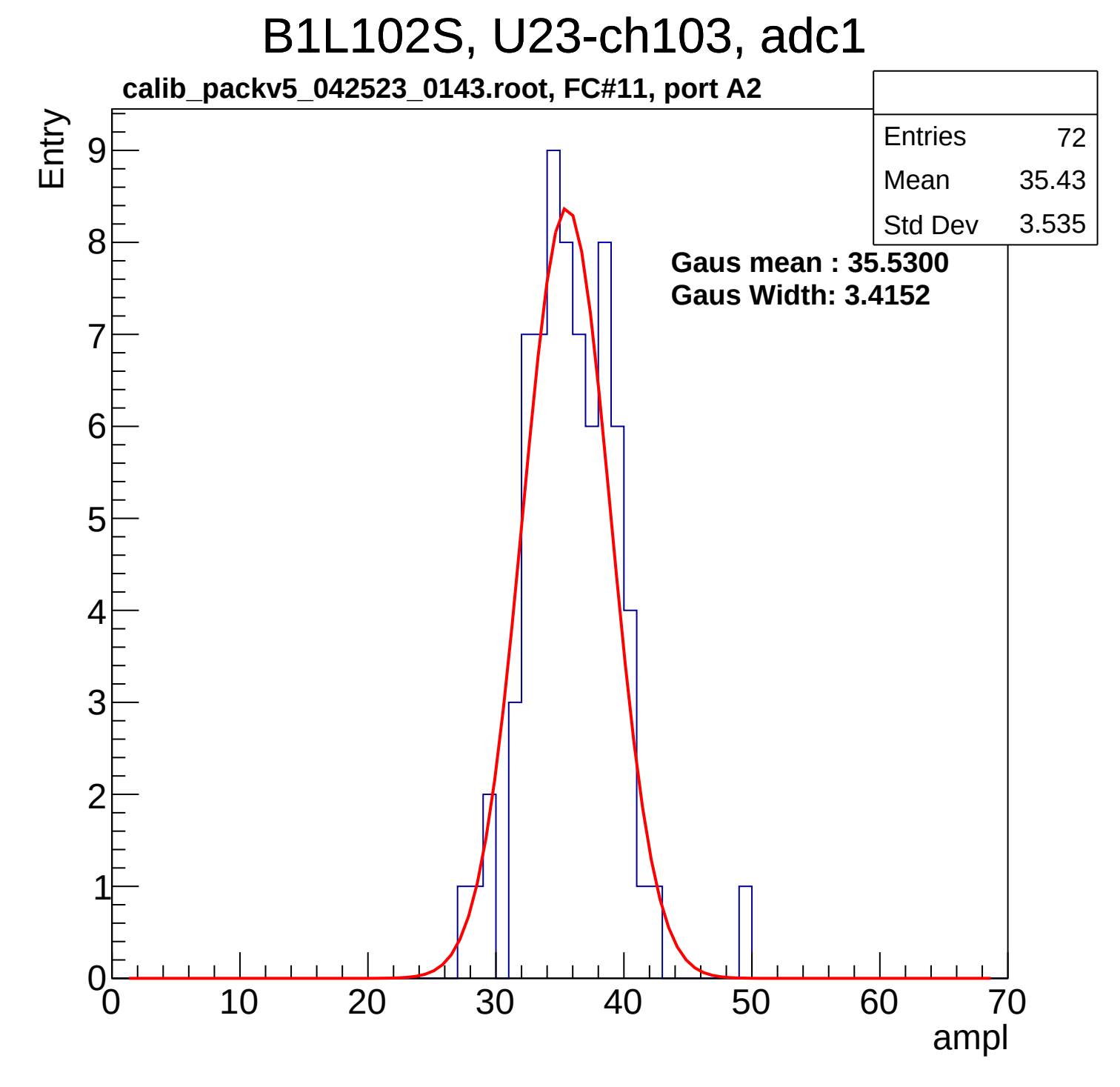

B1L102S, U23-ch102, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 53.81
Std Dev 3.8
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

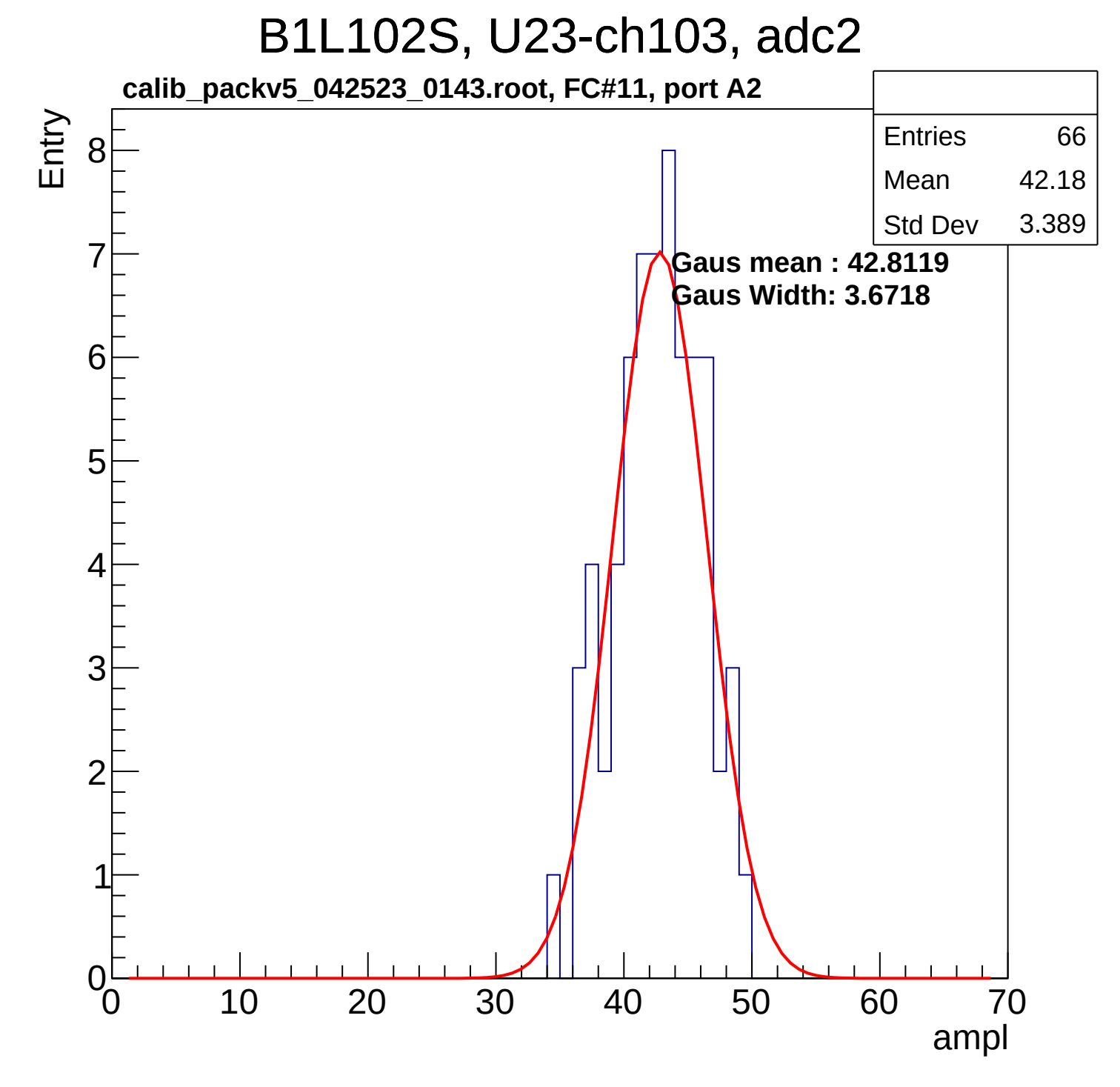

B1L102S, U23-ch102, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.62
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

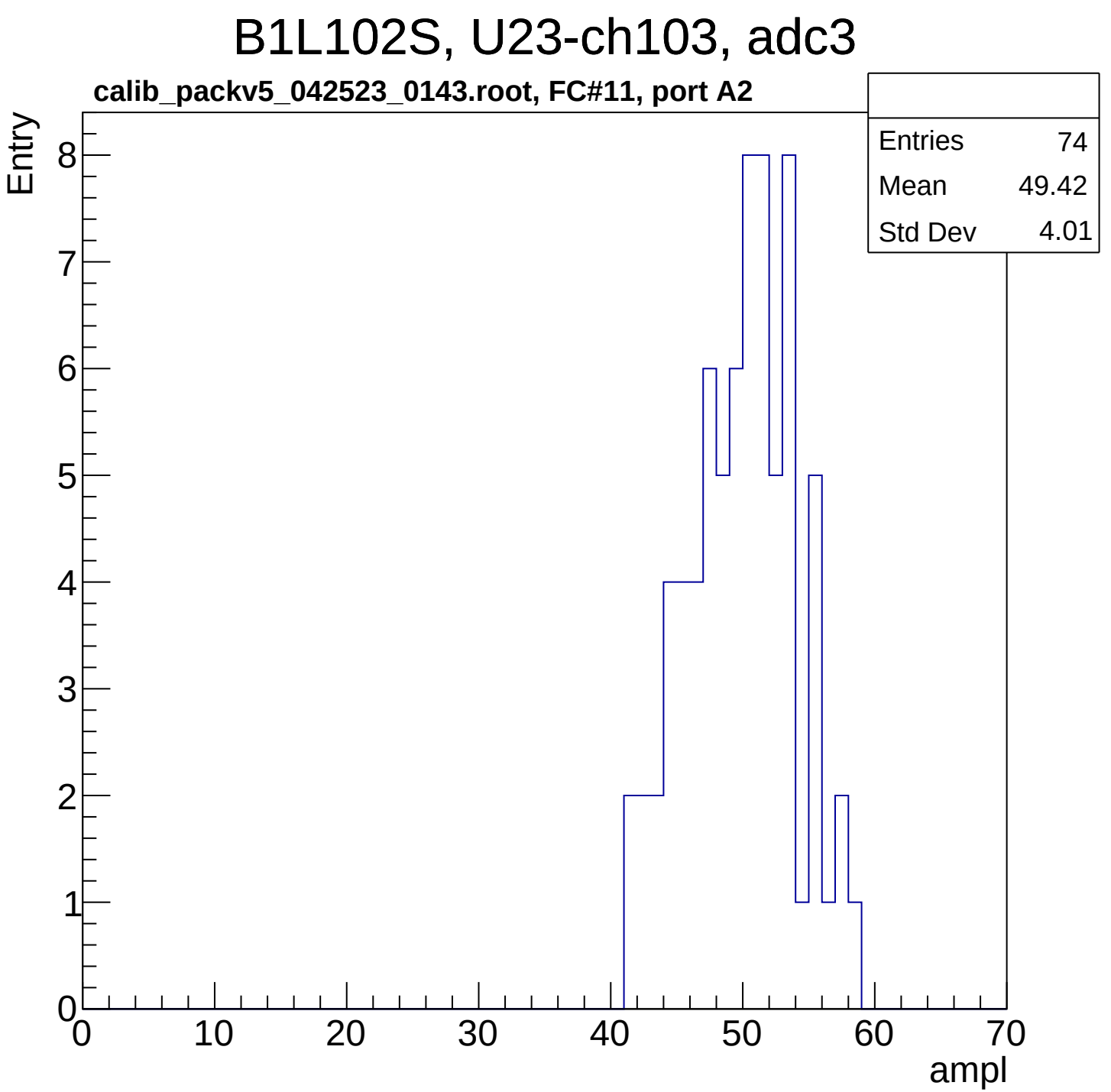

B1L102S, U23-ch102, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 57.07
Std Dev 15.36
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

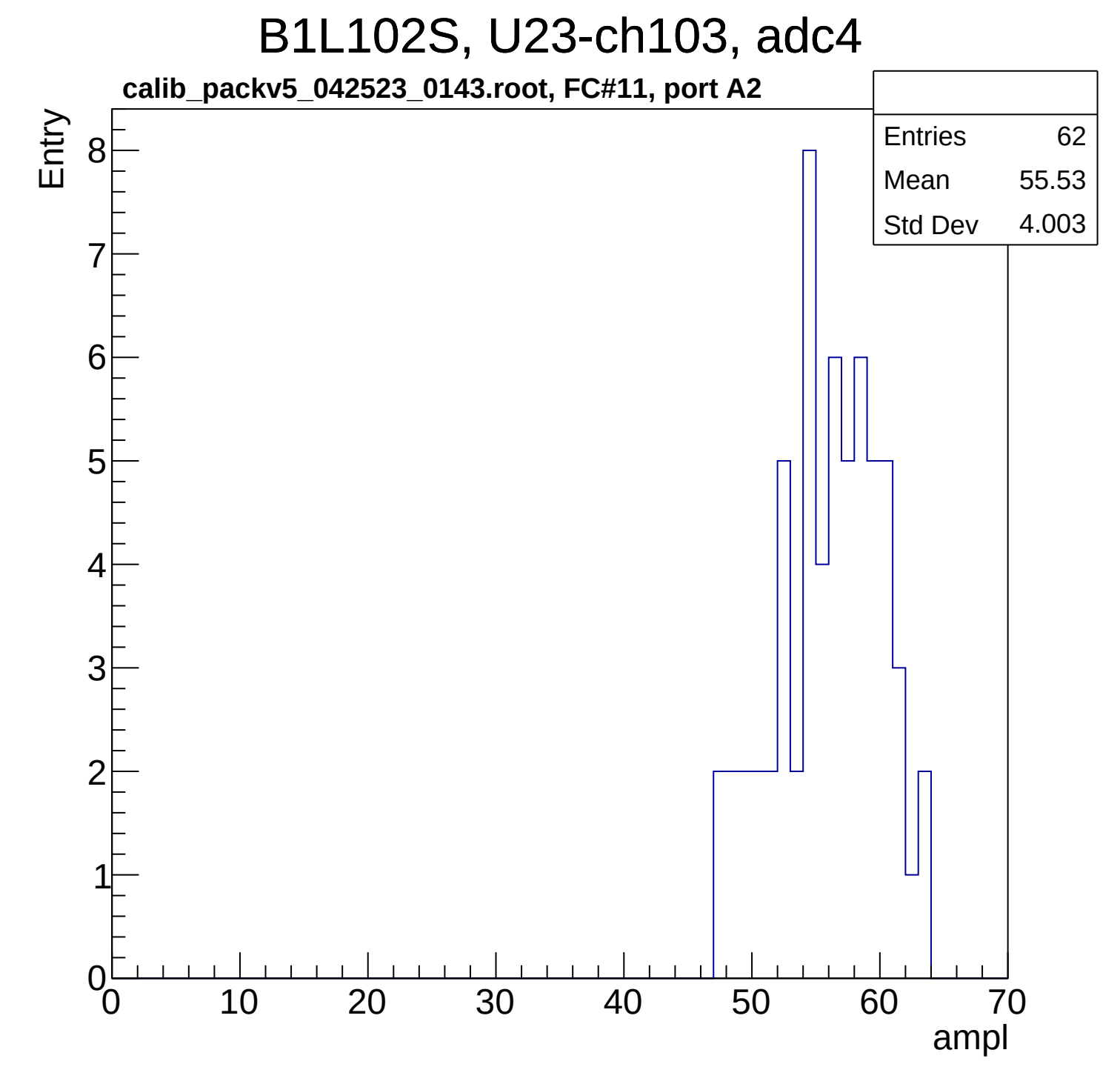

B1L102S, U23-ch102, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 40.33
Std Dev 28.52
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

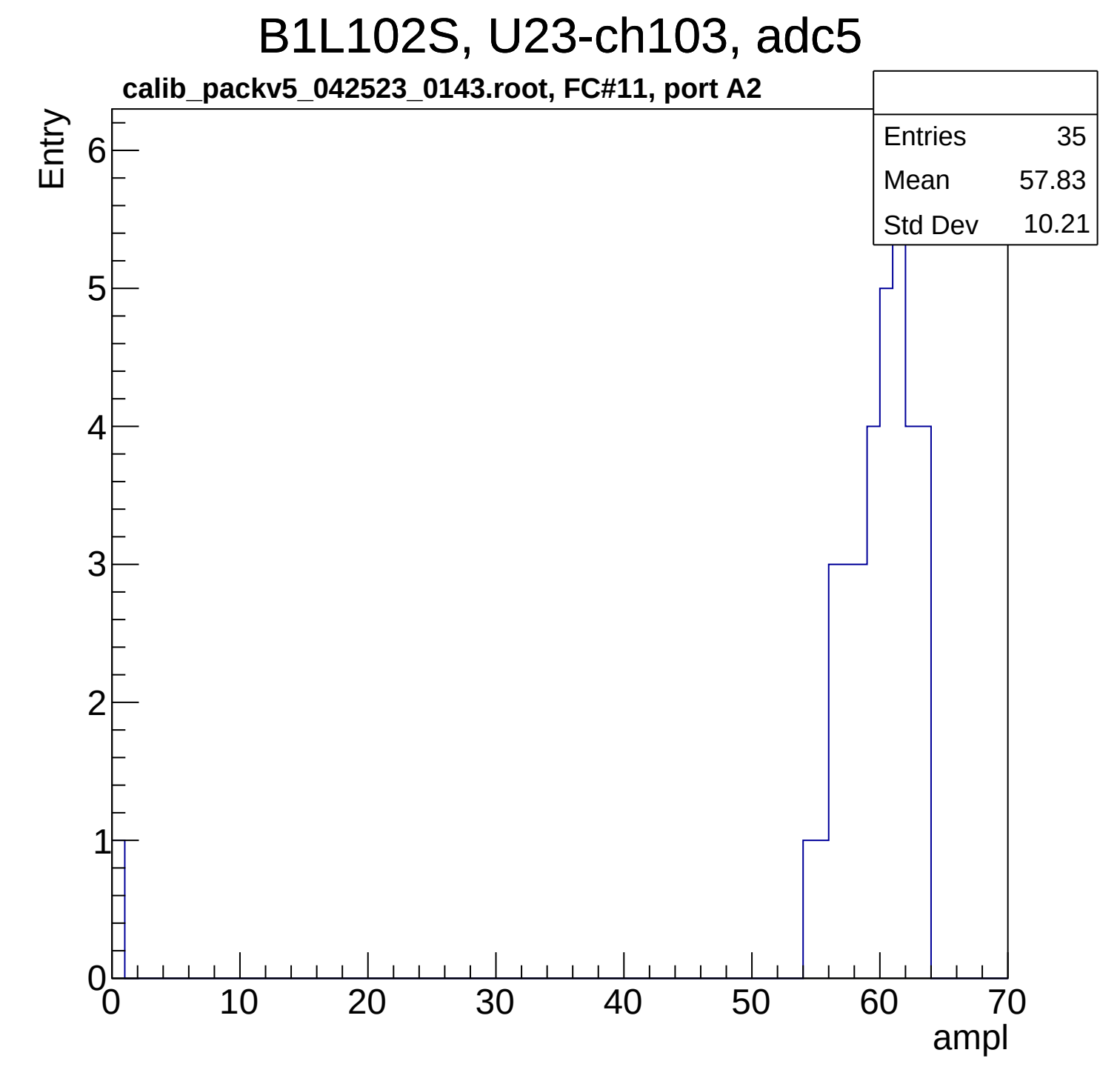

B1L102S, U23-ch103, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 28.15
Std Dev 4.419
Gaus mean : 29.3320
Gaus Width: 3.3116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

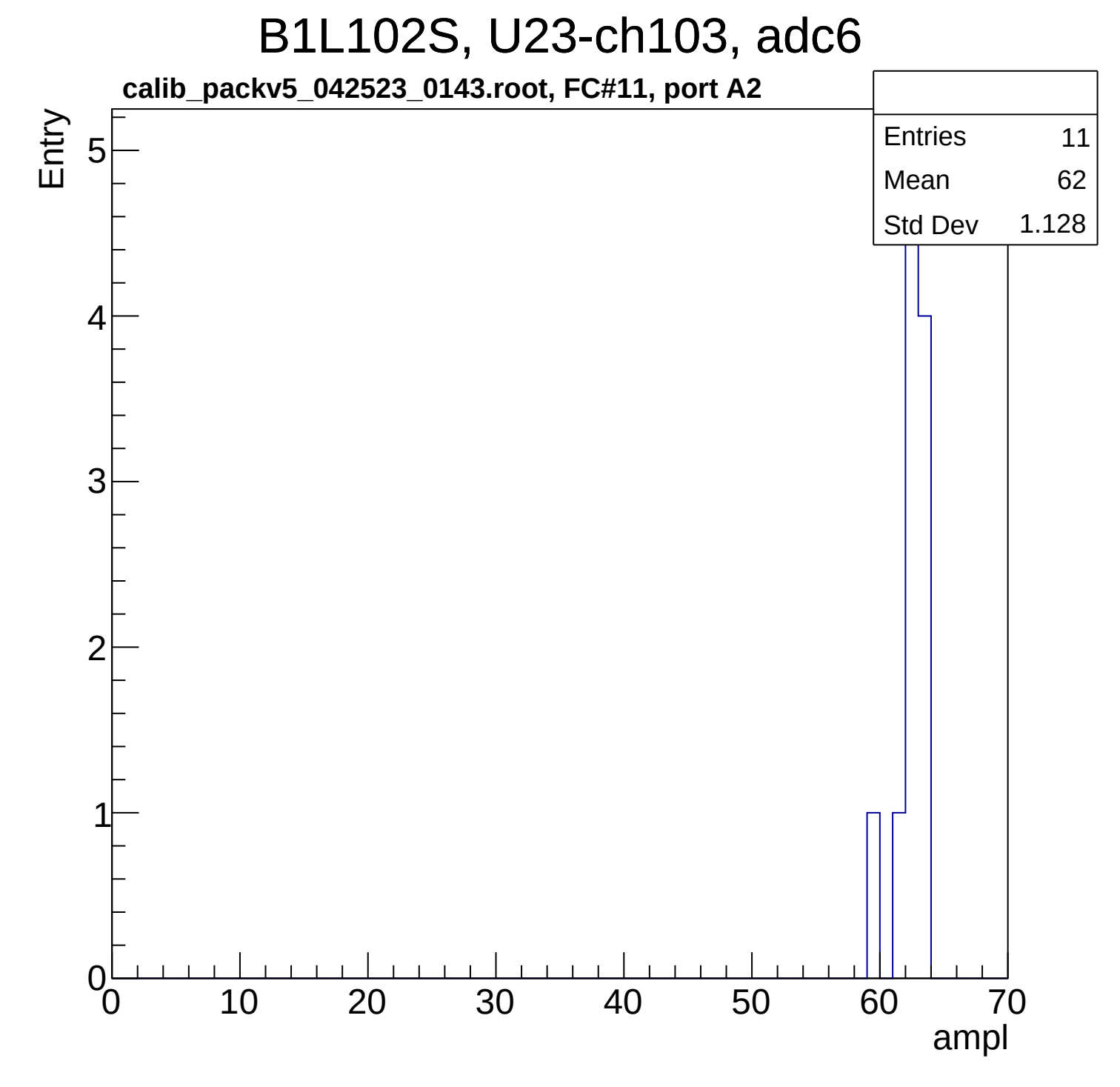

B1L102S, U23-ch103, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 35.43
Std Dev 3.535
Gaus mean : 35.5300
Gaus Width: 3.4152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

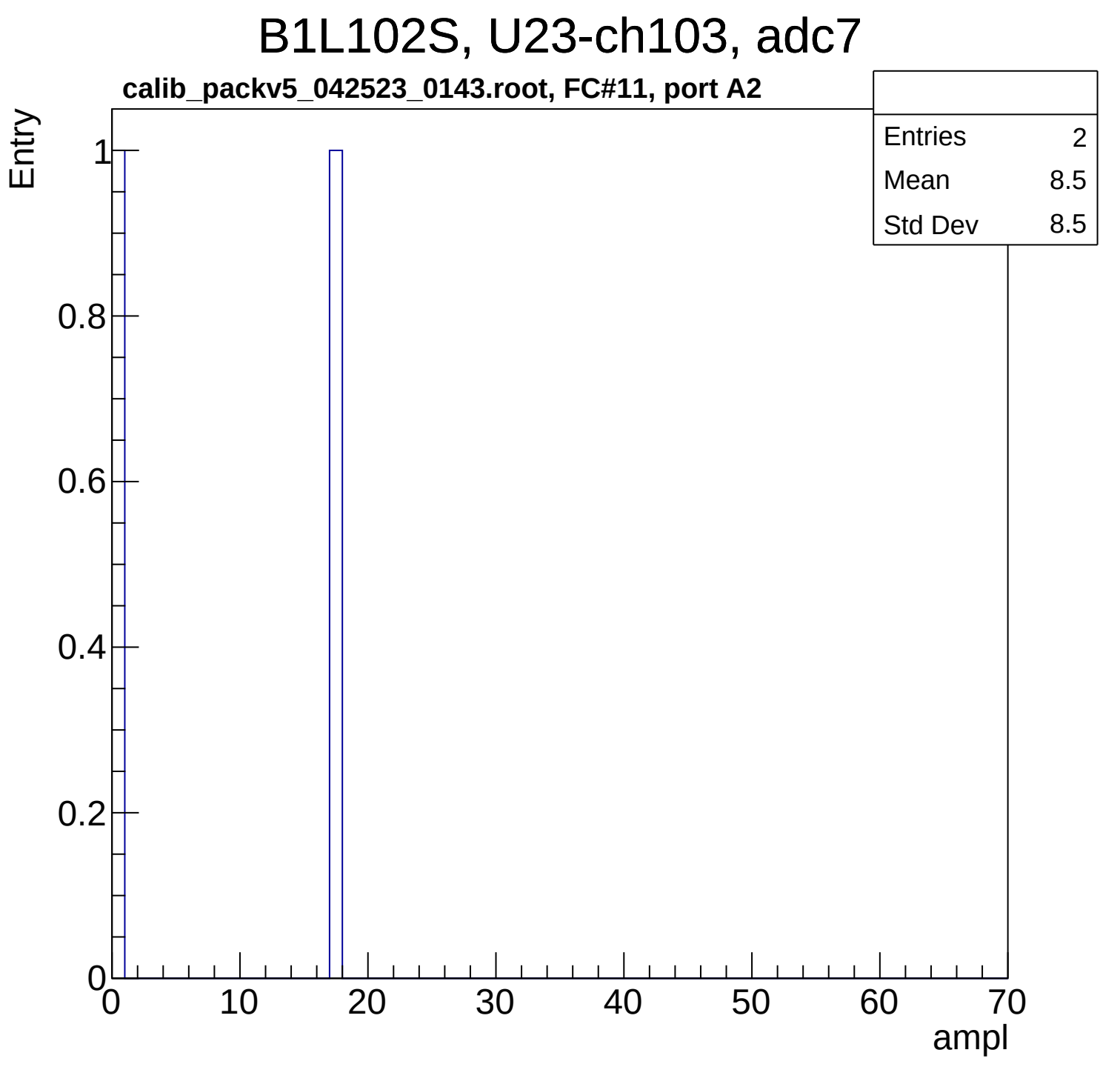

B1L102S, U23-ch103, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 42.18
Std Dev 3.389
Gaus mean : 42.8119
Gaus Width: 3.6718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

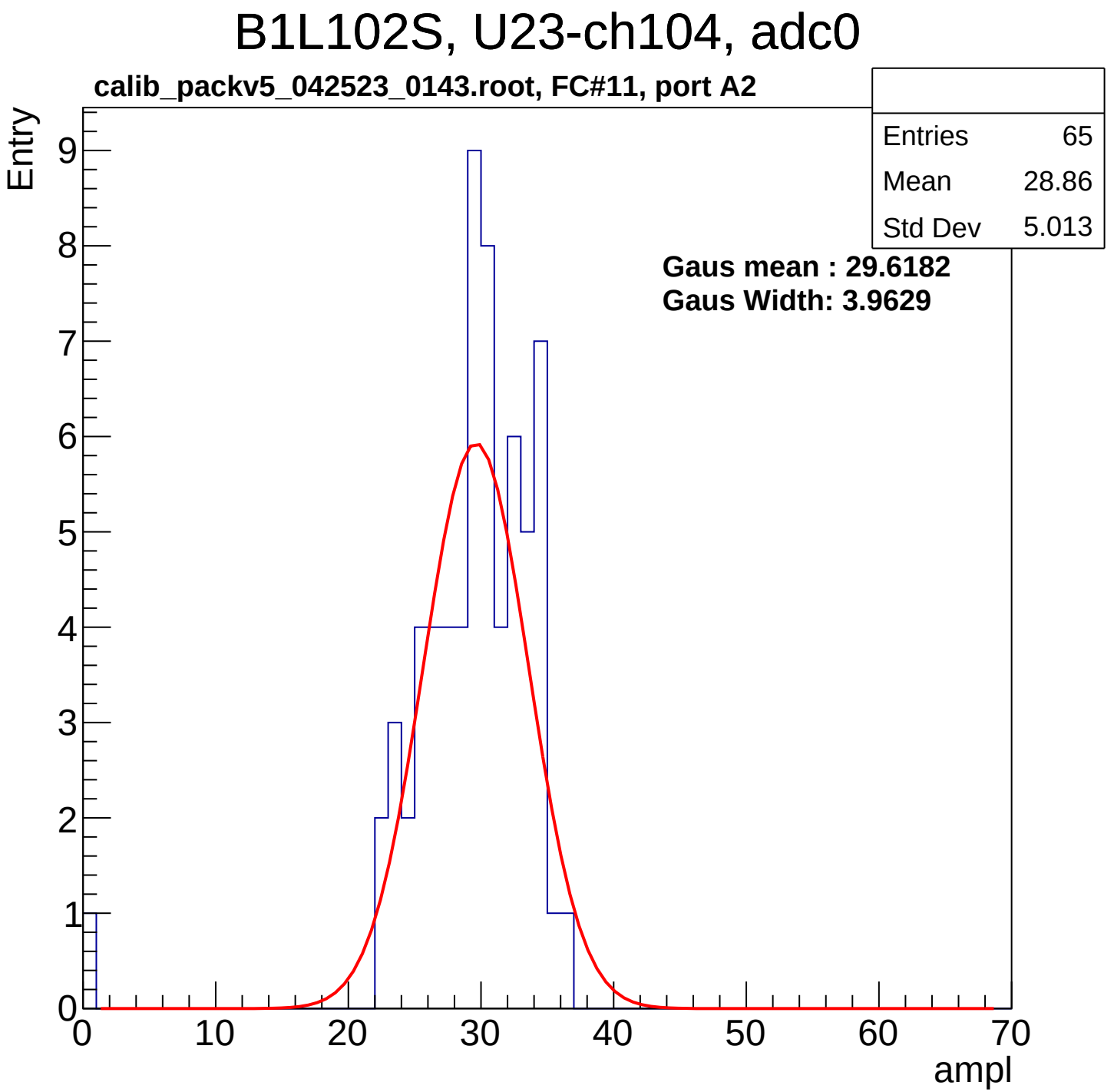

B1L102S, U23-ch103, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 49.42
Std Dev 4.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

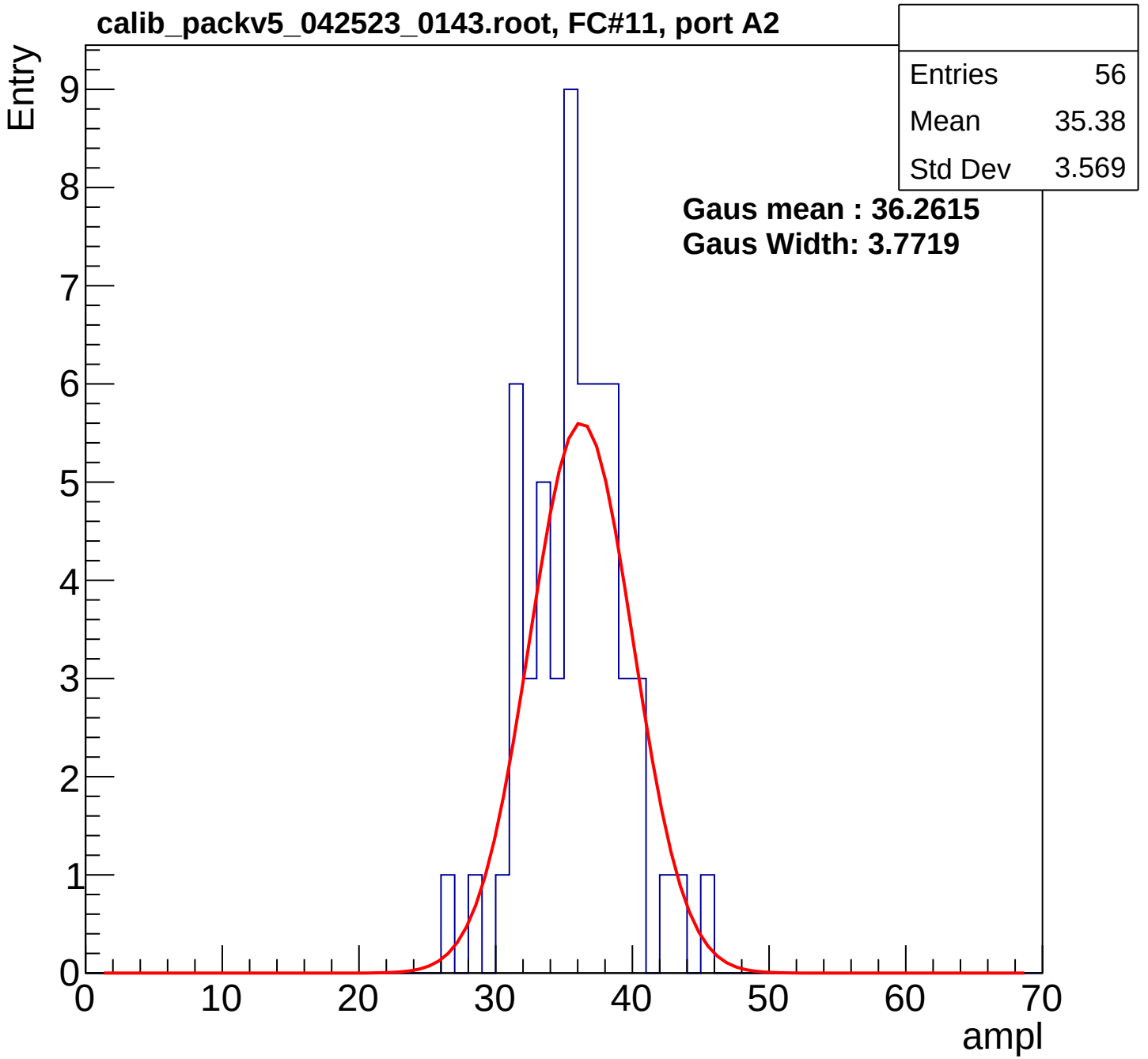

B1L102S, U23-ch103, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 55.53
Std Dev 4.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

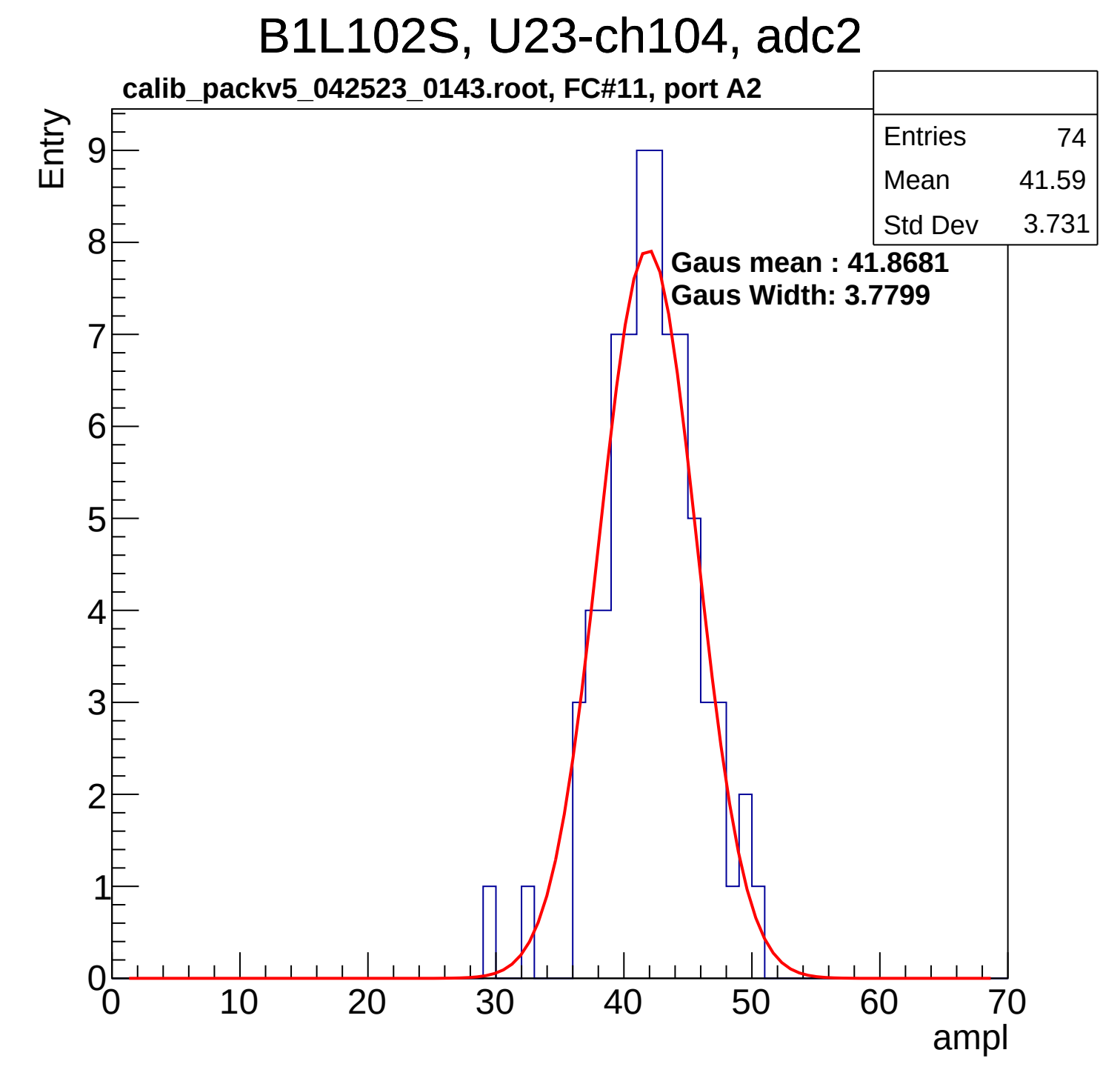

B1L102S, U23-ch103, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 57.83
Std Dev 10.21
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

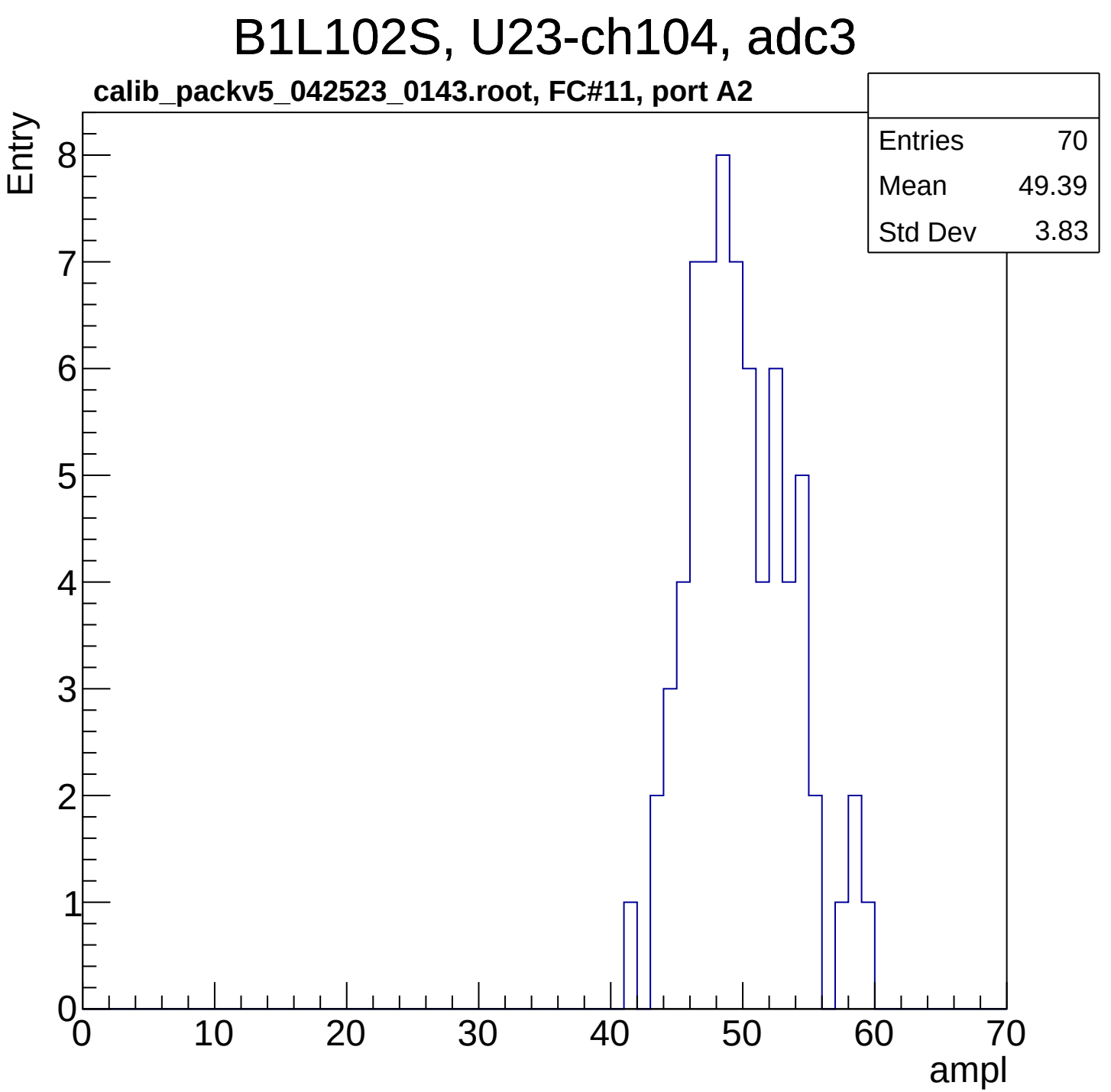

B1L102S, U23-ch103, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 62
Std Dev 1.128
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

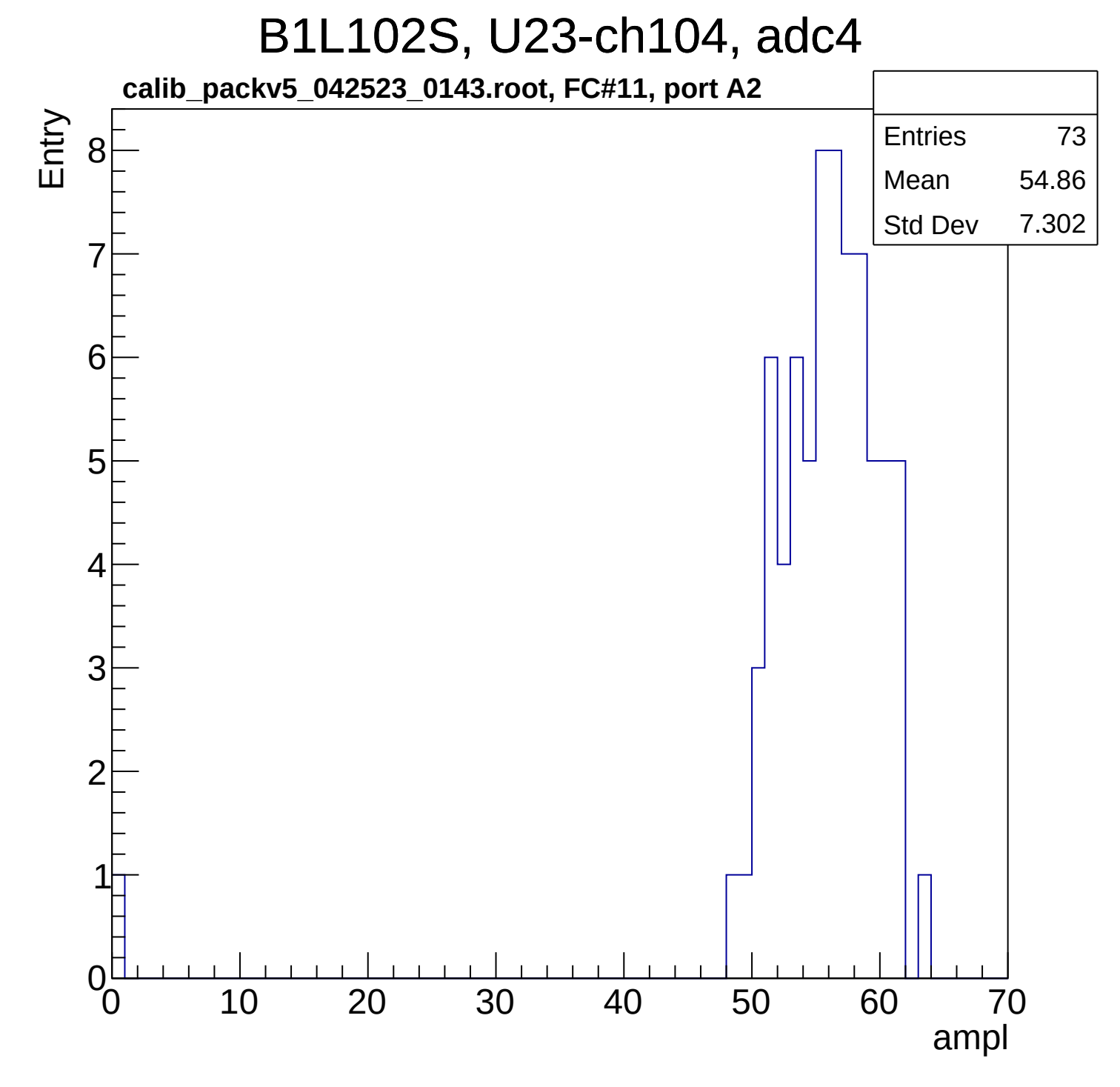

B1L102S, U23-ch103, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

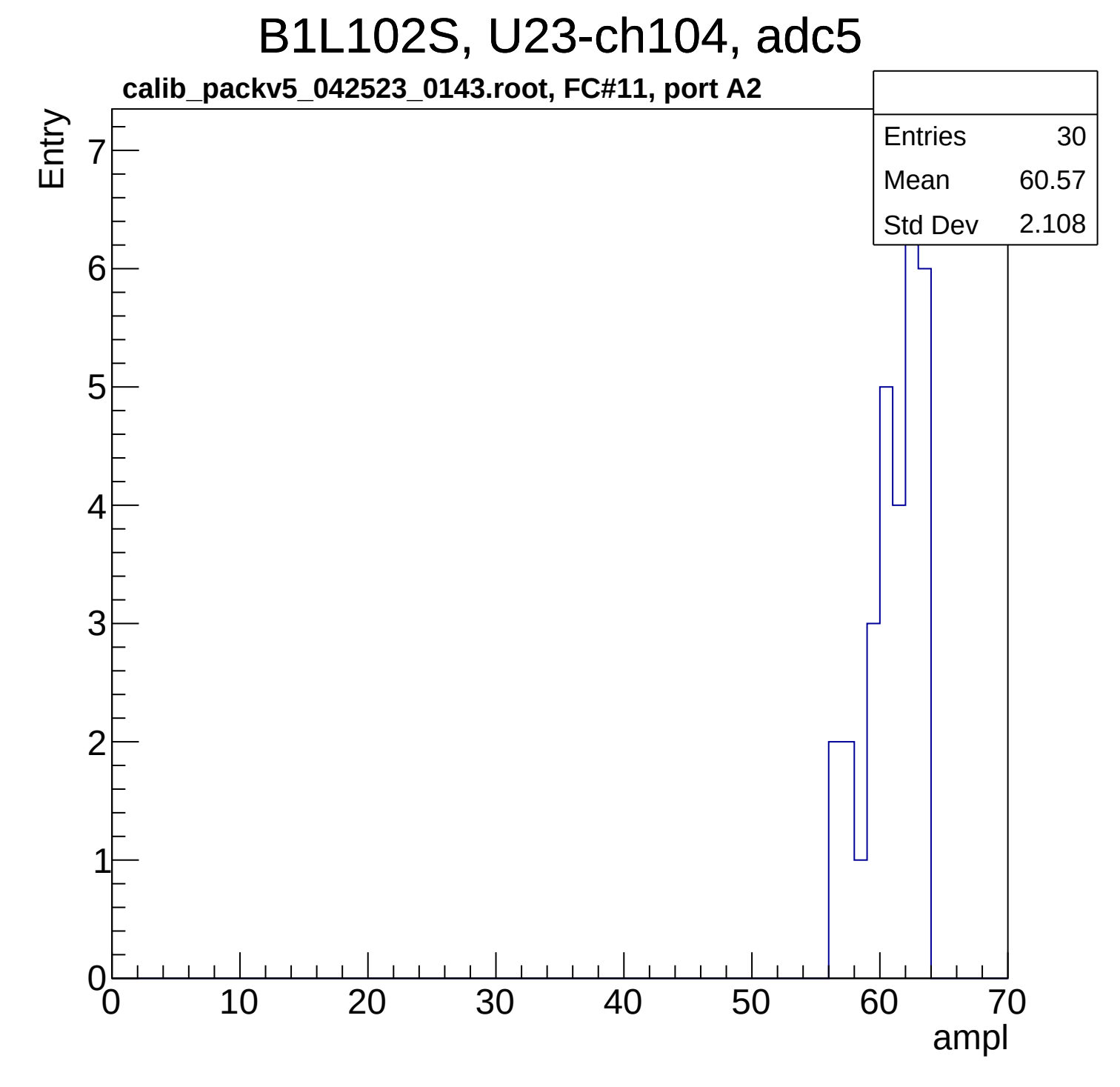

B1L102S, U23-ch104, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 28.86
Std Dev 5.013
Gaus mean : 29.6182
Gaus Width: 3.9629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

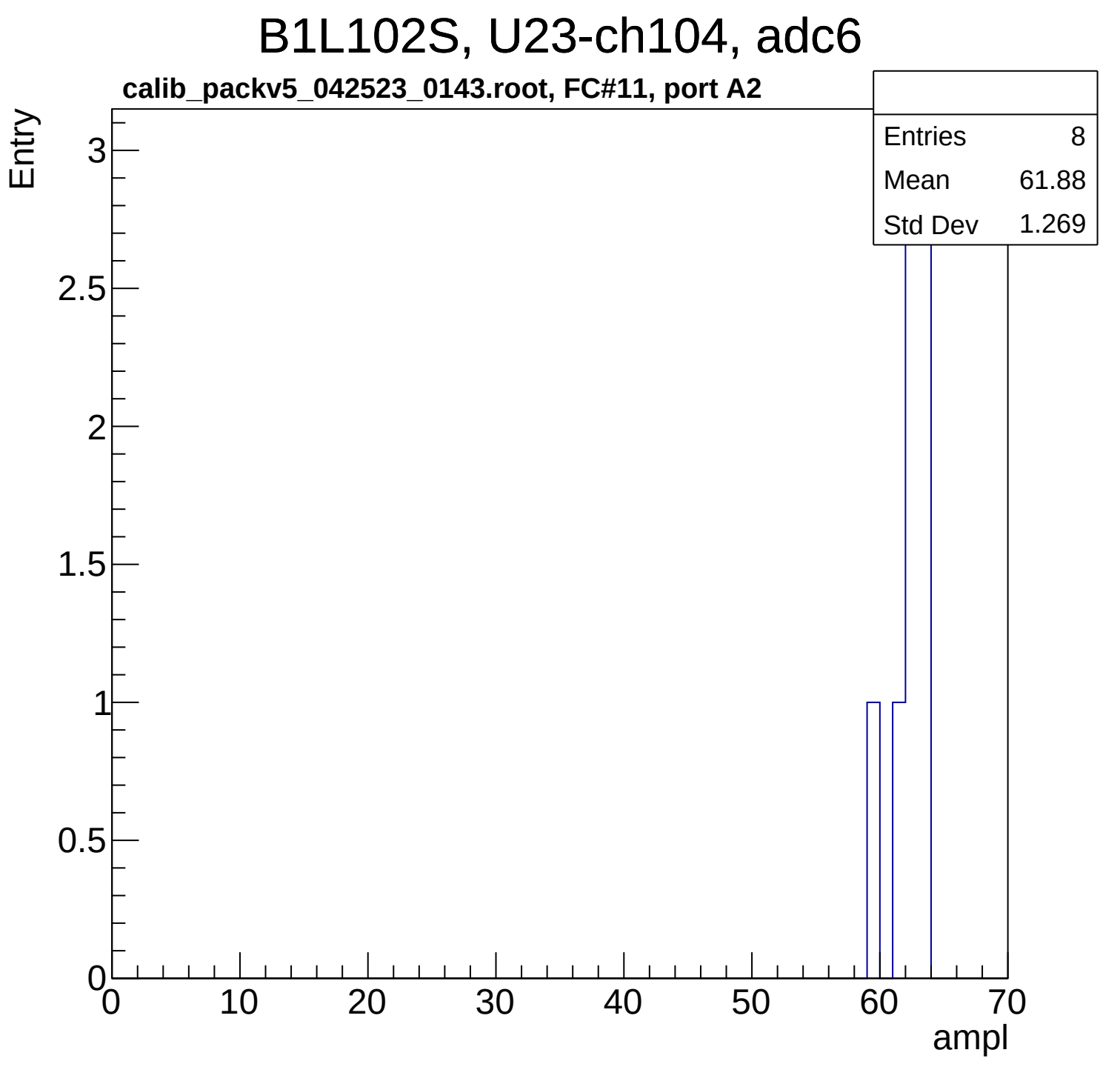

B1L102S, U23-ch104, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 35.38
Std Dev 3.569
Gaus mean : 36.2615
Gaus Width: 3.7719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

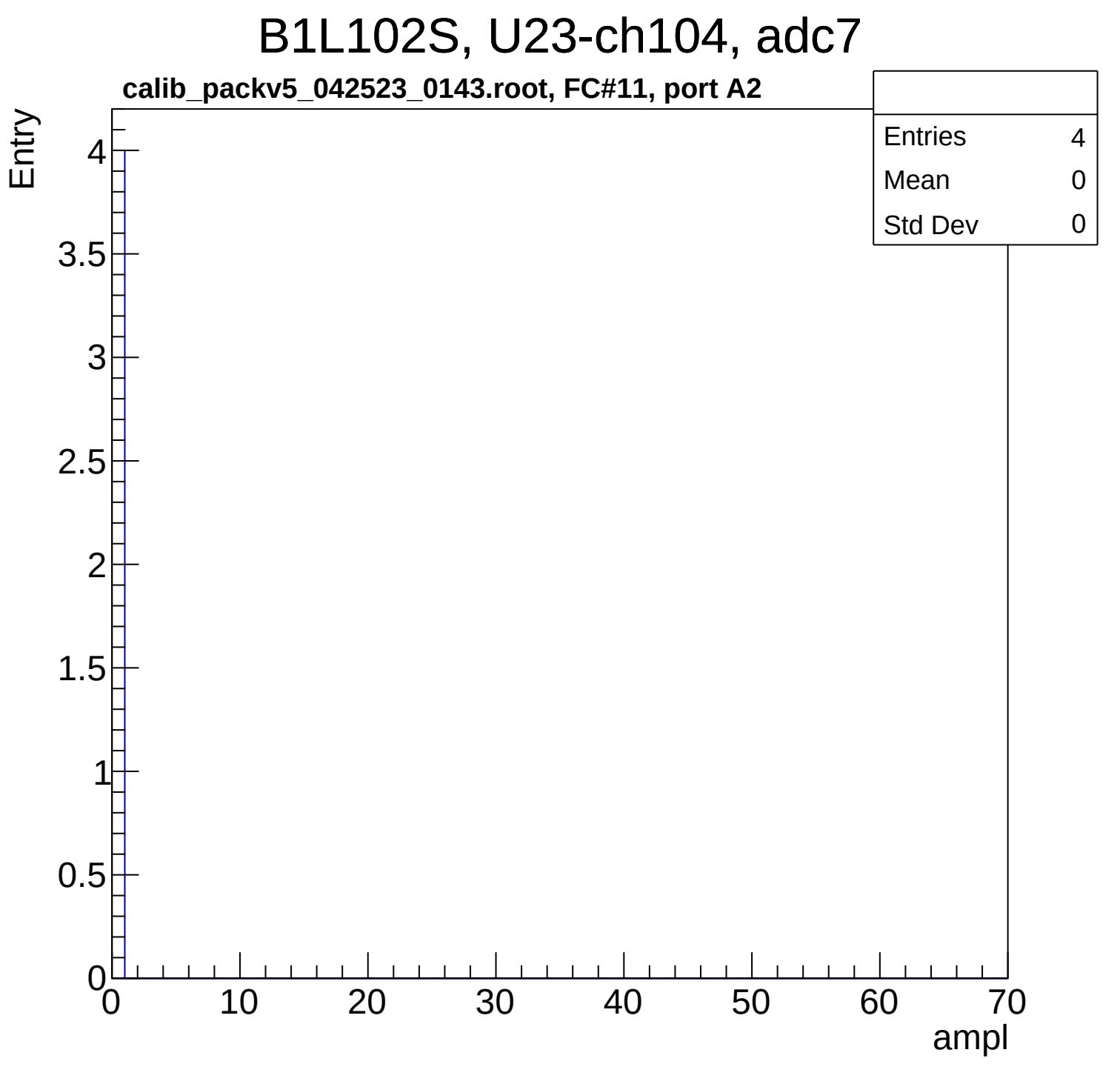

B1L102S, U23-ch104, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 41.59
Std Dev 3.731
Gaus mean : 41.8681
Gaus Width: 3.7799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

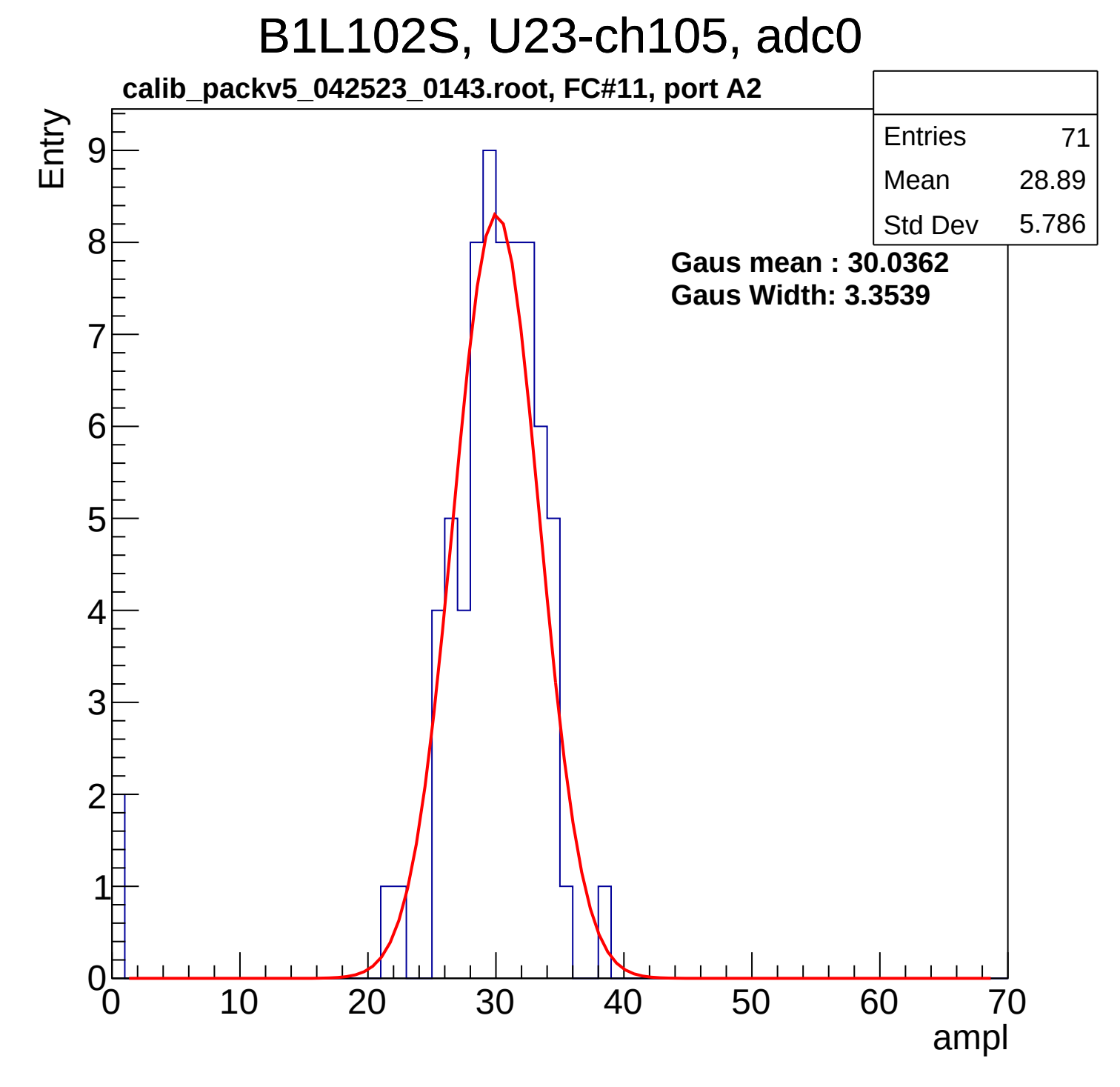

B1L102S, U23-ch104, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 49.39
Std Dev 3.83
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

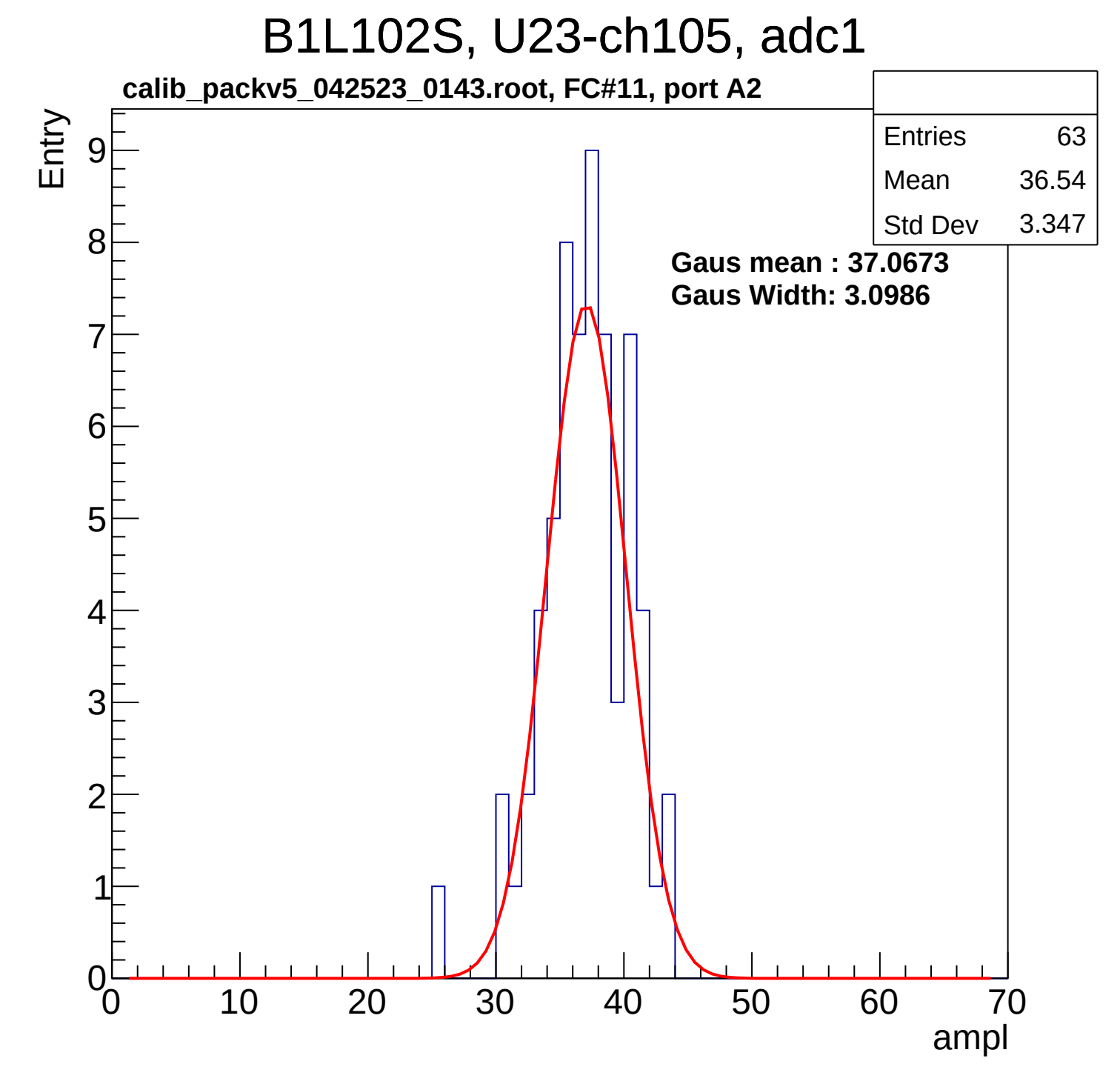

B1L102S, U23-ch104, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 54.86
Std Dev 7.302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

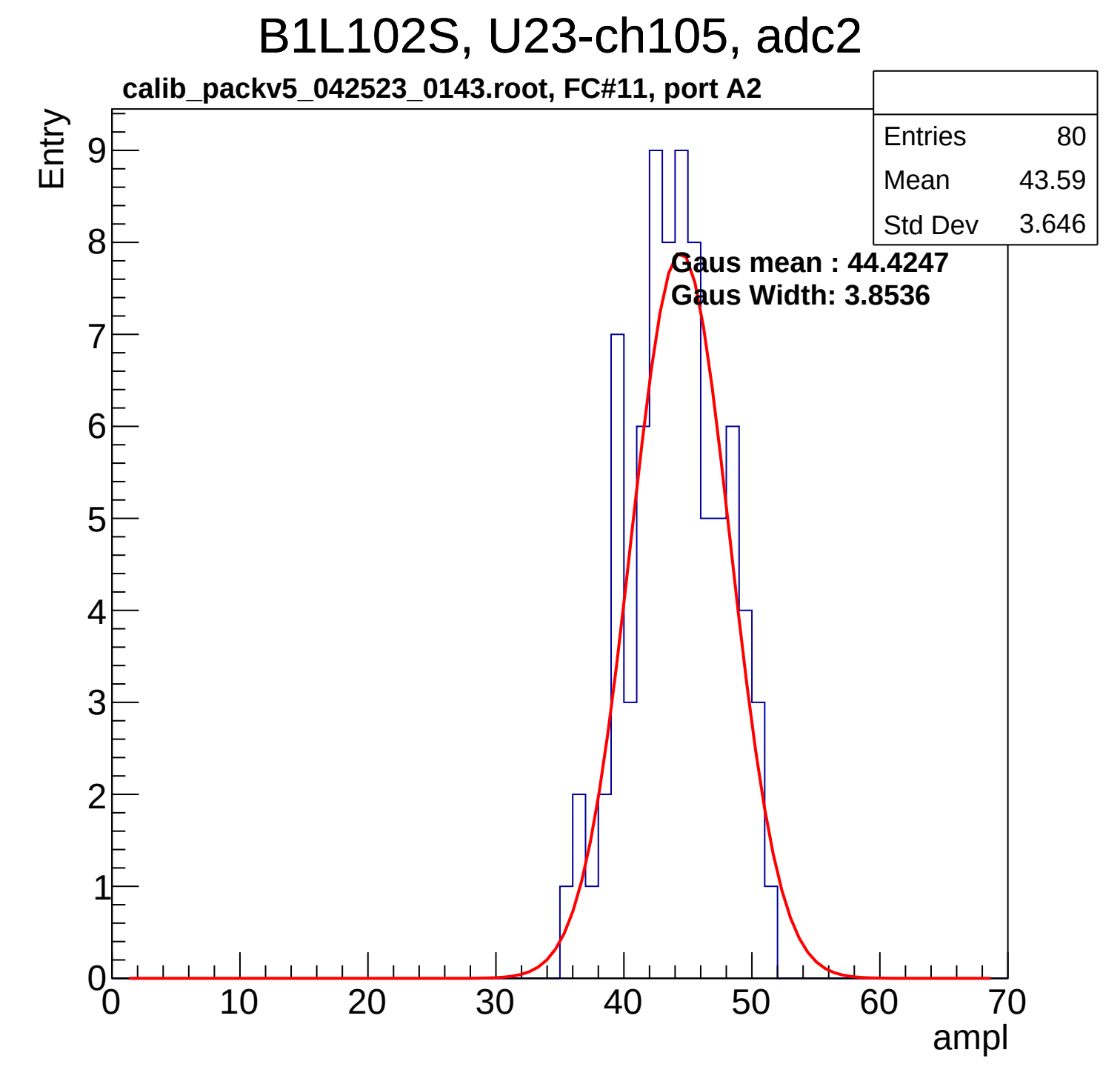

B1L102S, U23-ch104, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 30
Mean 60.57
Std Dev 2.108
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

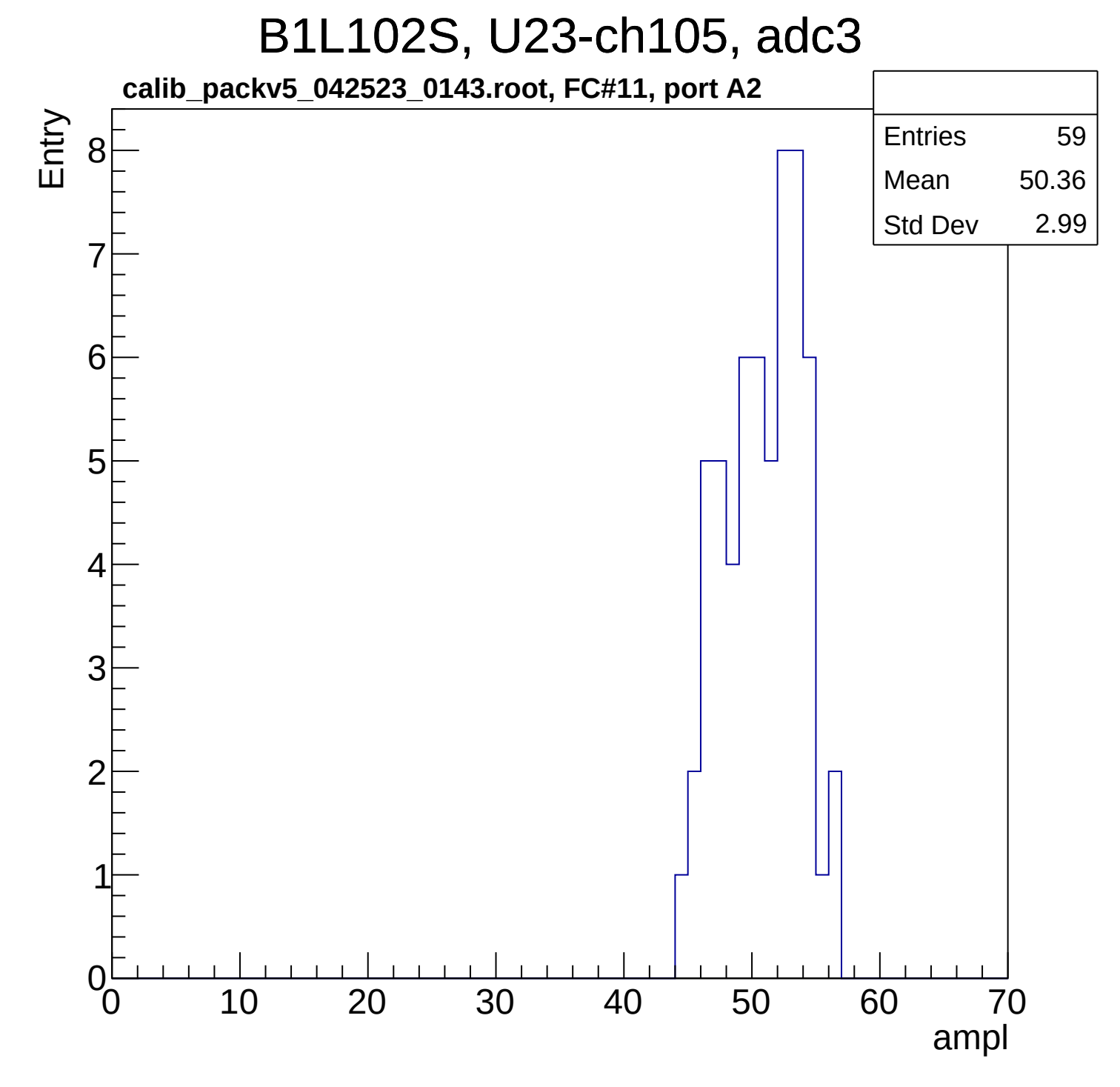

B1L102S, U23-ch104, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.88
Std Dev 1.269
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

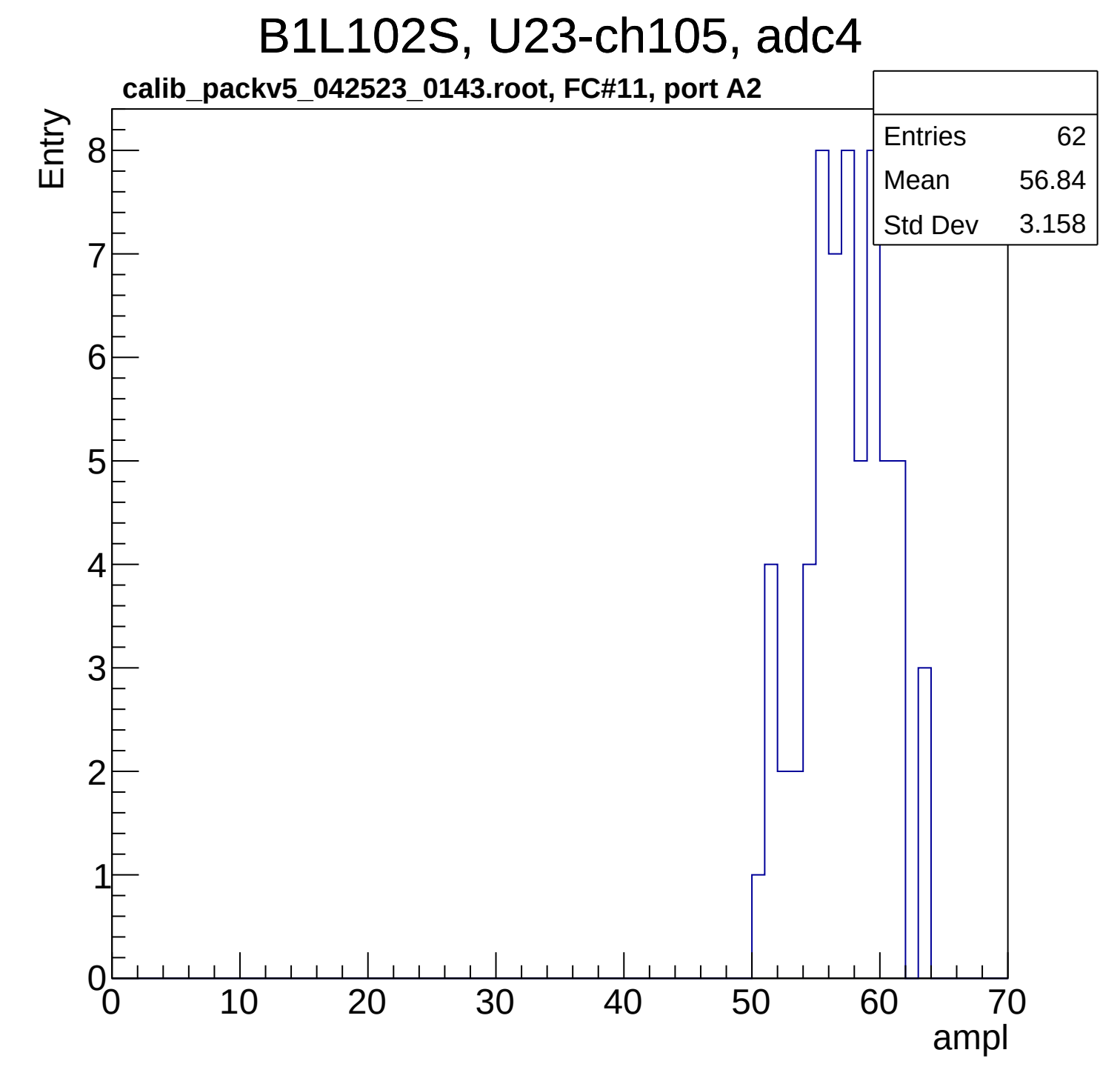

B1L102S, U23-ch104, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

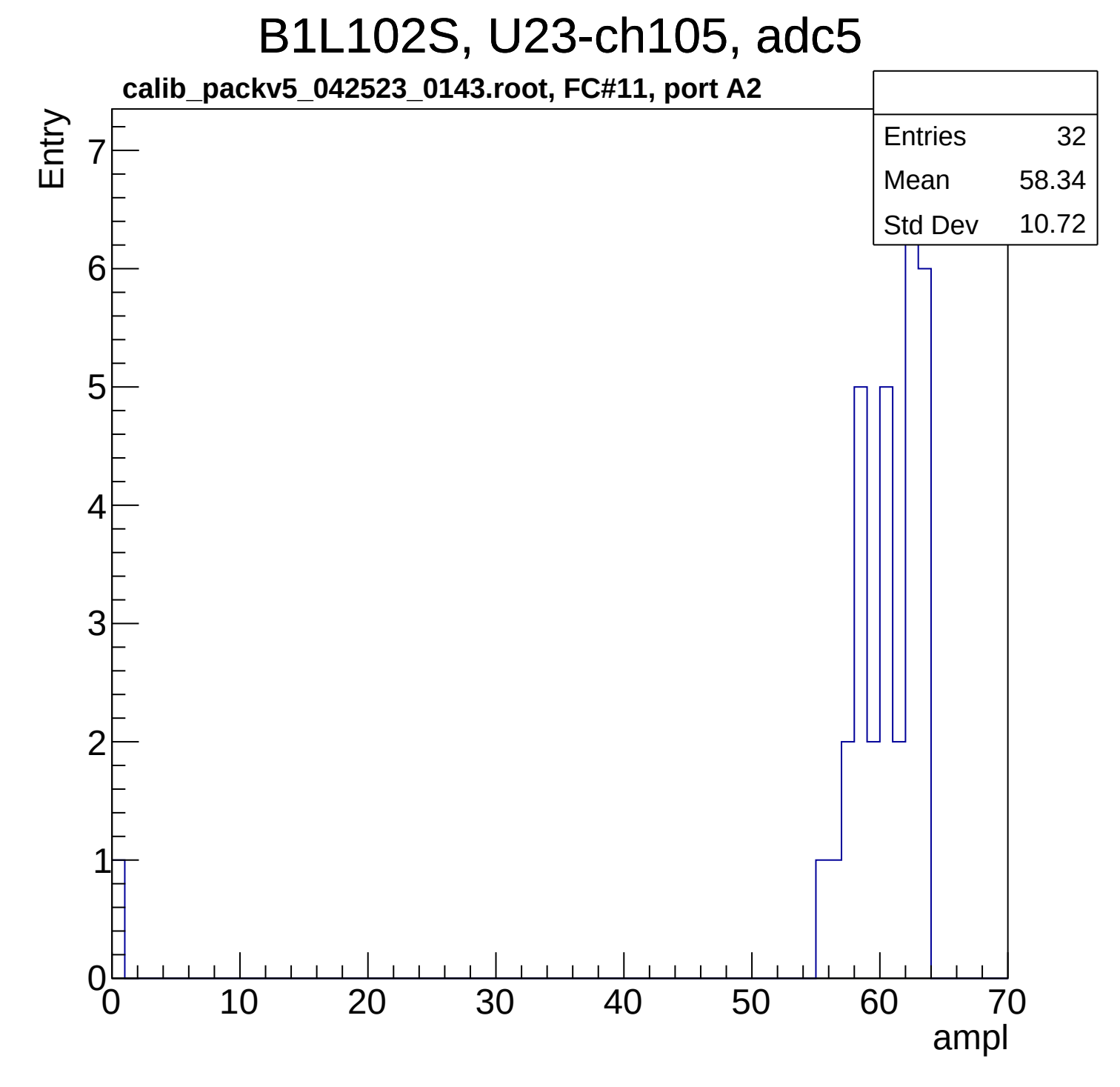

B1L102S, U23-ch105, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.89
Std Dev 5.786
Gaus mean : 30.0362
Gaus Width: 3.3539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

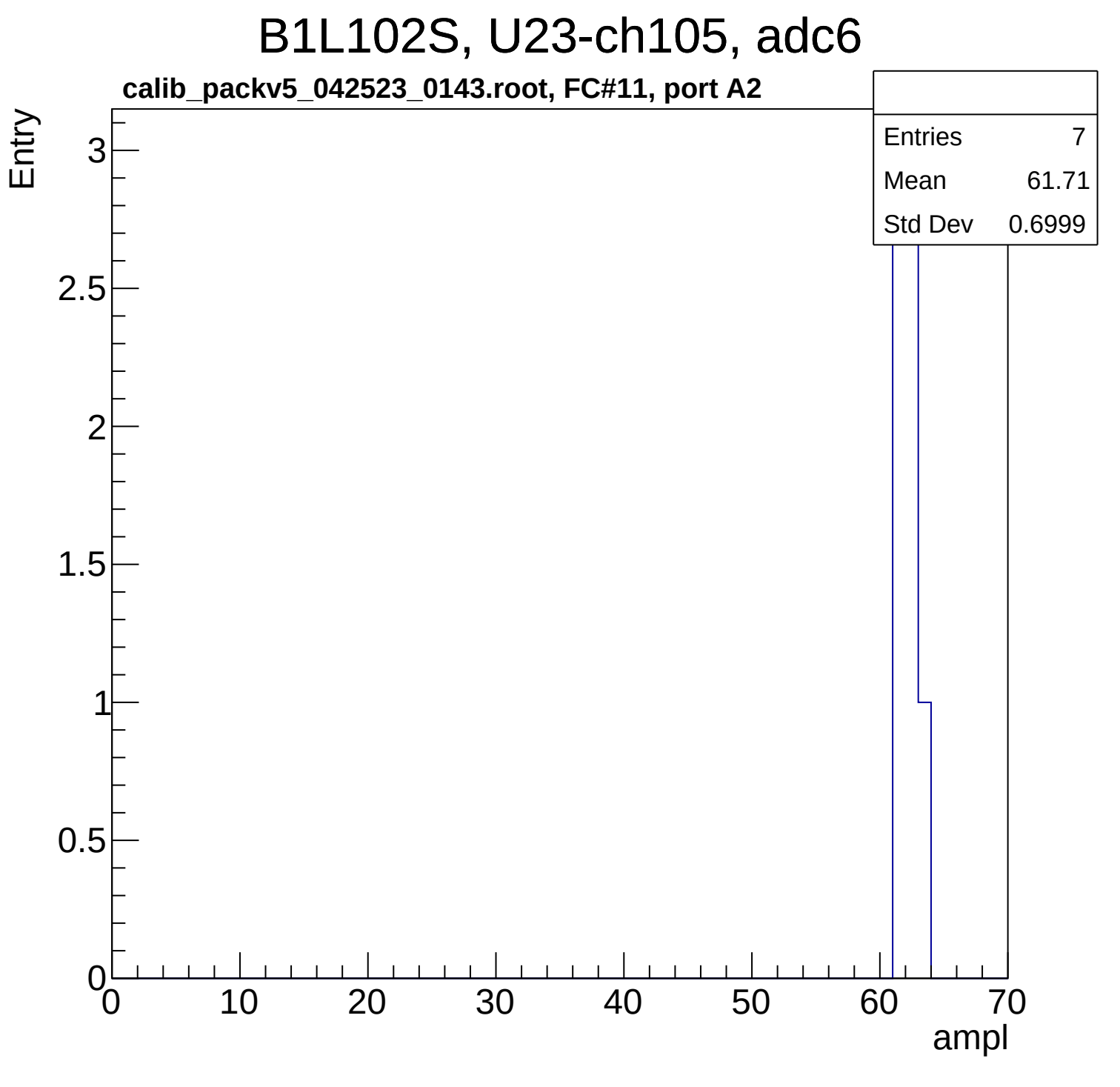

B1L102S, U23-ch105, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 36.54
Std Dev 3.347
Gaus mean : 37.0673
Gaus Width: 3.0986
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

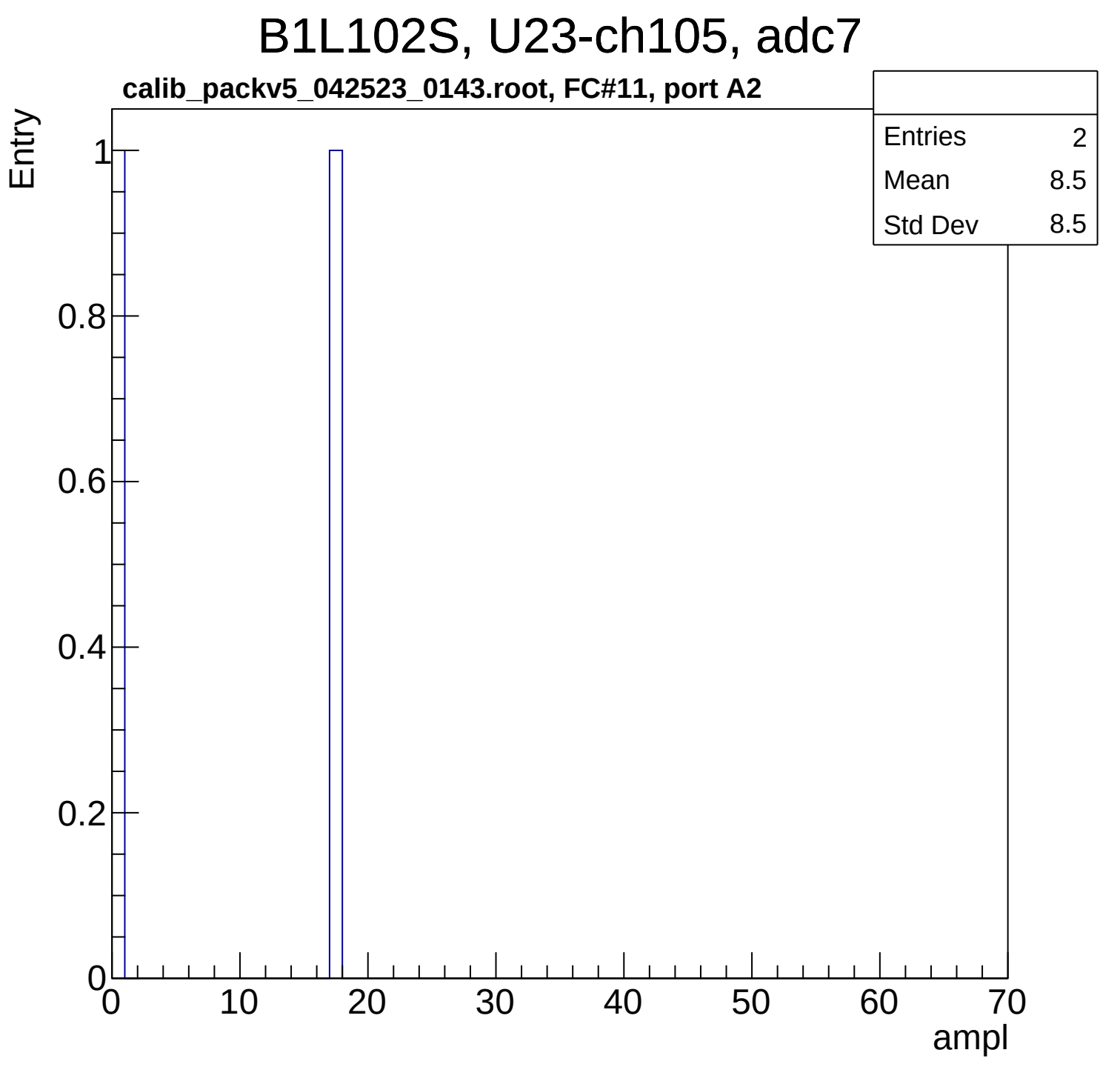

B1L102S, U23-ch105, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 43.59
Std Dev 3.646
Gaus mean : 44.4247
Gaus Width: 3.8536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

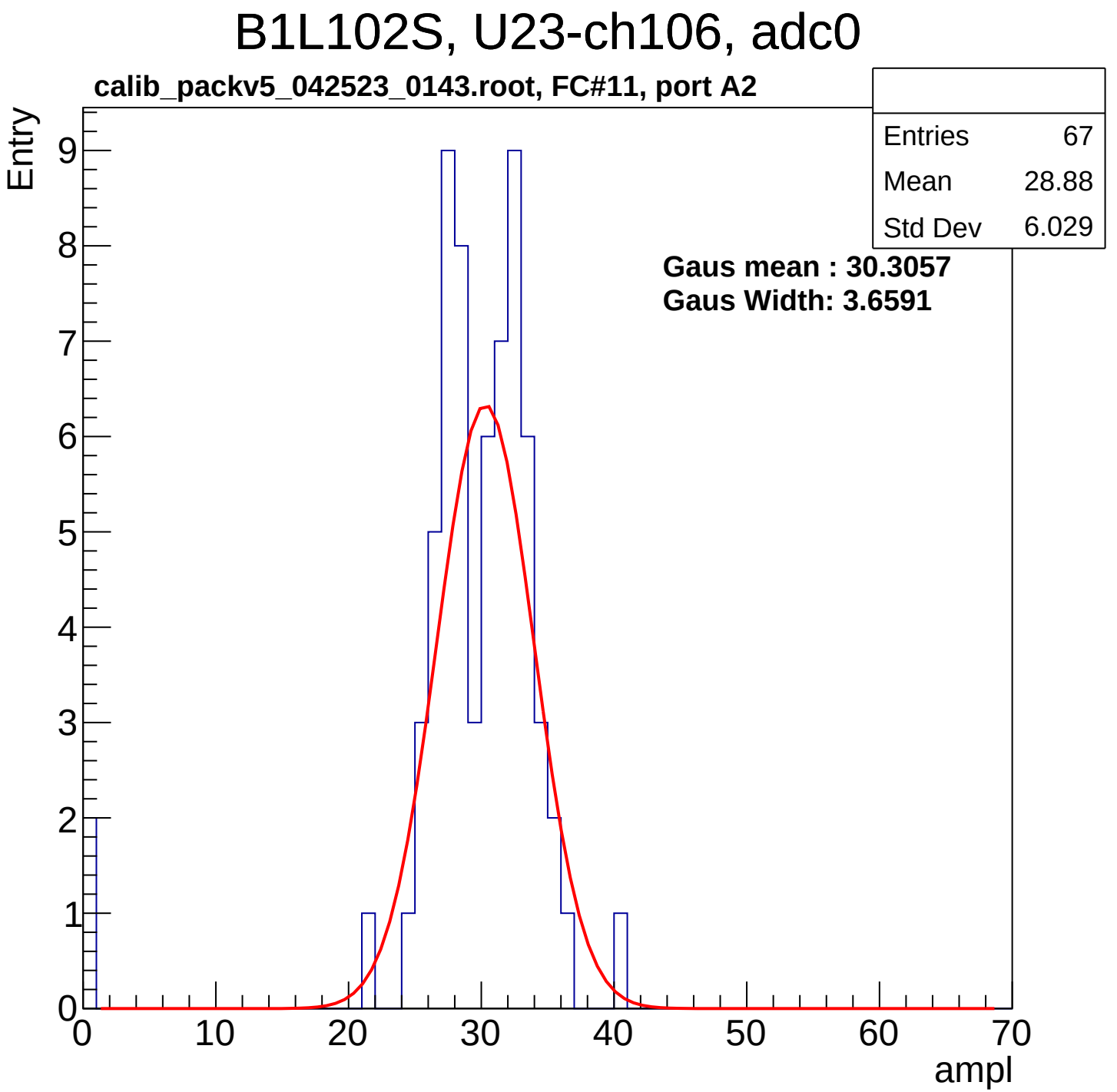

B1L102S, U23-ch105, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 50.36
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

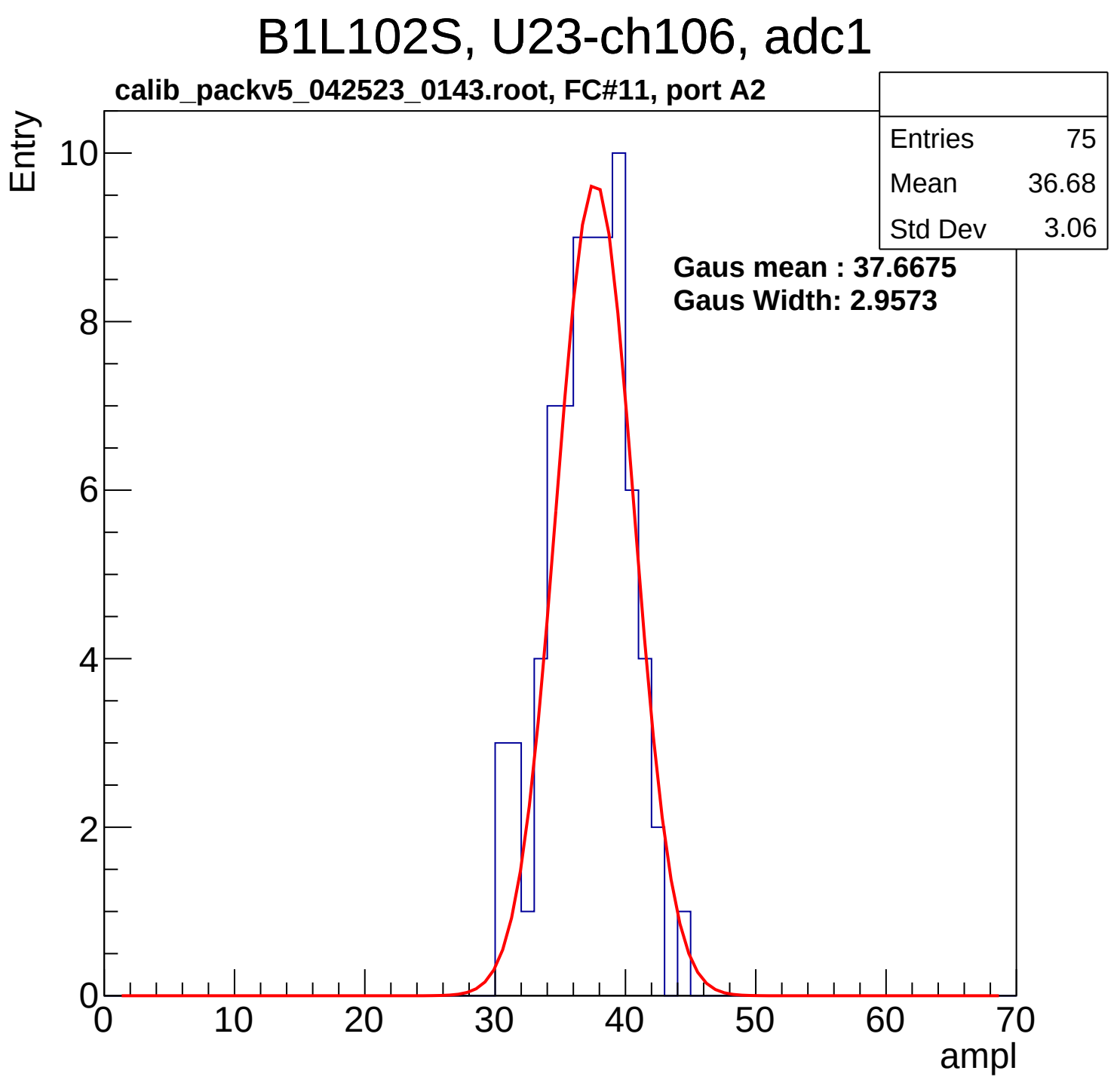

B1L102S, U23-ch105, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 56.84
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

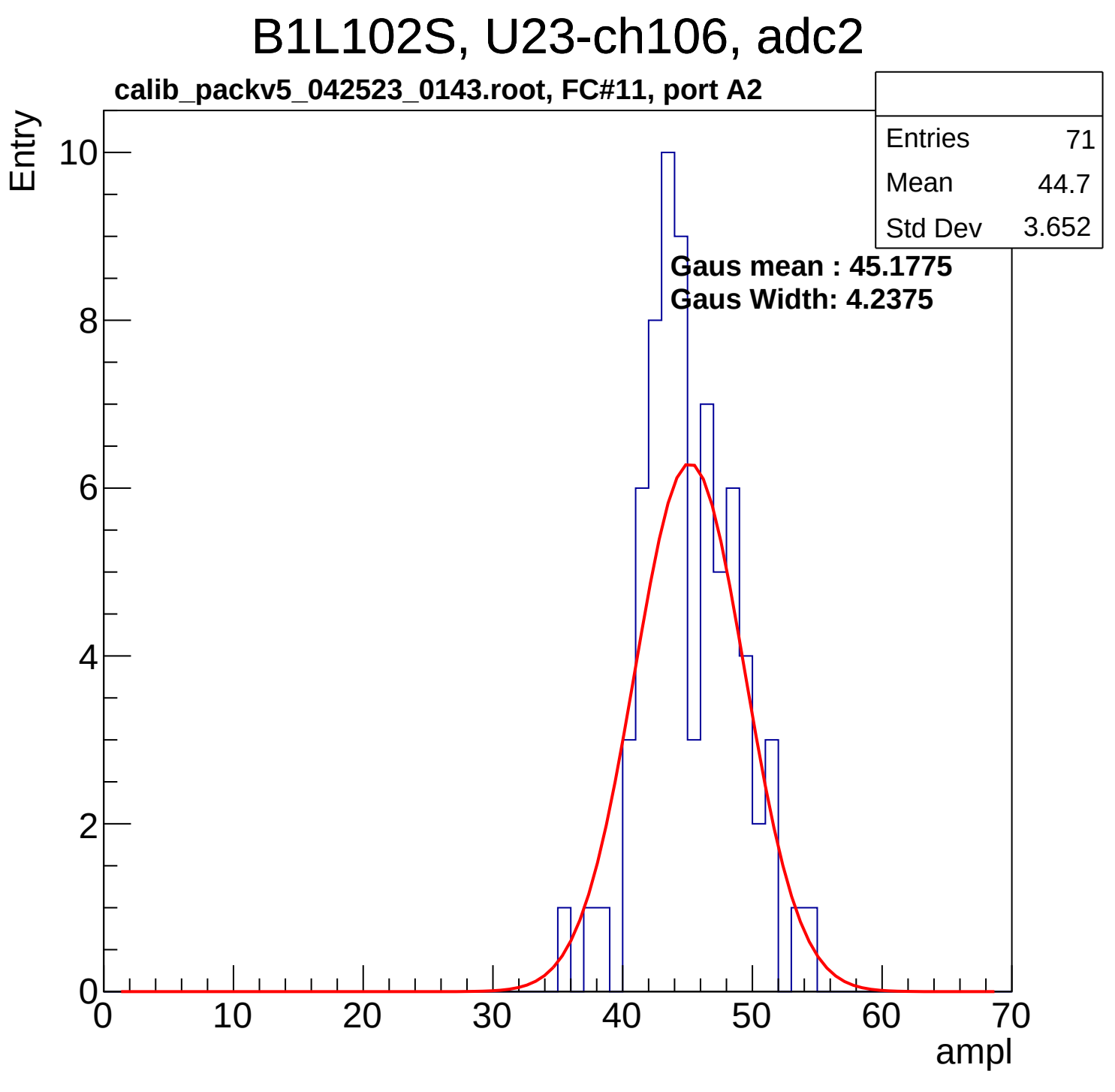

B1L102S, U23-ch105, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 32
Mean 58.34
Std Dev 10.72
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

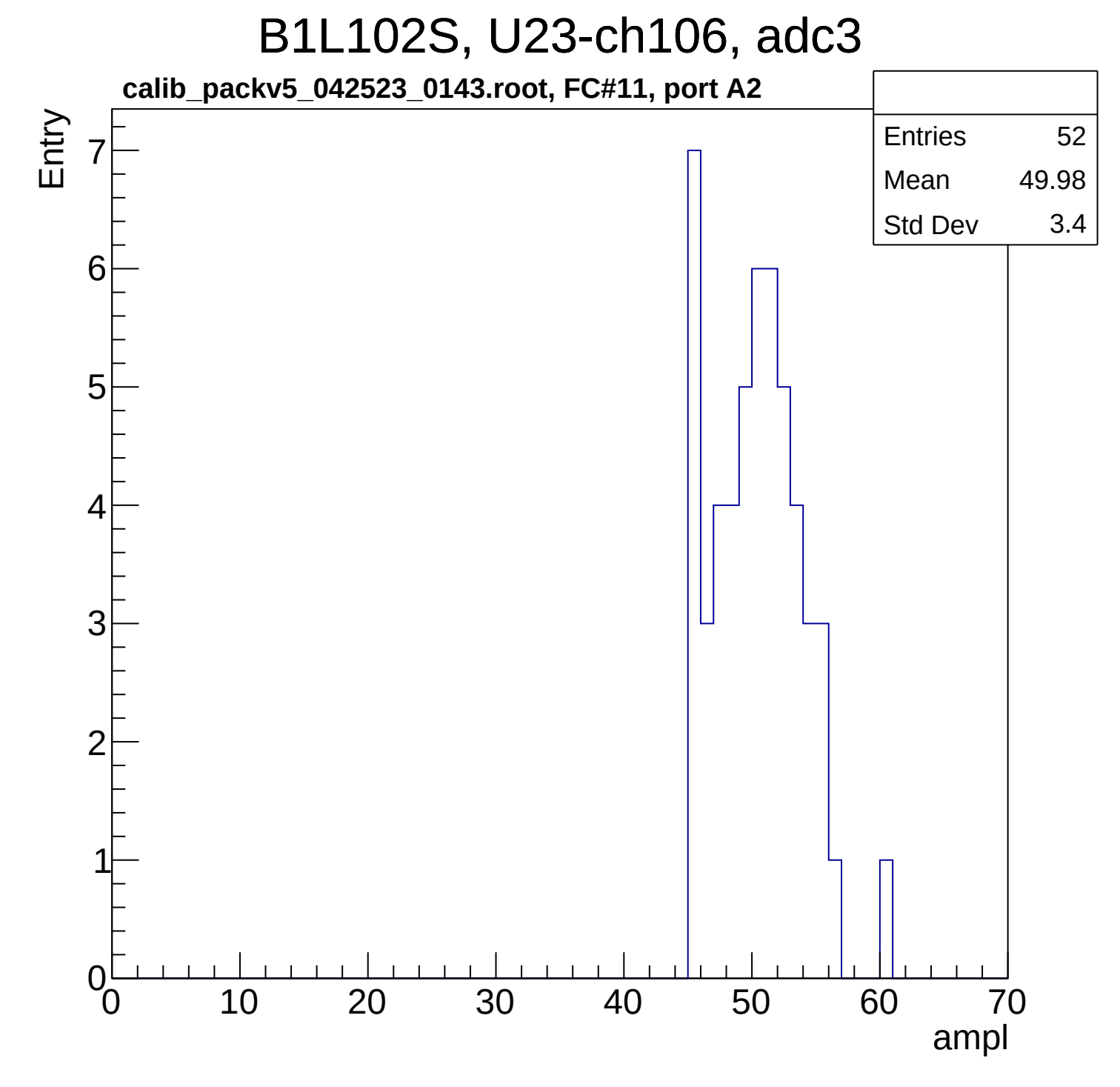

B1L102S, U23-ch105, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 61.71
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

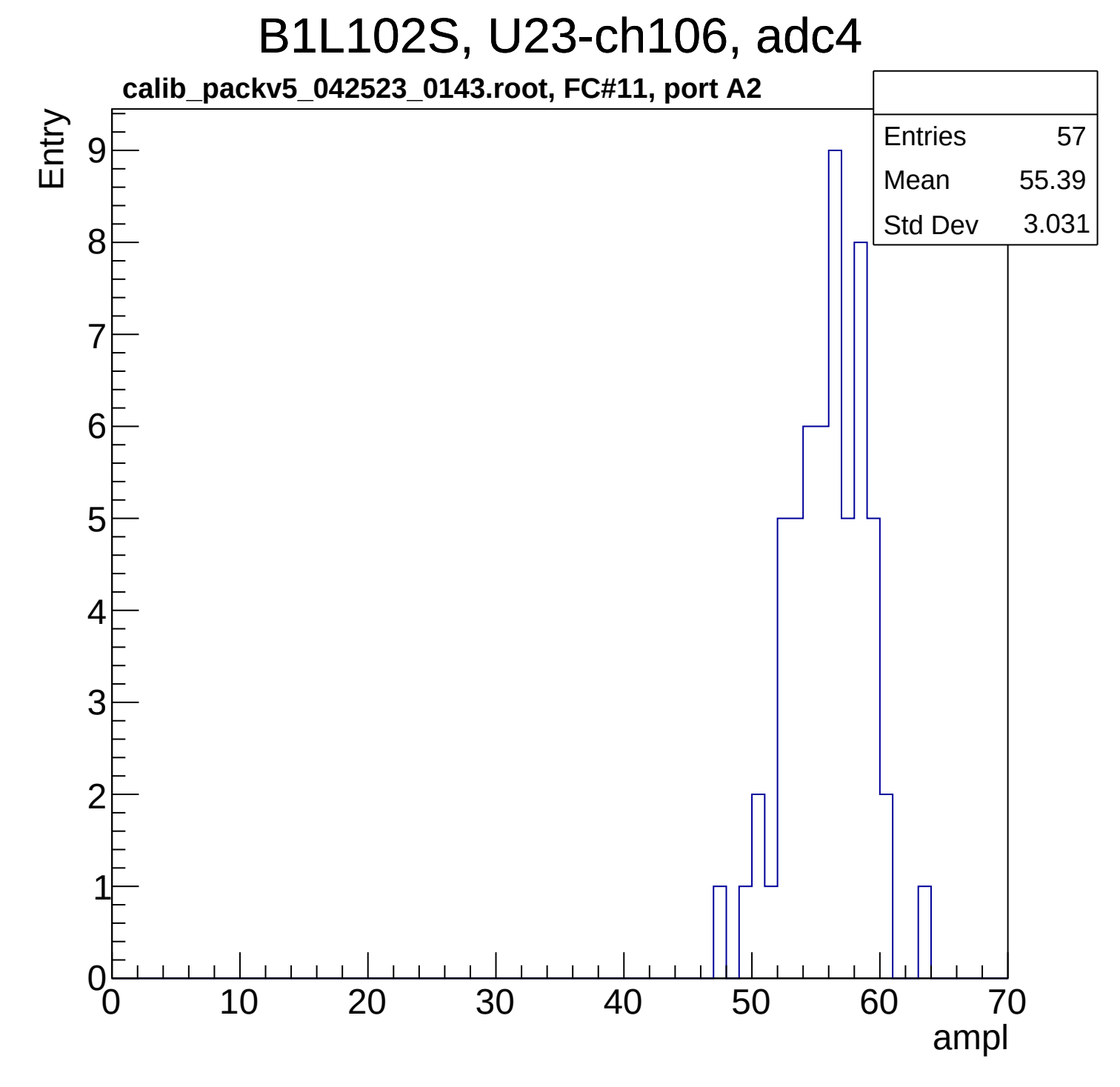

B1L102S, U23-ch105, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

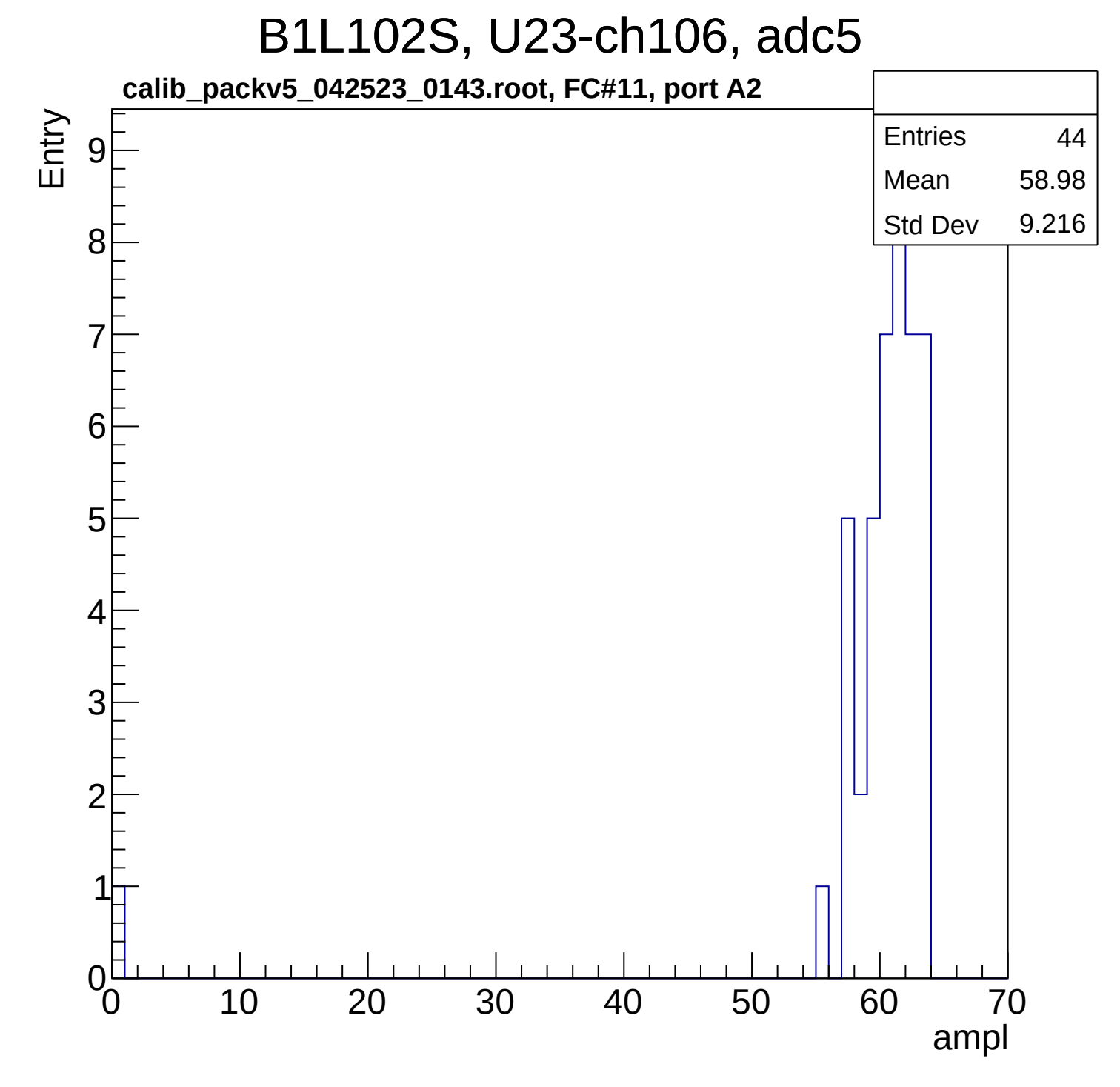

B1L102S, U23-ch106, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 28.88
Std Dev 6.029
Gaus mean : 30.3057
Gaus Width: 3.6591
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

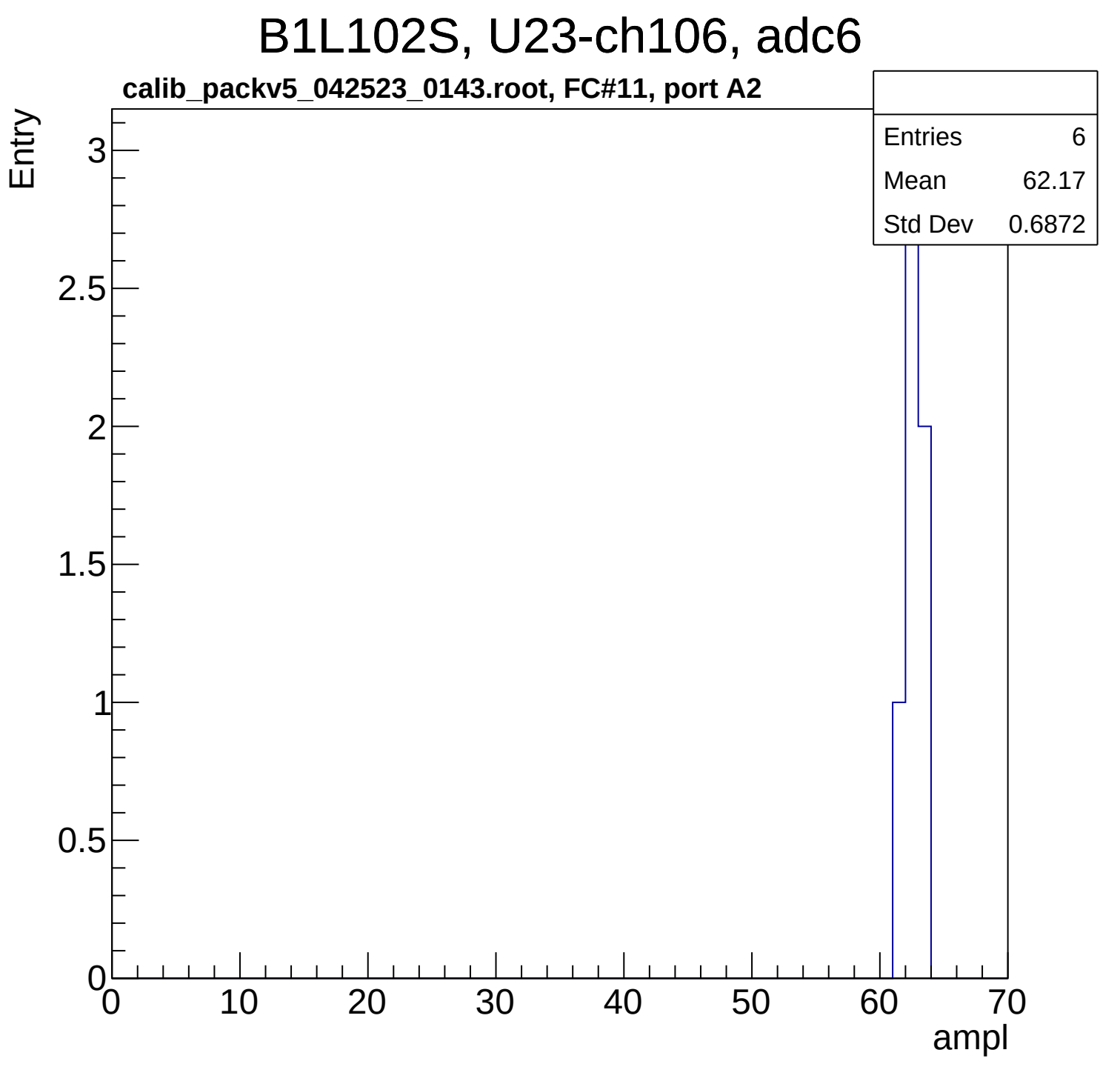

B1L102S, U23-ch106, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 36.68
Std Dev 3.06
Gaus mean : 37.6675
Gaus Width: 2.9573
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

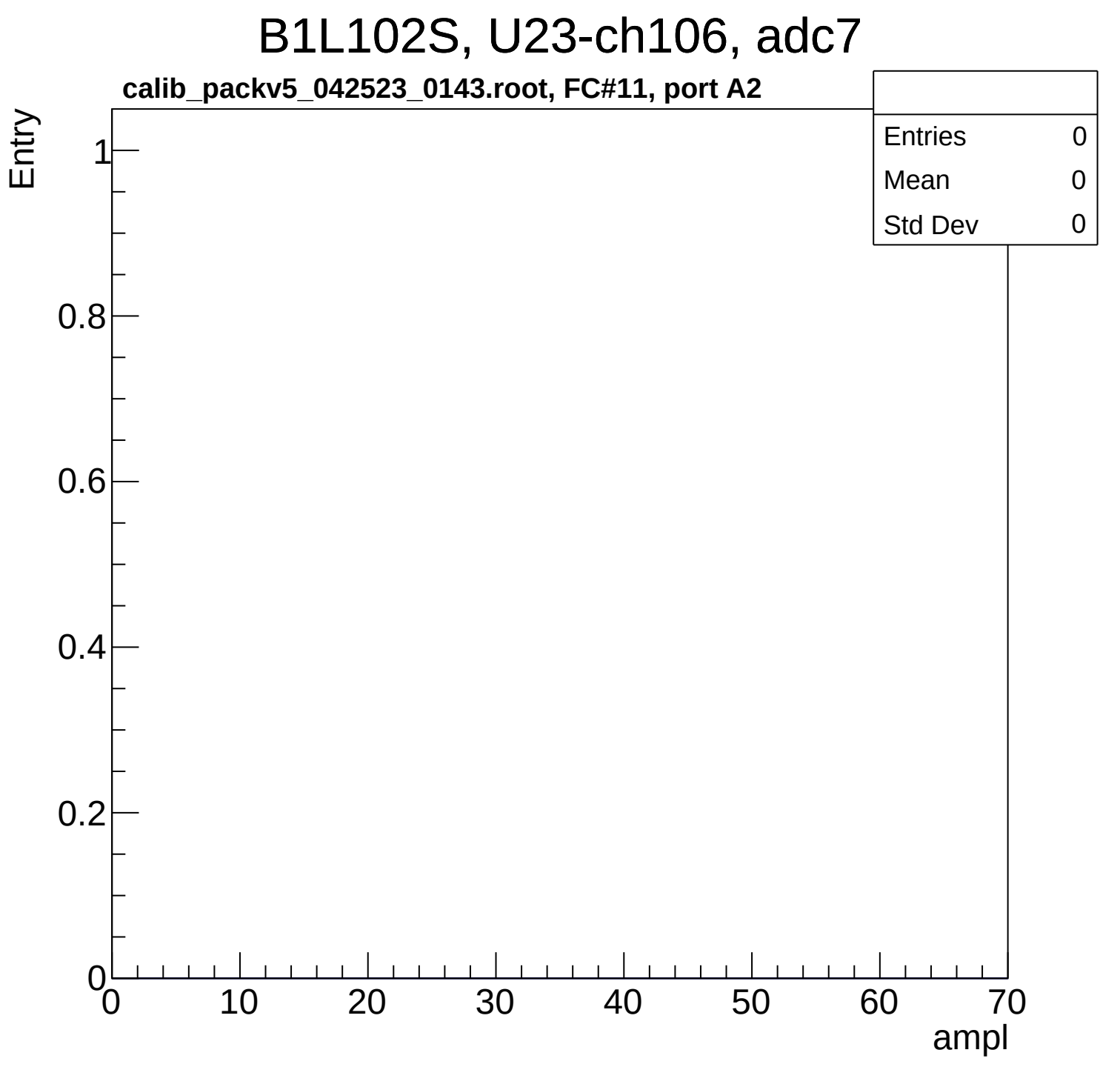

B1L102S, U23-ch106, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 44.7
Std Dev 3.652
Gaus mean : 45.1775
Gaus Width: 4.2375
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

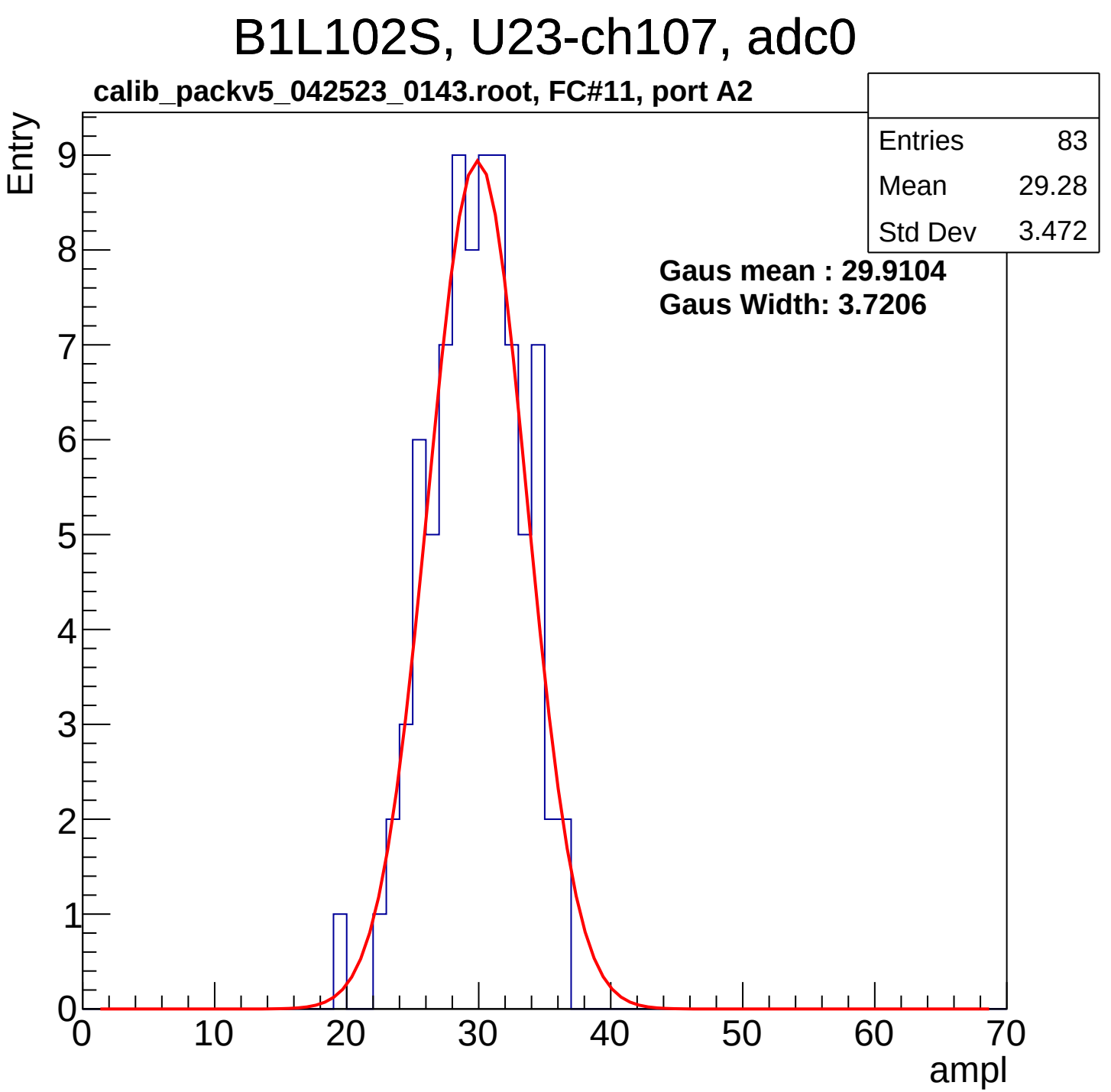

B1L102S, U23-ch106, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 49.98
Std Dev 3.4
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

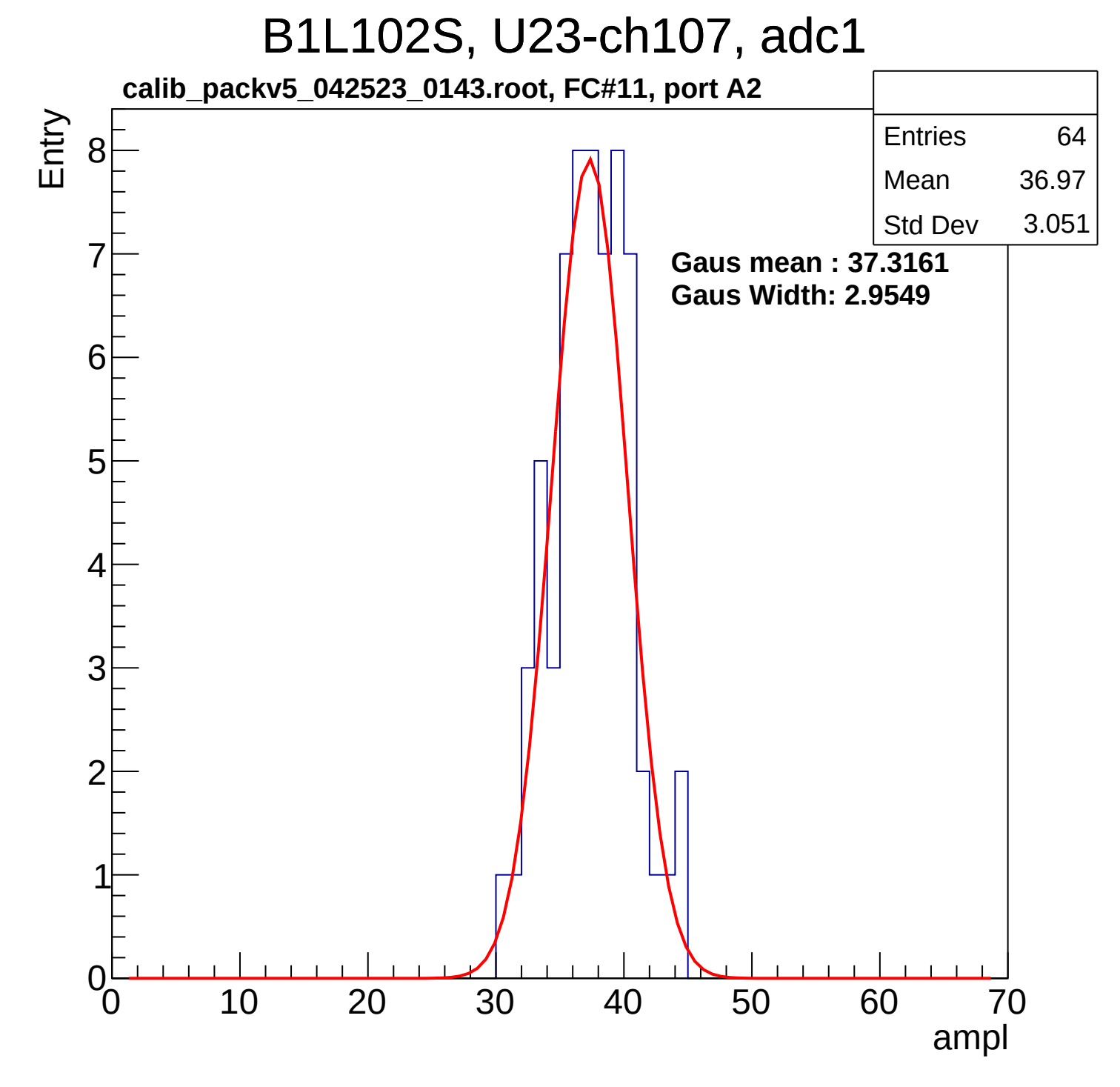

B1L102S, U23-ch106, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.39
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

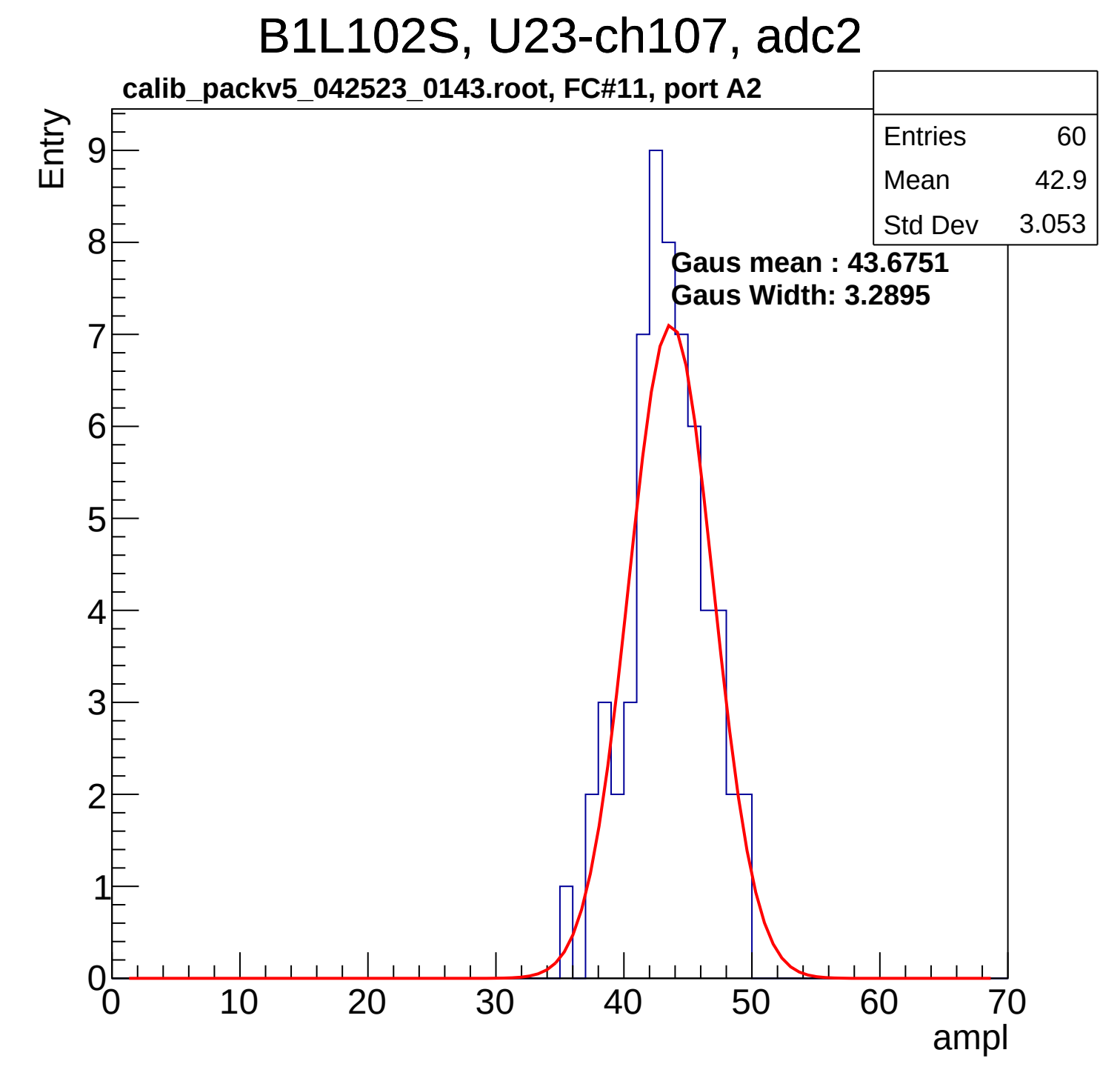

B1L102S, U23-ch106, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 58.98
Std Dev 9.216
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

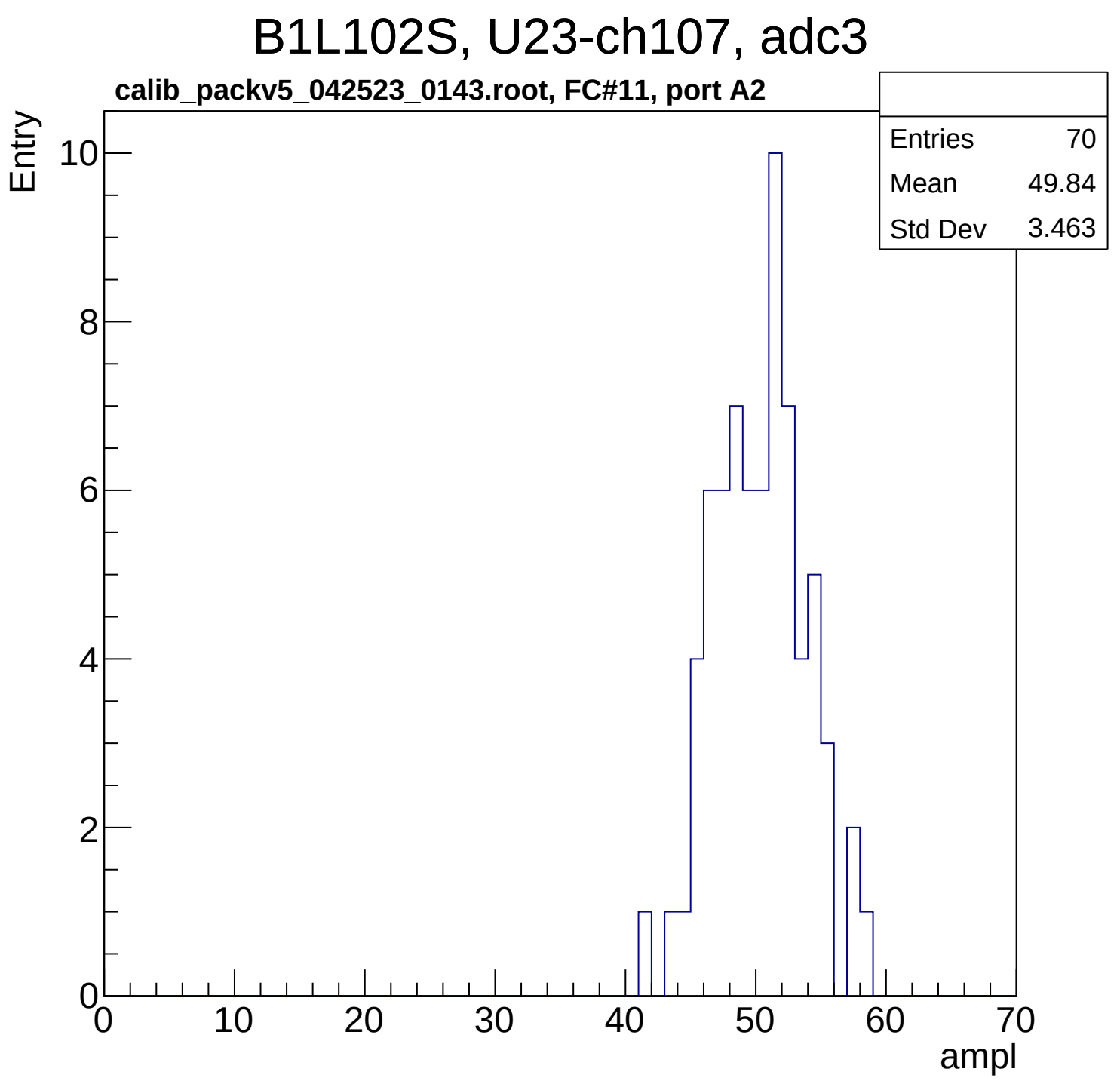

B1L102S, U23-ch106, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

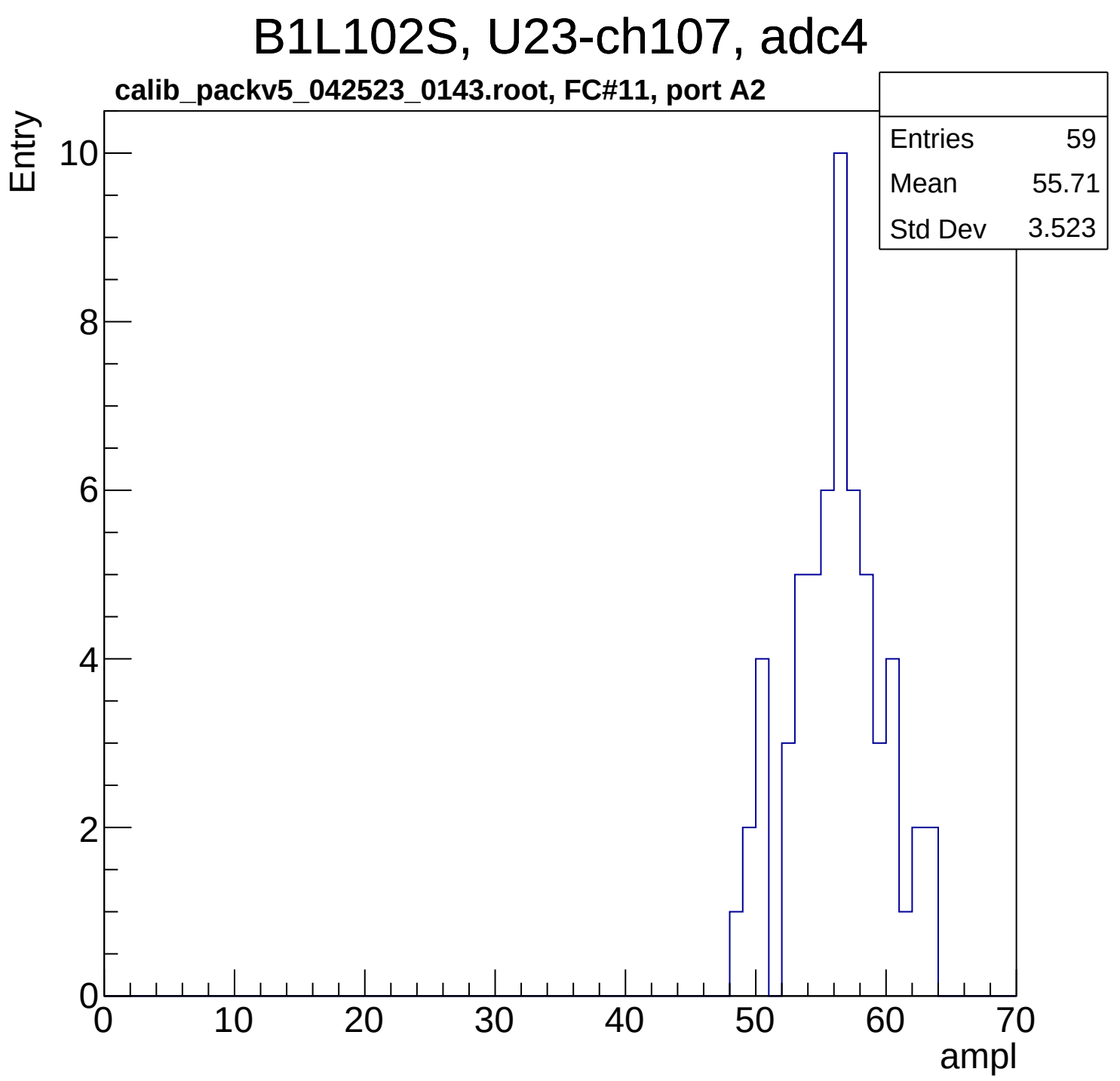

B1L102S, U23-ch106, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

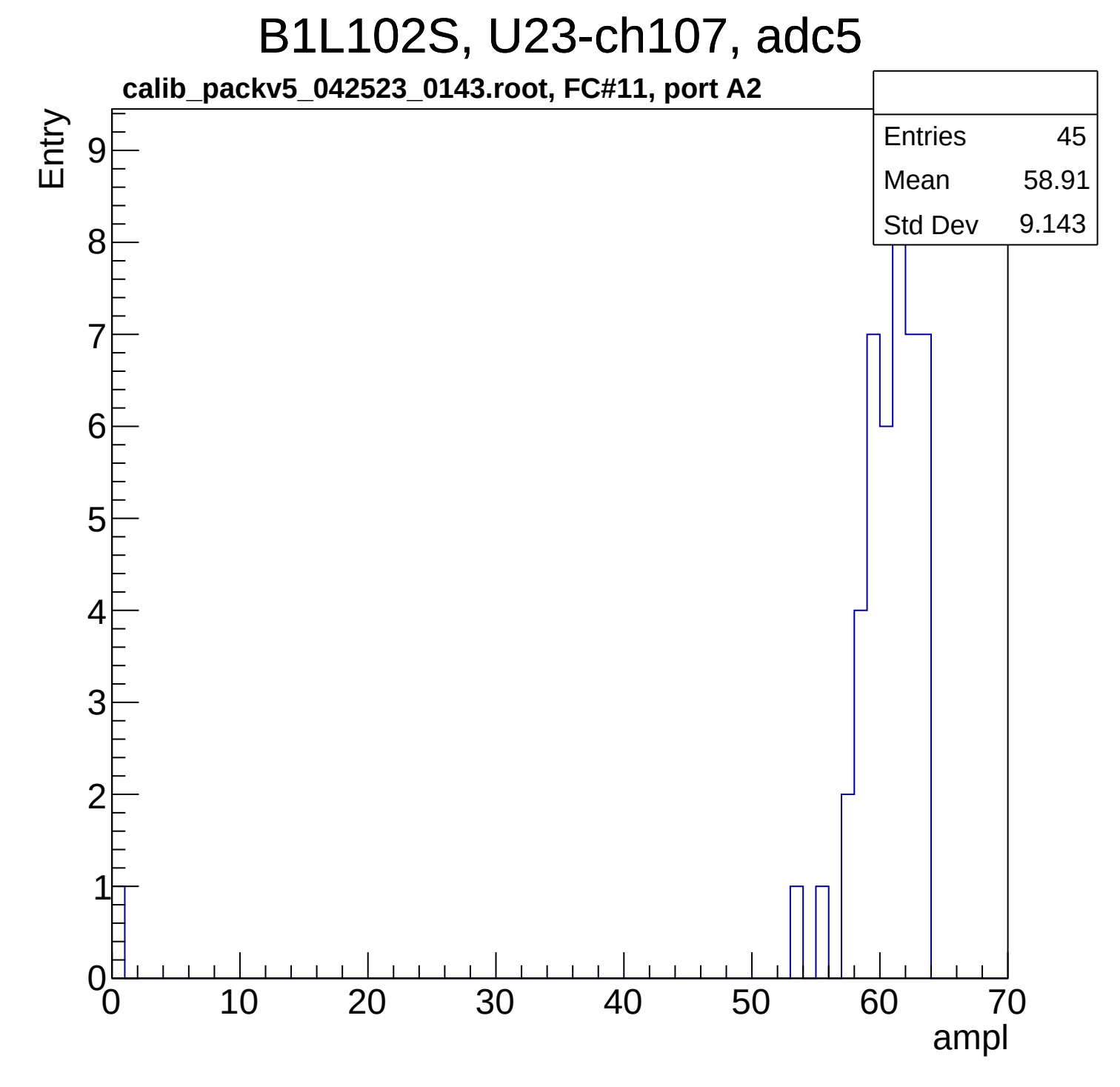

B1L102S, U23-ch107, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 29.28
Std Dev 3.472
Gaus mean : 29.9104
Gaus Width: 3.7206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

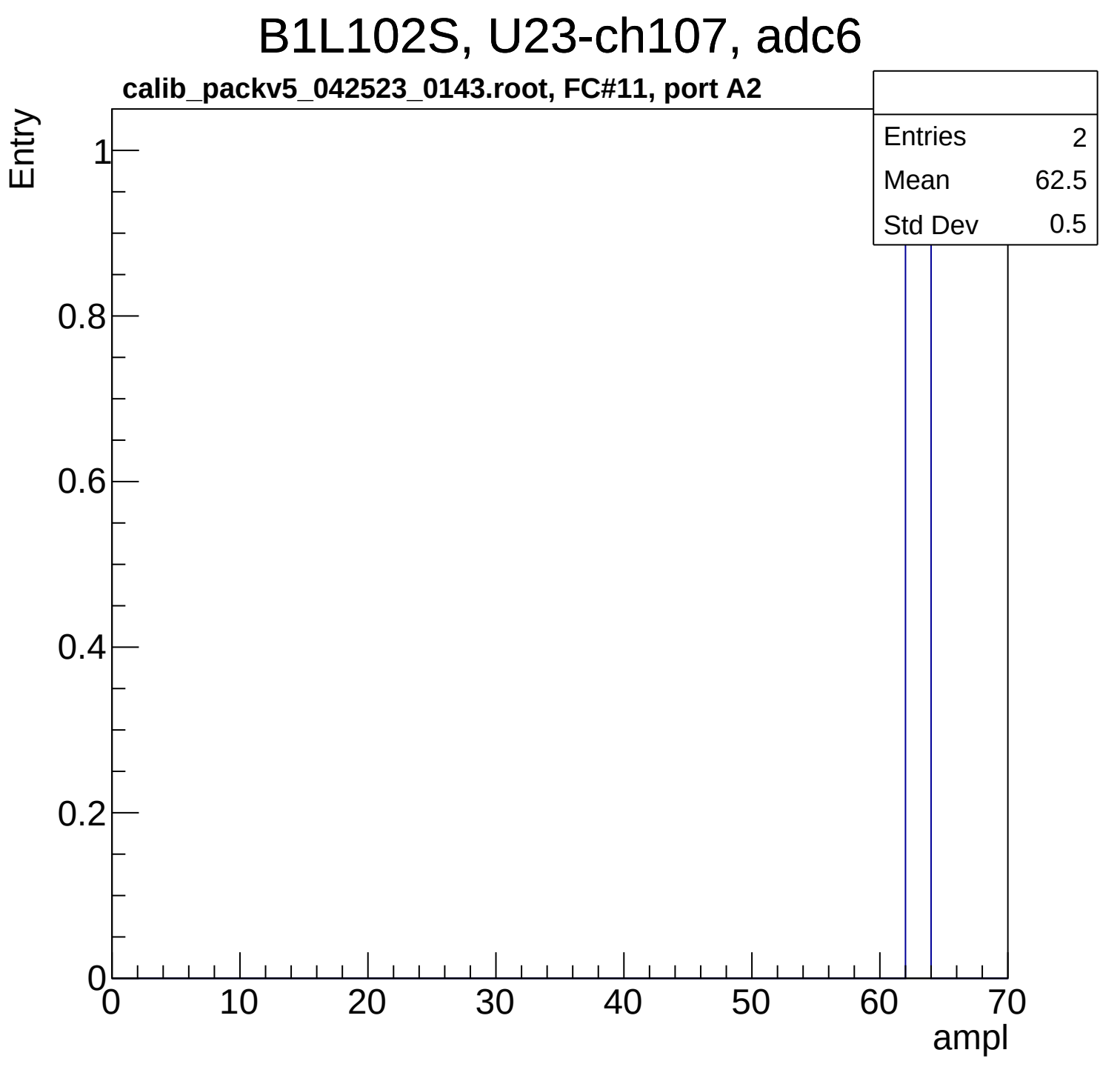

B1L102S, U23-ch107, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 36.97
Std Dev 3.051
Gaus mean : 37.3161
Gaus Width: 2.9549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

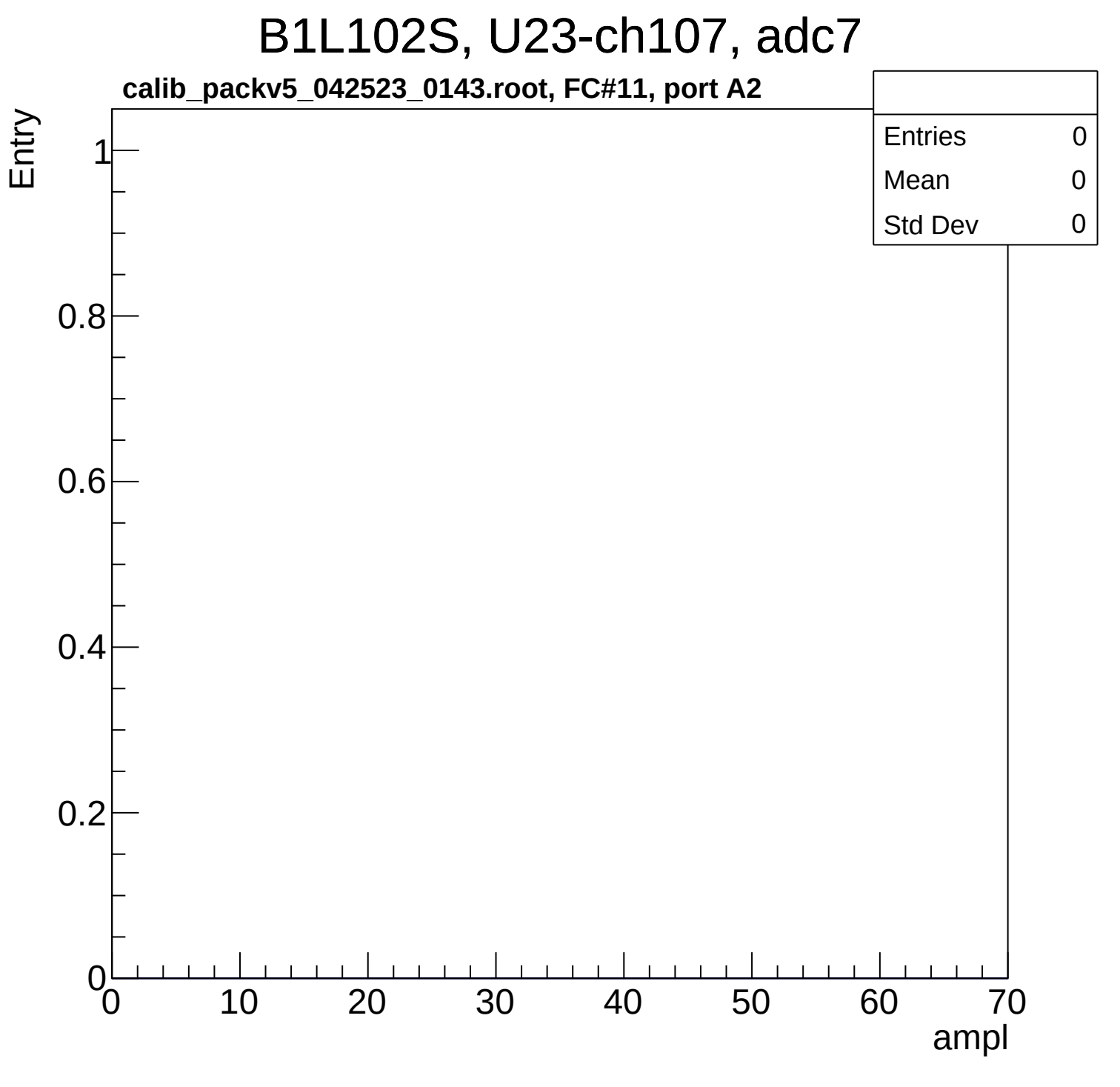

B1L102S, U23-ch107, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 42.9
Std Dev 3.053
Gaus mean : 43.6751
Gaus Width: 3.2895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

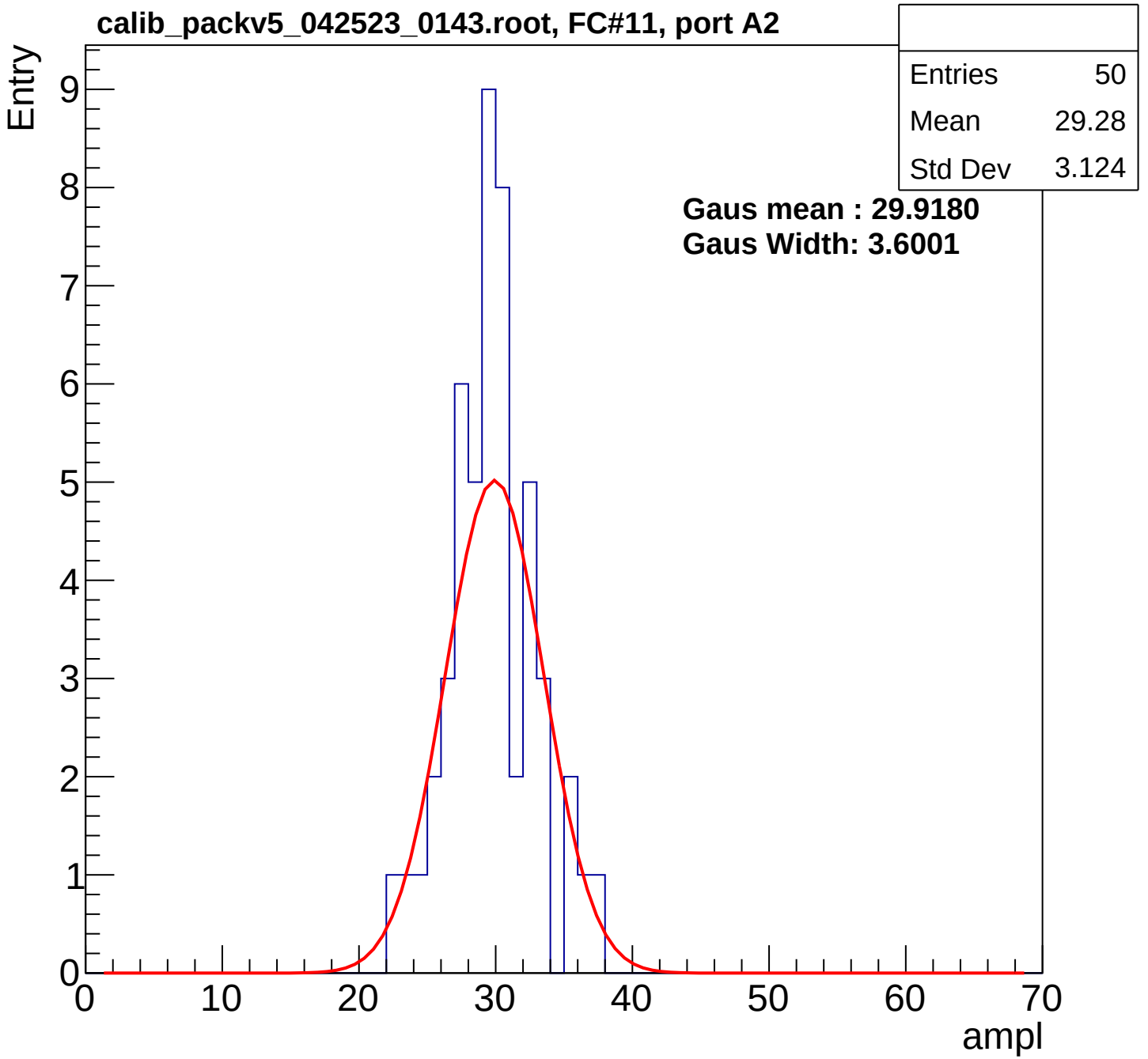

B1L102S, U23-ch107, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 49.84
Std Dev 3.463
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

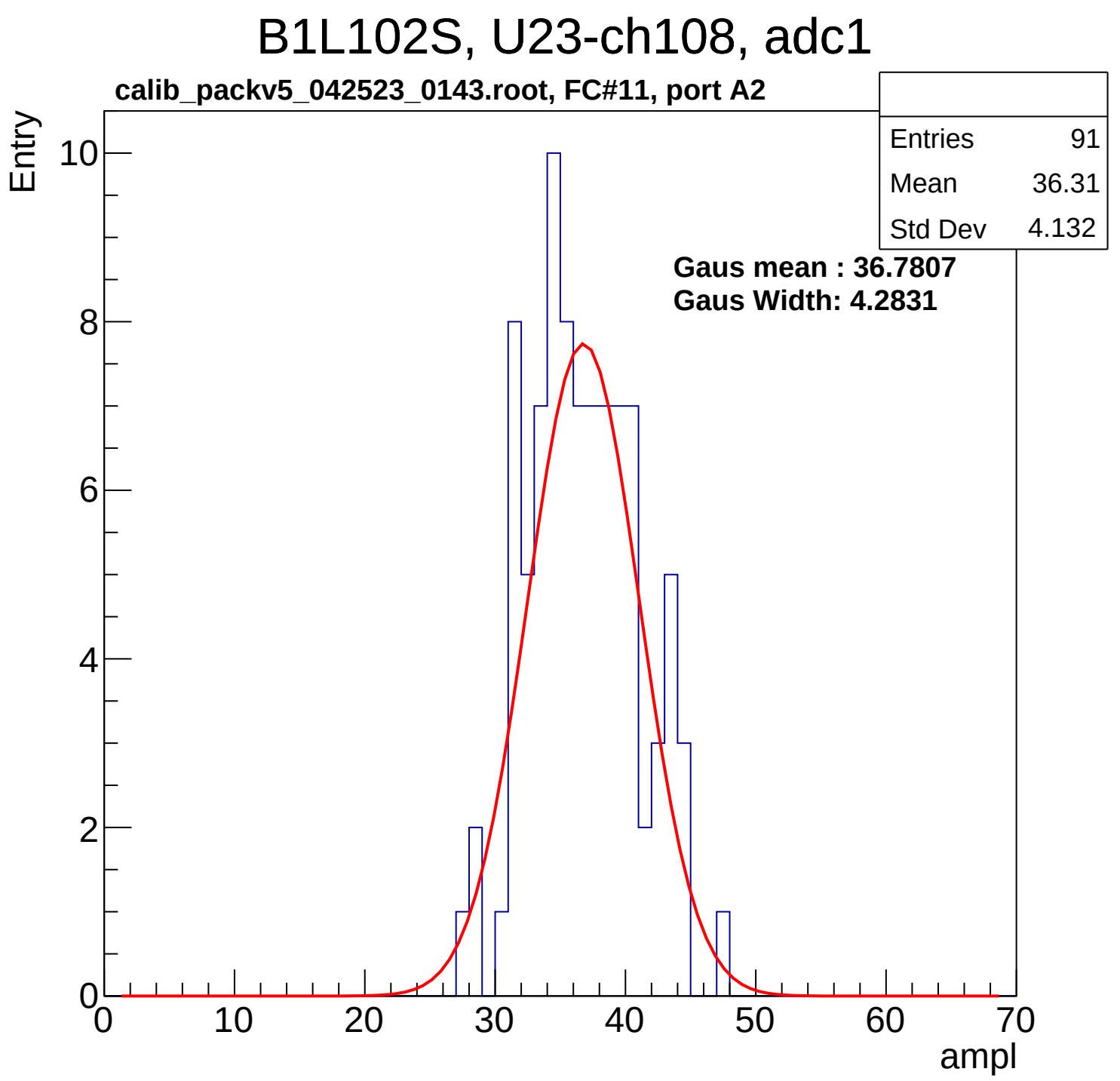

B1L102S, U23-ch107, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 55.71
Std Dev 3.523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

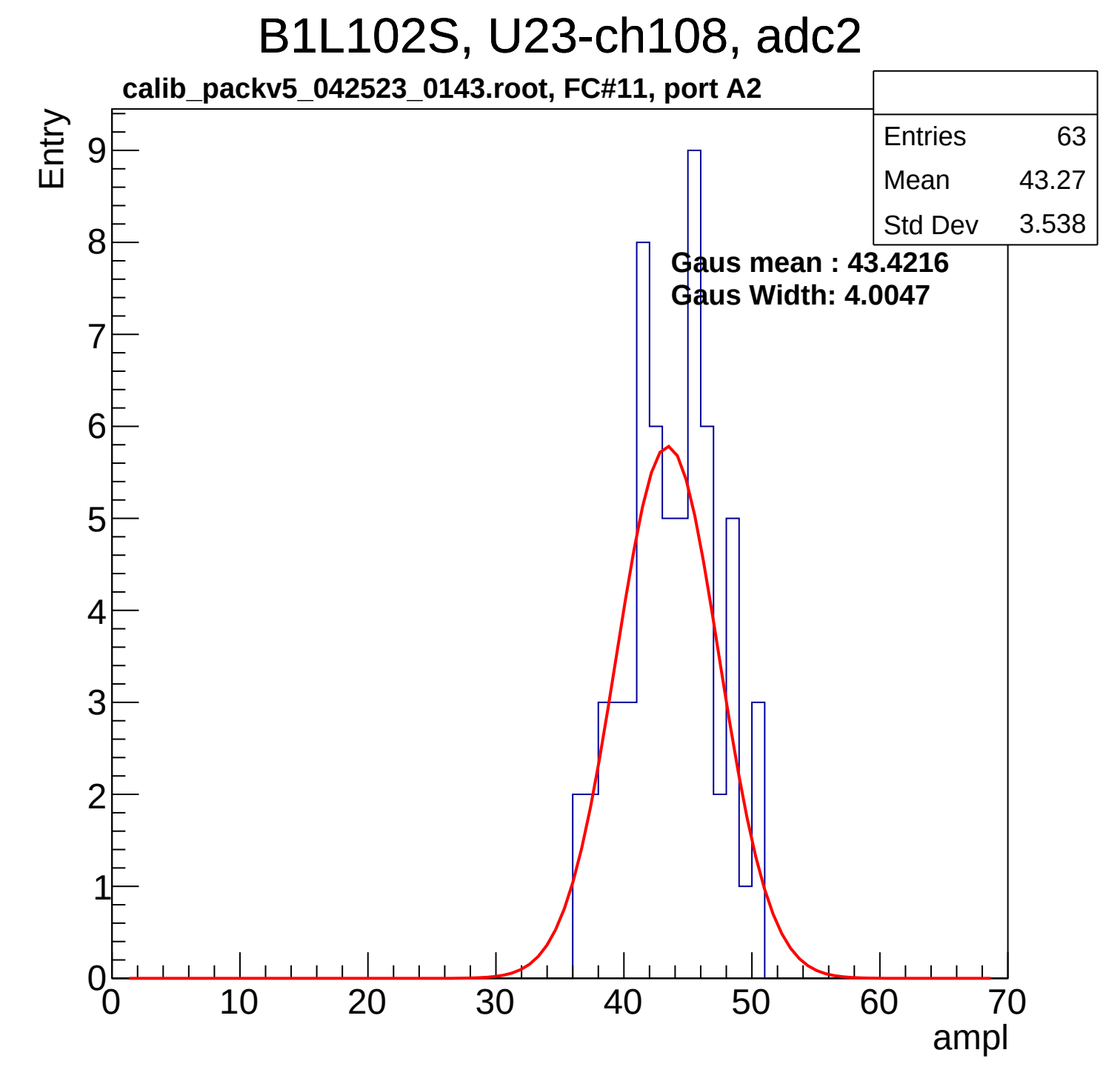

B1L102S, U23-ch107, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.91
Std Dev 9.143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

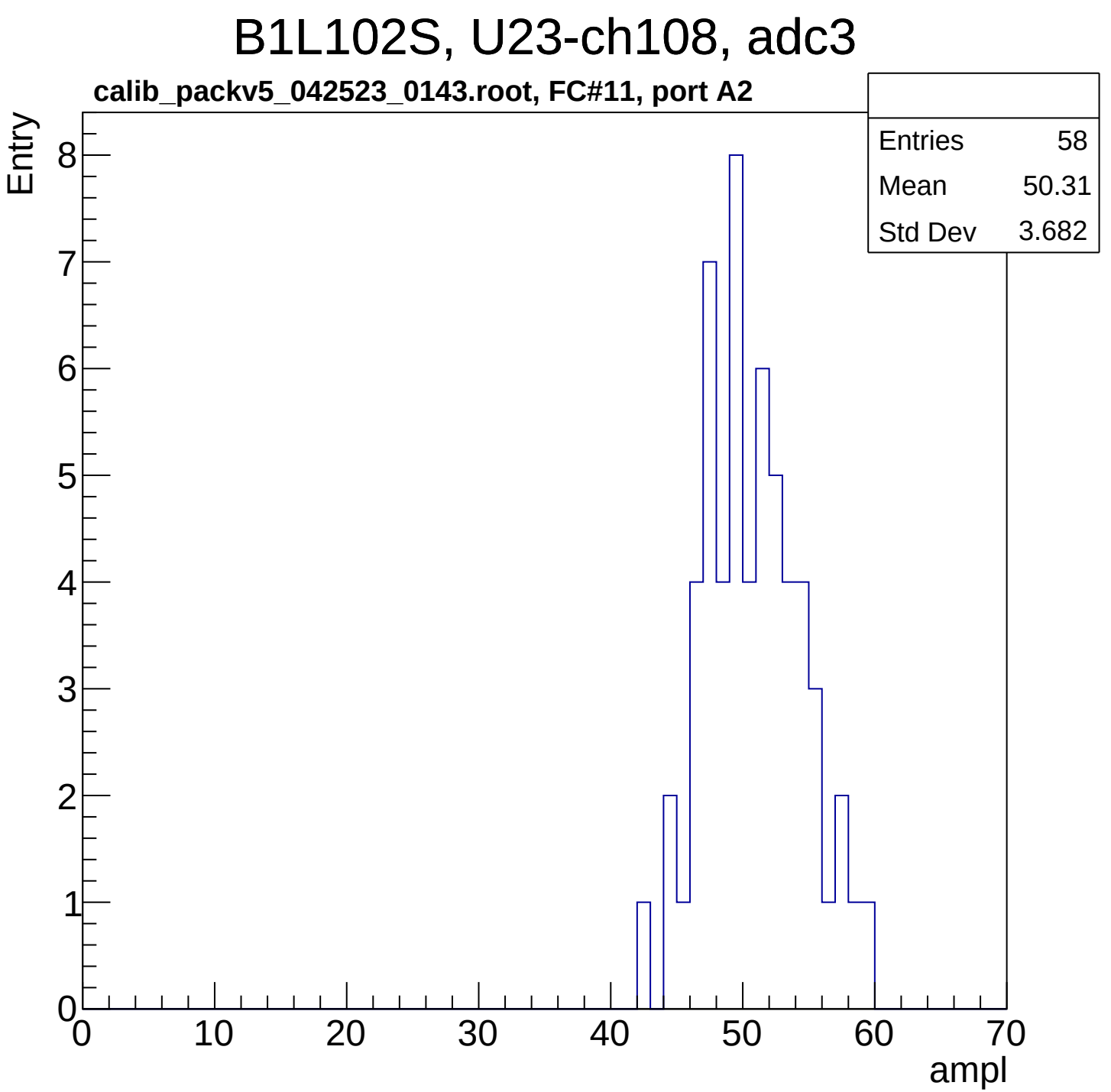

B1L102S, U23-ch107, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

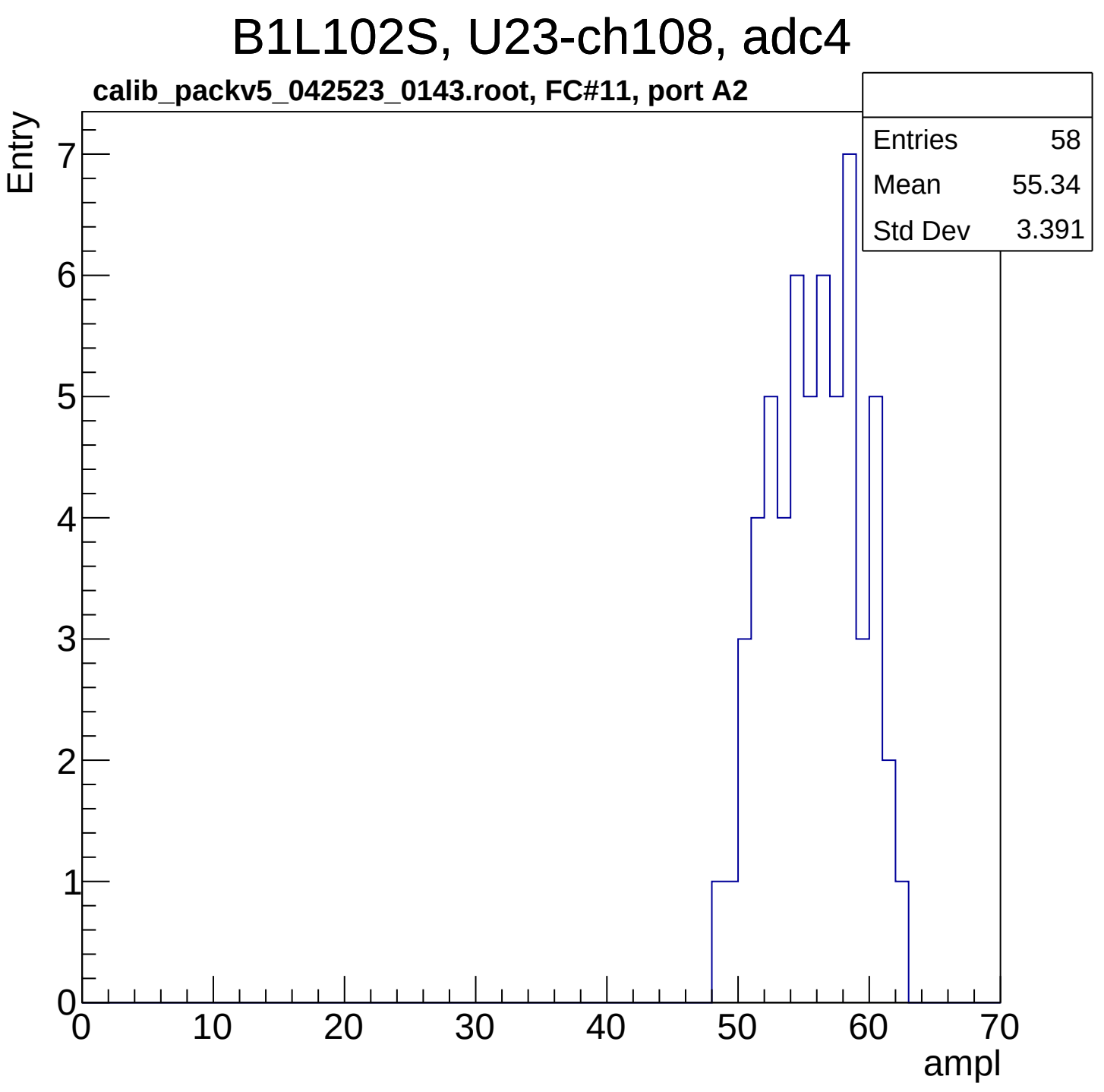

B1L102S, U23-ch107, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

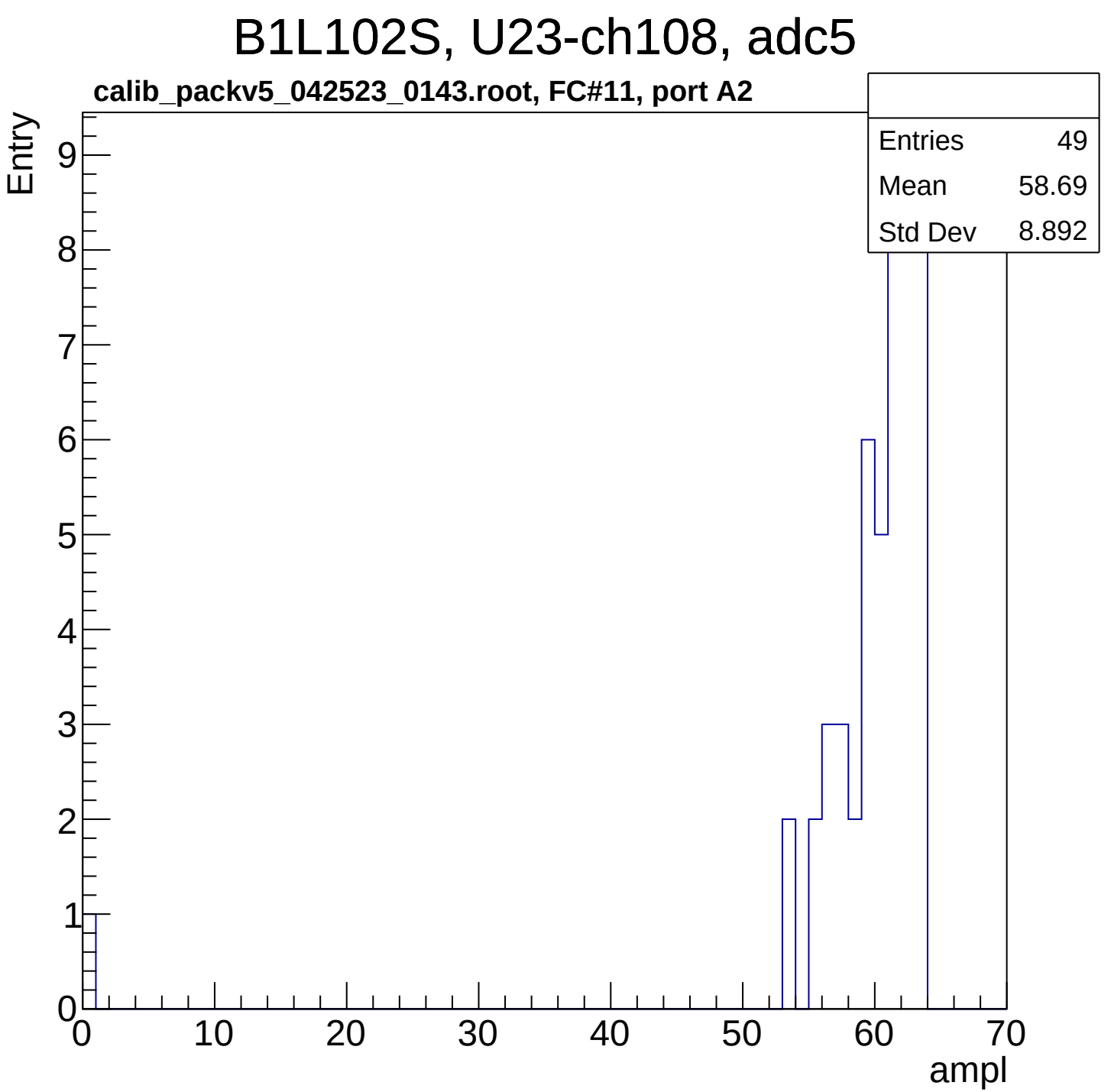

B1L102S, U23-ch108, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 29.28
Std Dev 3.124
Gaus mean : 29.9180
Gaus Width: 3.6001
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

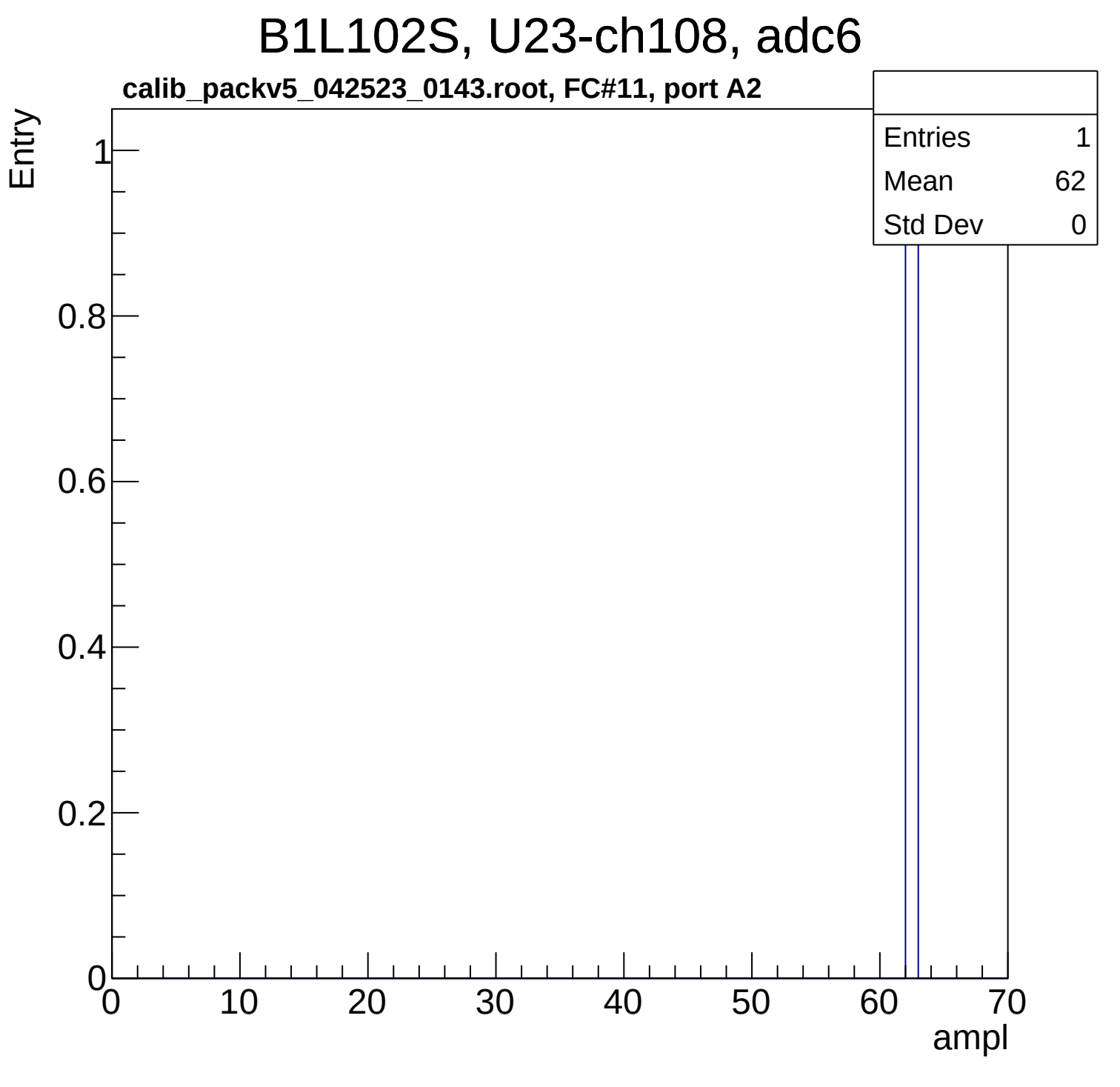

B1L102S, U23-ch108, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 91
Mean 36.31
Std Dev 4.132
Gaus mean : 36.7807
Gaus Width: 4.2831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

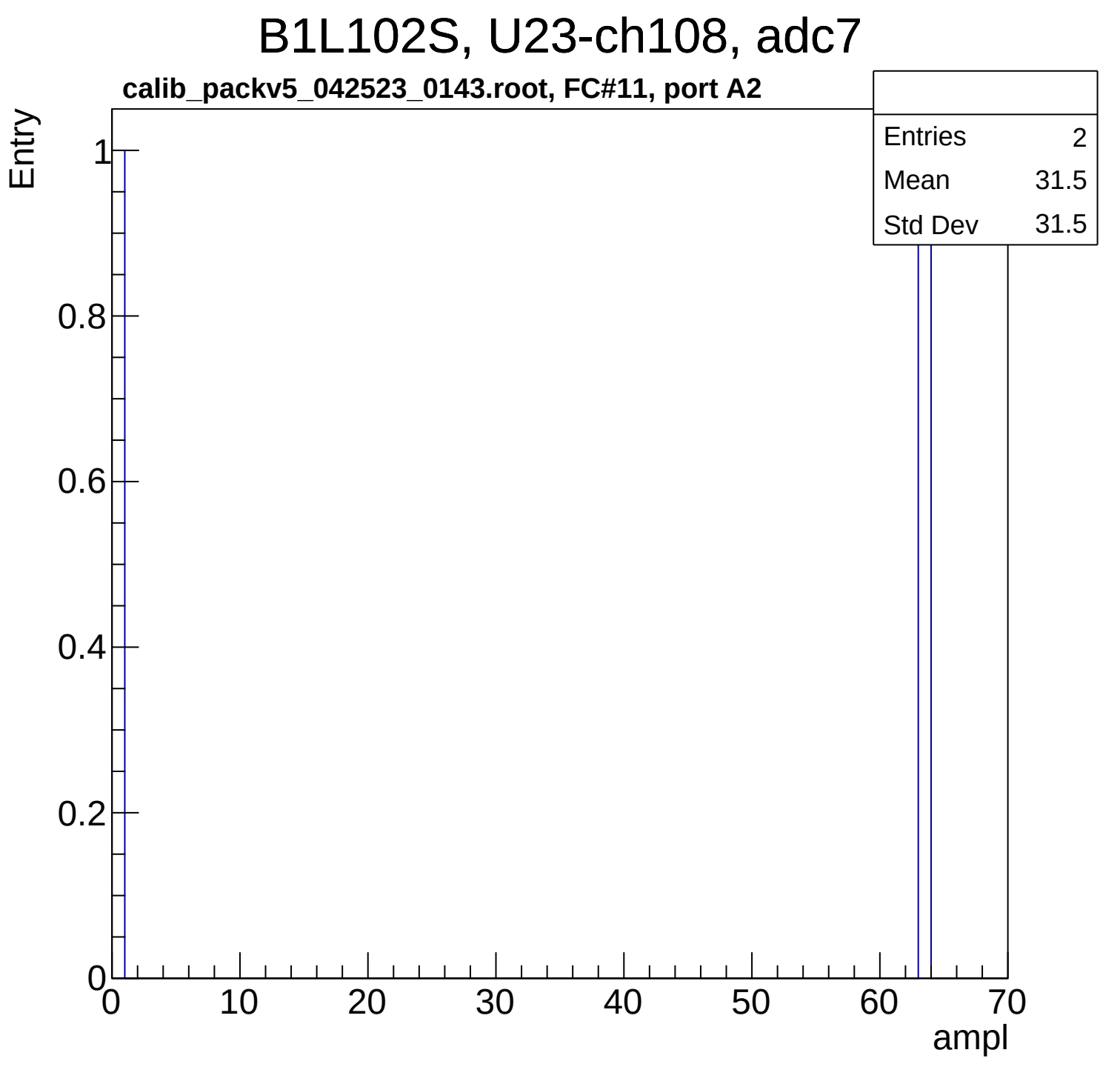

B1L102S, U23-ch108, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 43.27
Std Dev 3.538
Gaus mean : 43.4216
Gaus Width: 4.0047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

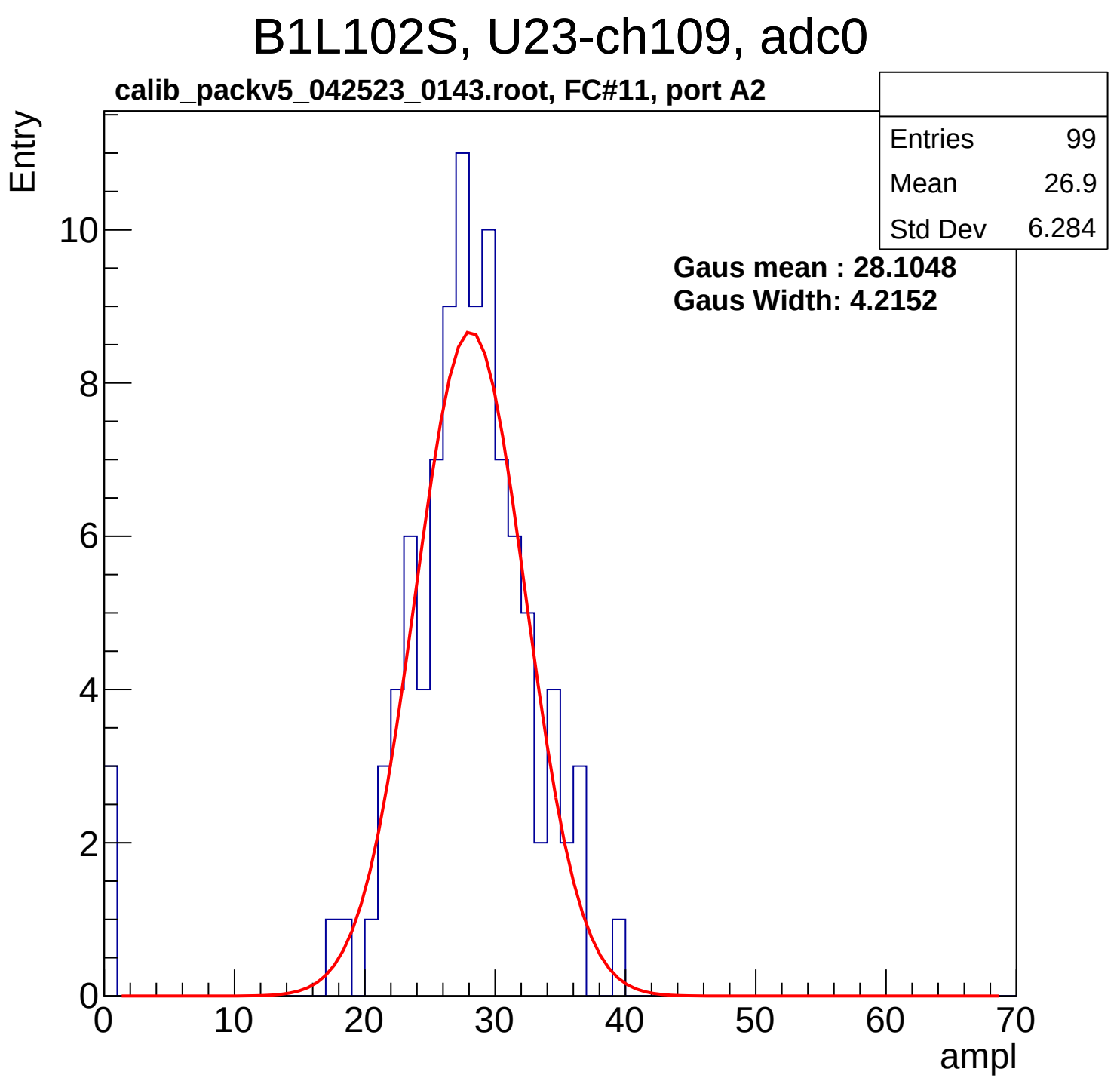

B1L102S, U23-ch108, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 50.31
Std Dev 3.682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

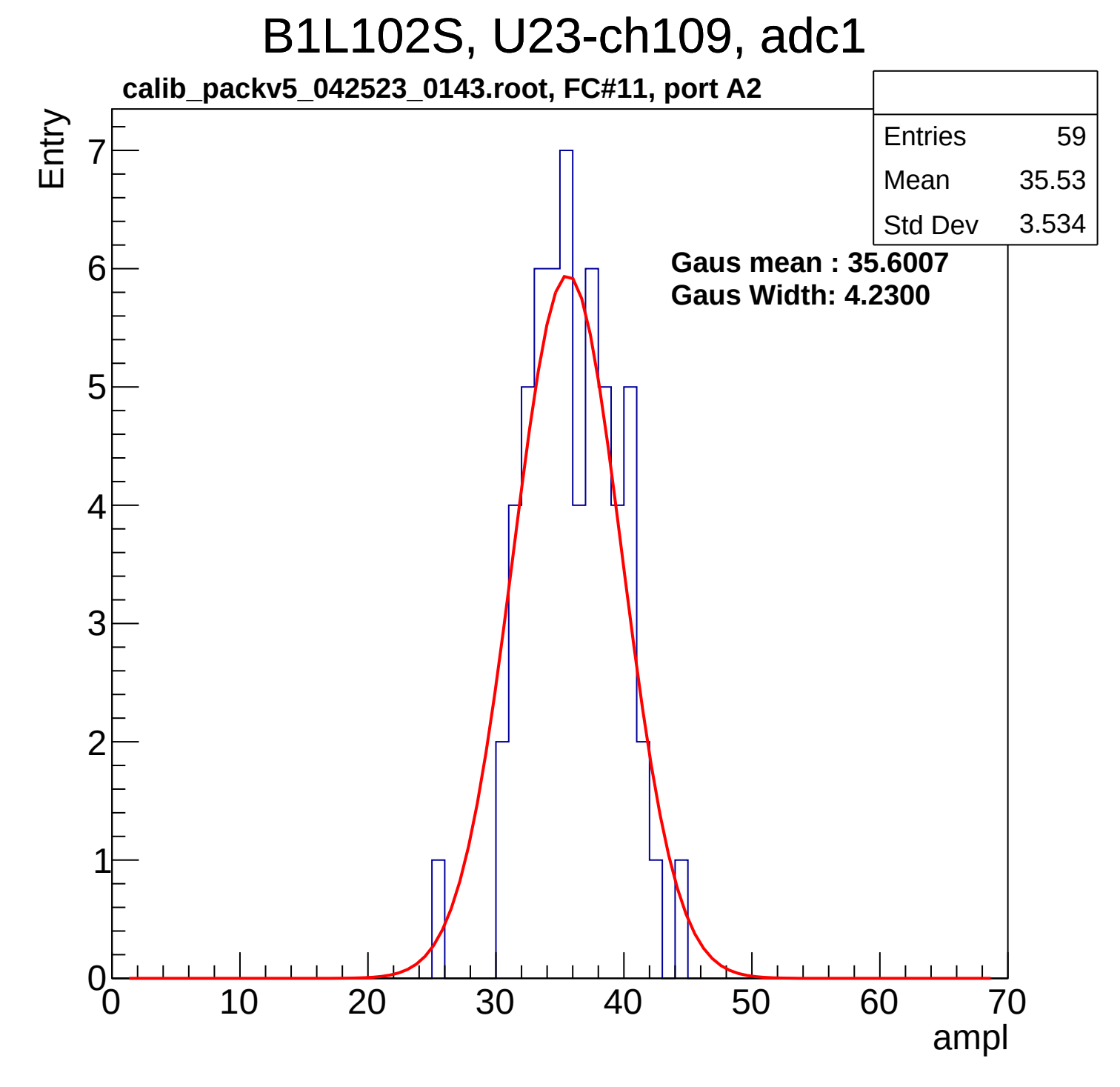

B1L102S, U23-ch108, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.34
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

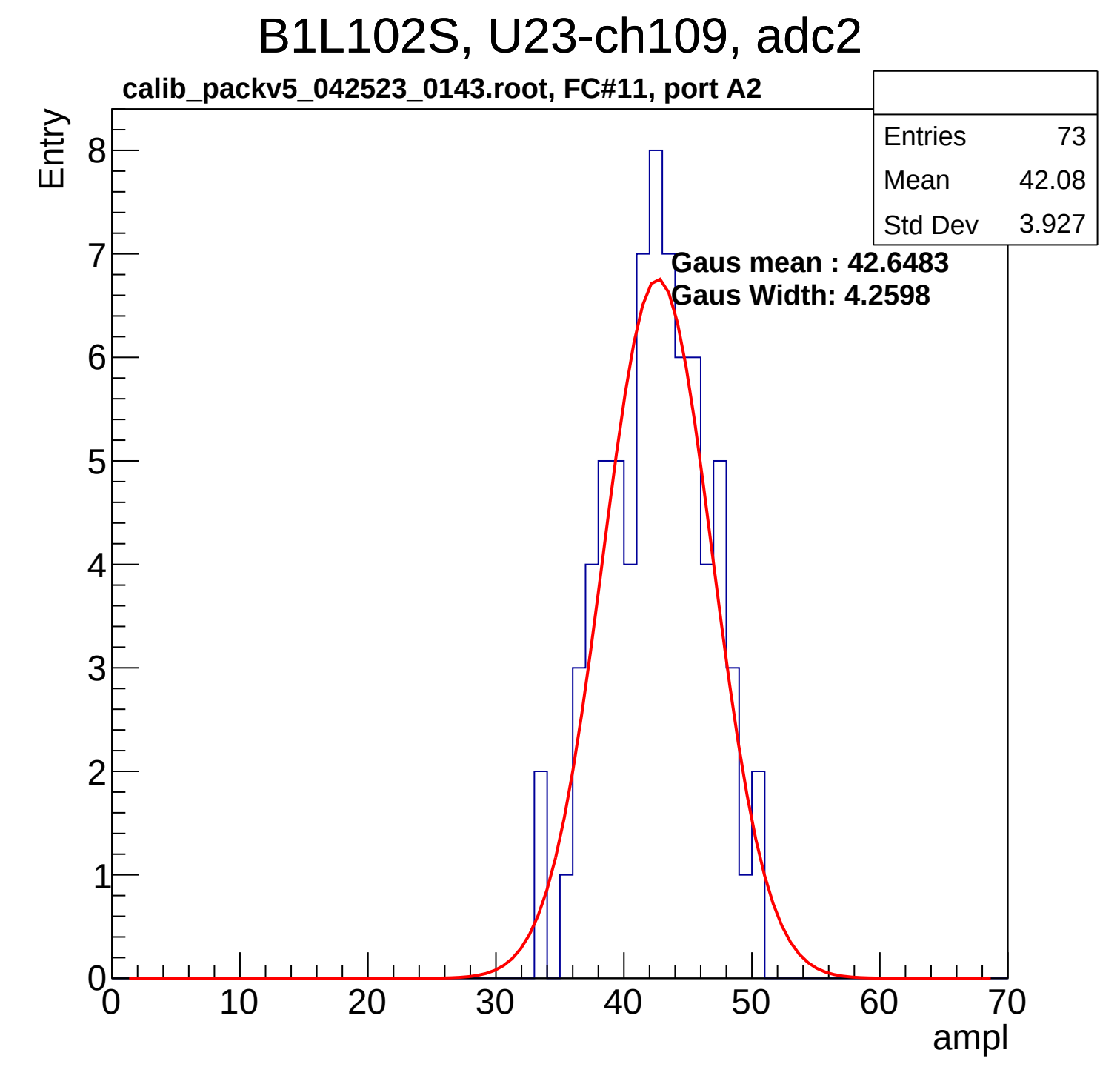

B1L102S, U23-ch108, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.69
Std Dev 8.892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

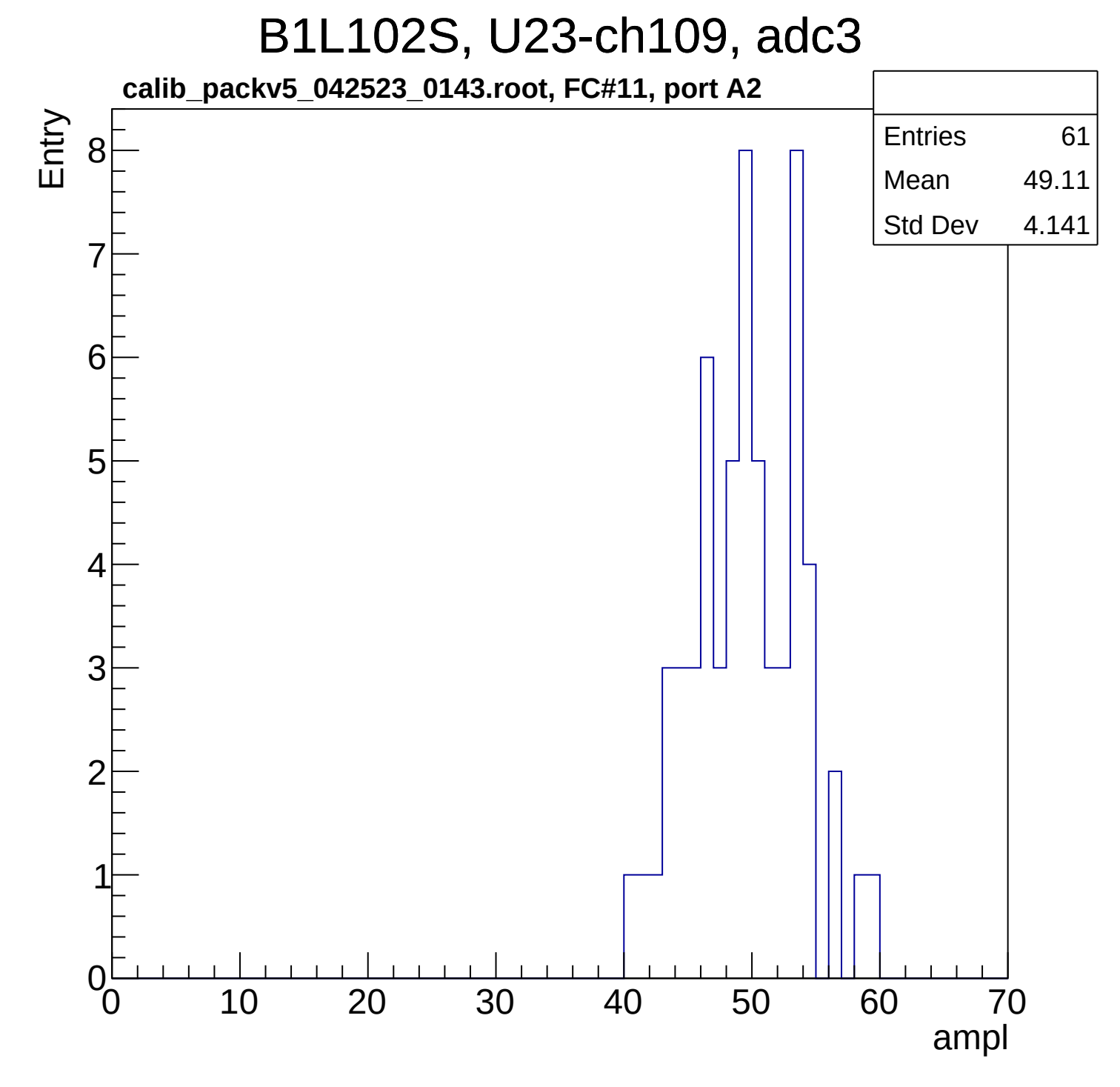

B1L102S, U23-ch108, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

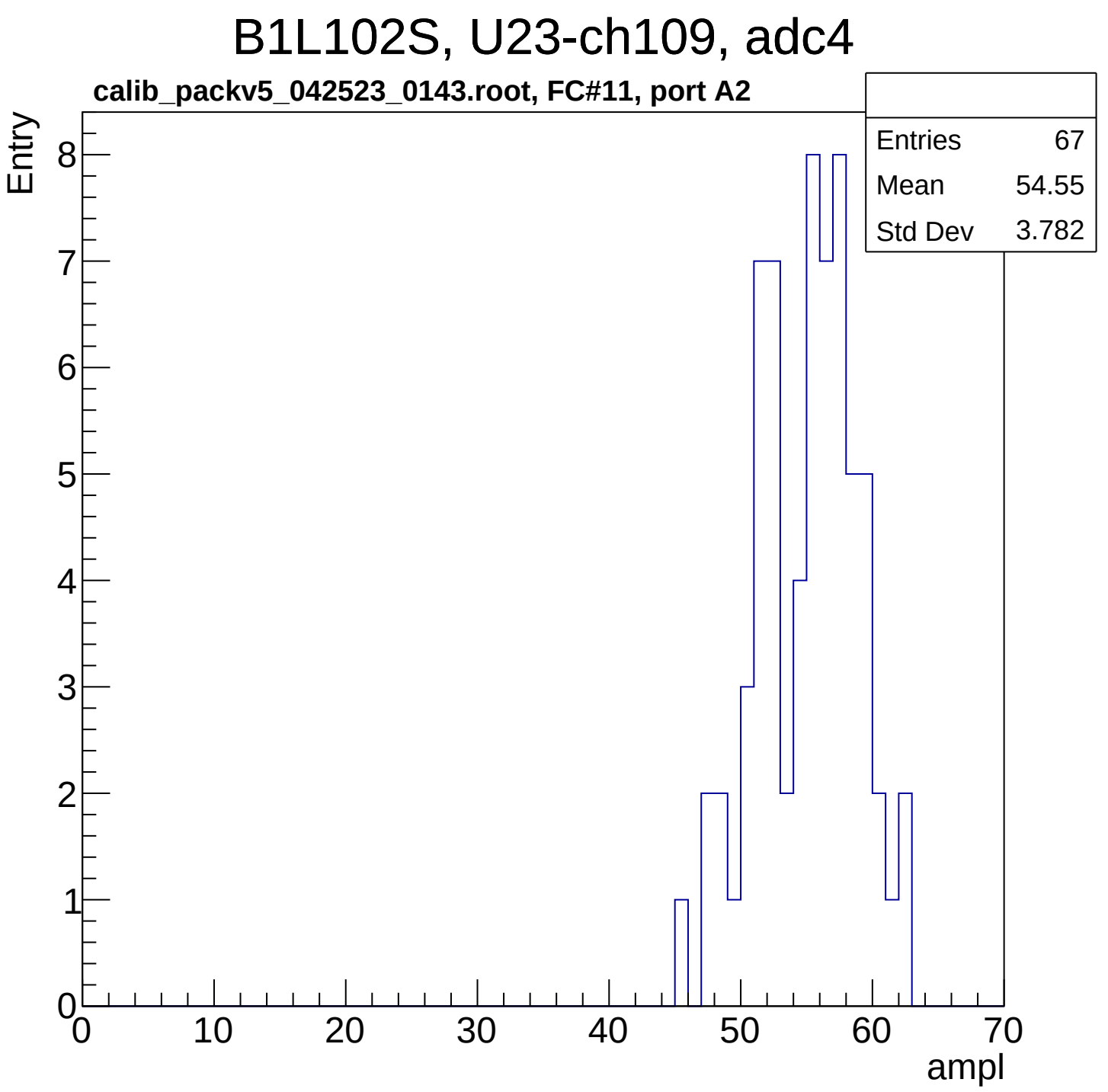

B1L102S, U23-ch108, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

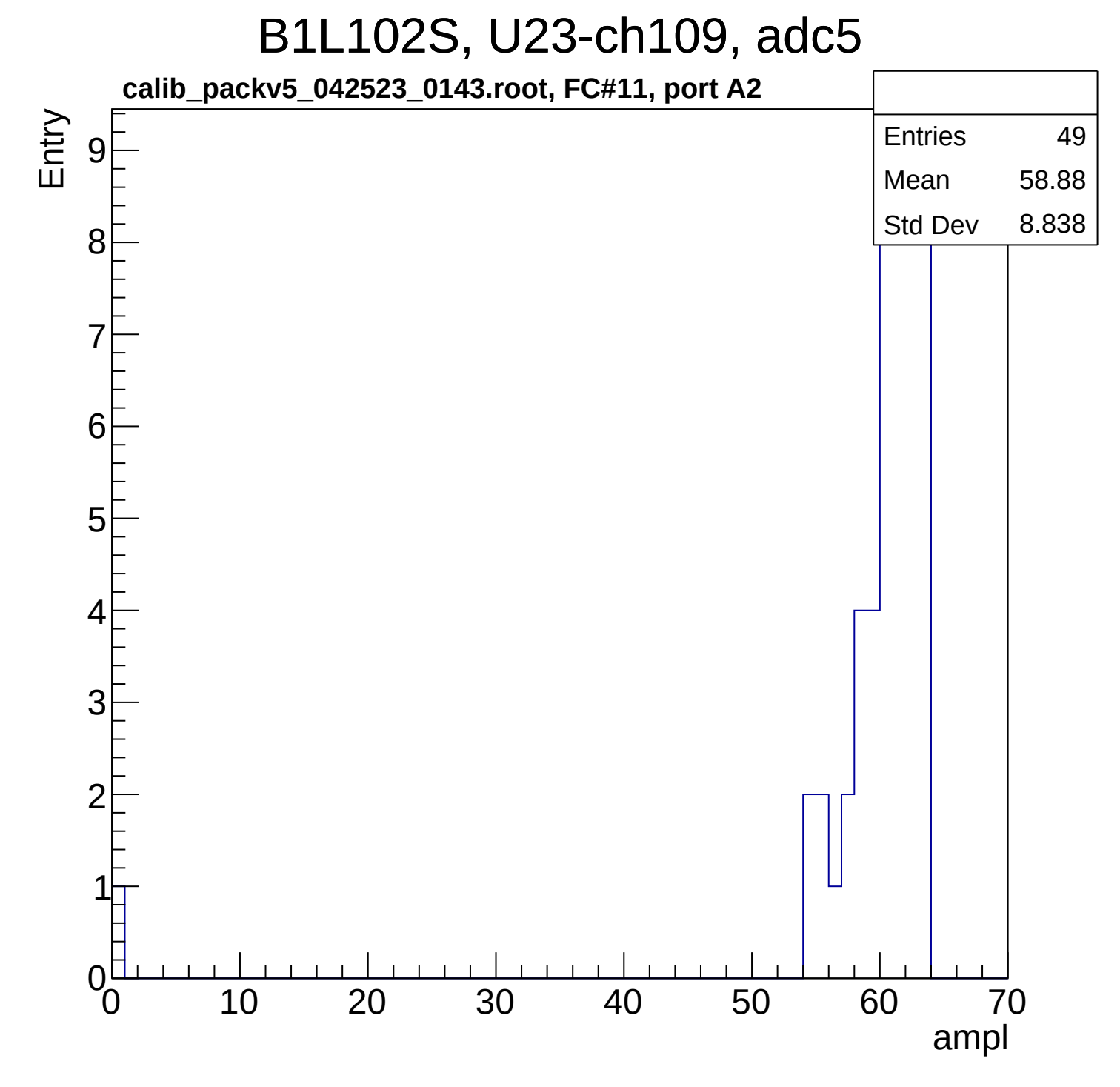

B1L102S, U23-ch109, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 99
Mean 26.9
Std Dev 6.284
Gaus mean : 28.1048
Gaus Width: 4.2152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

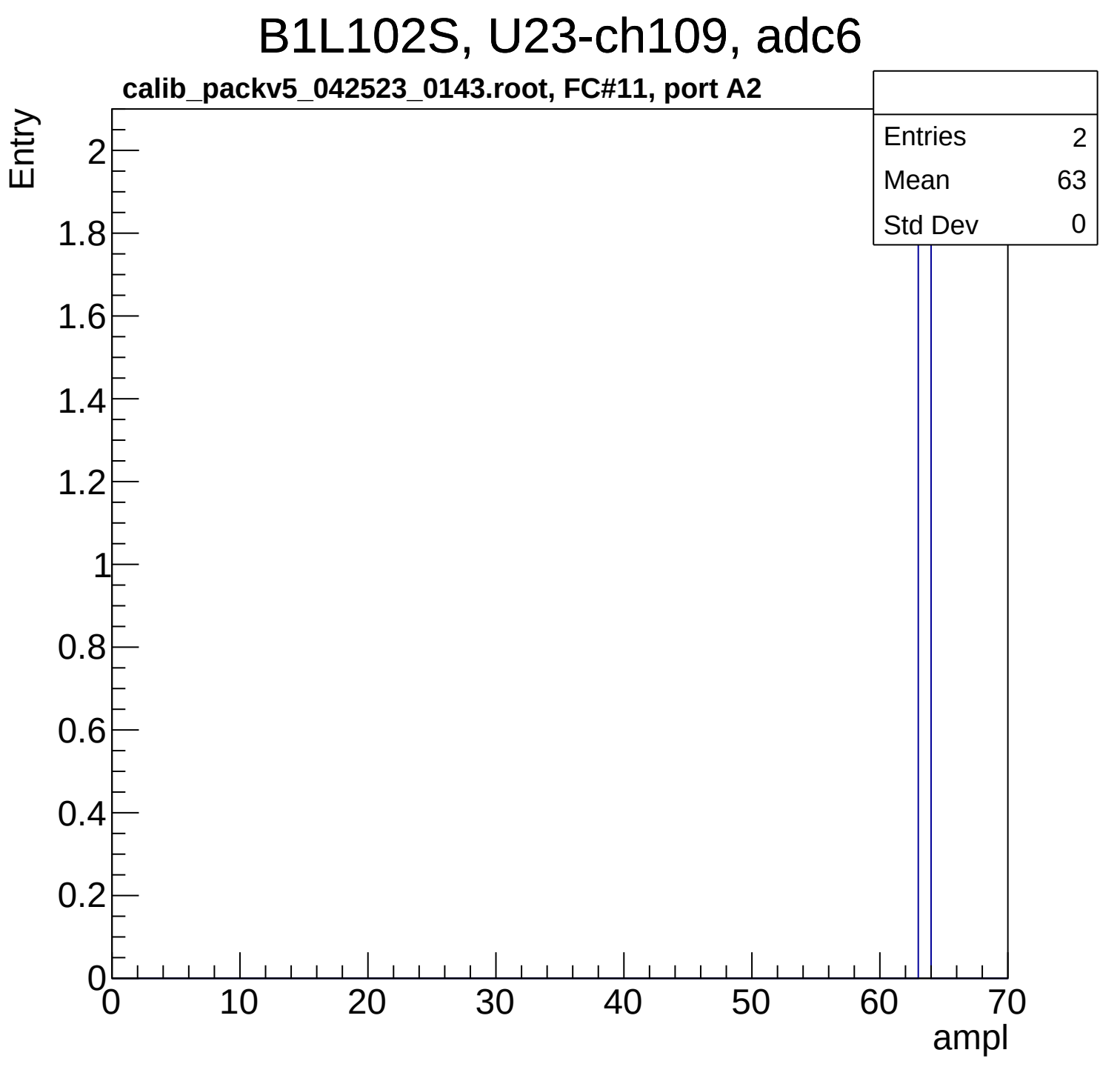

B1L102S, U23-ch109, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 35.53
Std Dev 3.534
Gaus mean : 35.6007
Gaus Width: 4.2300
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

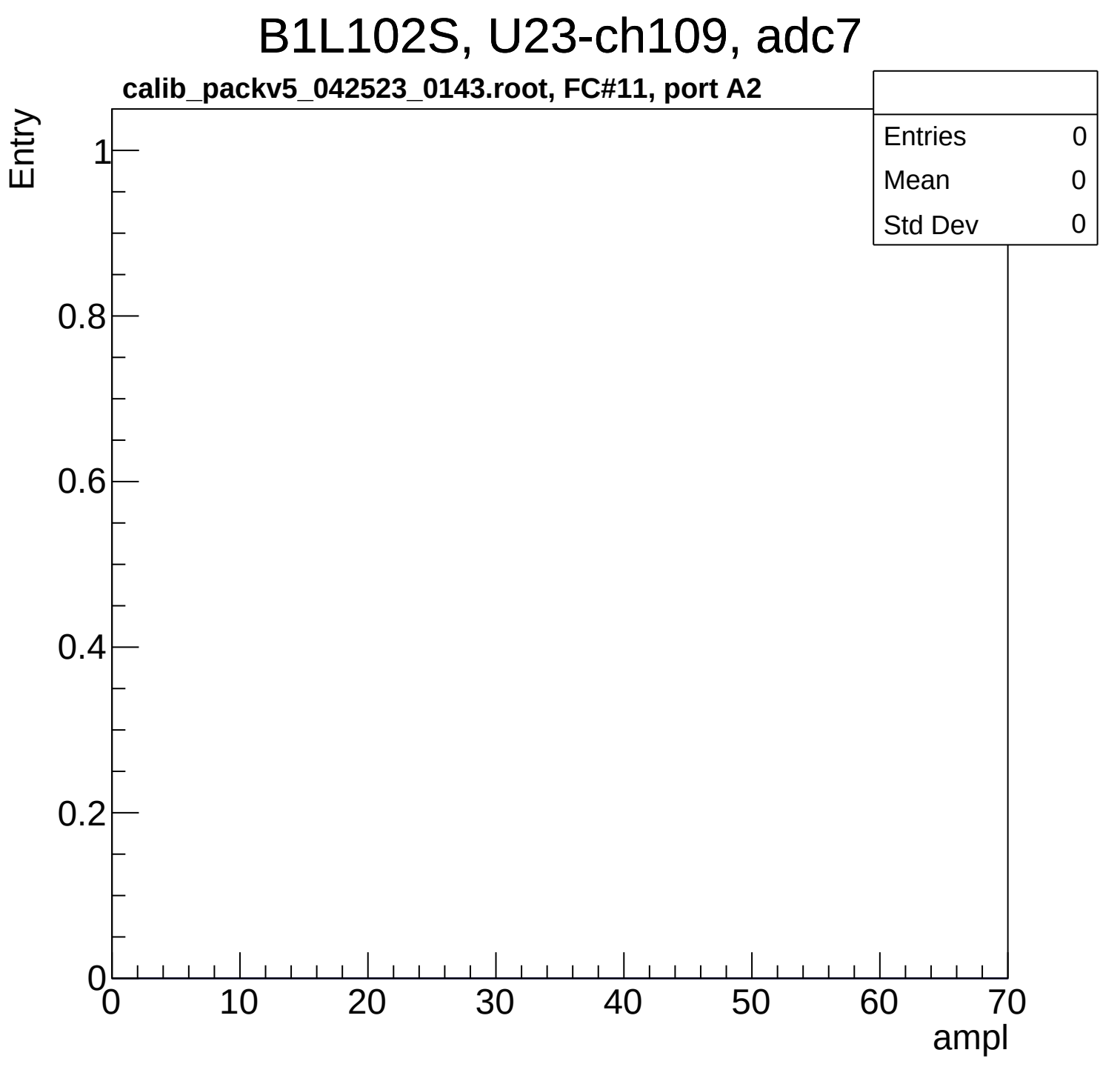

B1L102S, U23-ch109, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 42.08
Std Dev 3.927
Gaus mean : 42.6483
Gaus Width: 4.2598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

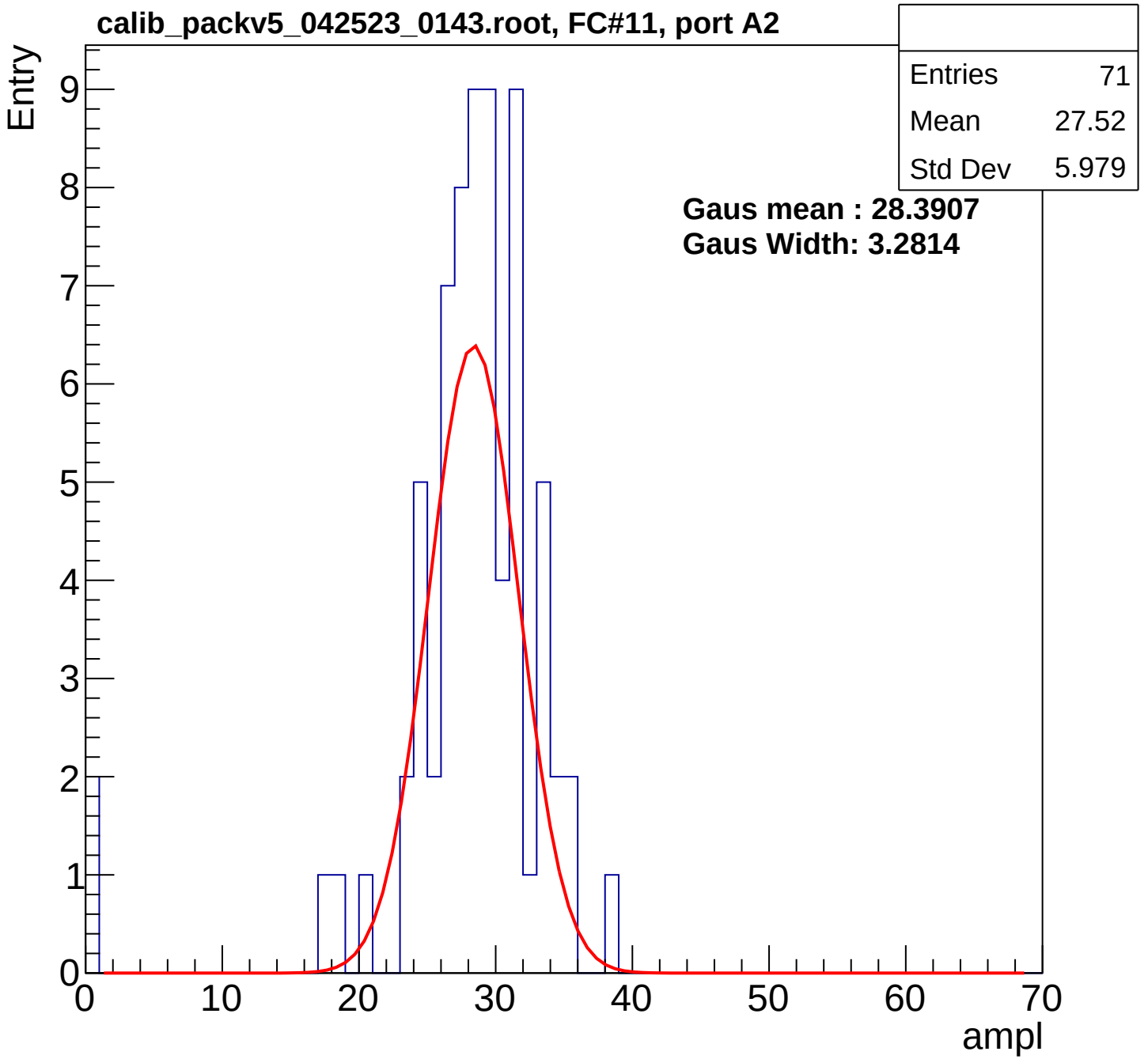

B1L102S, U23-ch109, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 49.11
Std Dev 4.141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

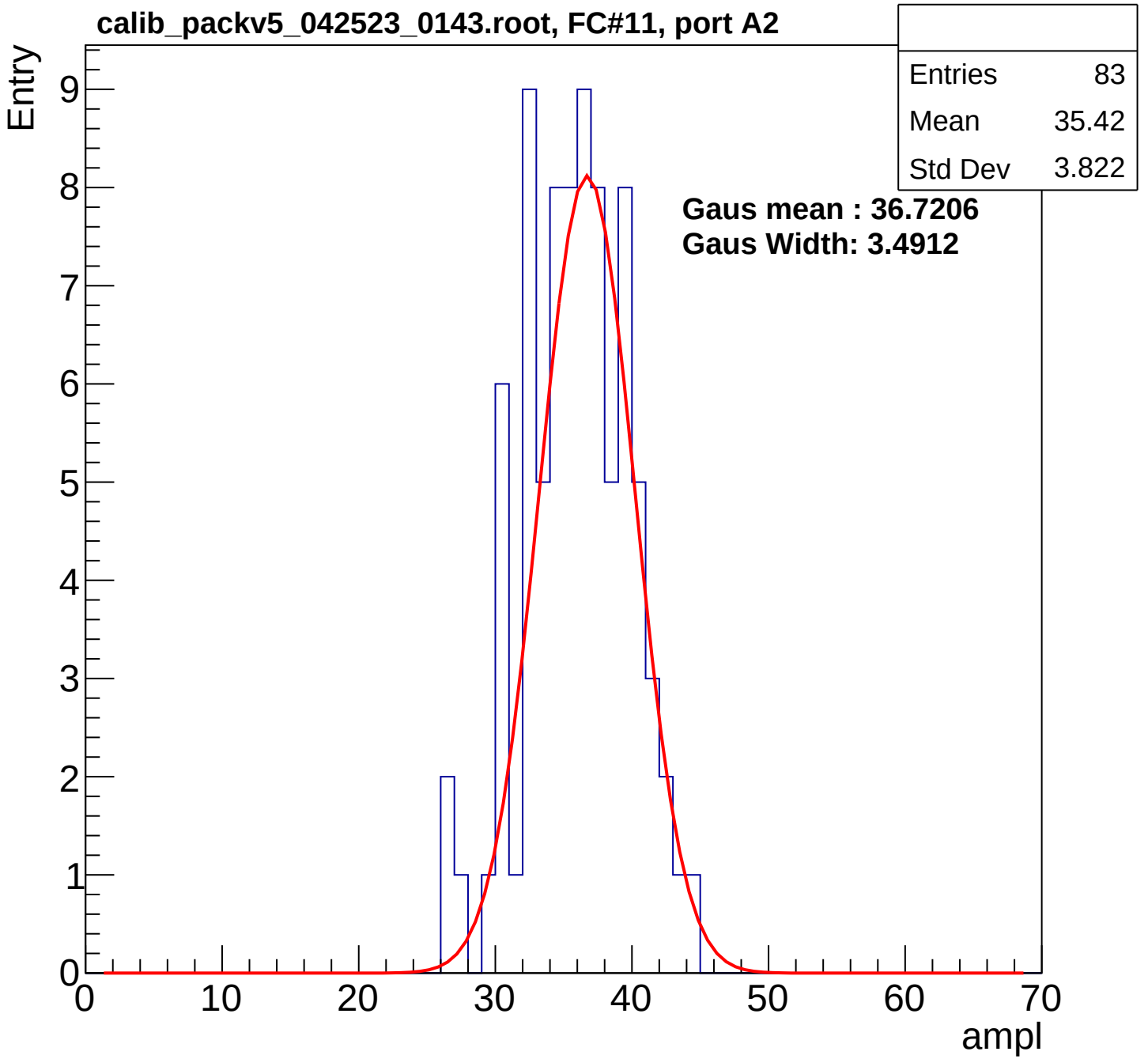

B1L102S, U23-ch109, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 54.55
Std Dev 3.782
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

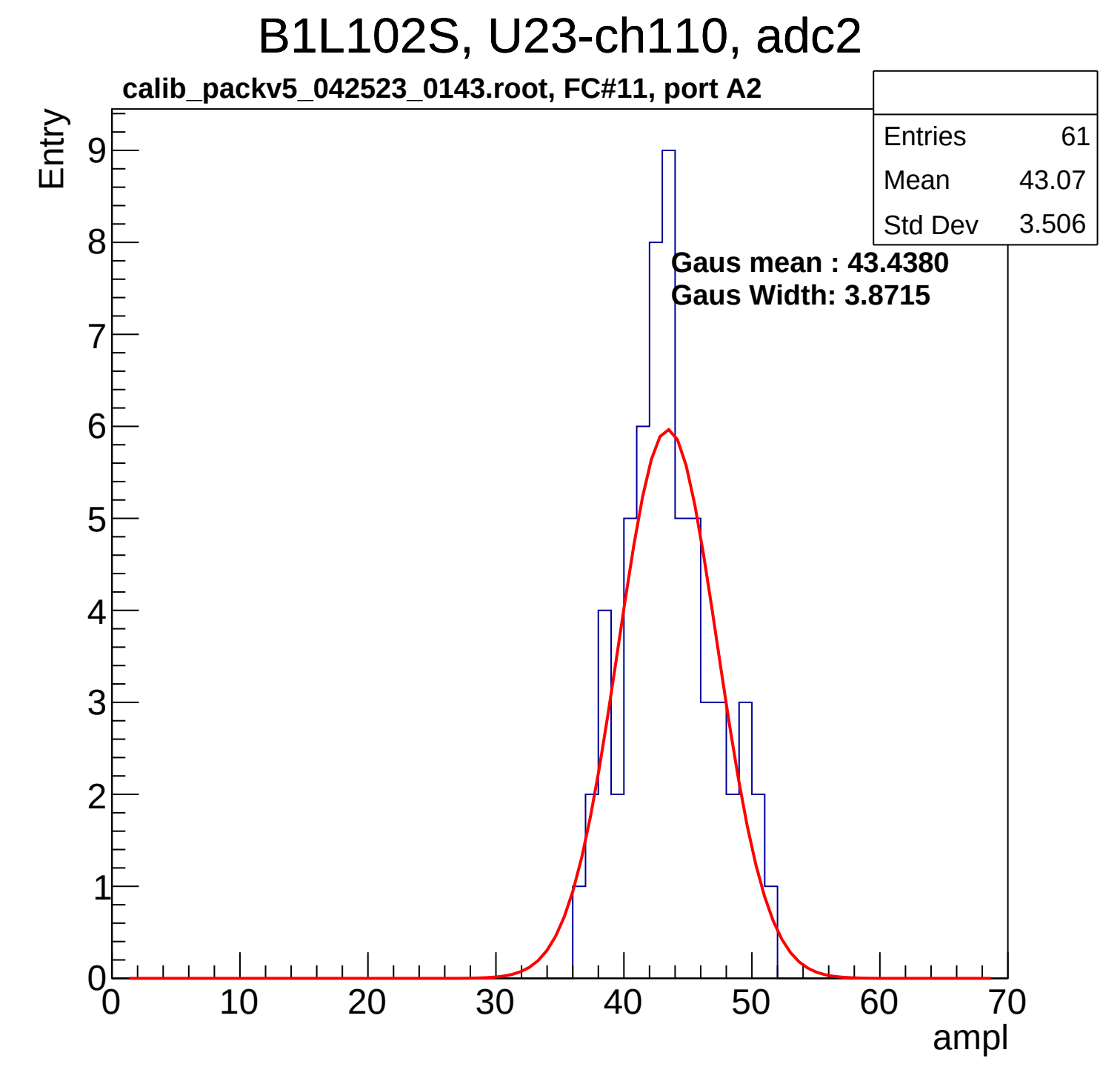

B1L102S, U23-ch109, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.88
Std Dev 8.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

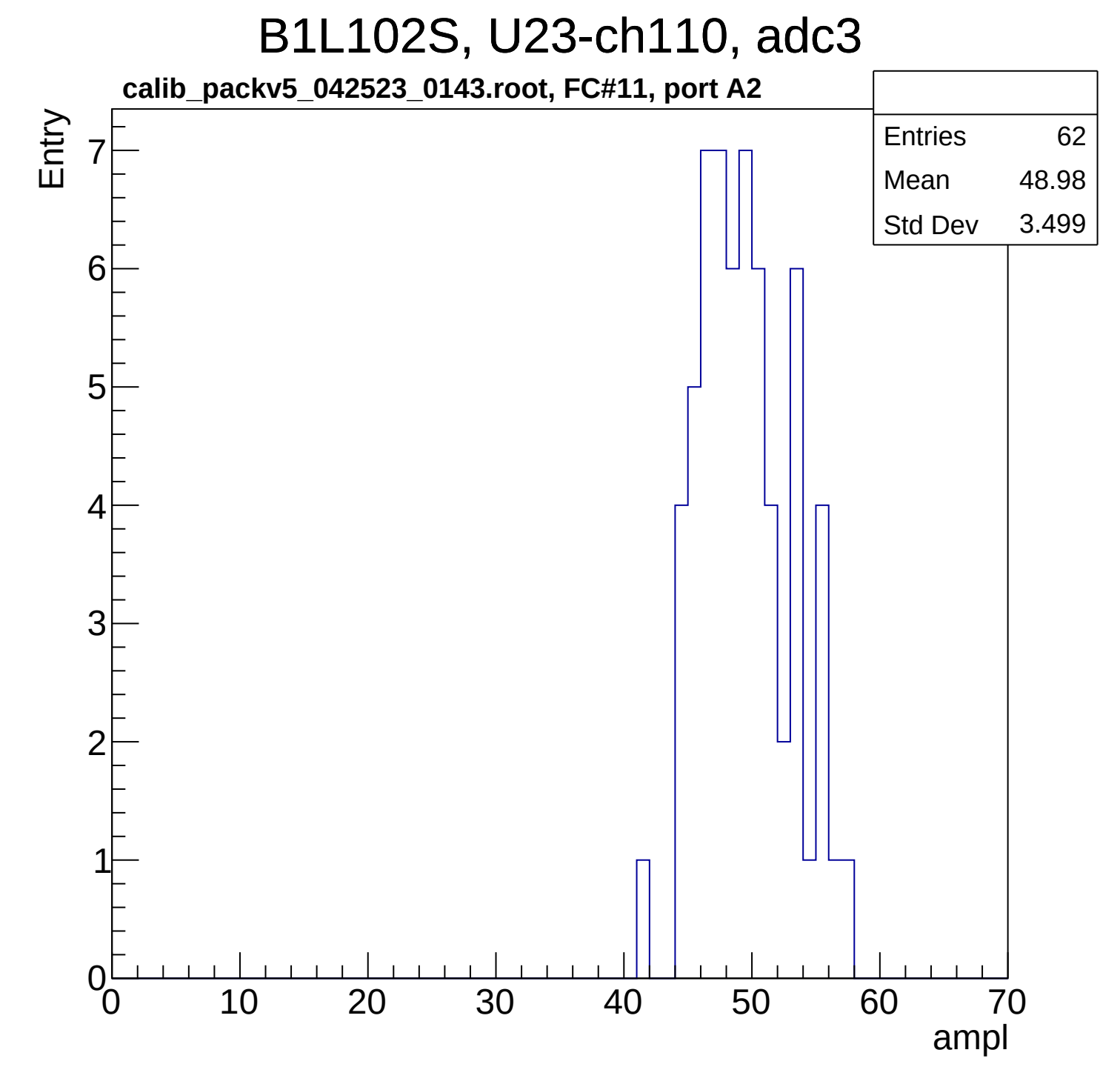

B1L102S, U23-ch109, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

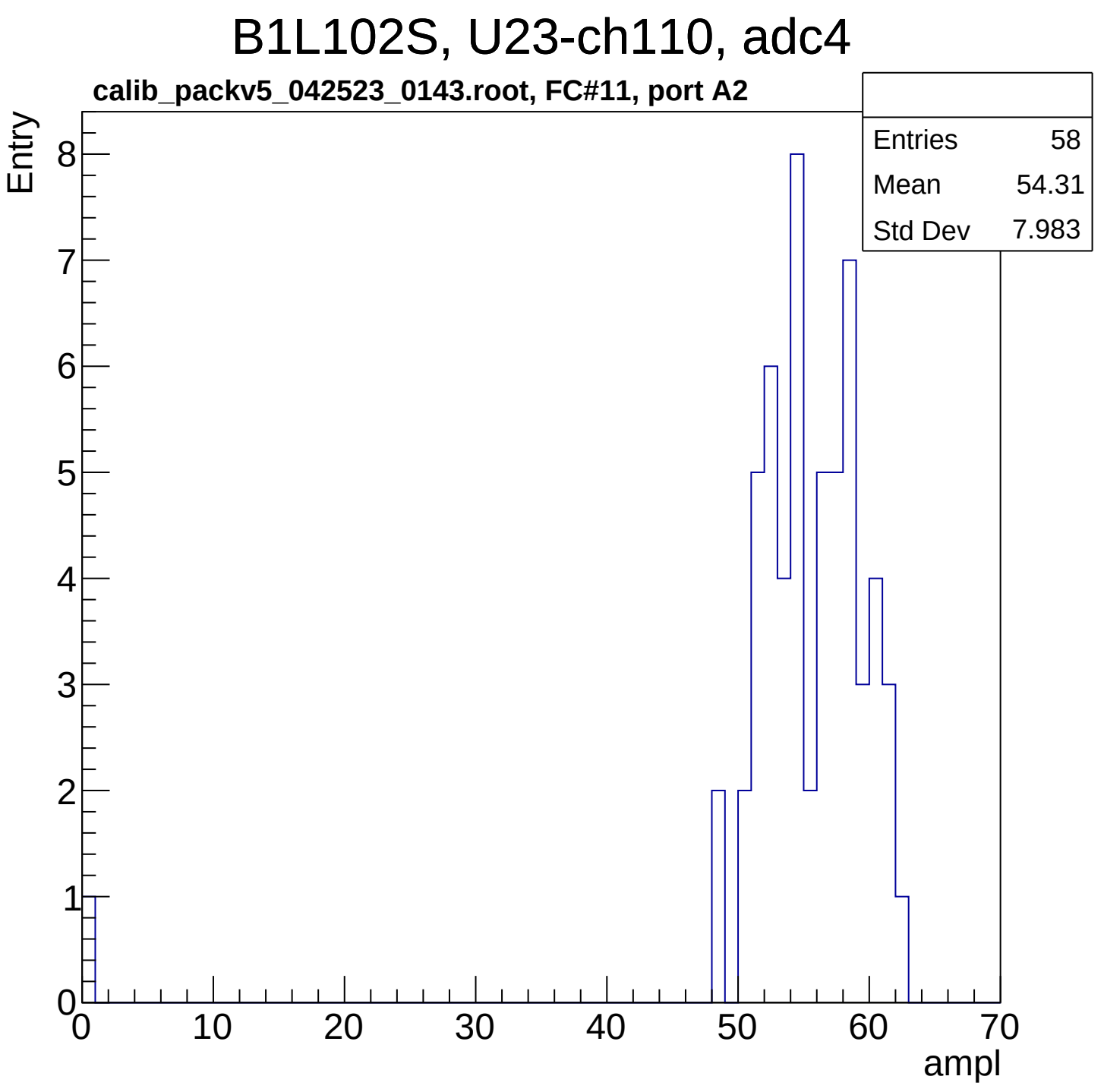

B1L102S, U23-ch109, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

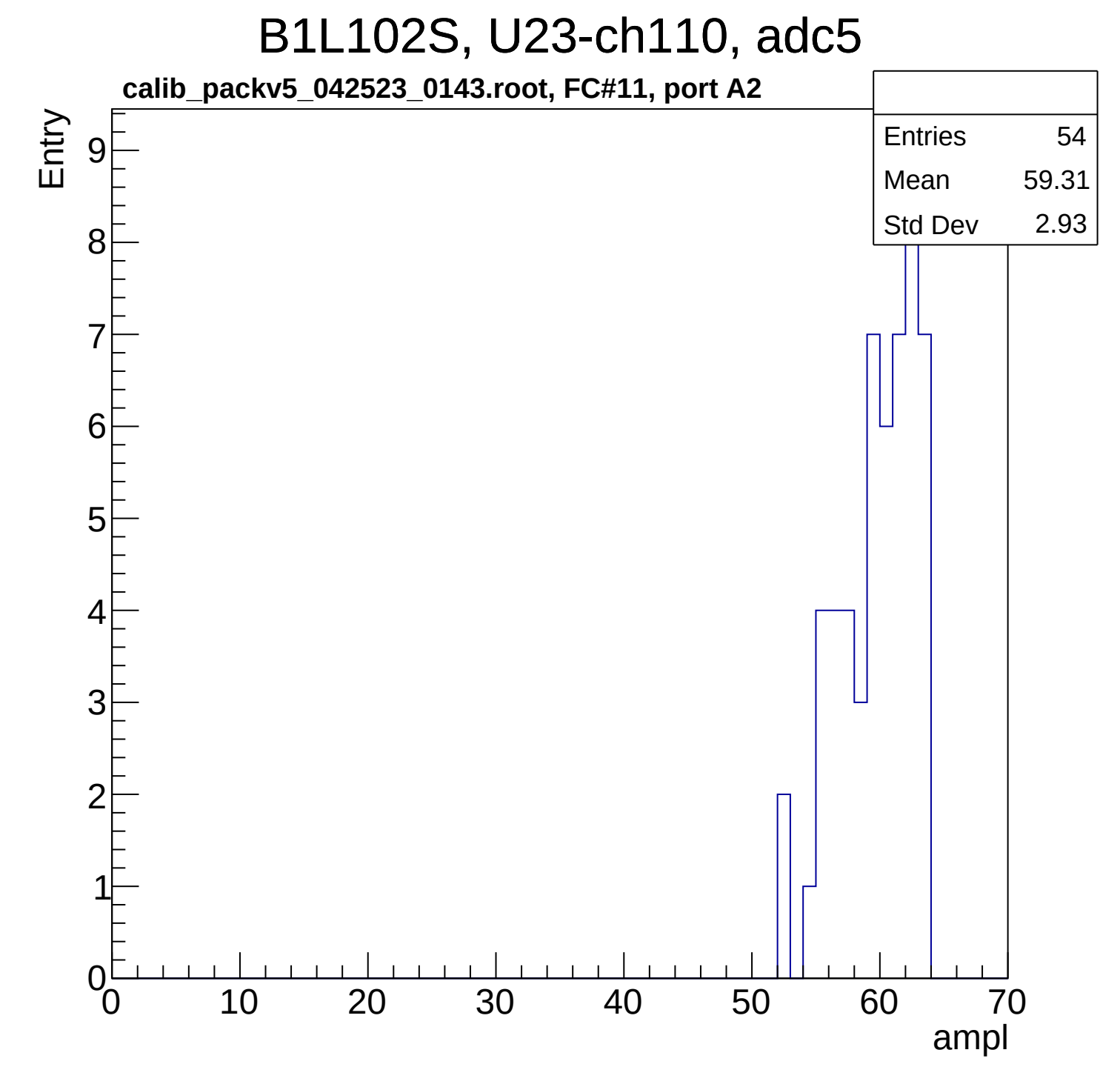

B1L102S, U23-ch110, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.52
Std Dev 5.979
Gaus mean : 28.3907
Gaus Width: 3.2814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

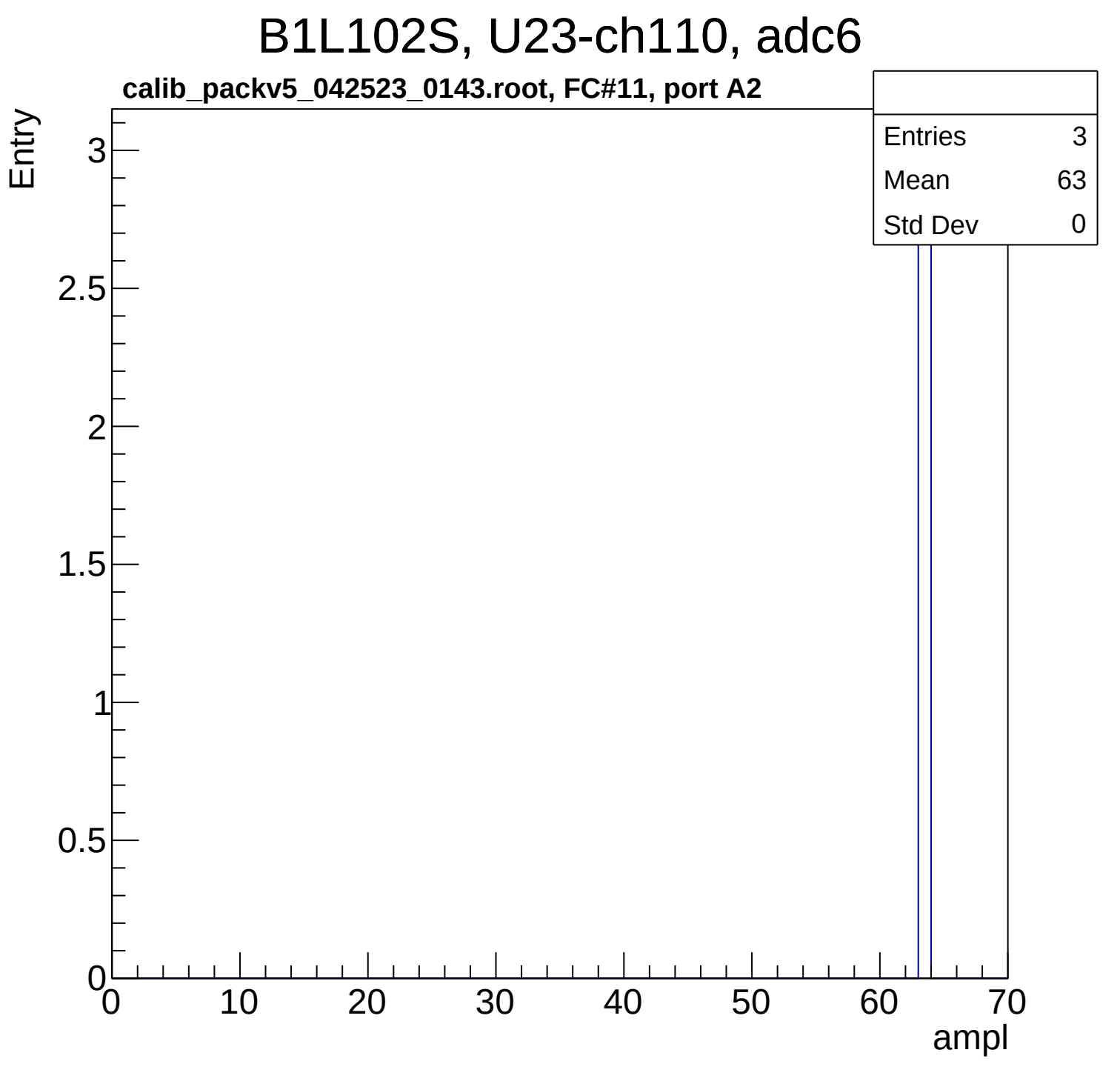

B1L102S, U23-ch110, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 35.42
Std Dev 3.822
Gaus mean : 36.7206
Gaus Width: 3.4912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

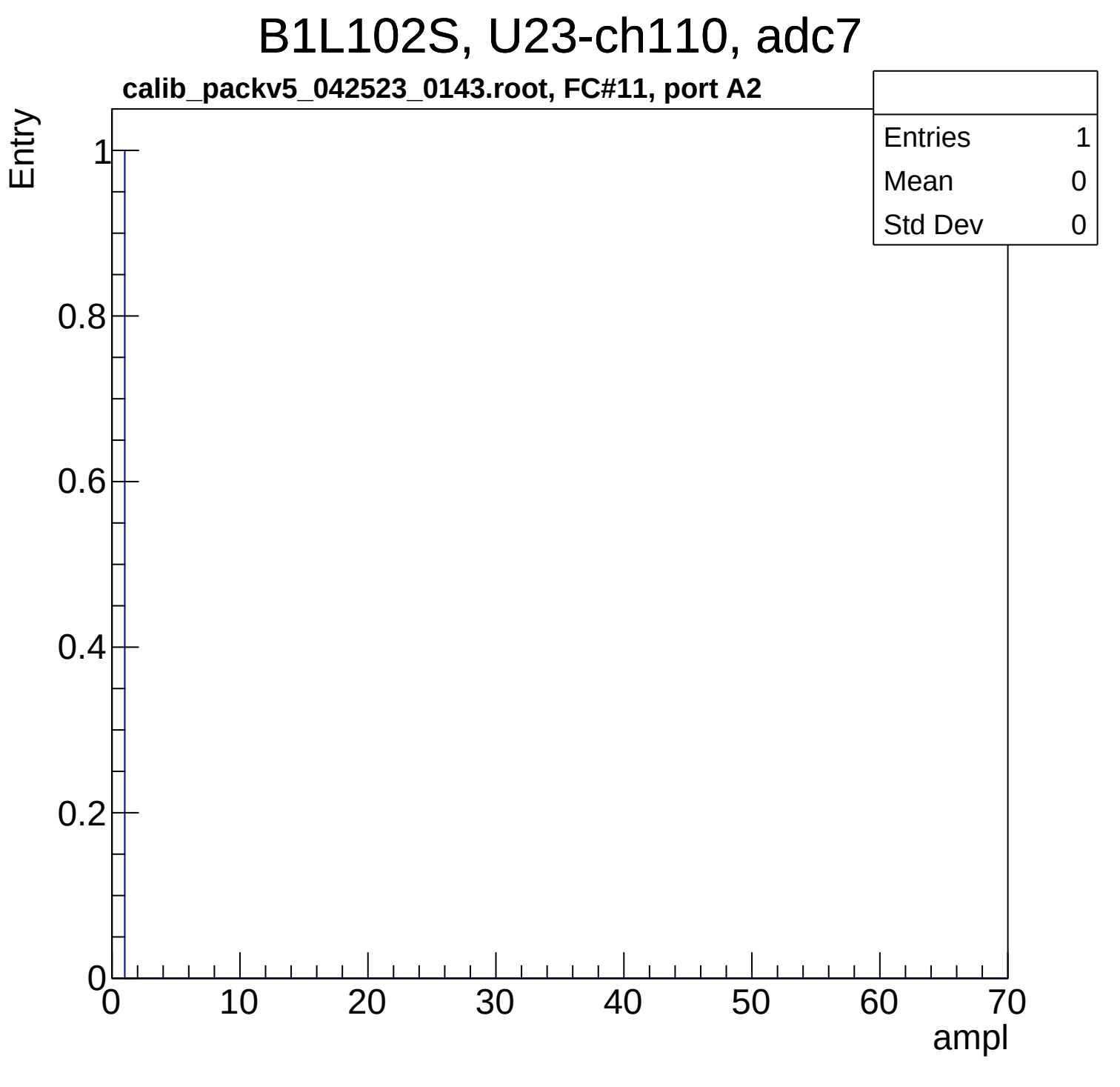

B1L102S, U23-ch110, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 43.07
Std Dev 3.506
Gaus mean : 43.4380
Gaus Width: 3.8715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

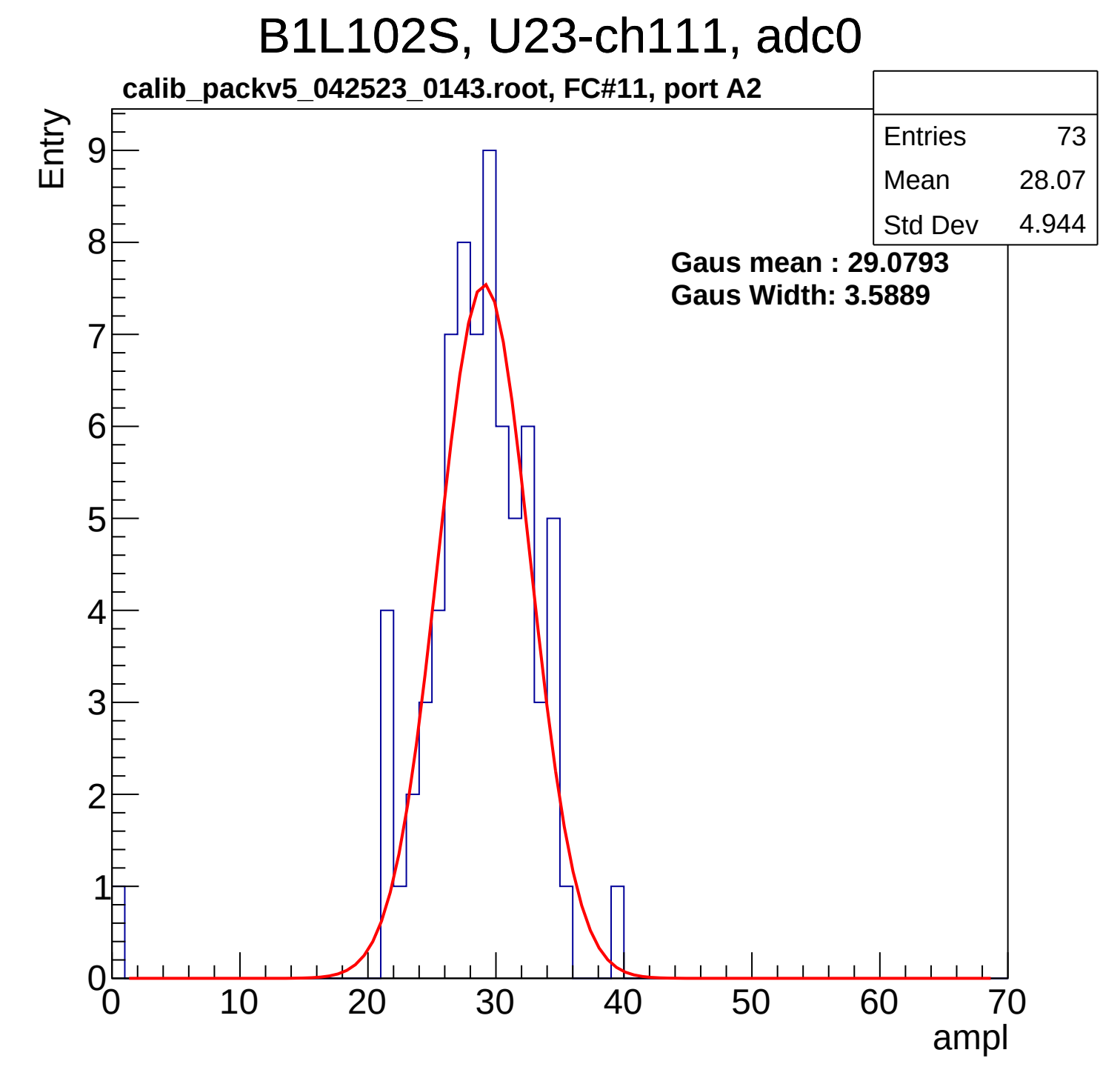

B1L102S, U23-ch110, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 48.98
Std Dev 3.499
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

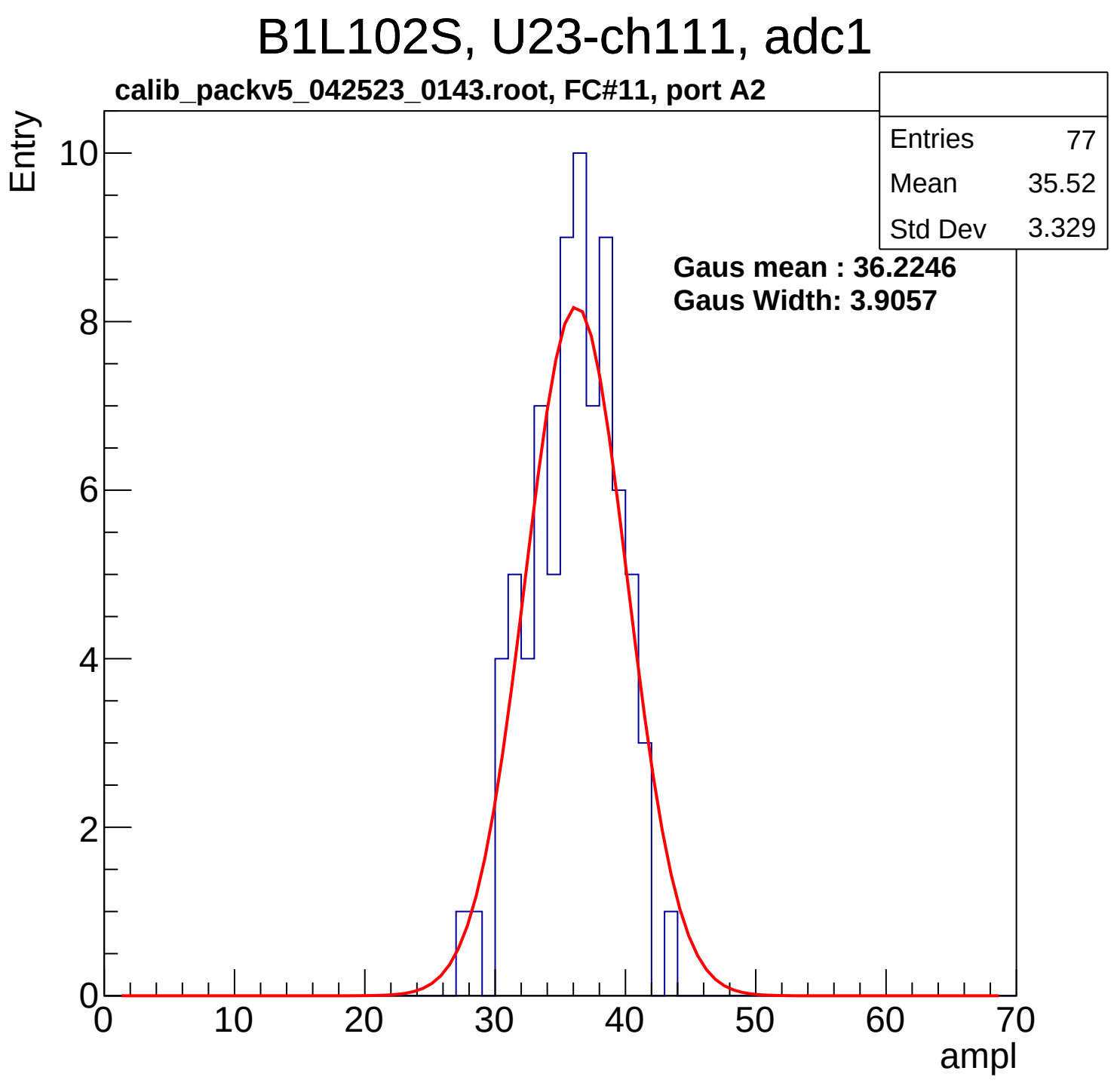

B1L102S, U23-ch110, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 54.31
Std Dev 7.983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

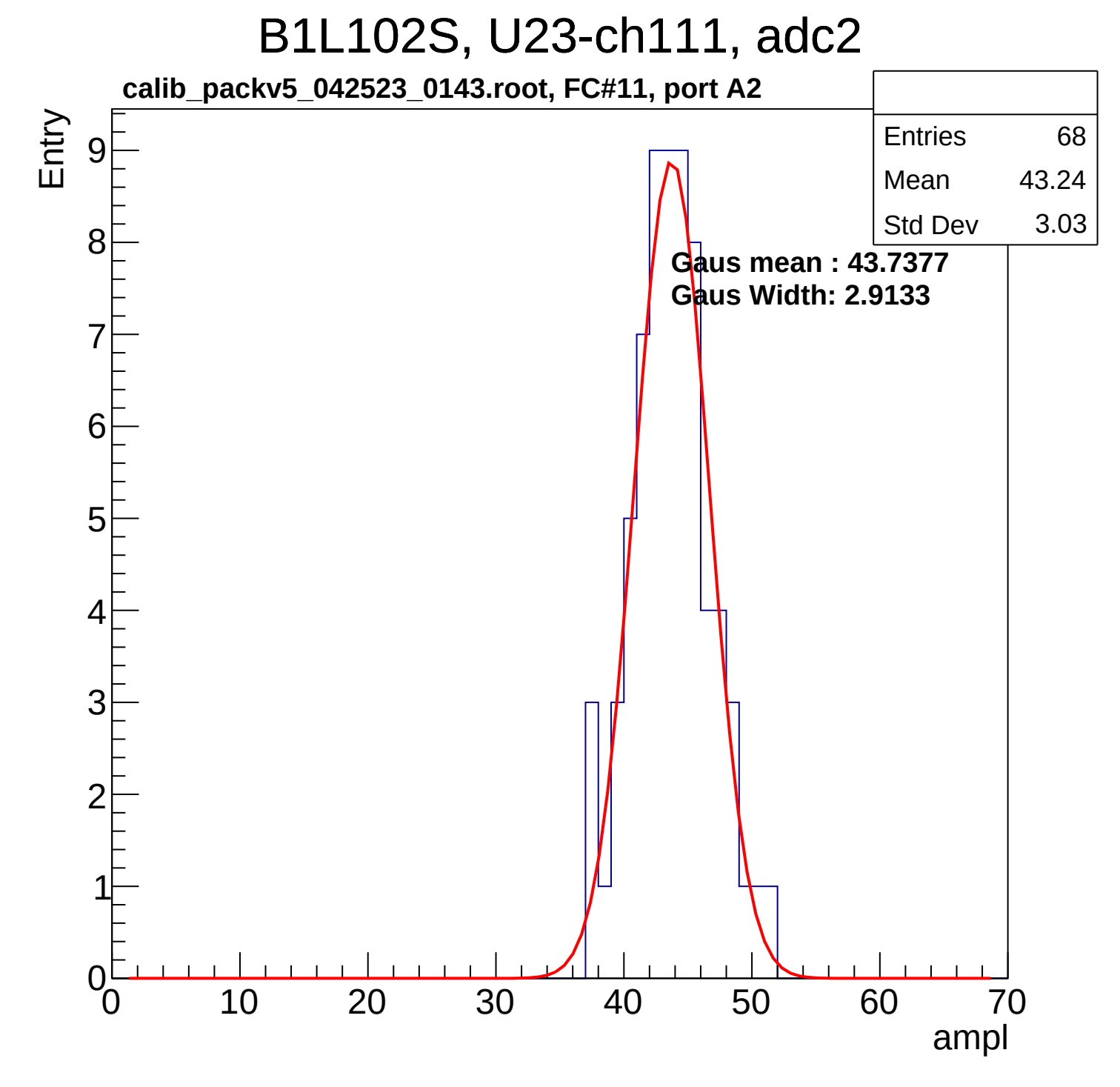

B1L102S, U23-ch110, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 59.31
Std Dev 2.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

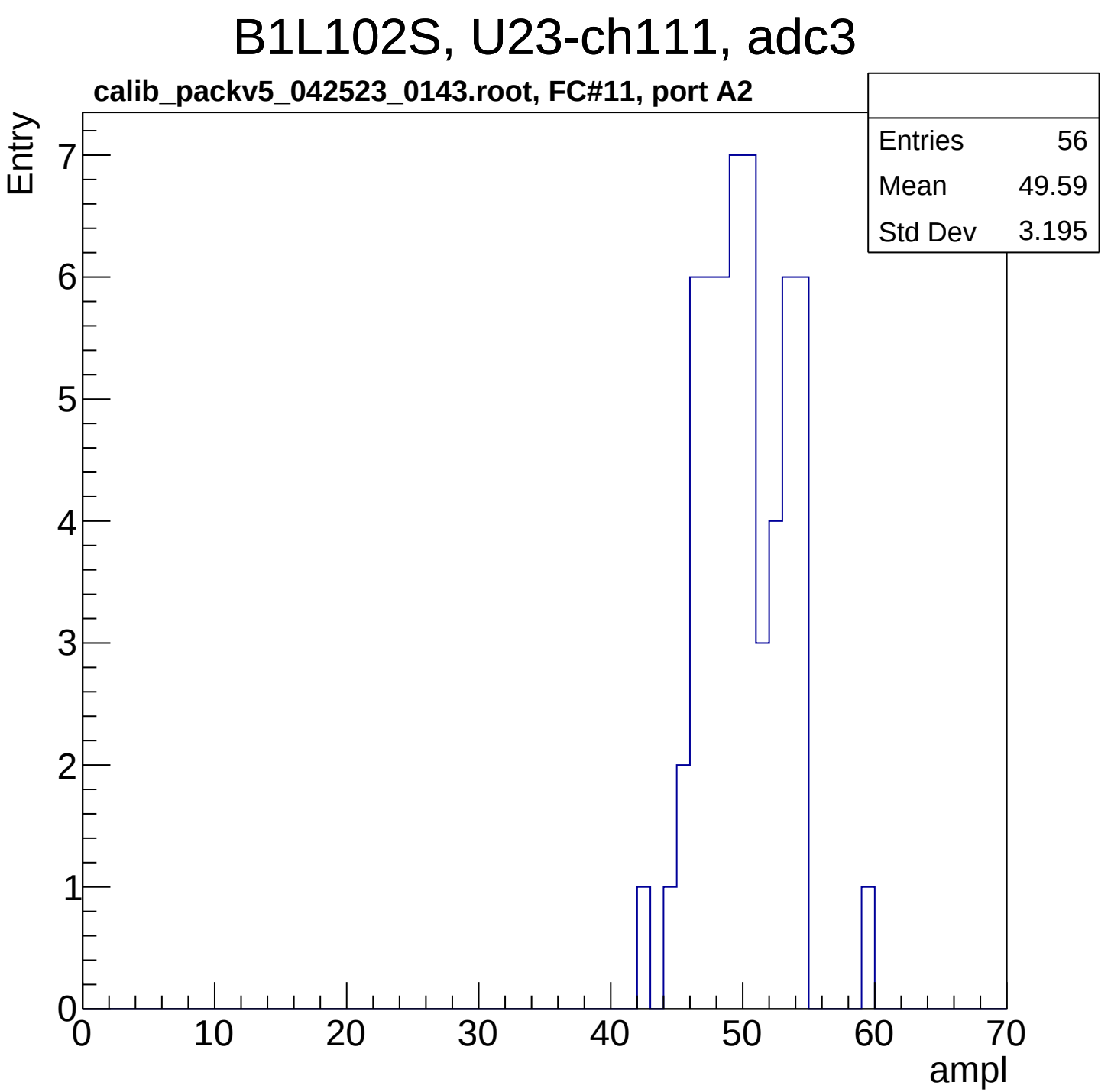

B1L102S, U23-ch110, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

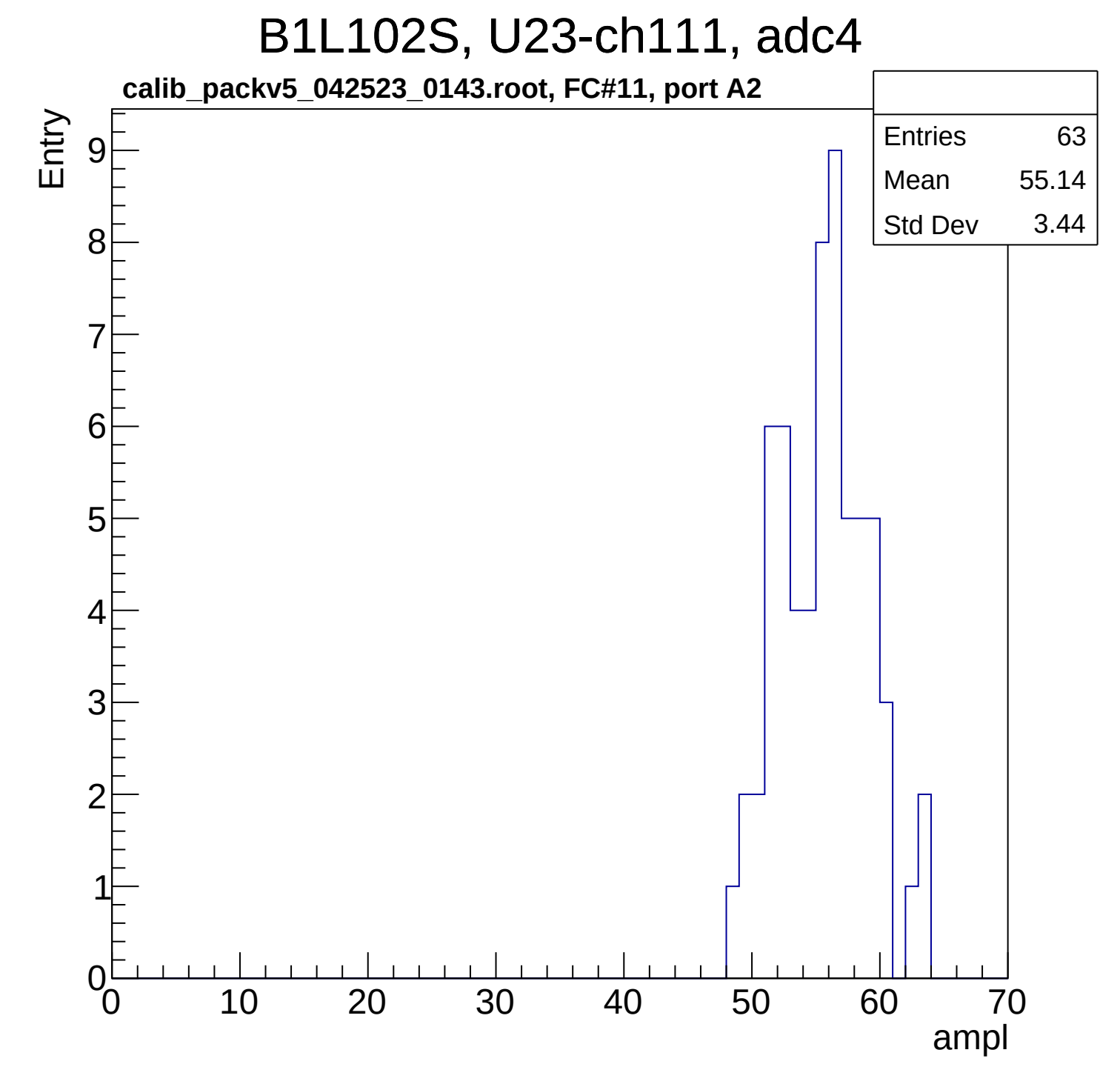

B1L102S, U23-ch110, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

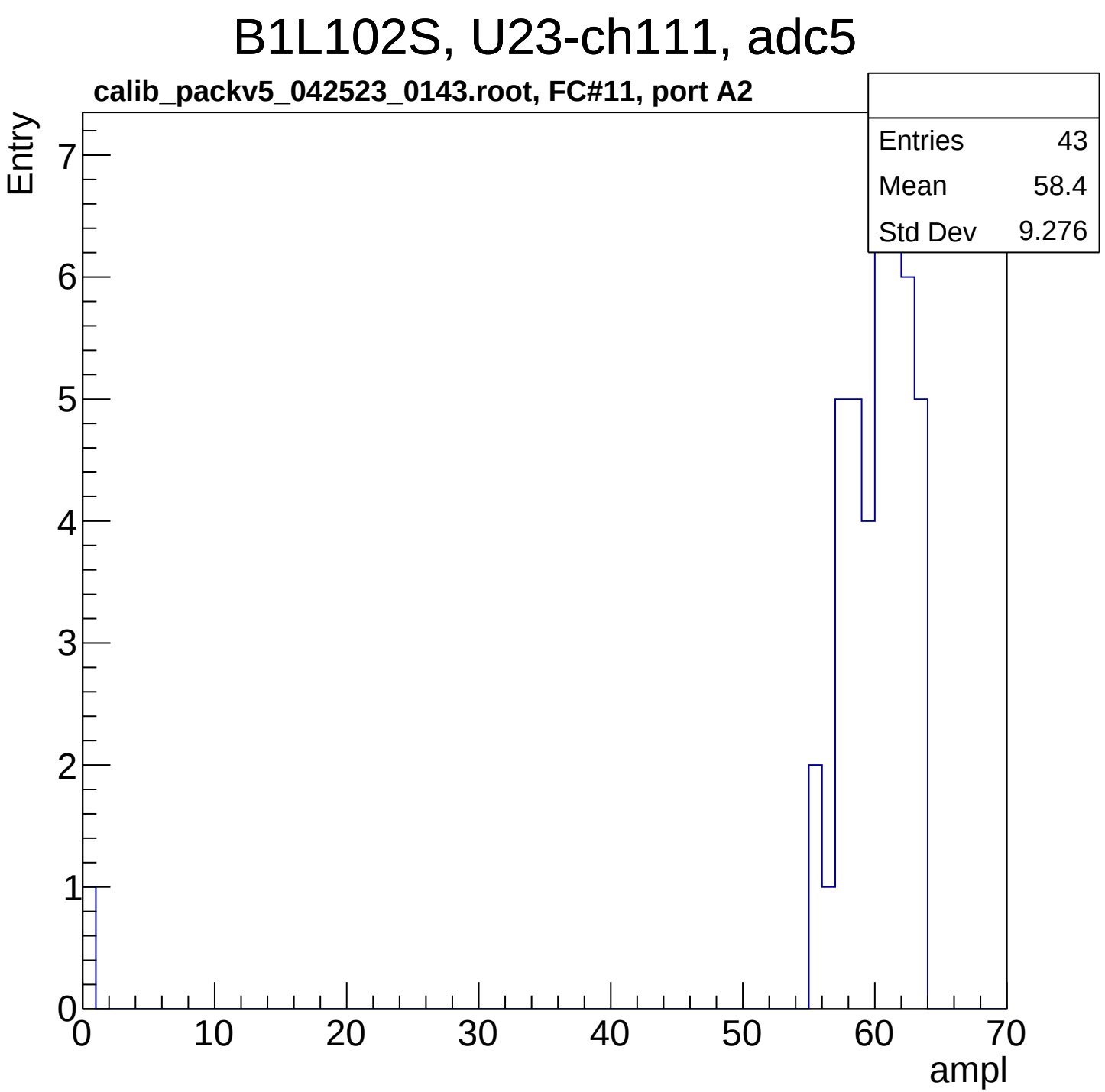

B1L102S, U23-ch111, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 28.07
Std Dev 4.944
Gaus mean : 29.0793
Gaus Width: 3.5889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

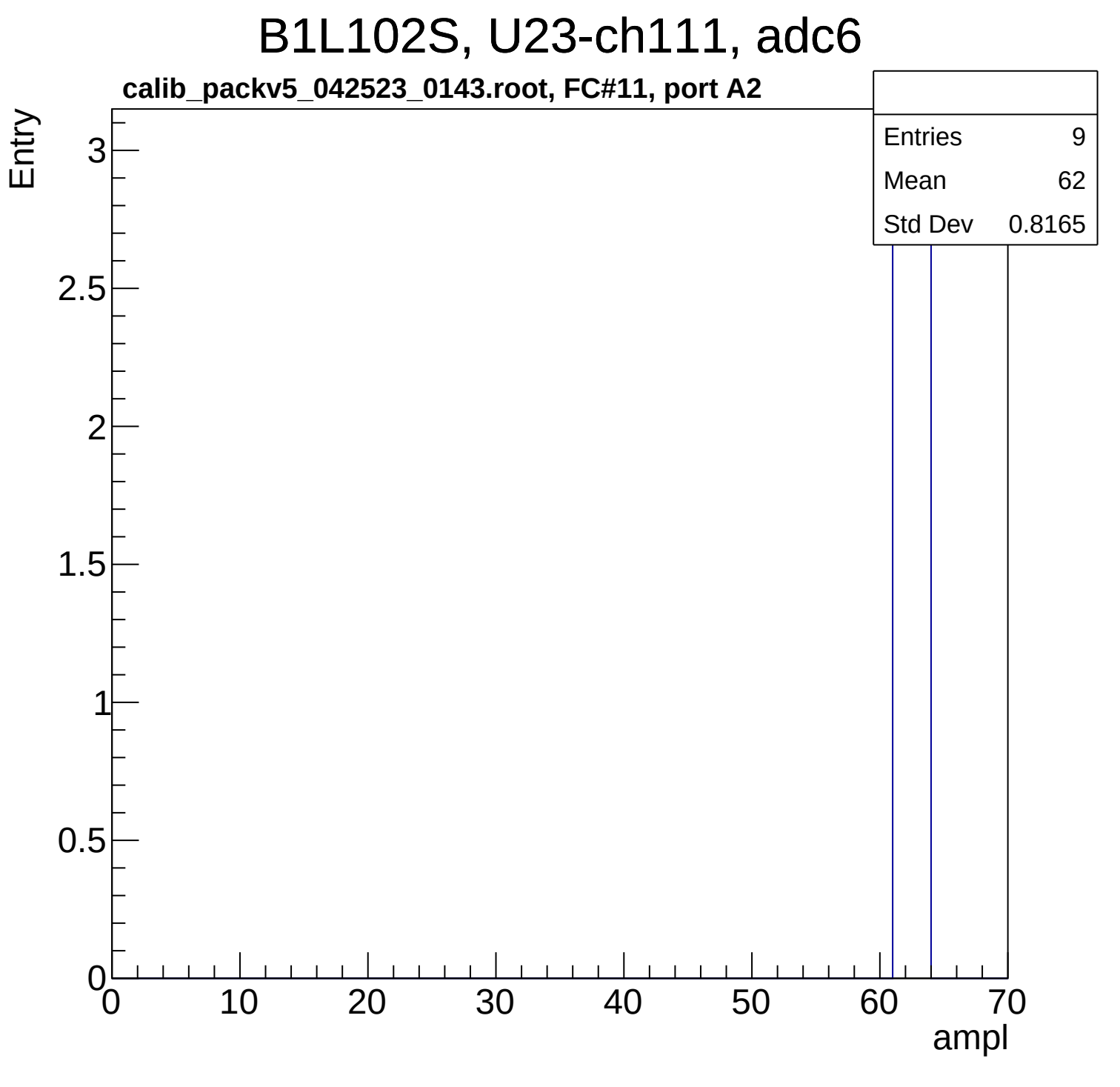

B1L102S, U23-ch111, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 35.52
Std Dev 3.329
Gaus mean : 36.2246
Gaus Width: 3.9057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

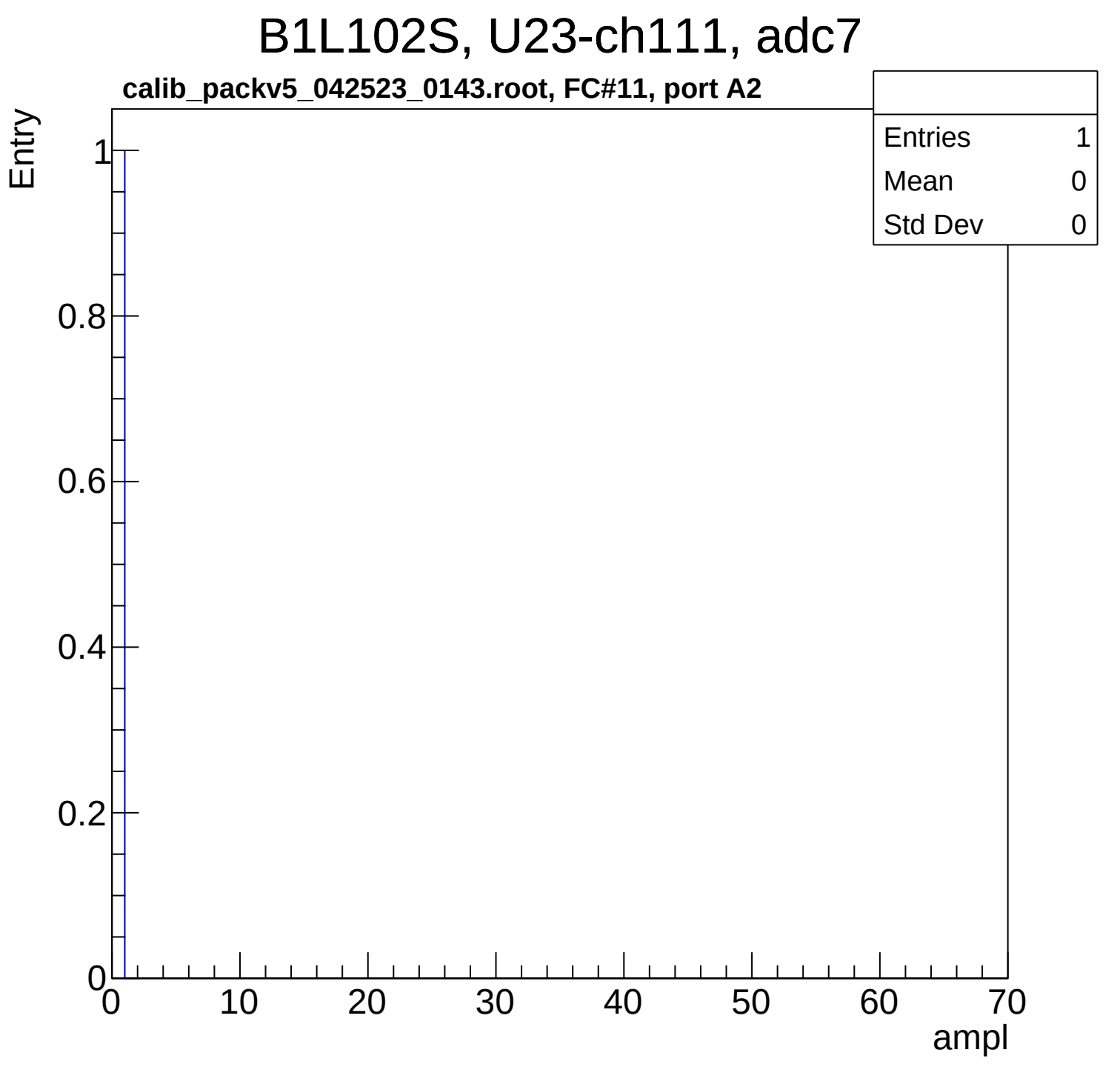

B1L102S, U23-ch111, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 43.24
Std Dev 3.03
Gaus mean : 43.7377
Gaus Width: 2.9133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

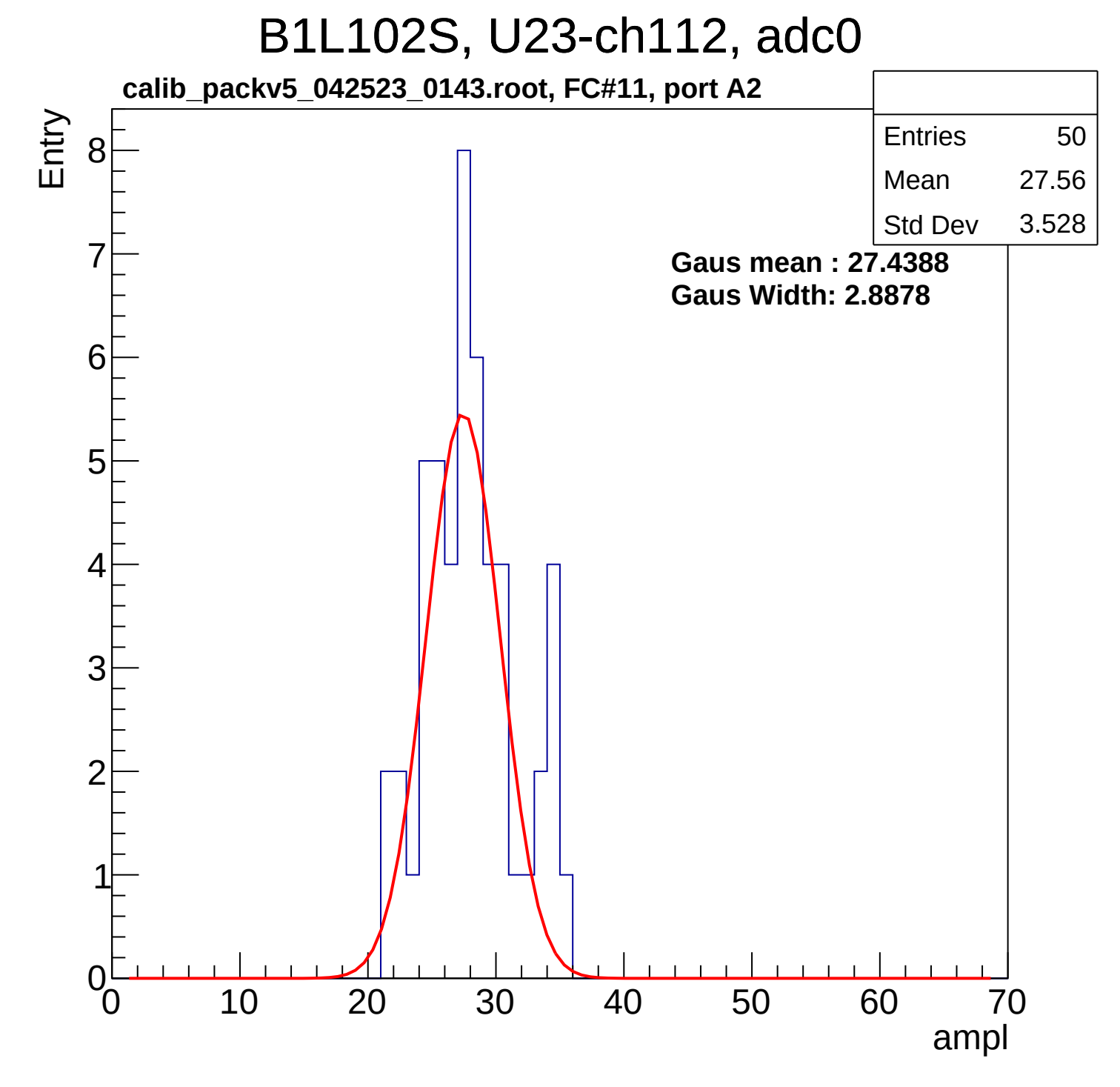

B1L102S, U23-ch111, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 49.59
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

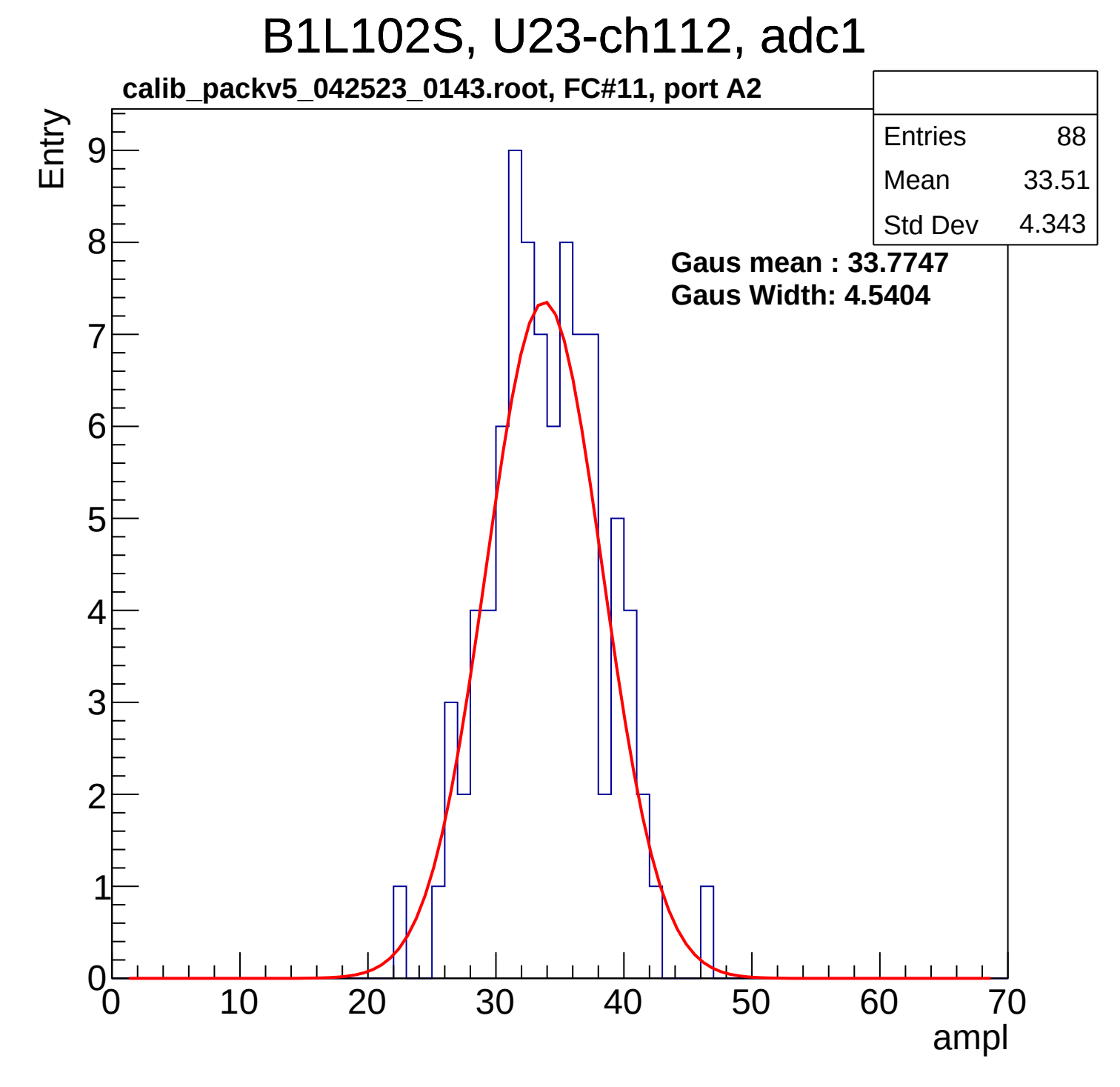

B1L102S, U23-ch111, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 55.14
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

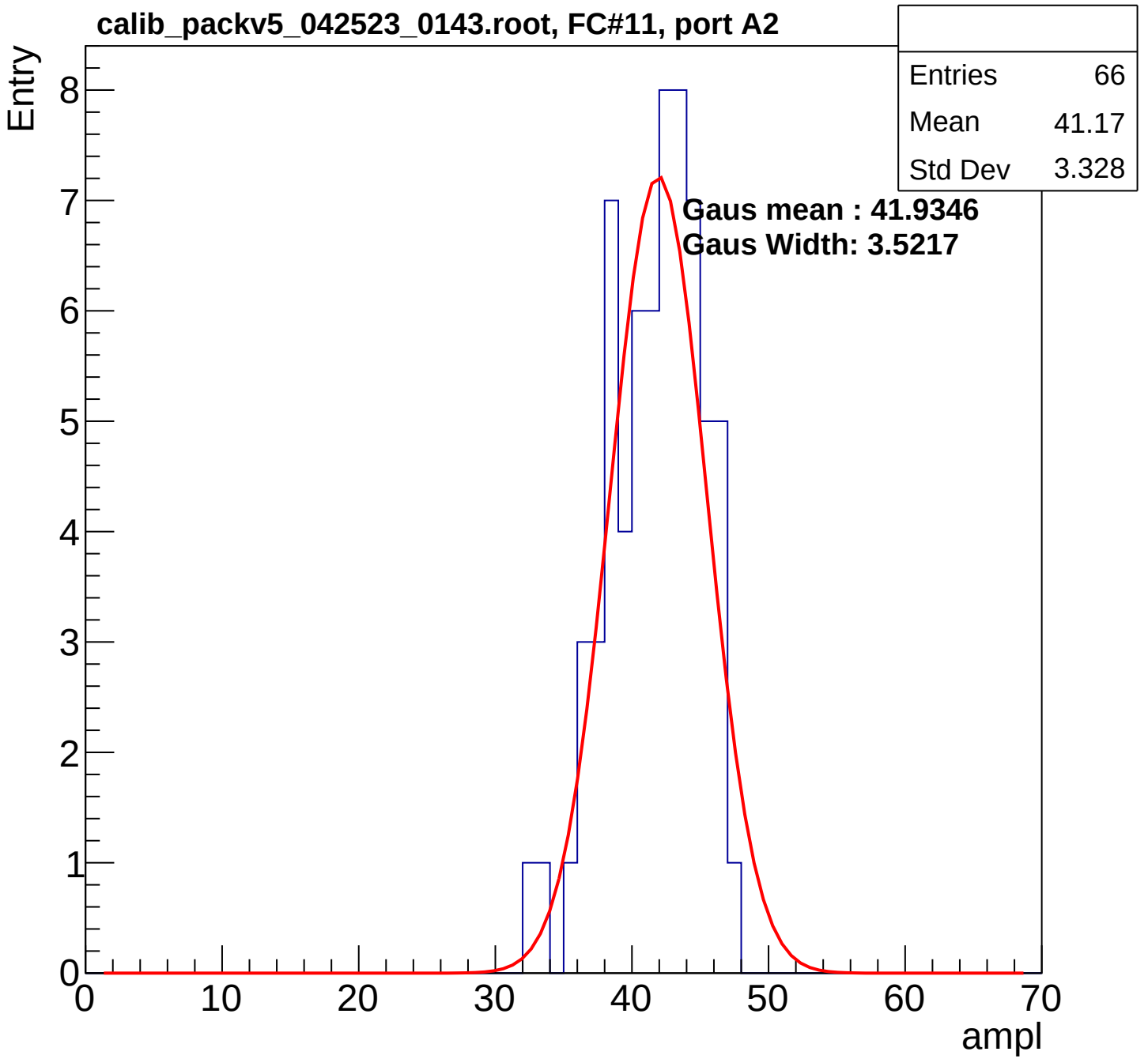

B1L102S, U23-ch111, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 58.4
Std Dev 9.276
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

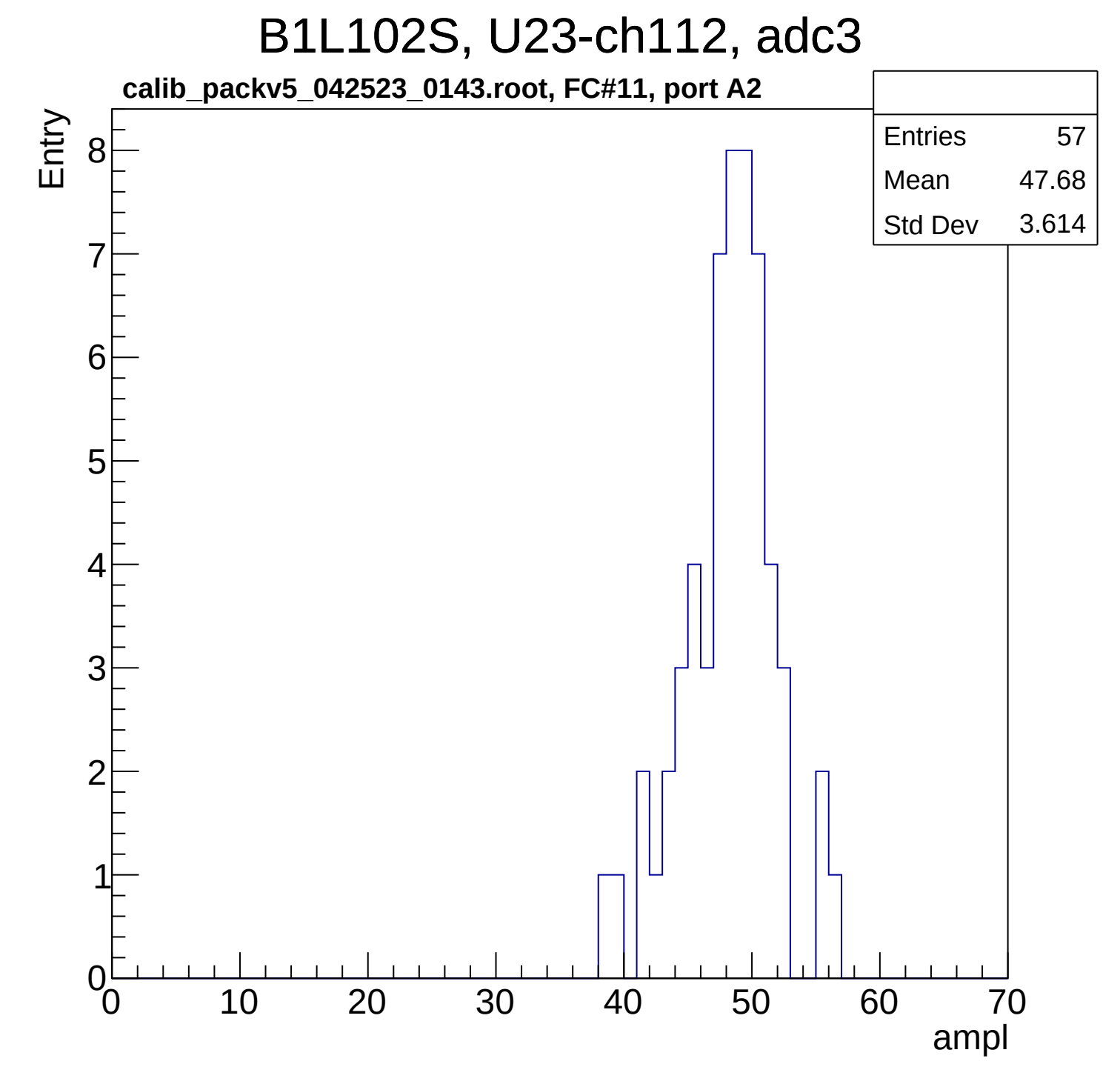

B1L102S, U23-ch111, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

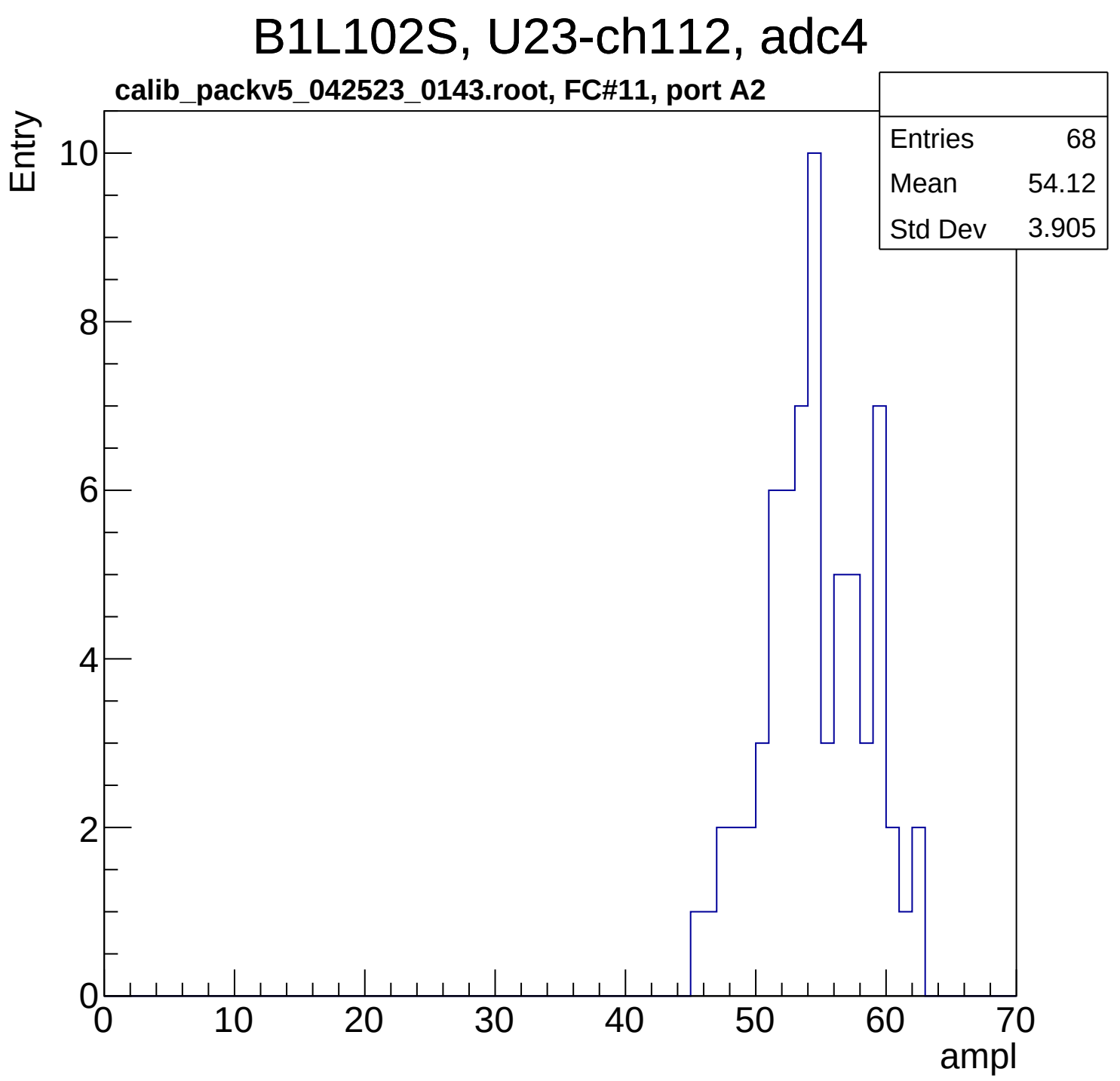

B1L102S, U23-ch111, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

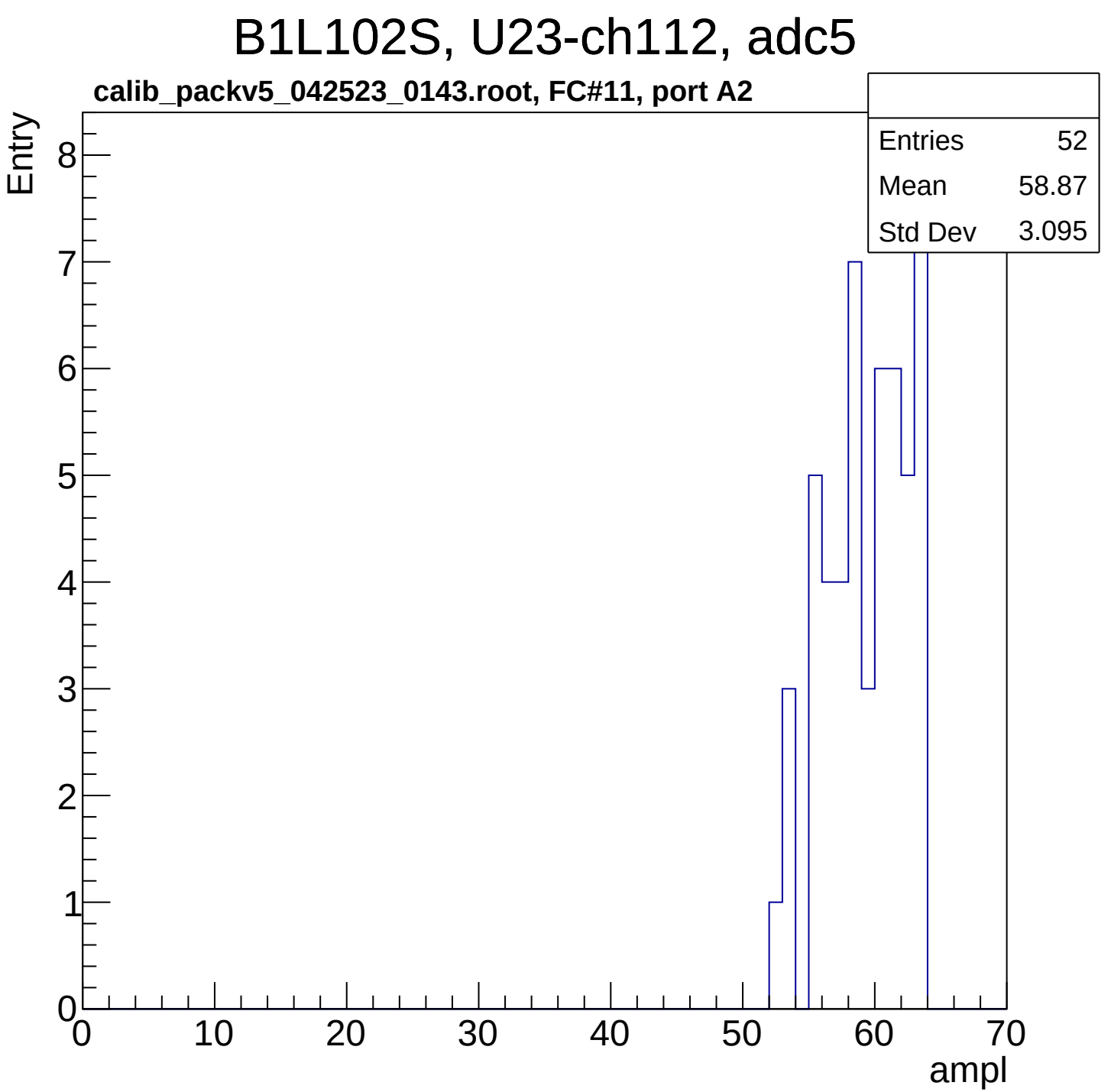

B1L102S, U23-ch112, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 27.56
Std Dev 3.528
Gaus mean : 27.4388
Gaus Width: 2.8878
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

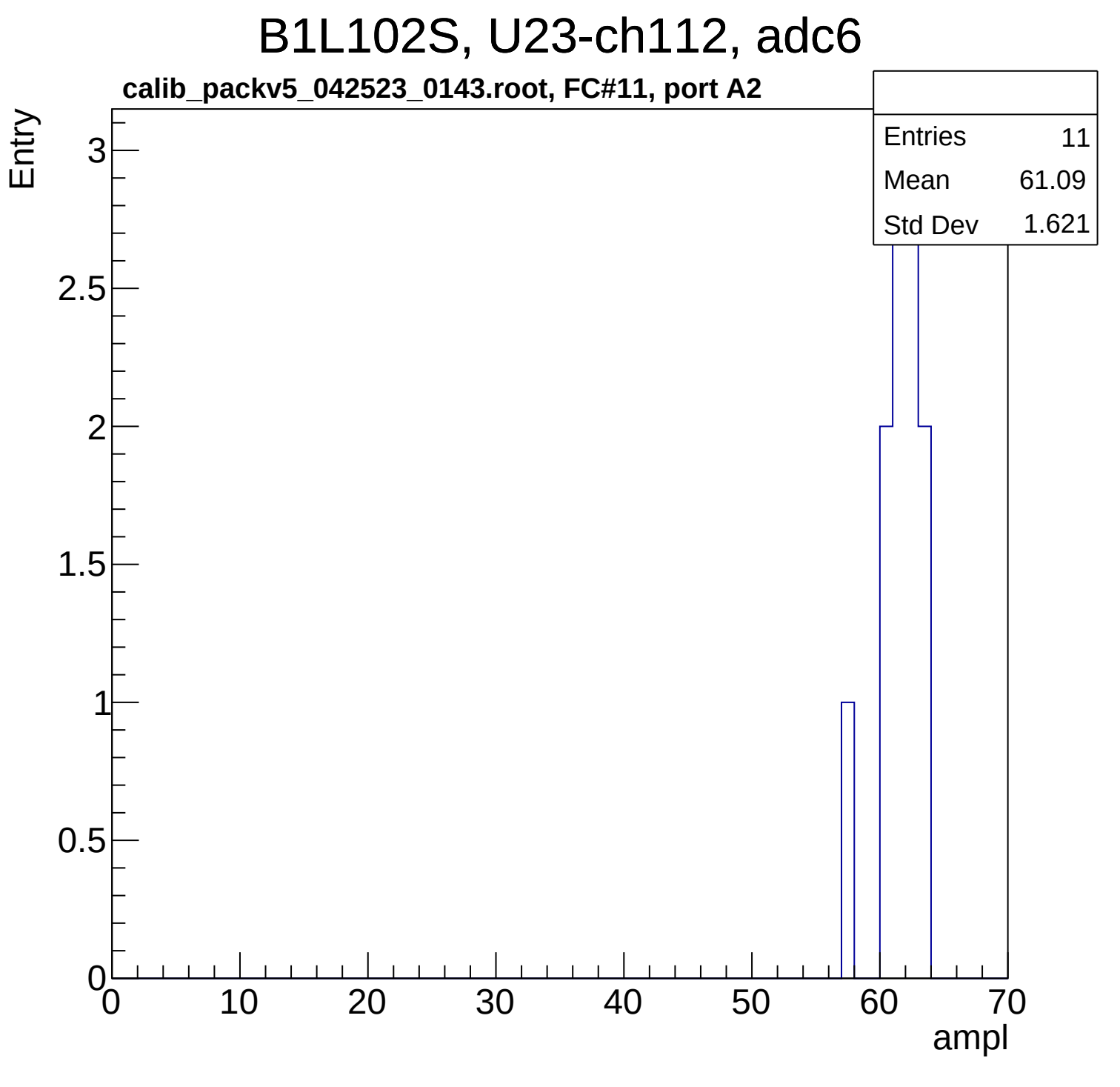

B1L102S, U23-ch112, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 33.51
Std Dev 4.343
Gaus mean : 33.7747
Gaus Width: 4.5404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

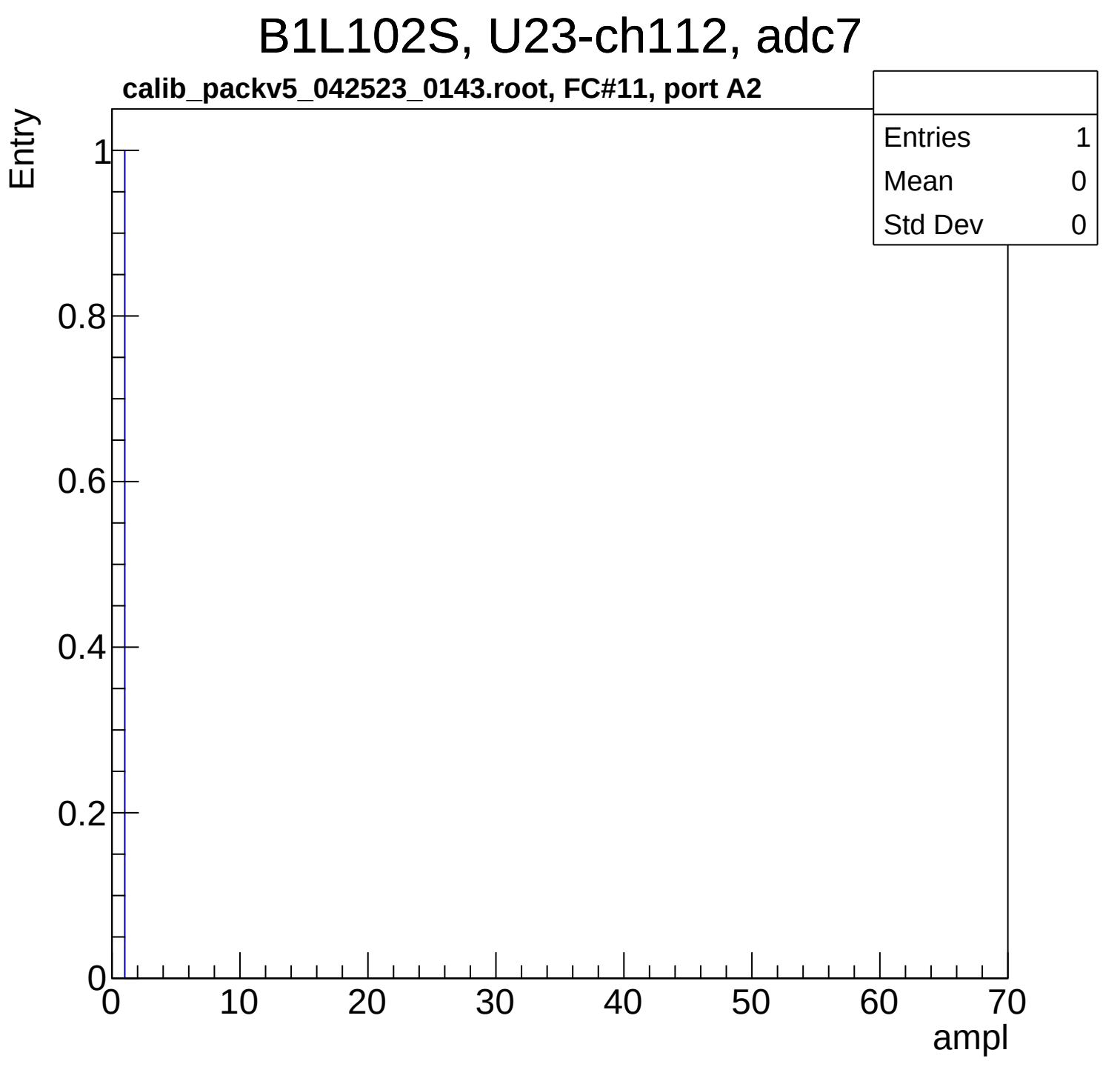

B1L102S, U23-ch112, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 41.17
Std Dev 3.328
Gaus mean : 41.9346
Gaus Width: 3.5217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

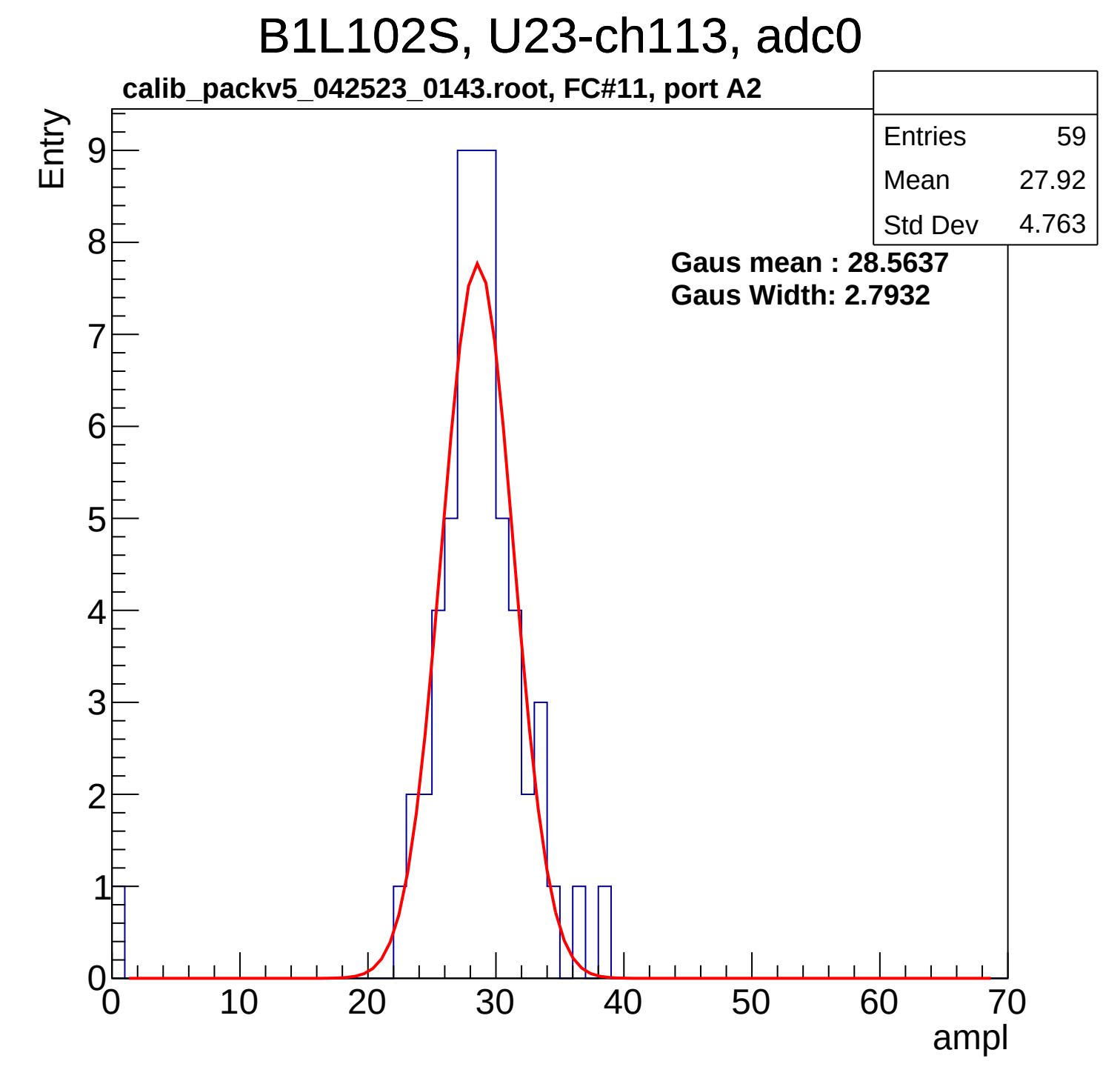

B1L102S, U23-ch112, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 47.68
Std Dev 3.614
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

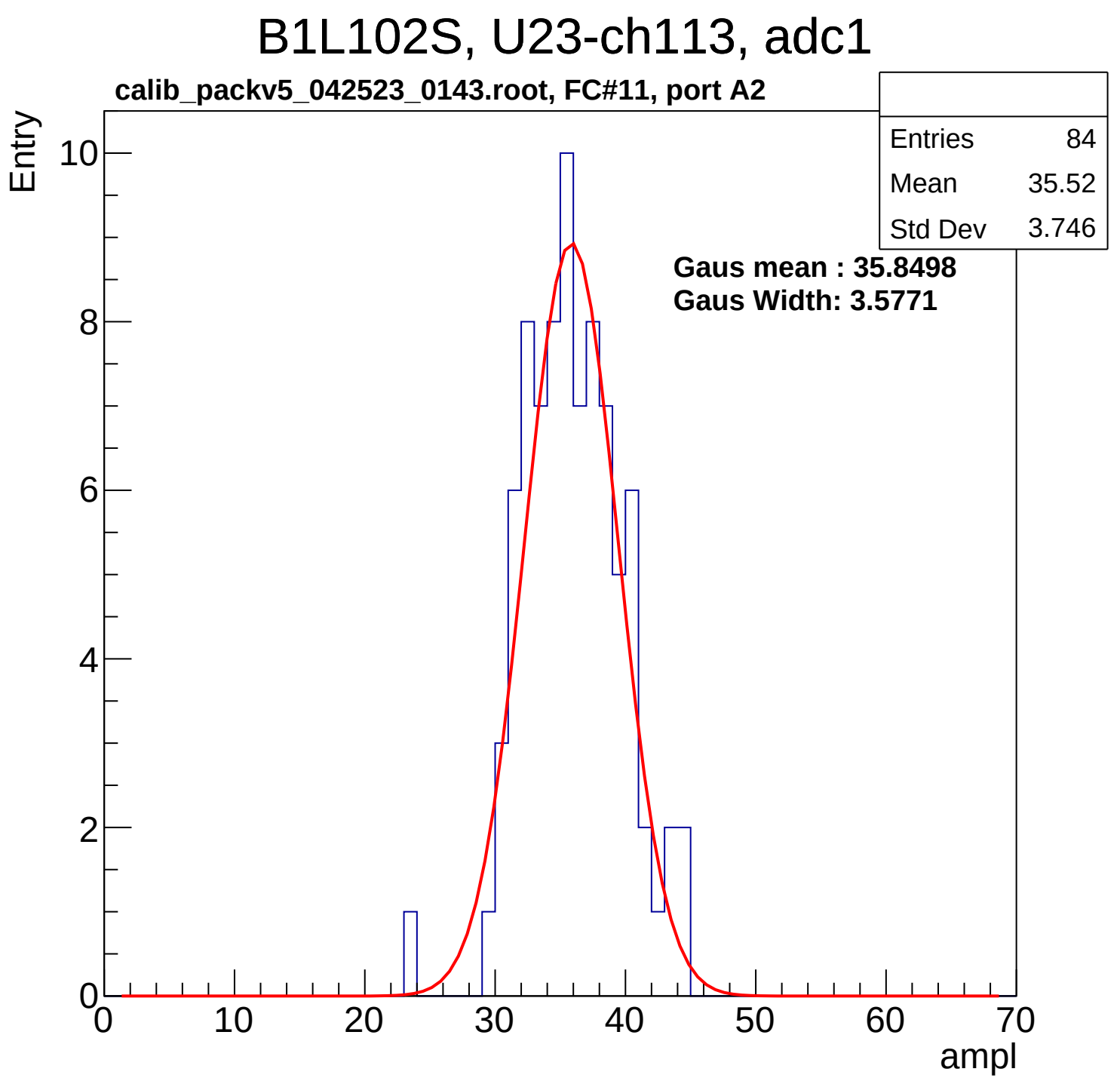

B1L102S, U23-ch112, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 54.12
Std Dev 3.905
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

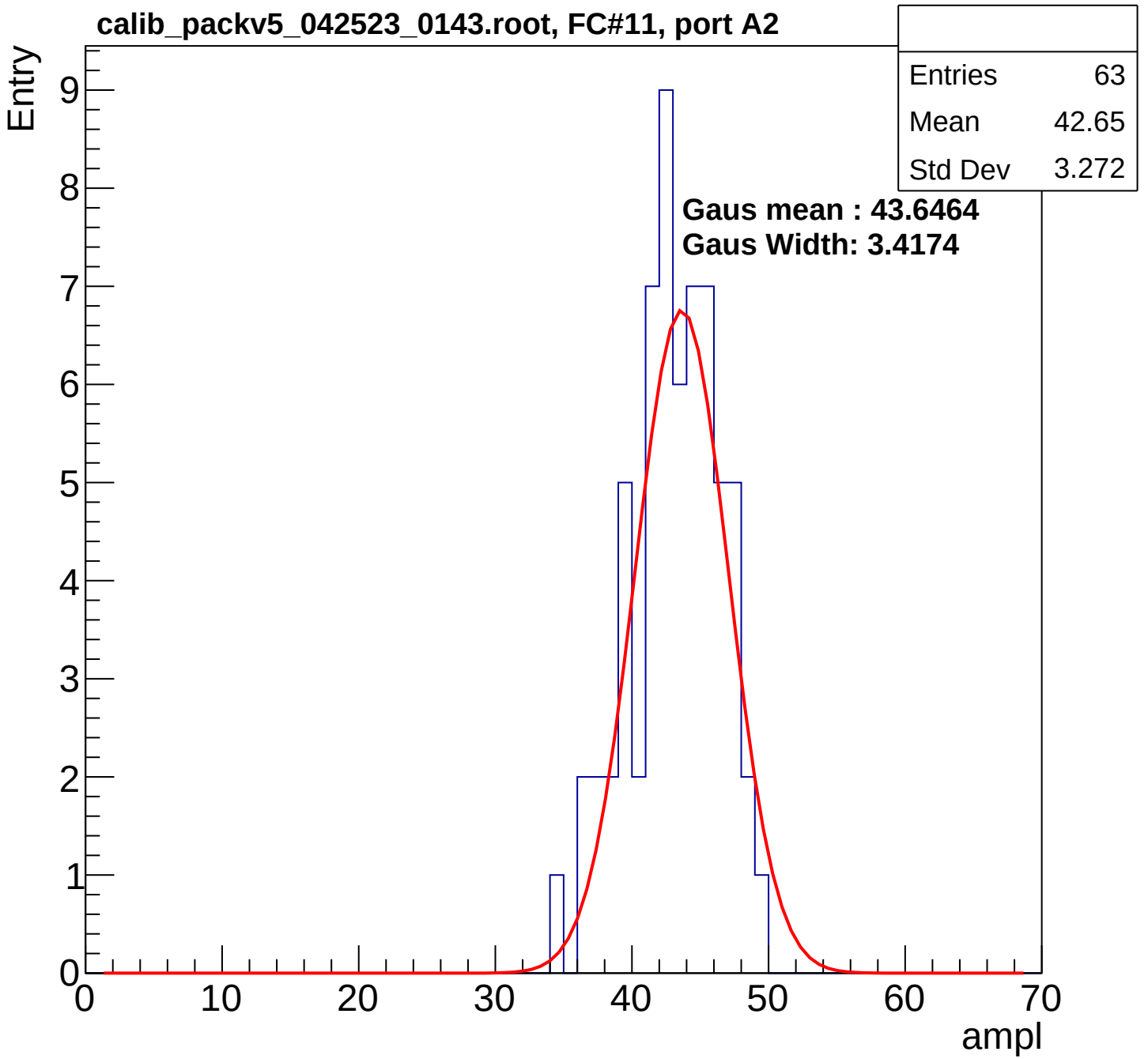

B1L102S, U23-ch112, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.87
Std Dev 3.095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

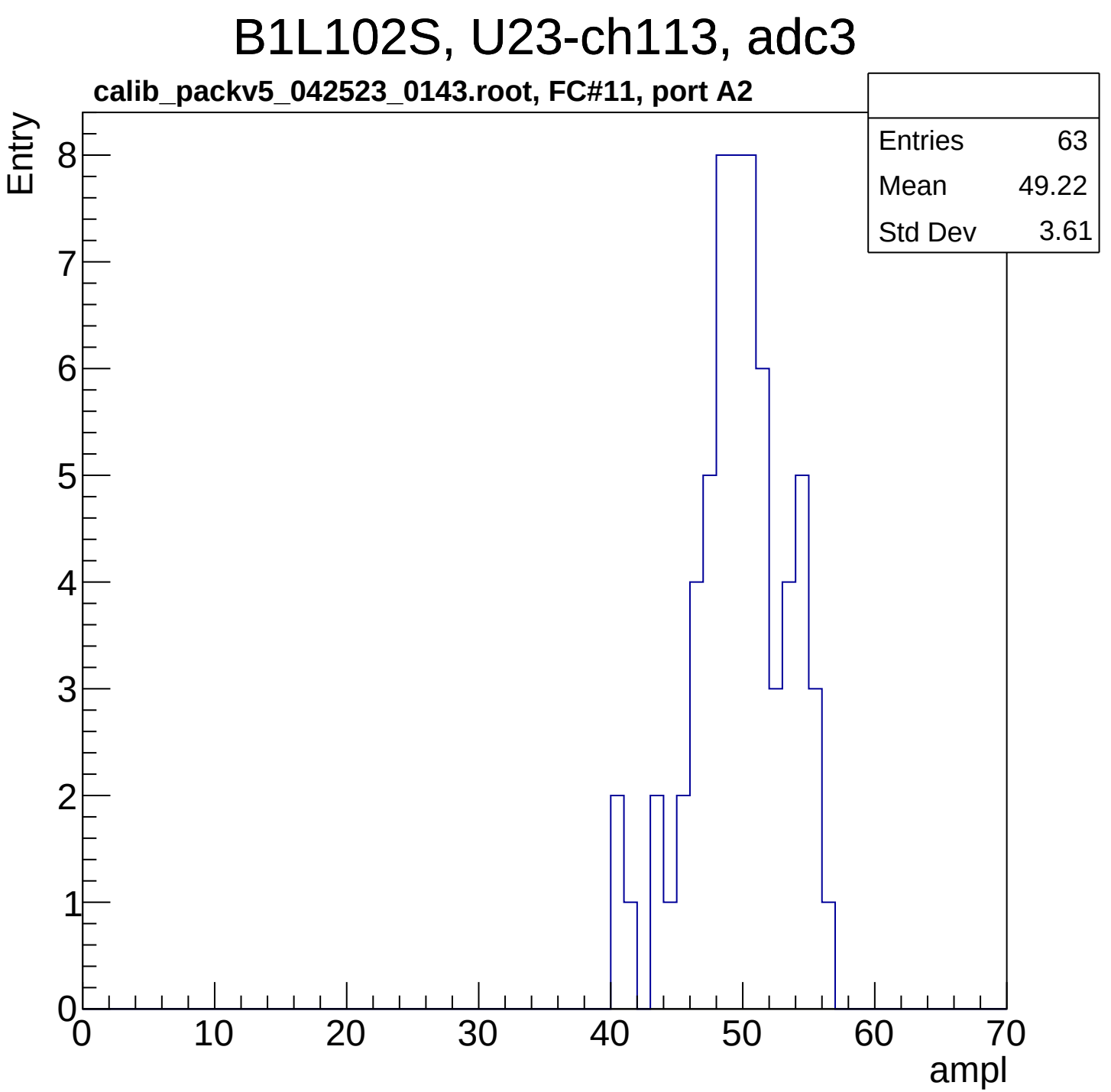

B1L102S, U23-ch112, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.09
Std Dev 1.621
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

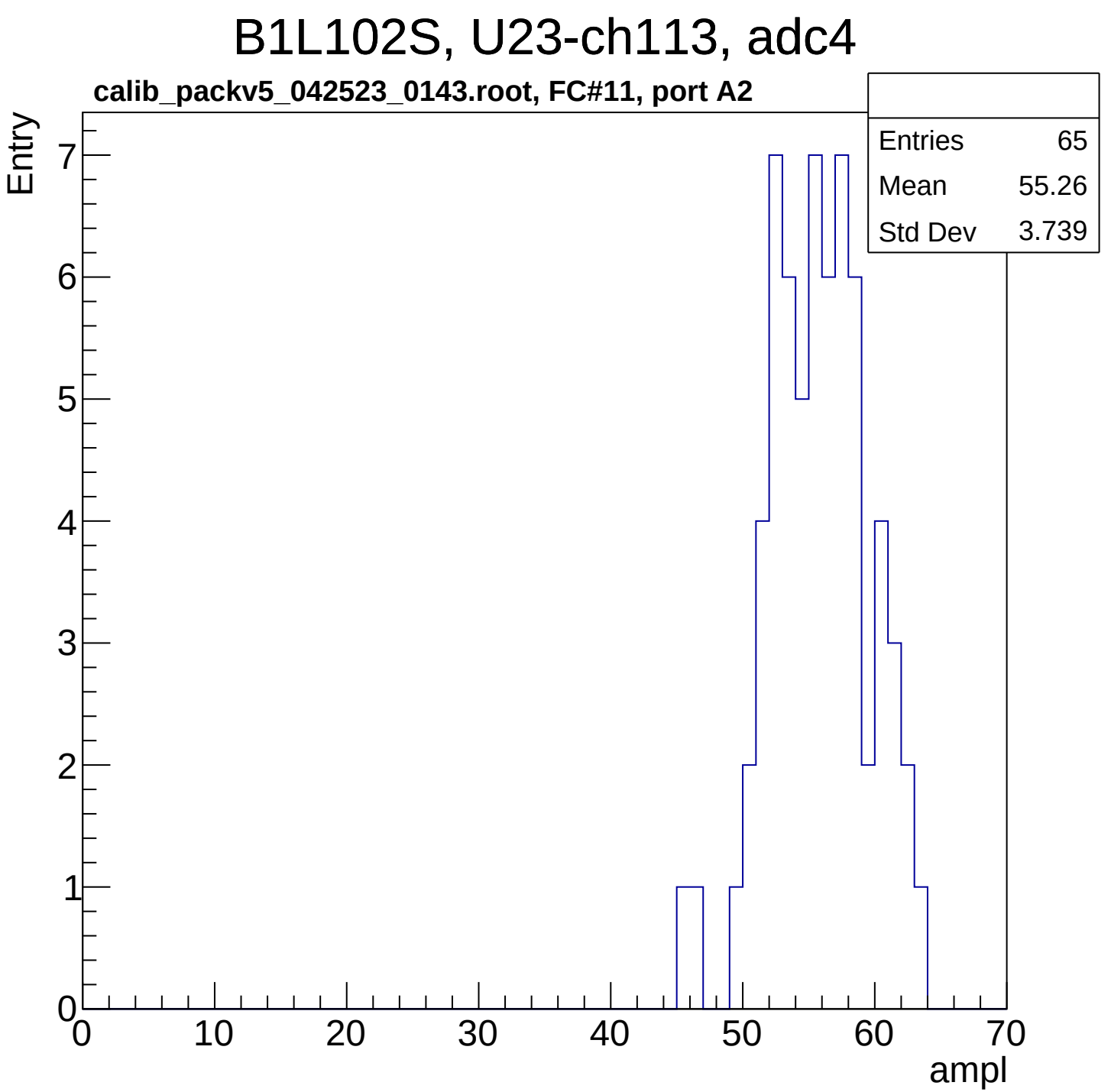

B1L102S, U23-ch112, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

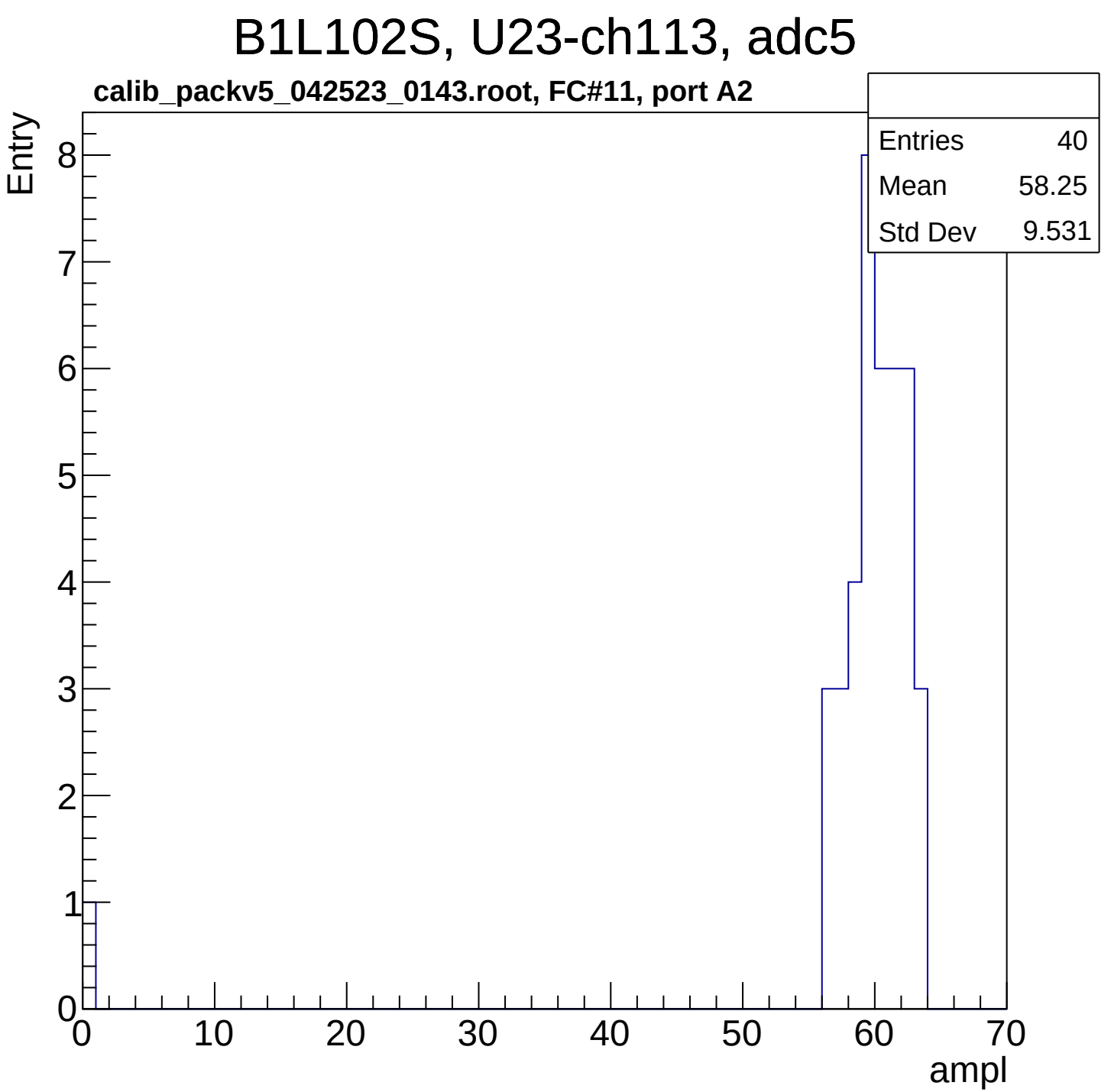

B1L102S, U23-ch113, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 27.92
Std Dev 4.763
Gaus mean : 28.5637
Gaus Width: 2.7932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

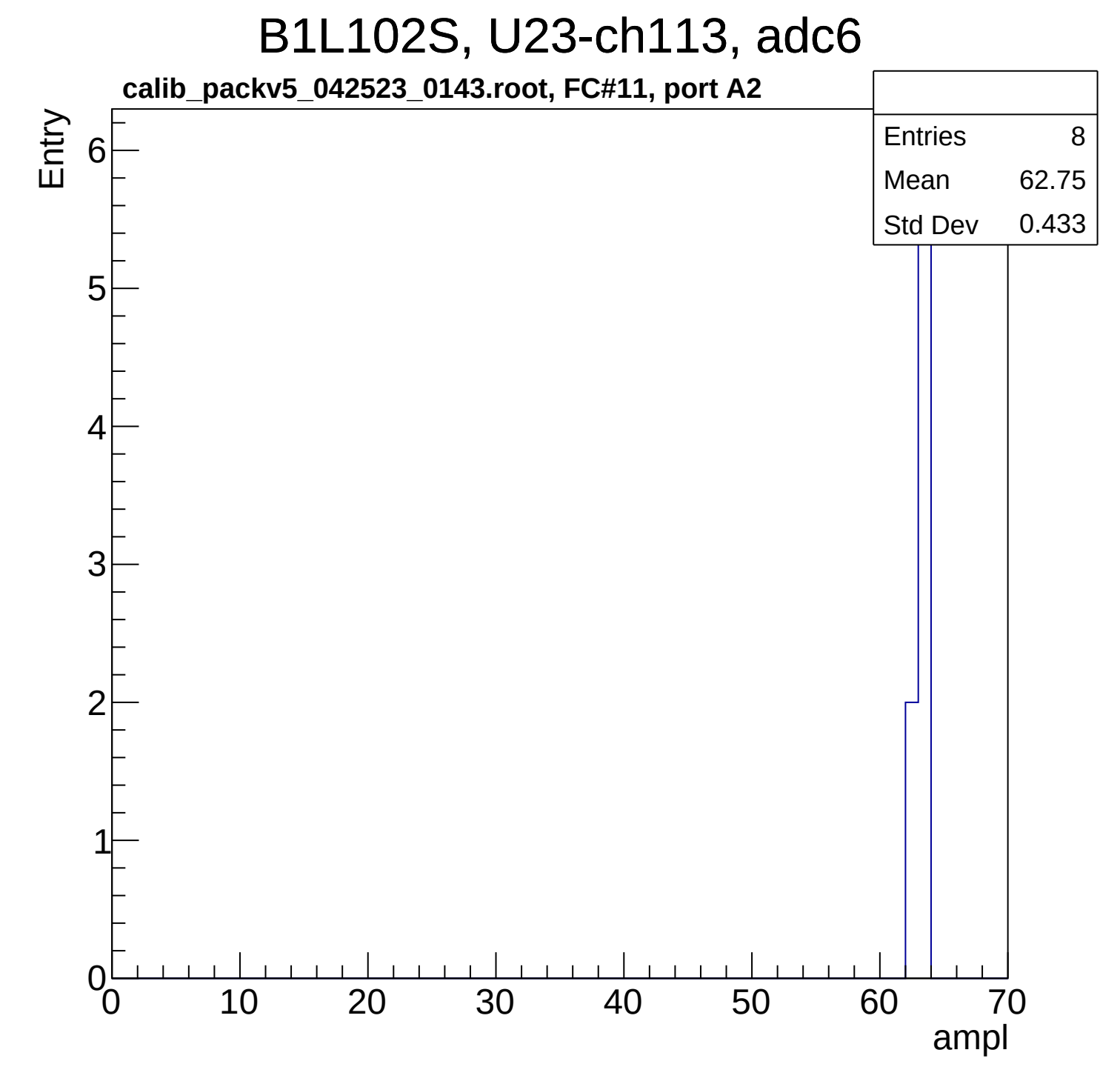

B1L102S, U23-ch113, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 35.52
Std Dev 3.746
Gaus mean : 35.8498
Gaus Width: 3.5771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

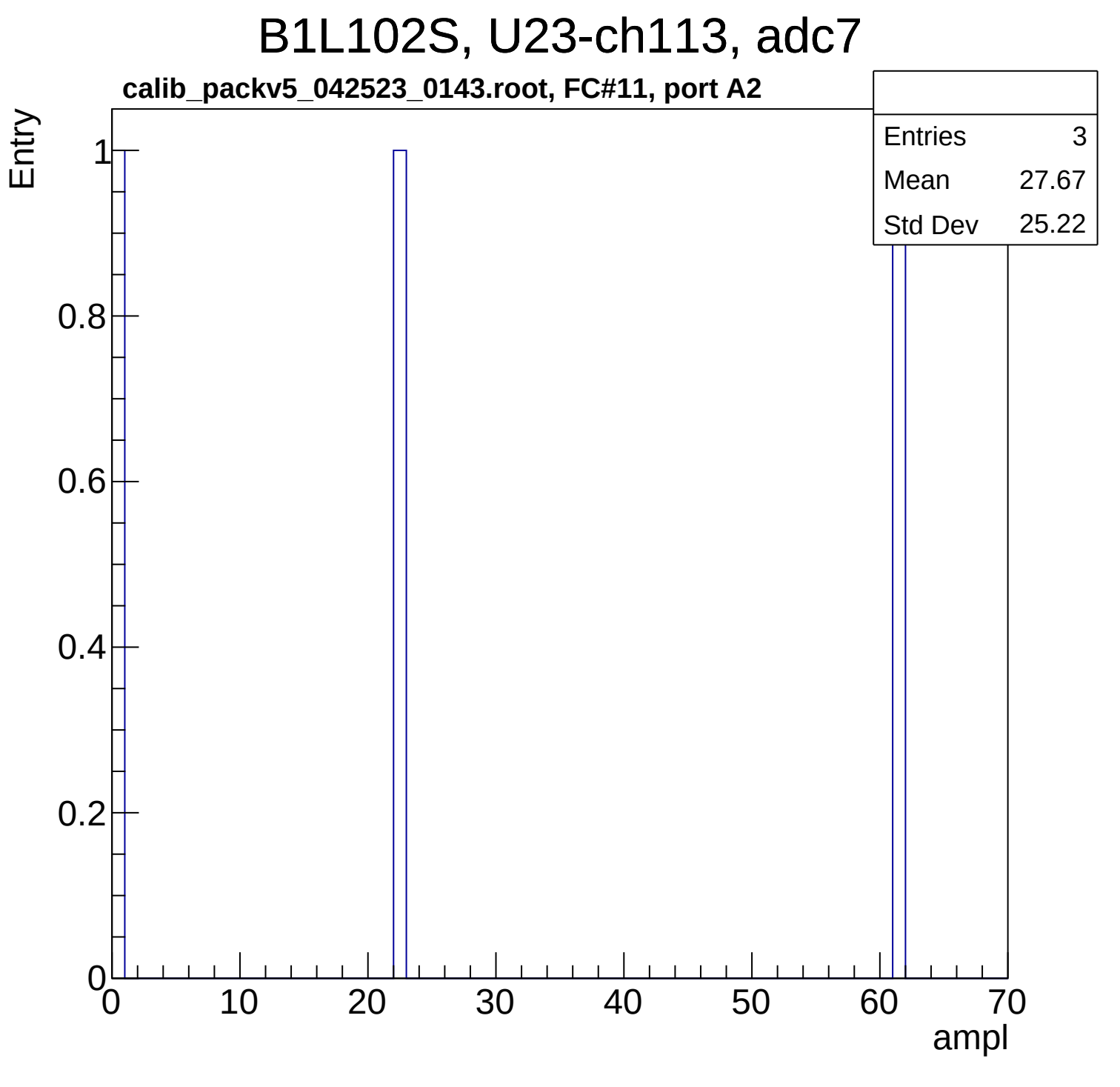

B1L102S, U23-ch113, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 42.65
Std Dev 3.272
Gaus mean : 43.6464
Gaus Width: 3.4174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

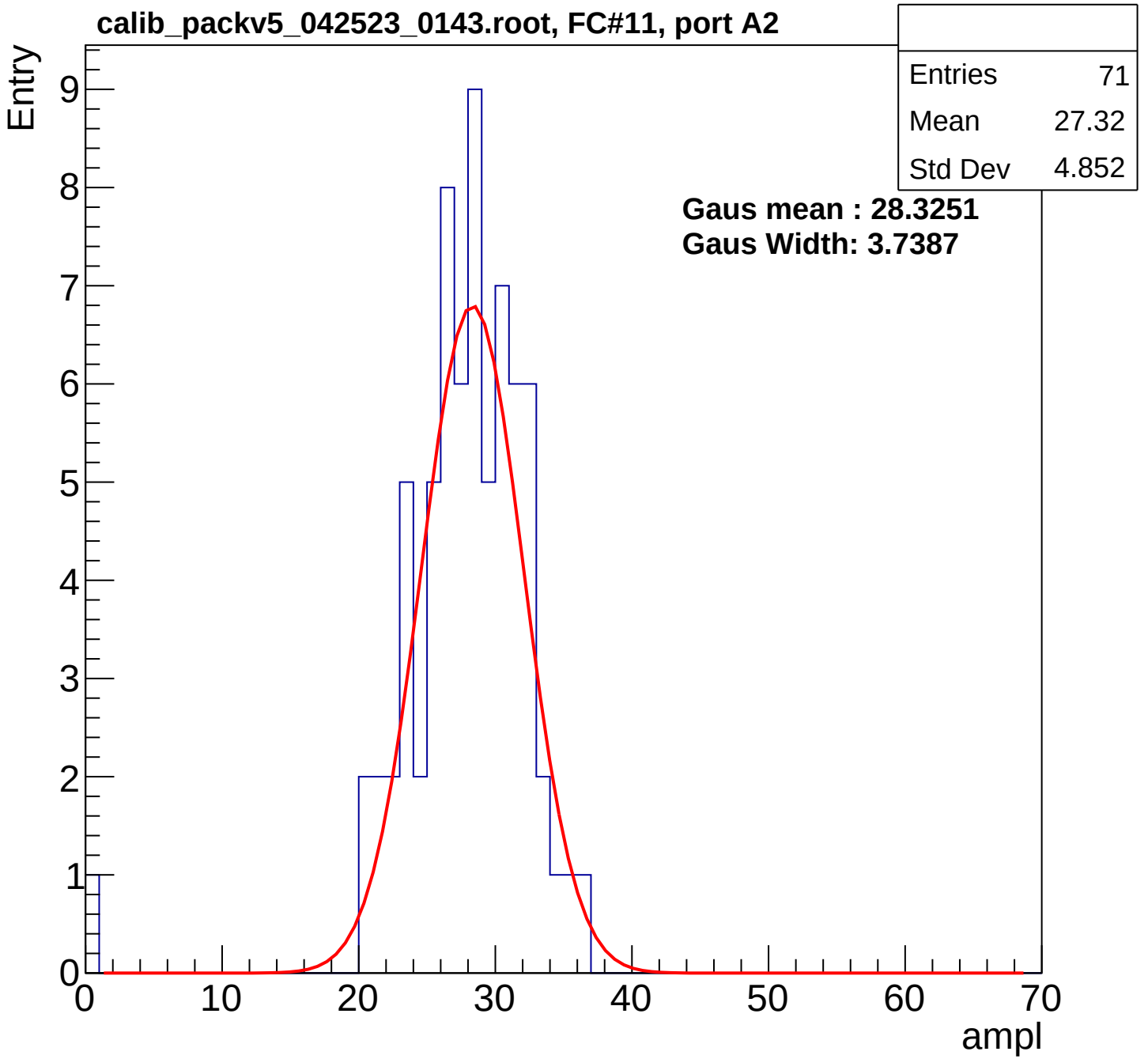

B1L102S, U23-ch113, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 49.22
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

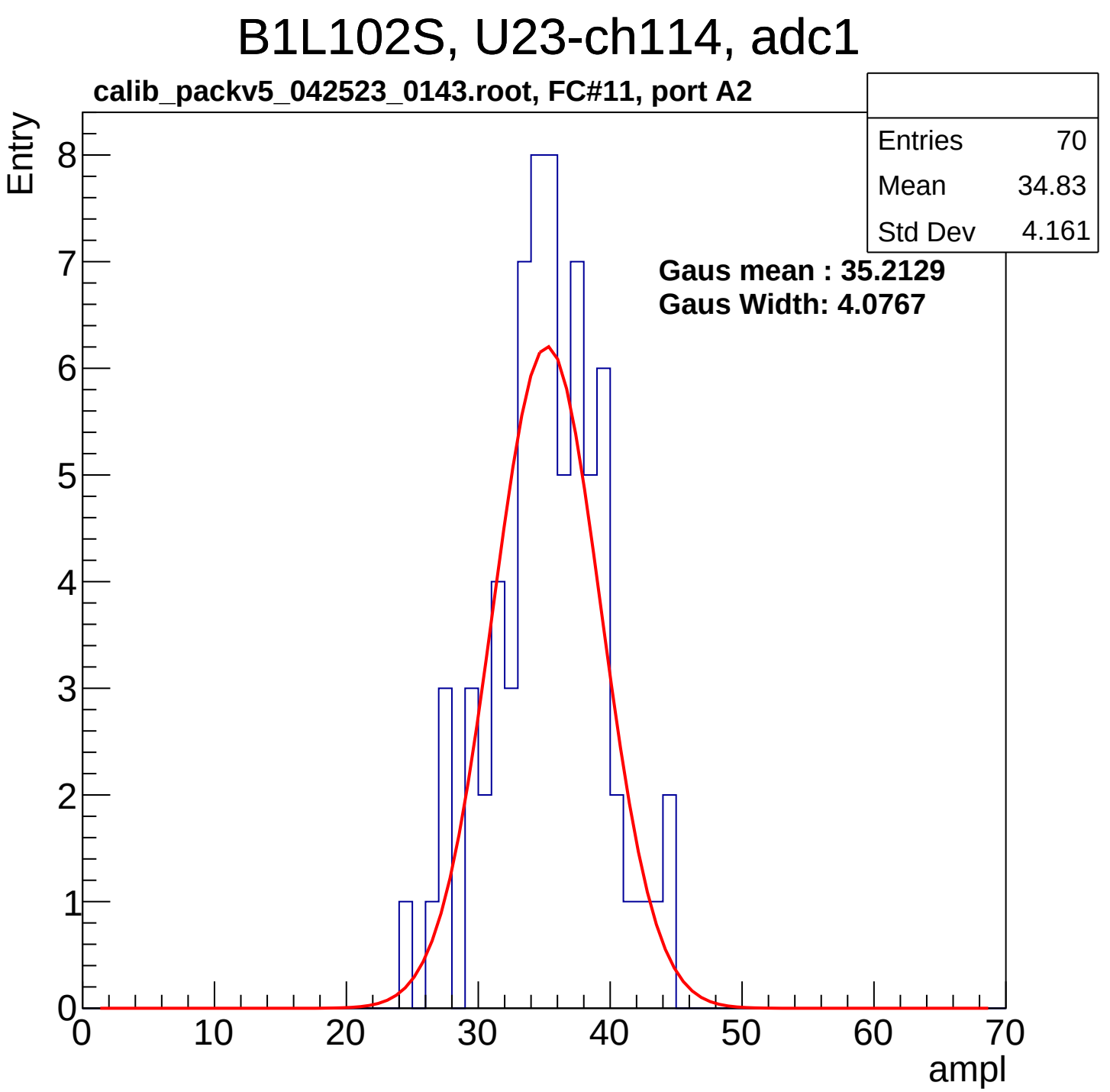

B1L102S, U23-ch113, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 55.26
Std Dev 3.739
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

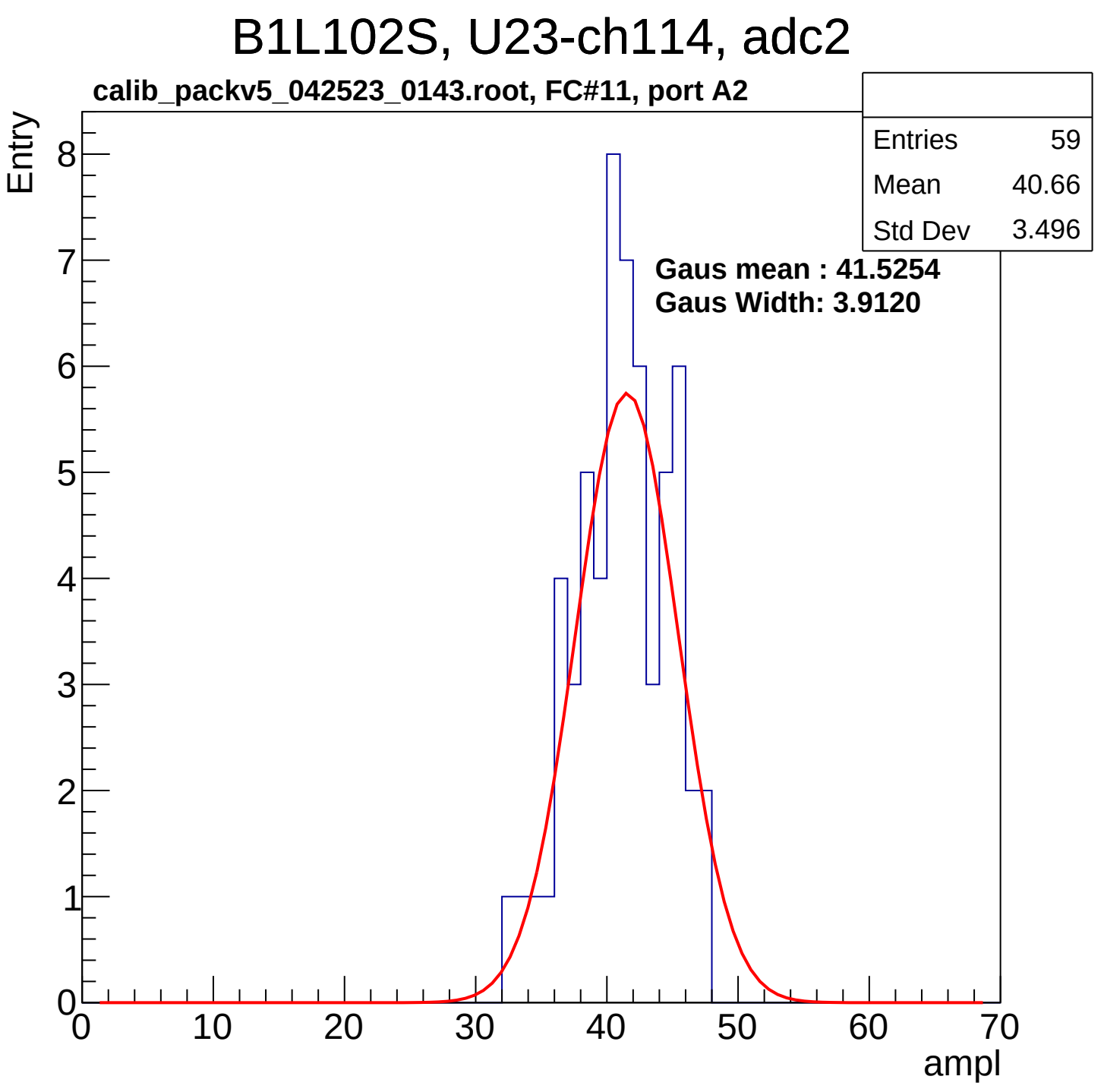

B1L102S, U23-ch113, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.25
Std Dev 9.531
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

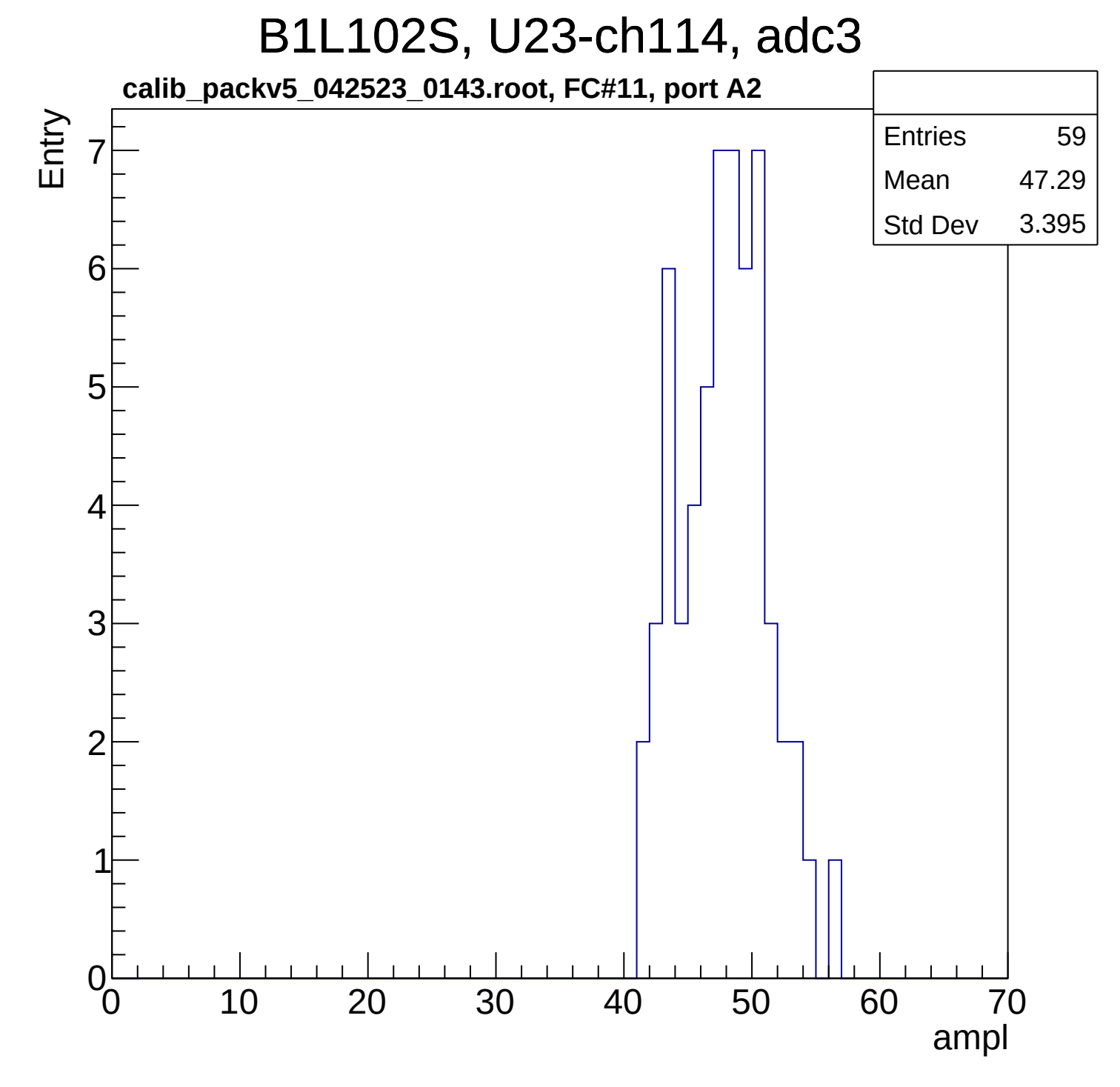

B1L102S, U23-ch113, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 62.75
Std Dev 0.433
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

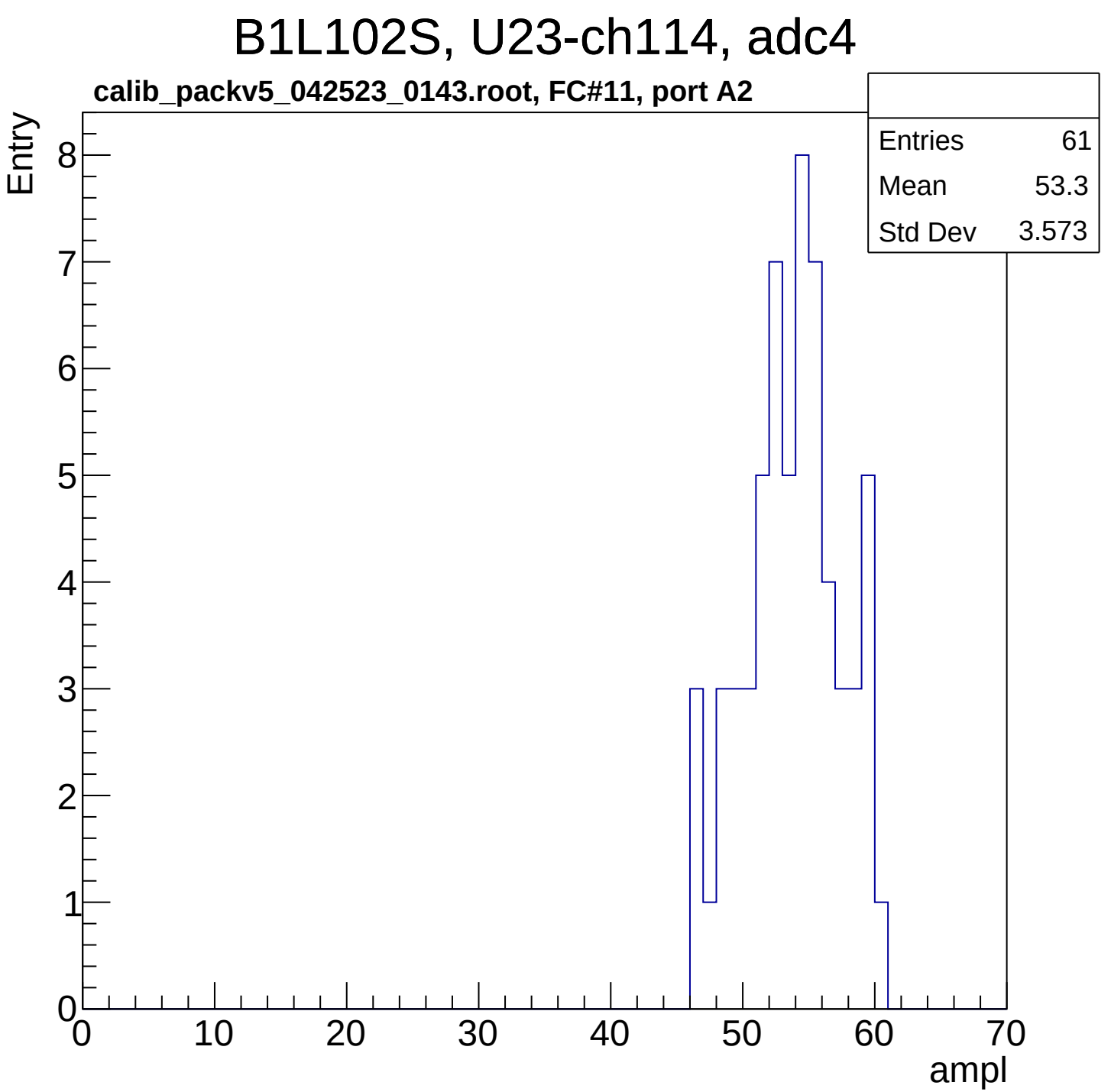

B1L102S, U23-ch113, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 27.67
Std Dev 25.22
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

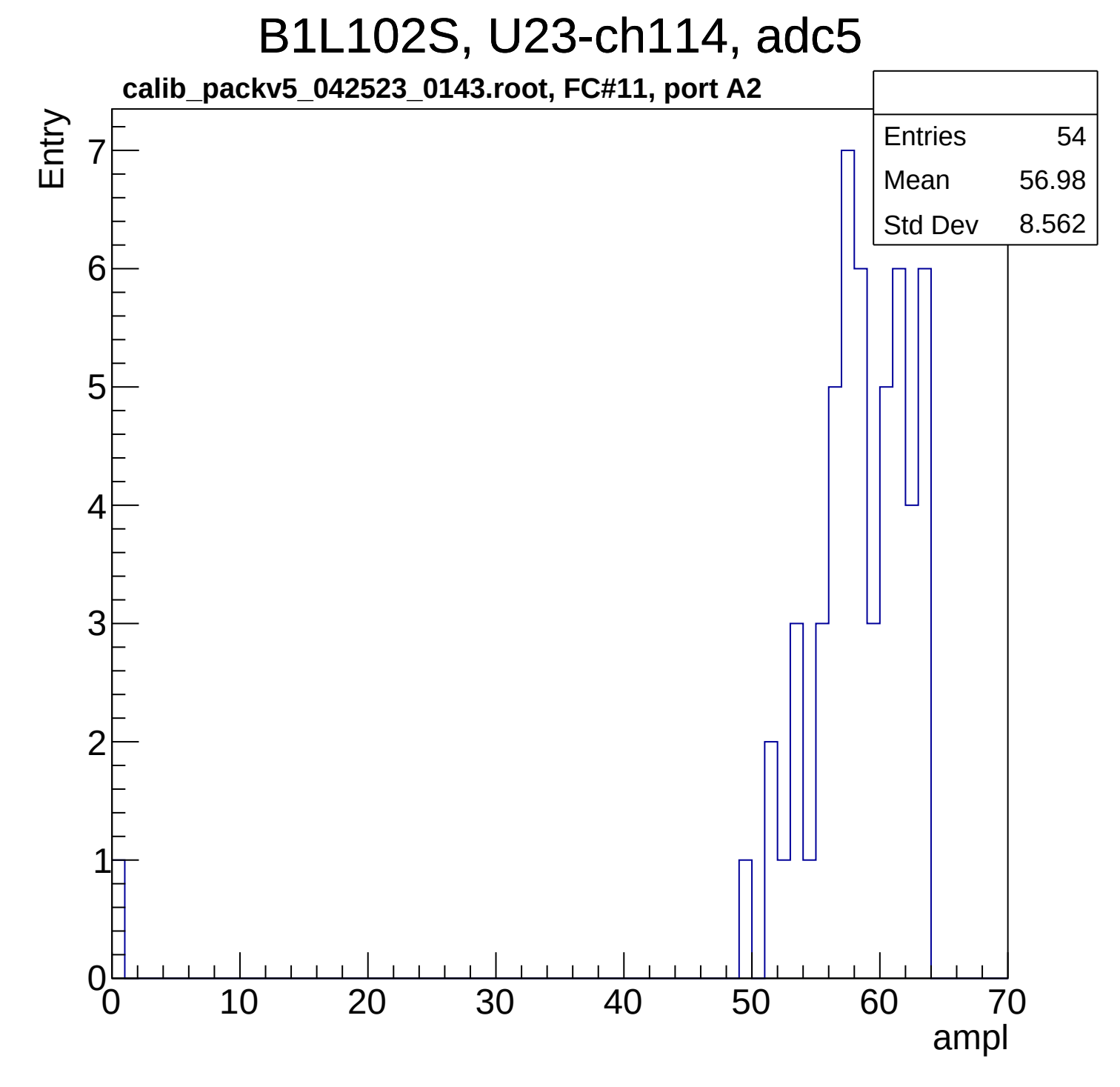

B1L102S, U23-ch114, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.32
Std Dev 4.852
Gaus mean : 28.3251
Gaus Width: 3.7387
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

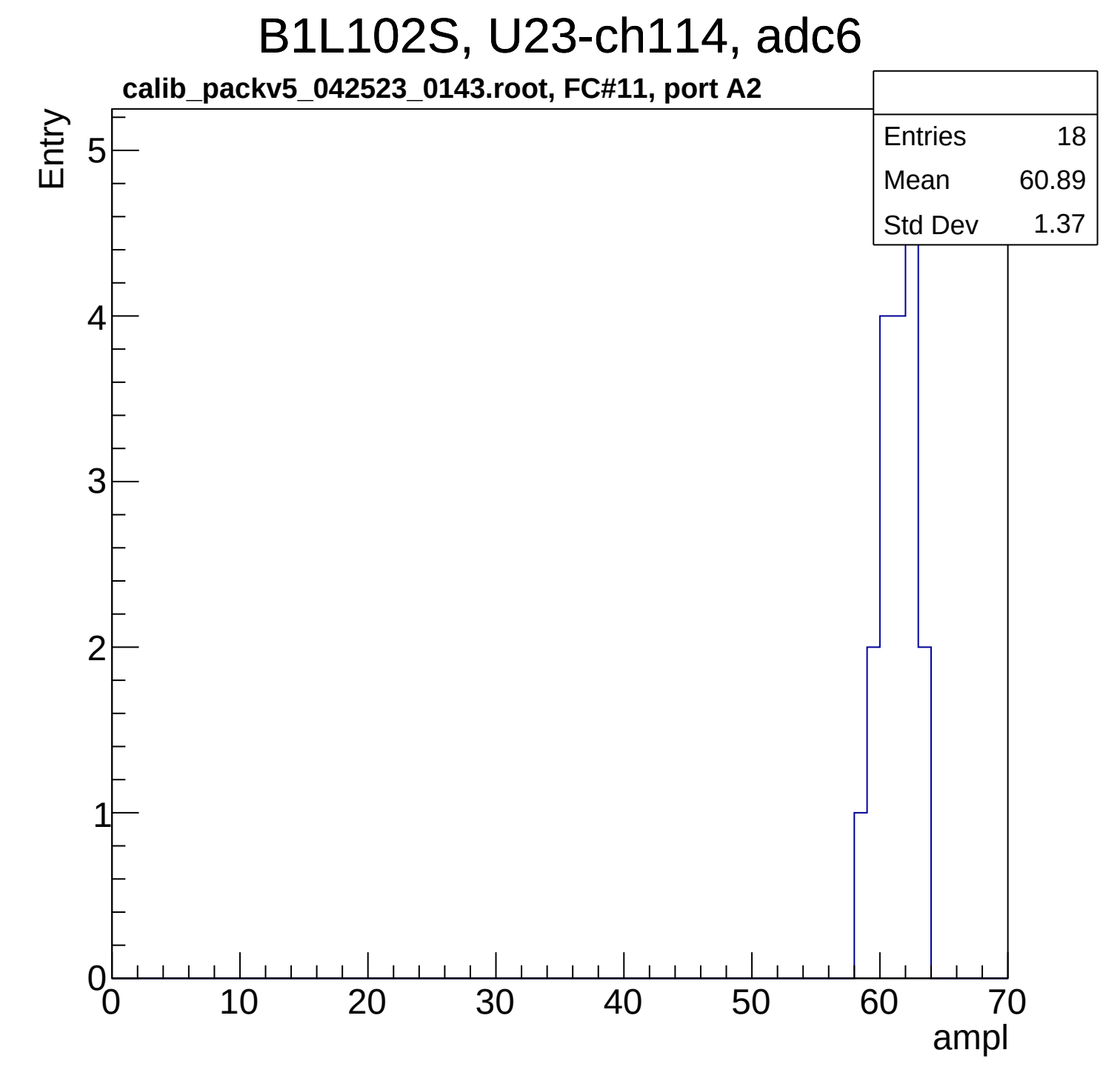

B1L102S, U23-ch114, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.83
Std Dev 4.161
Gaus mean : 35.2129
Gaus Width: 4.0767
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

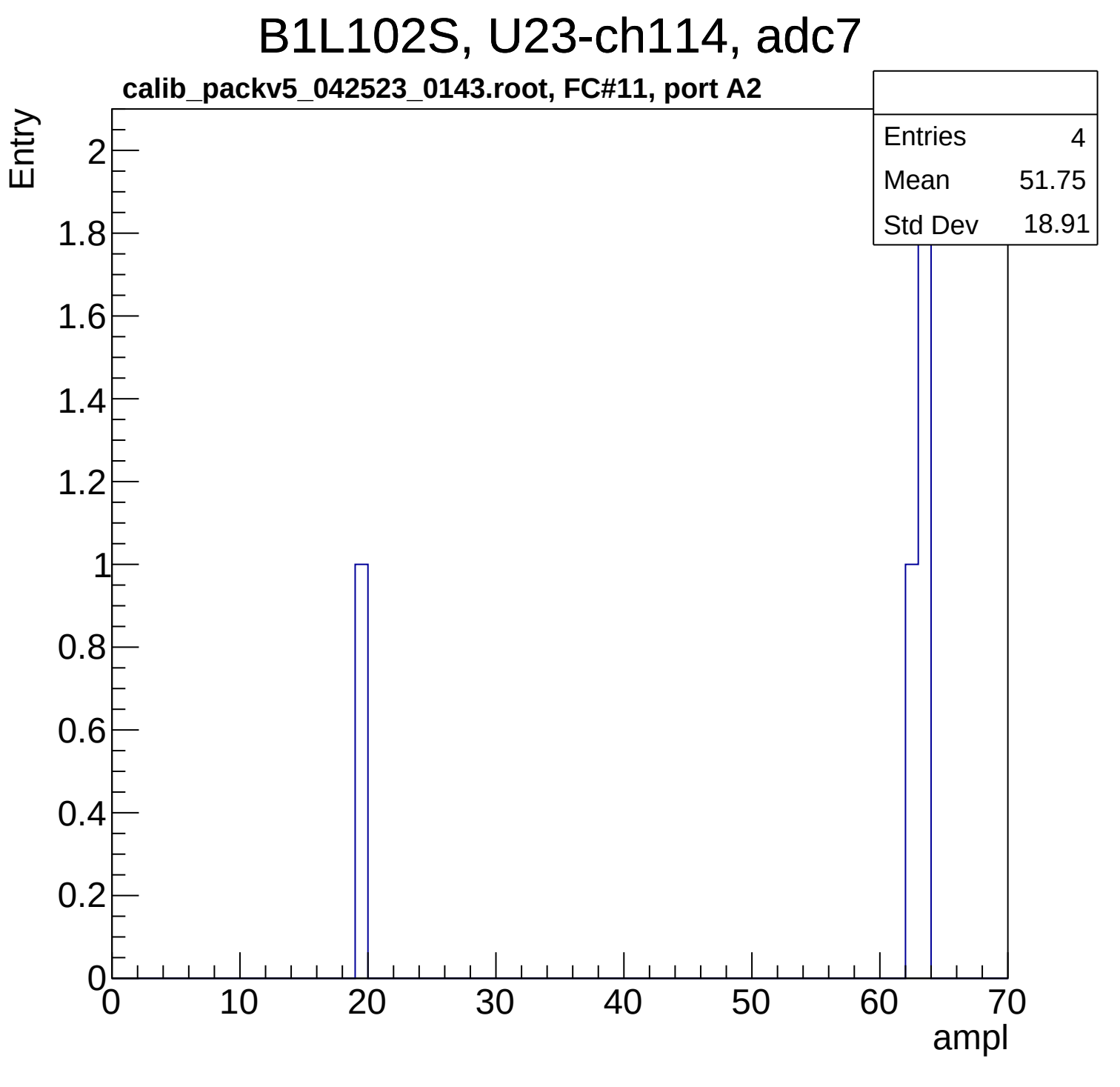

B1L102S, U23-ch114, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 40.66
Std Dev 3.496
Gaus mean : 41.5254
Gaus Width: 3.9120
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

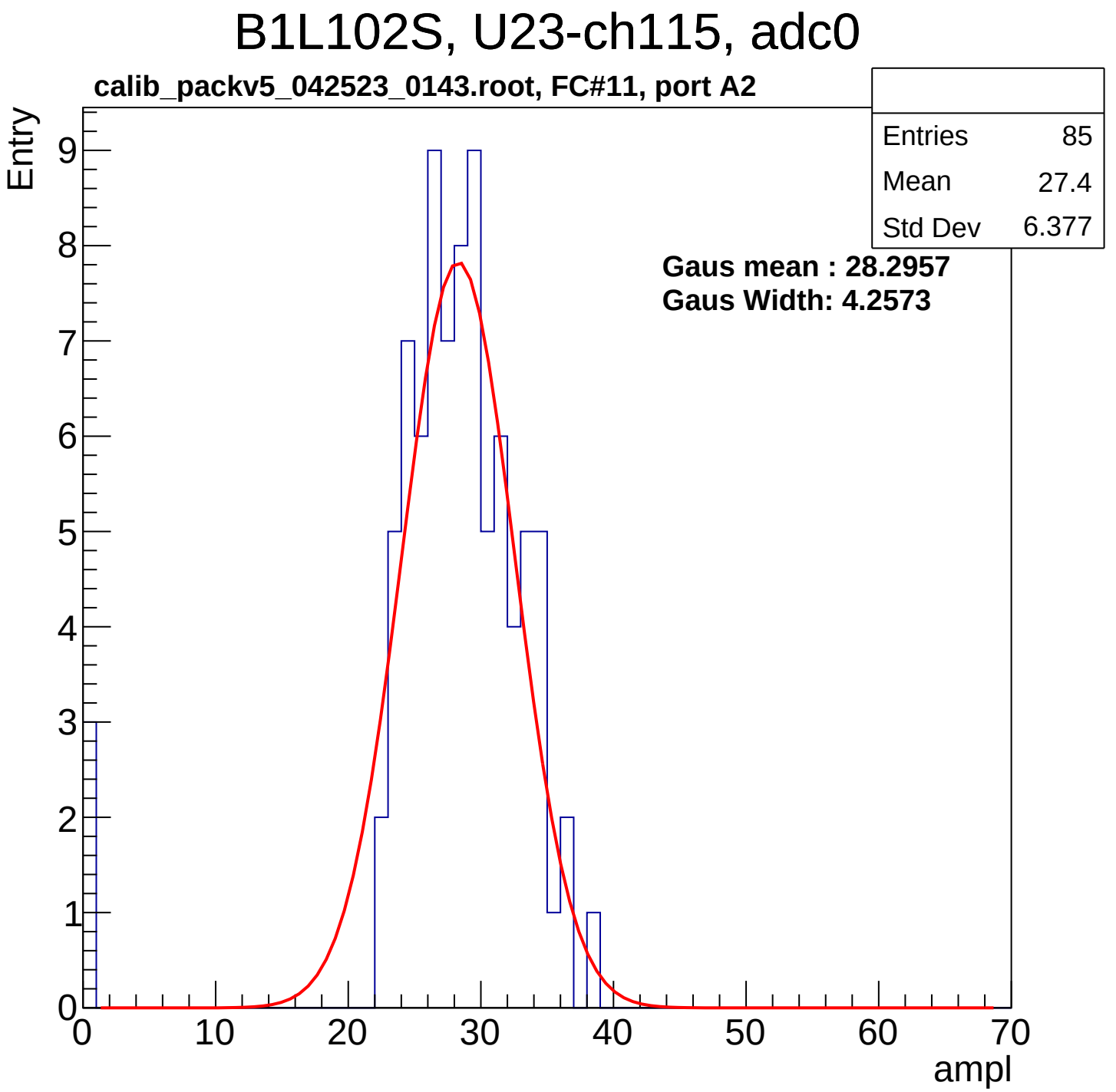

B1L102S, U23-ch114, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 47.29
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

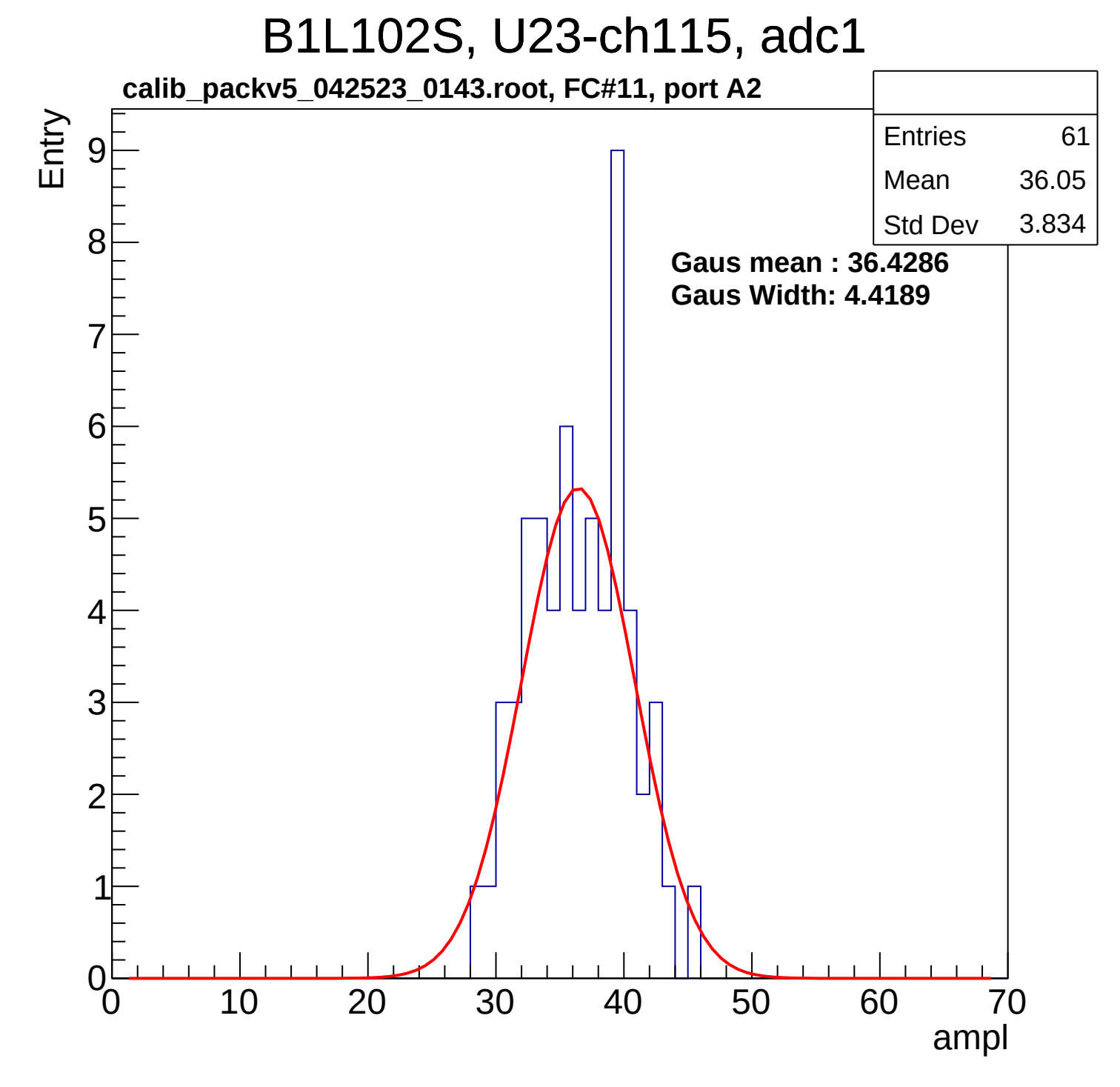

B1L102S, U23-ch114, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 53.3
Std Dev 3.573
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

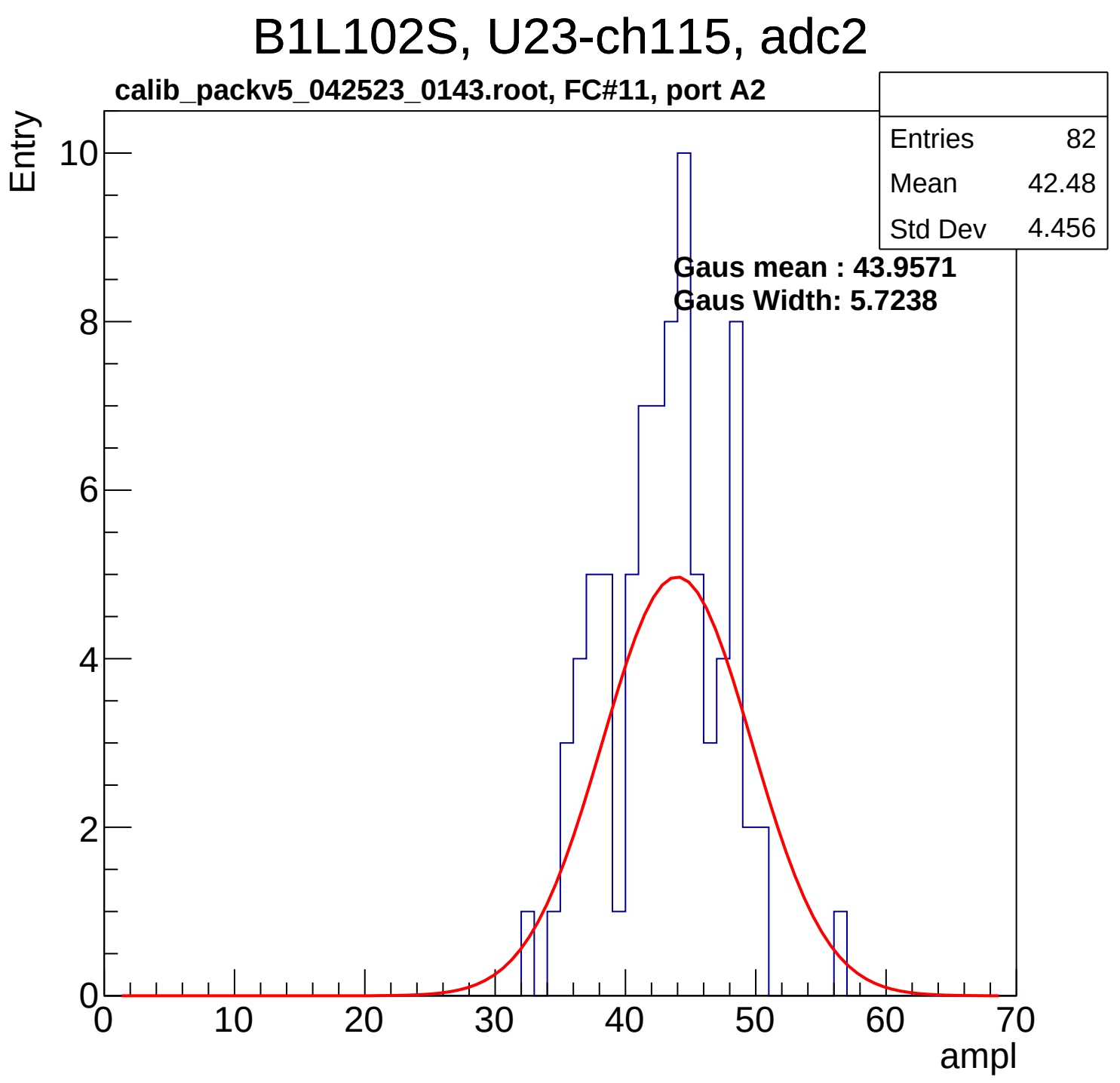

B1L102S, U23-ch114, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 56.98
Std Dev 8.562
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

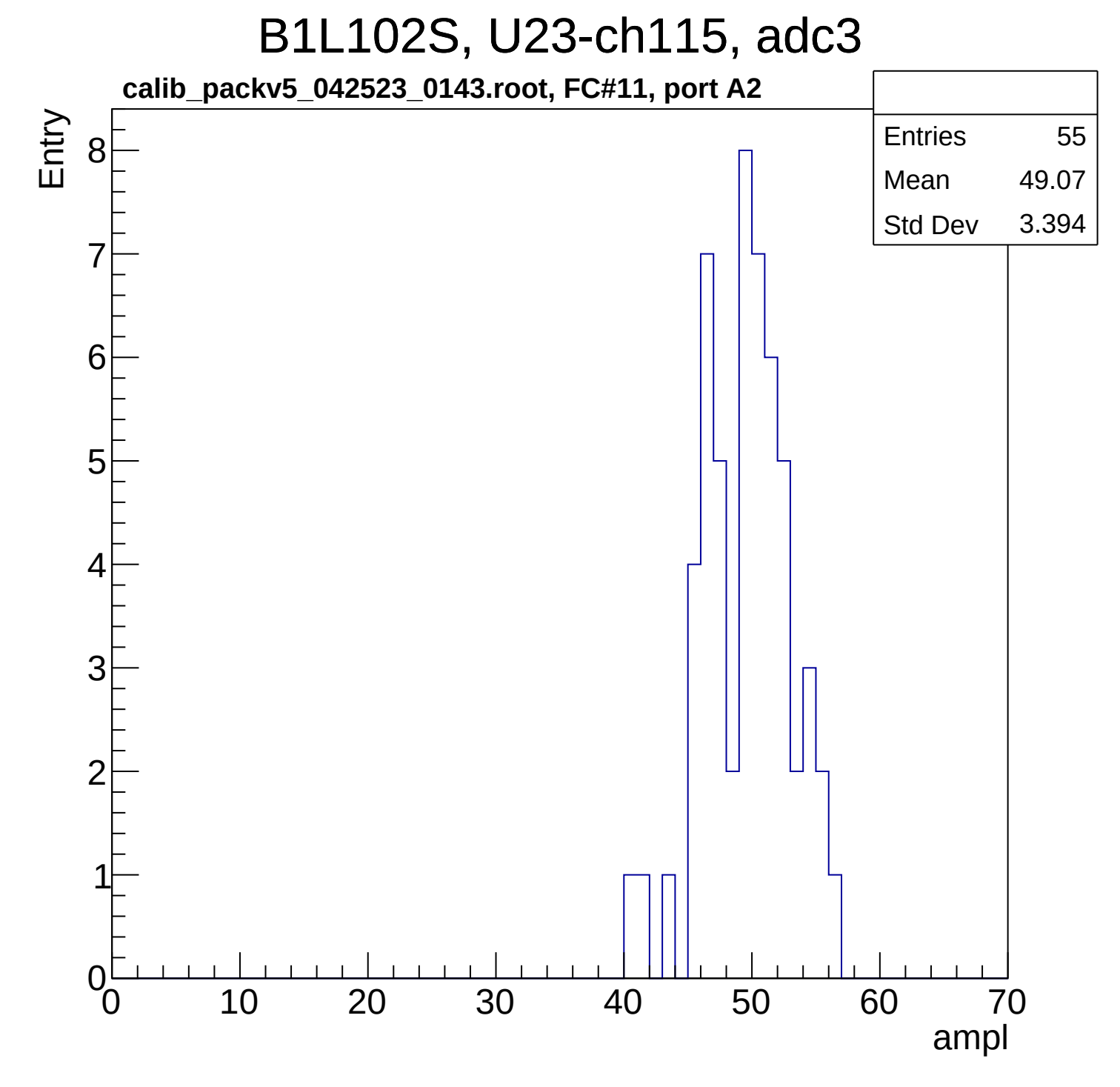

B1L102S, U23-ch114, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 60.89
Std Dev 1.37
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

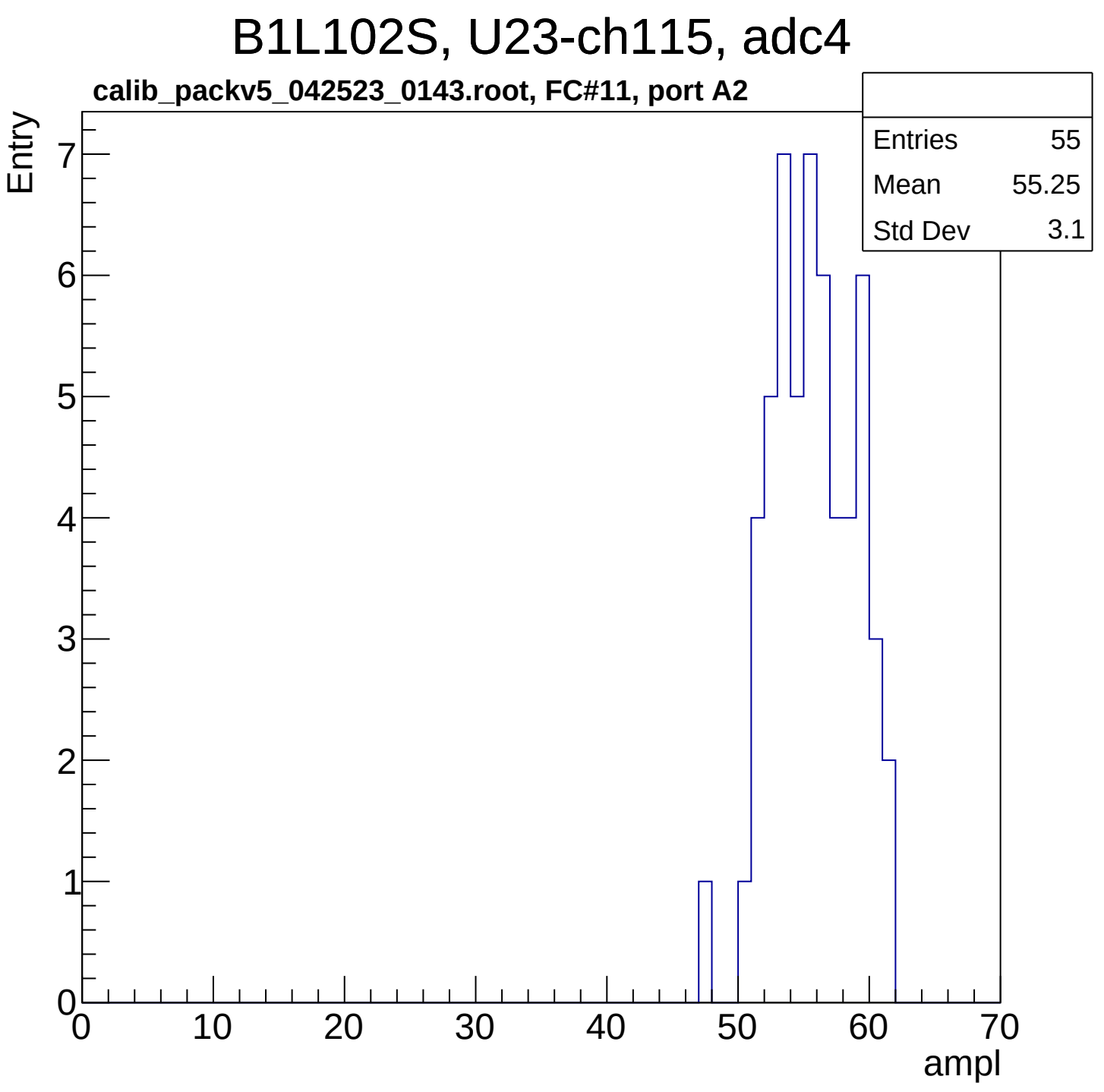

B1L102S, U23-ch114, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 51.75
Std Dev 18.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

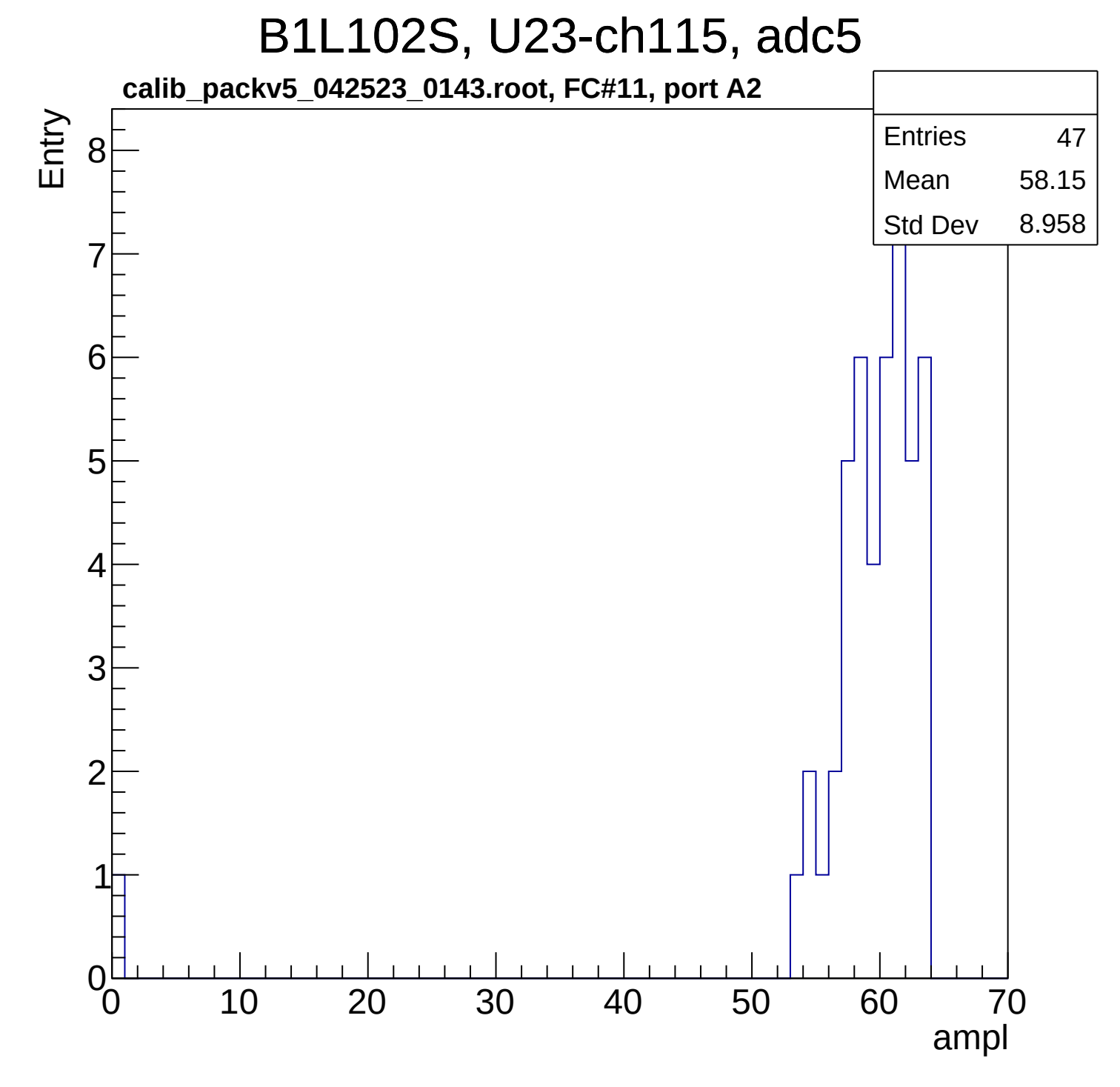

B1L102S, U23-ch115, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 27.4
Std Dev 6.377
Gaus mean : 28.2957
Gaus Width: 4.2573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

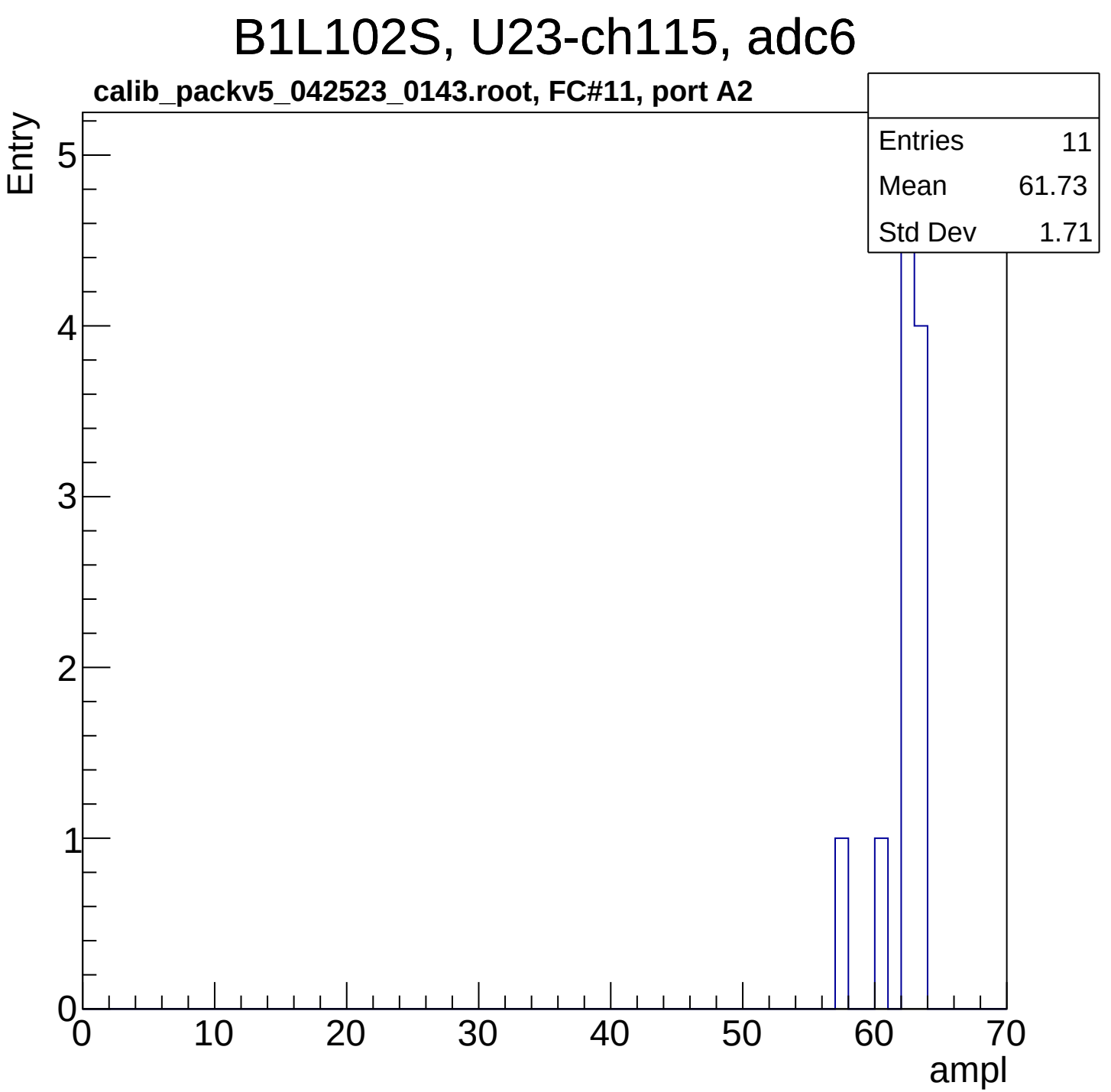

B1L102S, U23-ch115, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 36.05
Std Dev 3.834
Gaus mean : 36.4286
Gaus Width: 4.4189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

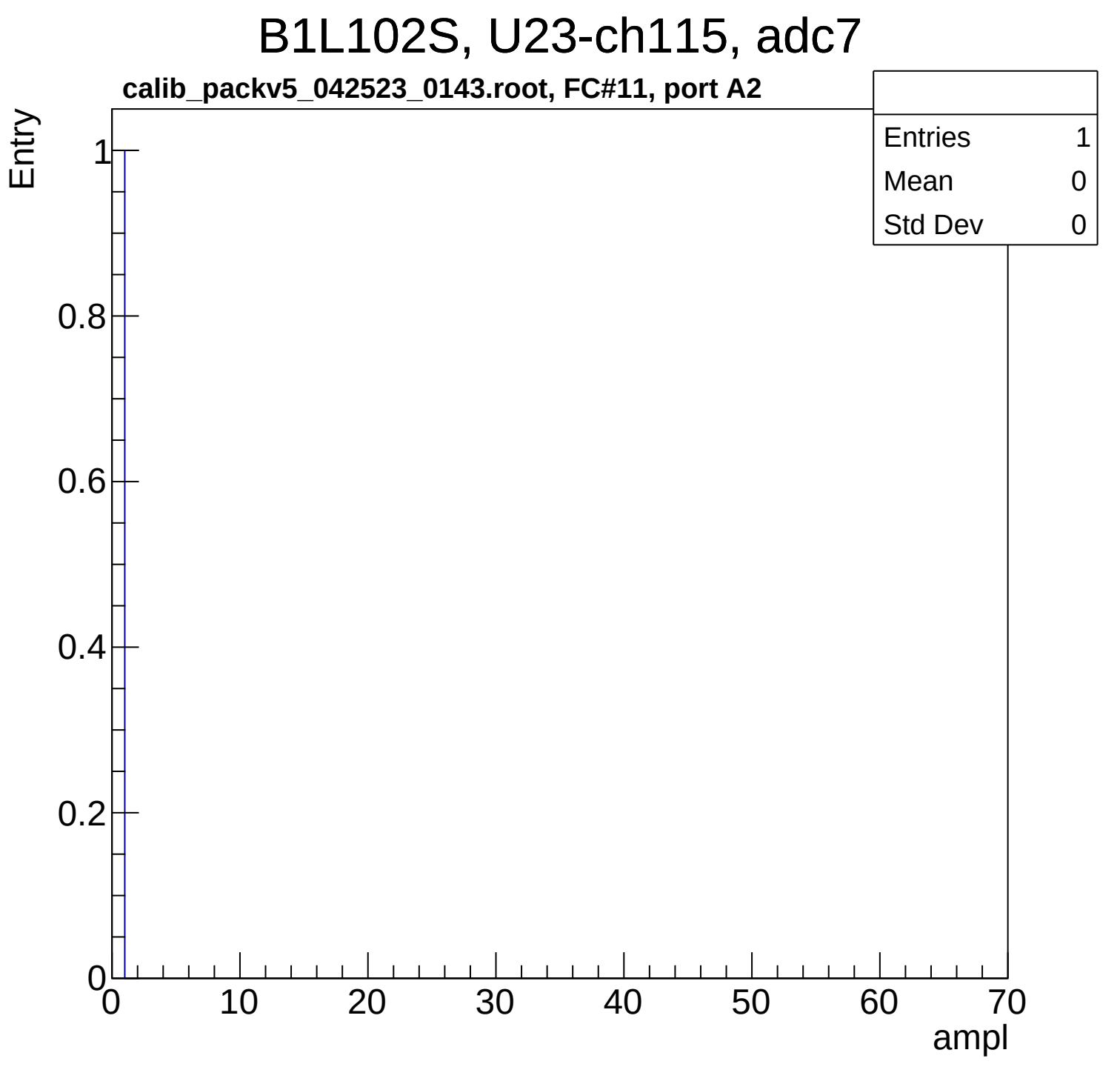

B1L102S, U23-ch115, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 42.48
Std Dev 4.456
Gaus mean : 43.9571
Gaus Width: 5.7238
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

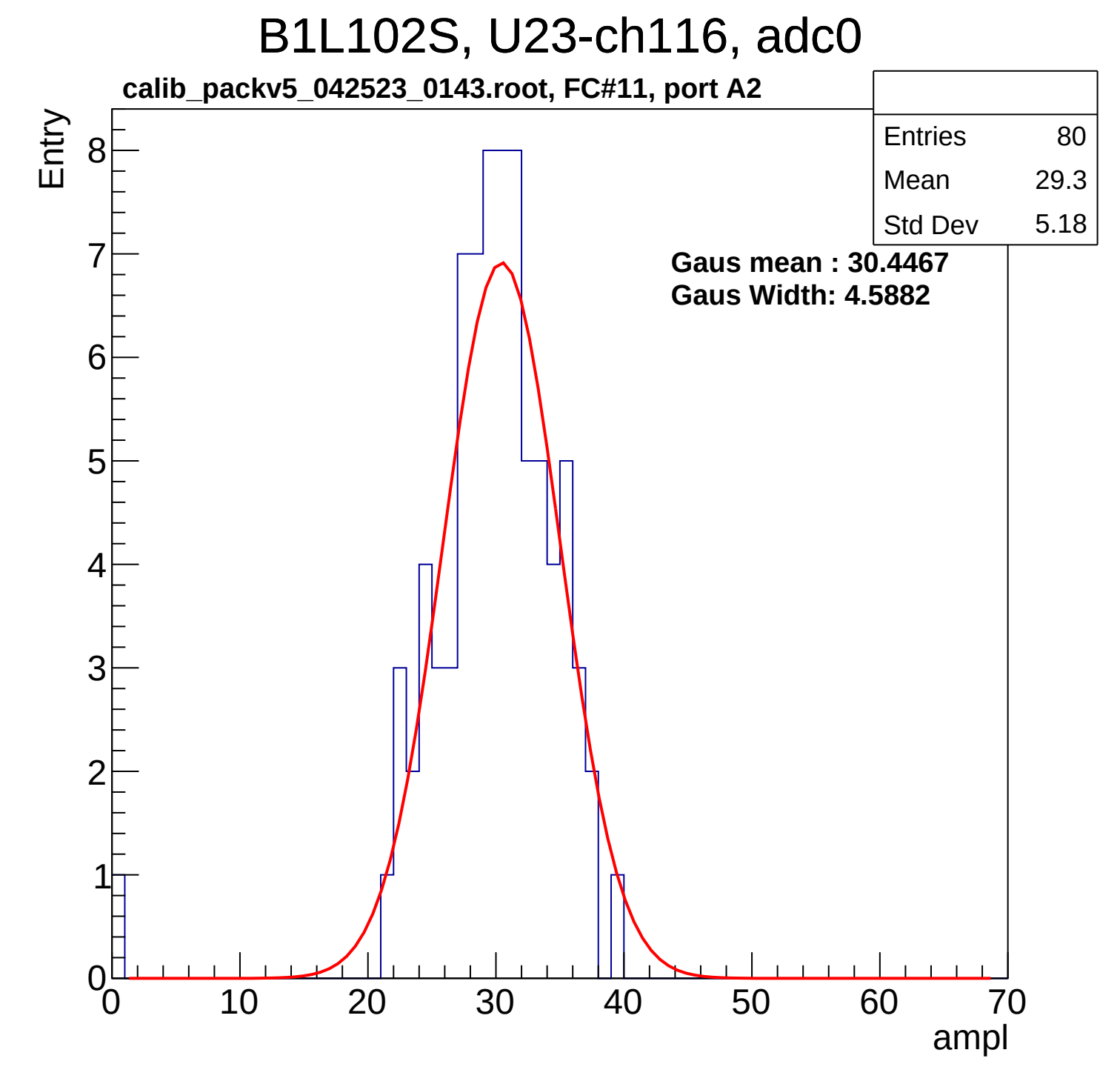

B1L102S, U23-ch115, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 49.07
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

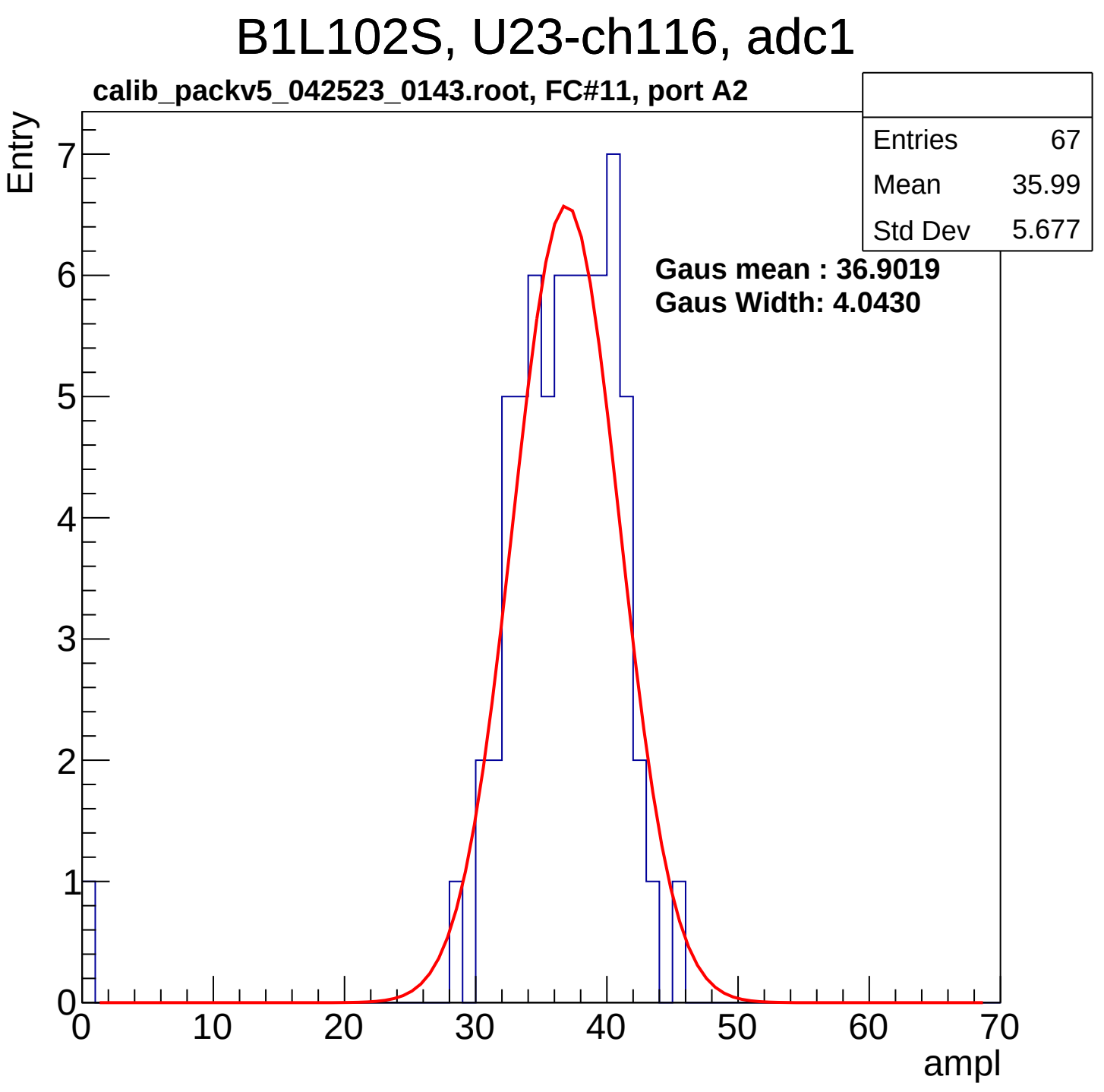

B1L102S, U23-ch115, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 55.25
Std Dev 3.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

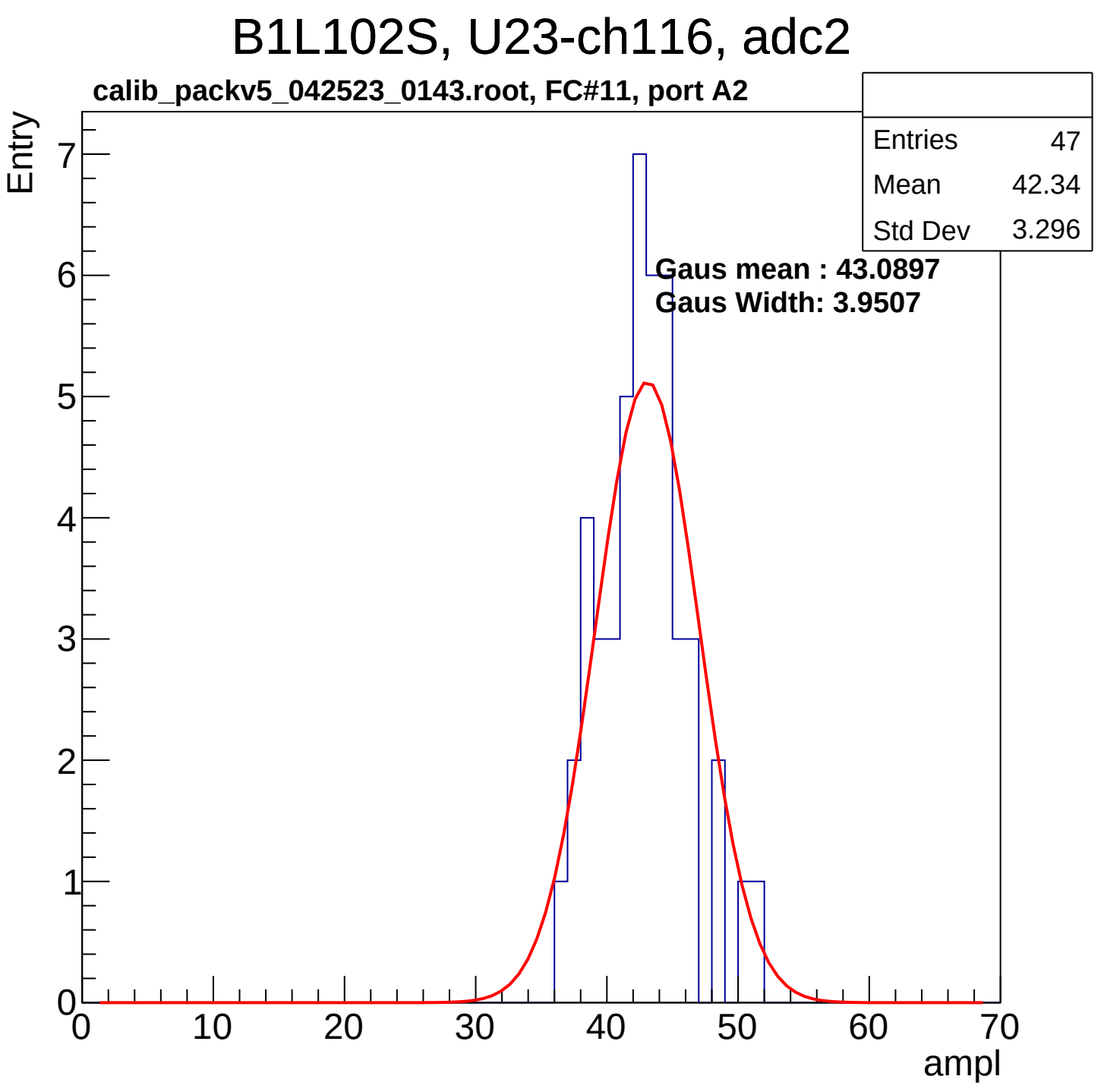

B1L102S, U23-ch115, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.15
Std Dev 8.958
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

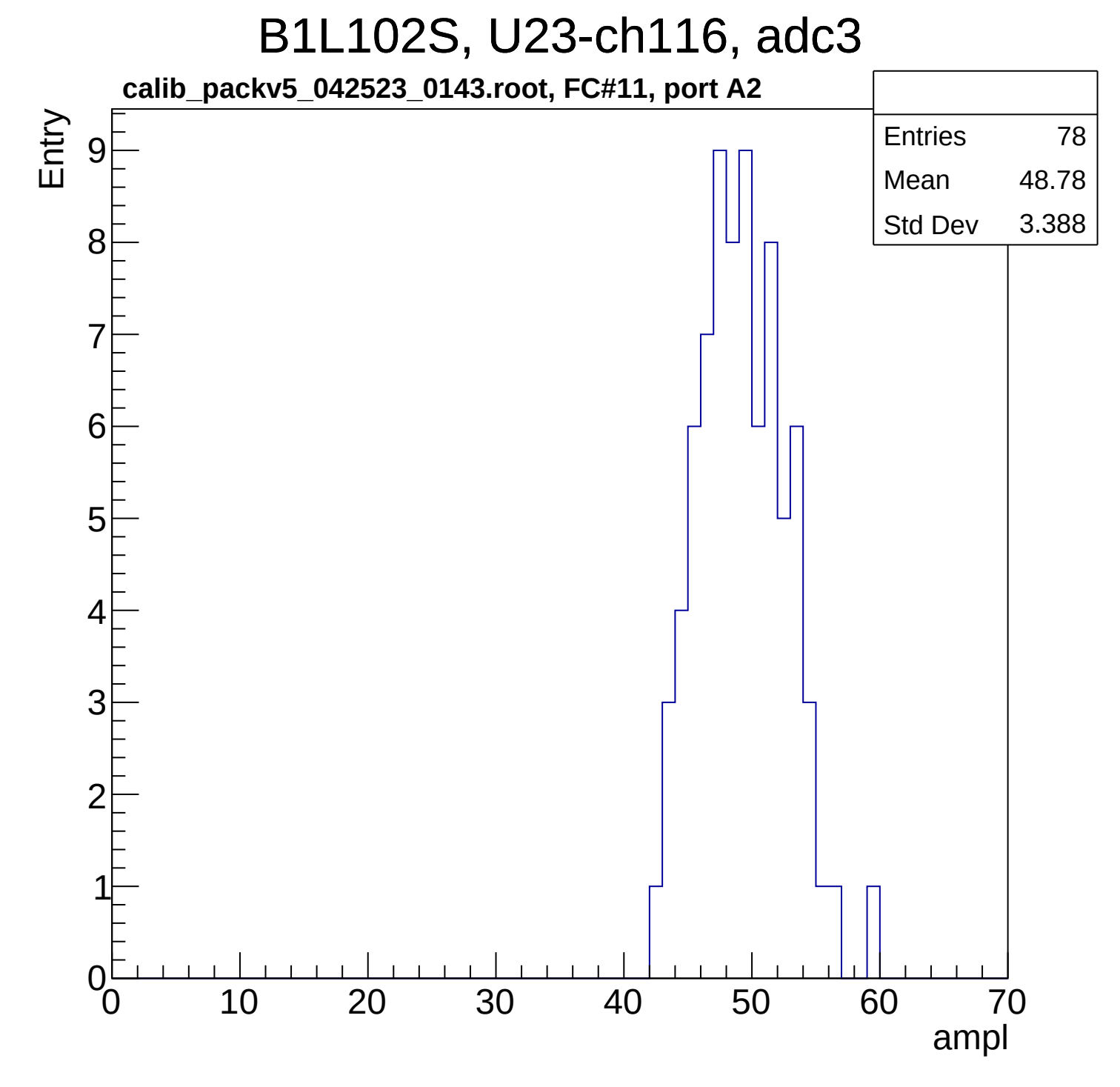

B1L102S, U23-ch115, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.73
Std Dev 1.71
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

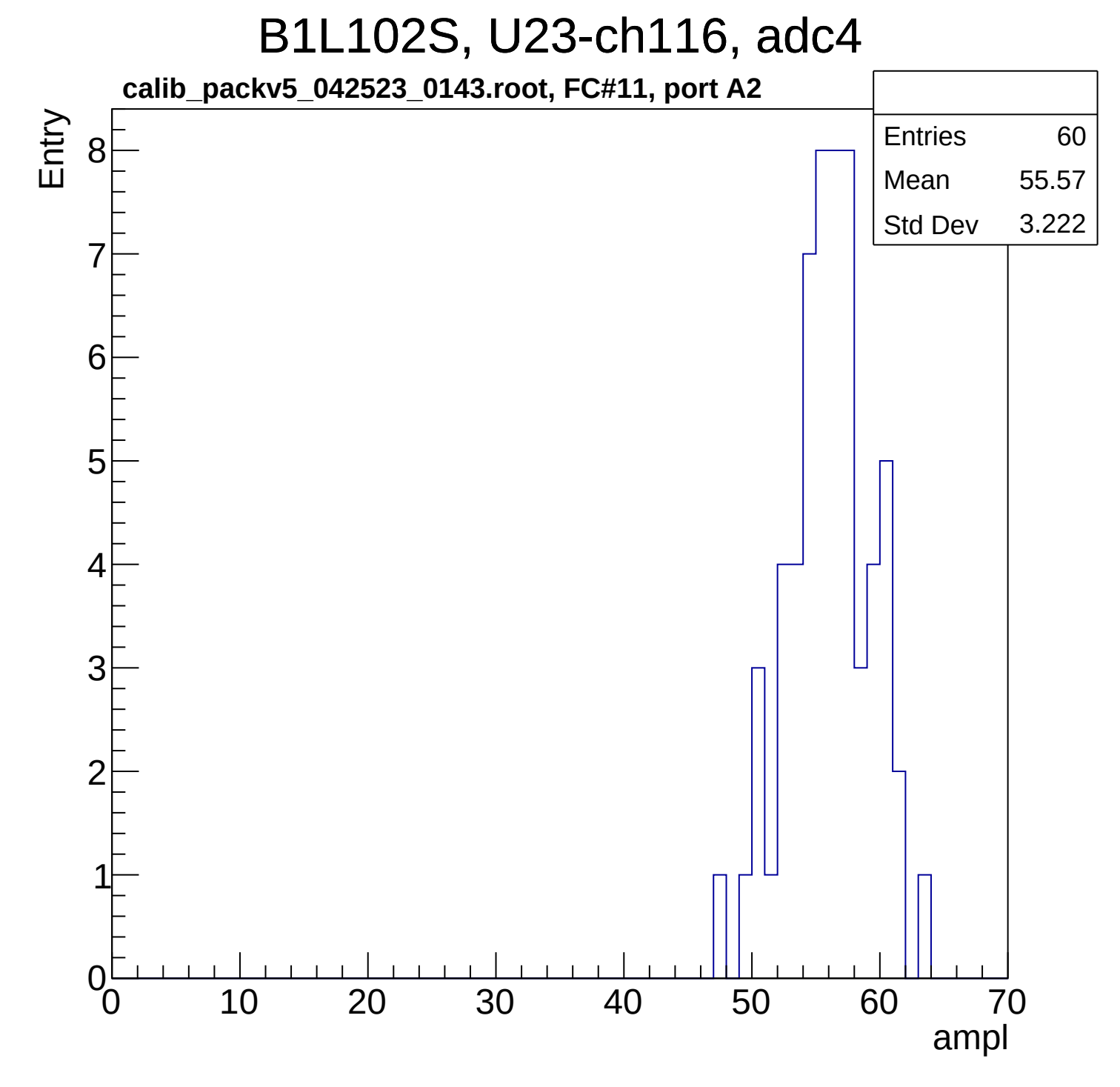

B1L102S, U23-ch115, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

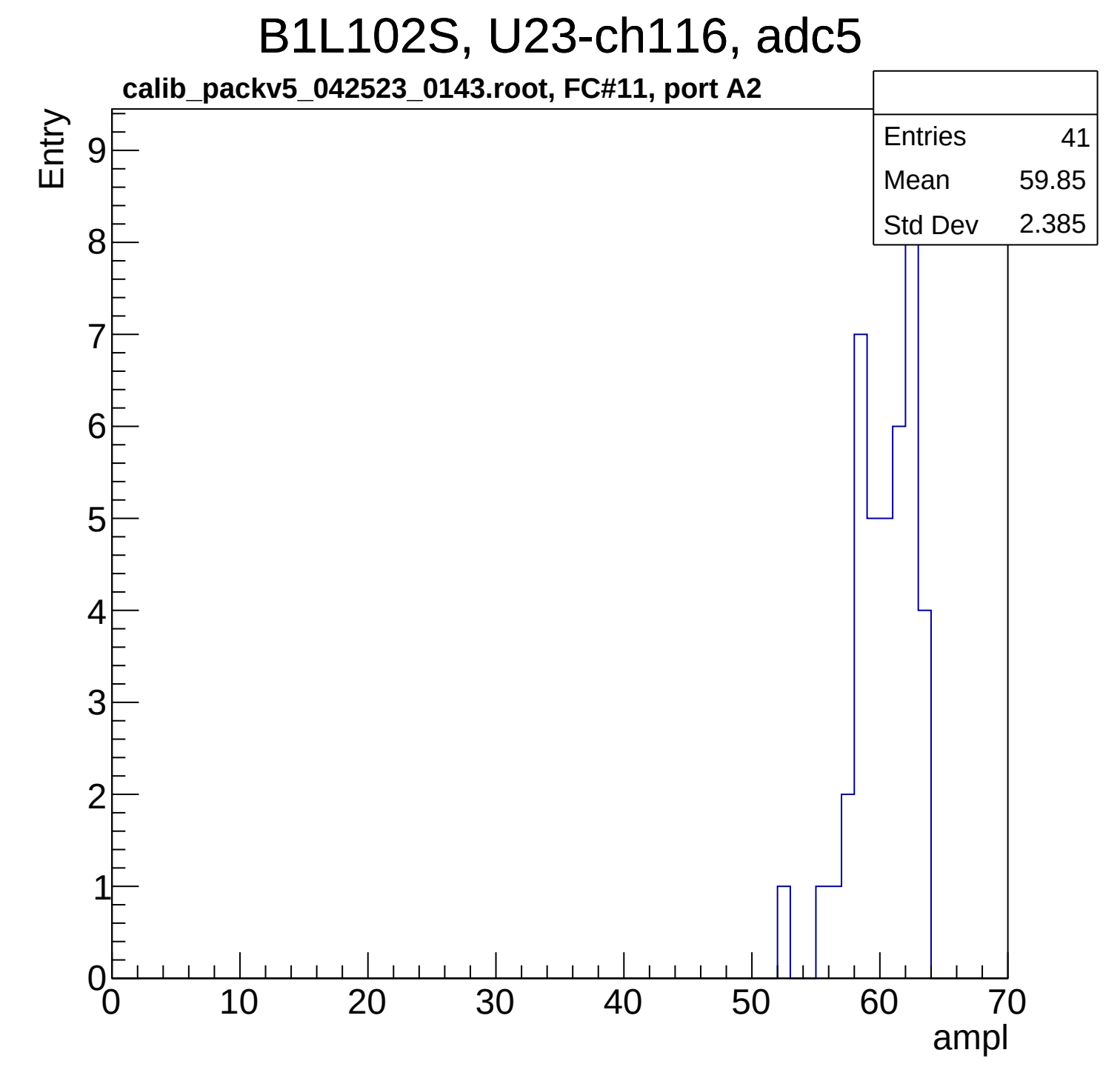

B1L102S, U23-ch116, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 29.3
Std Dev 5.18
Gaus mean : 30.4467
Gaus Width: 4.5882
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

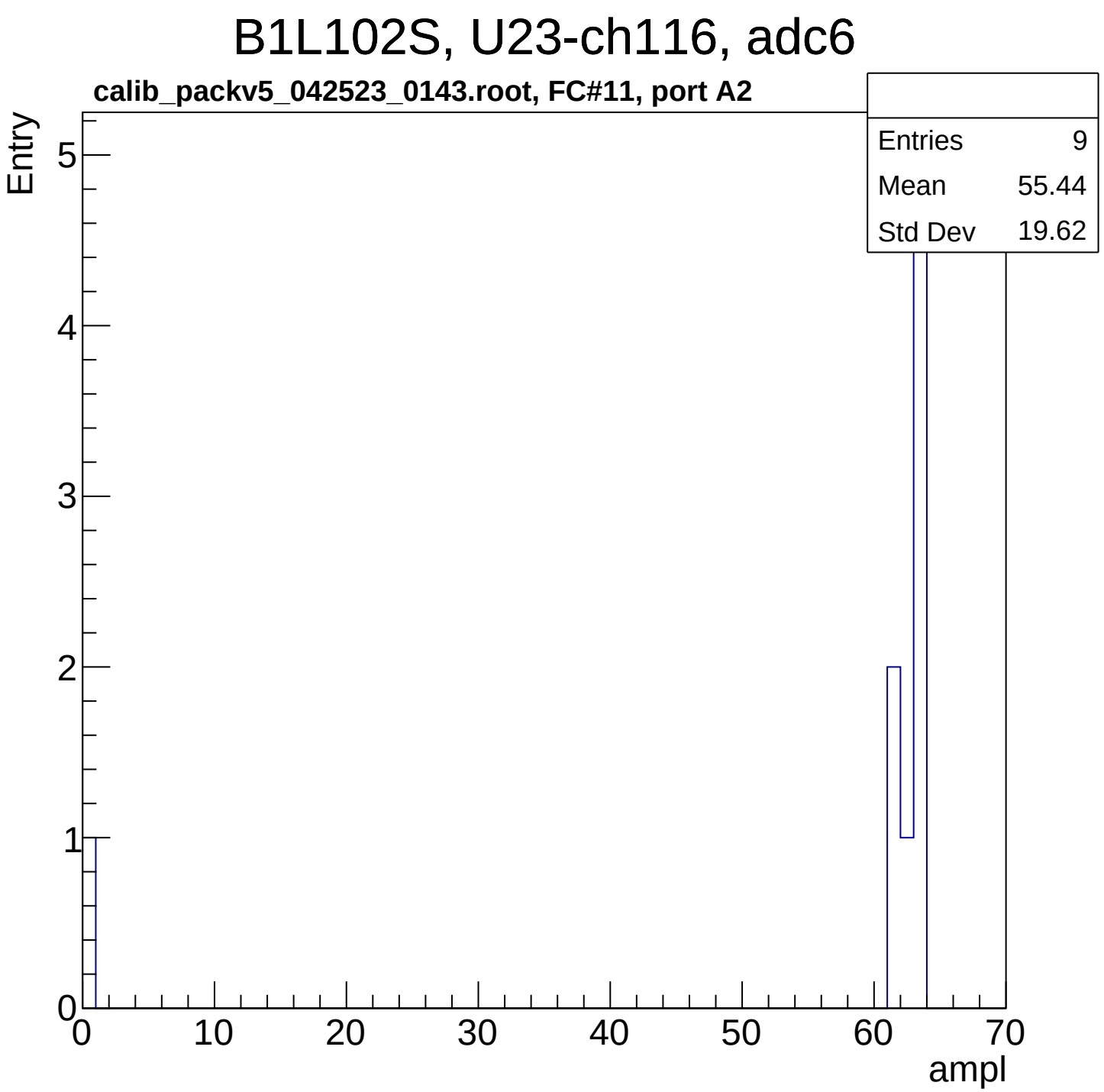

B1L102S, U23-ch116, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.99
Std Dev 5.677
Gaus mean : 36.9019
Gaus Width: 4.0430
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

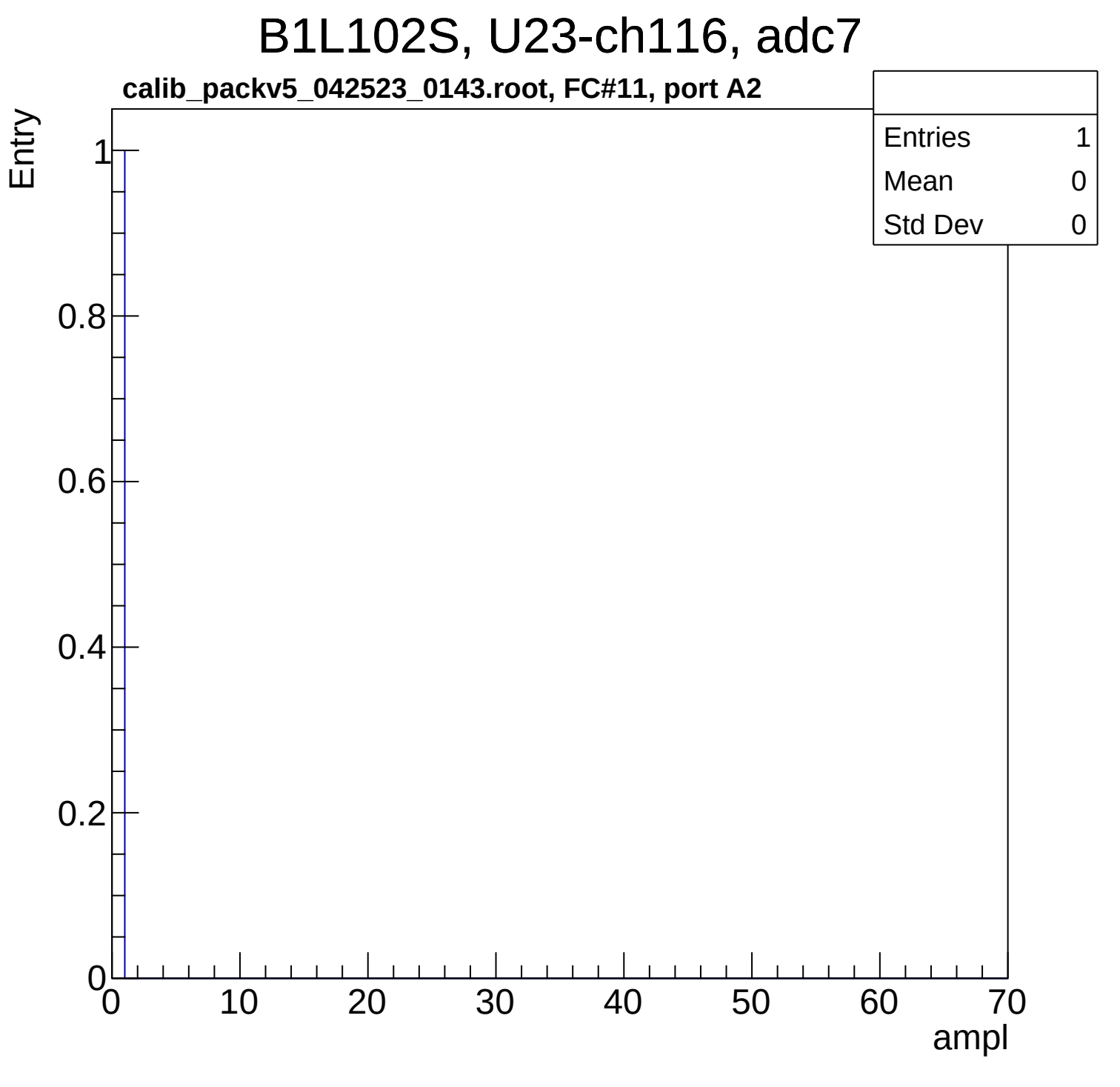

B1L102S, U23-ch116, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 42.34
Std Dev 3.296
Gaus mean : 43.0897
Gaus Width: 3.9507
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

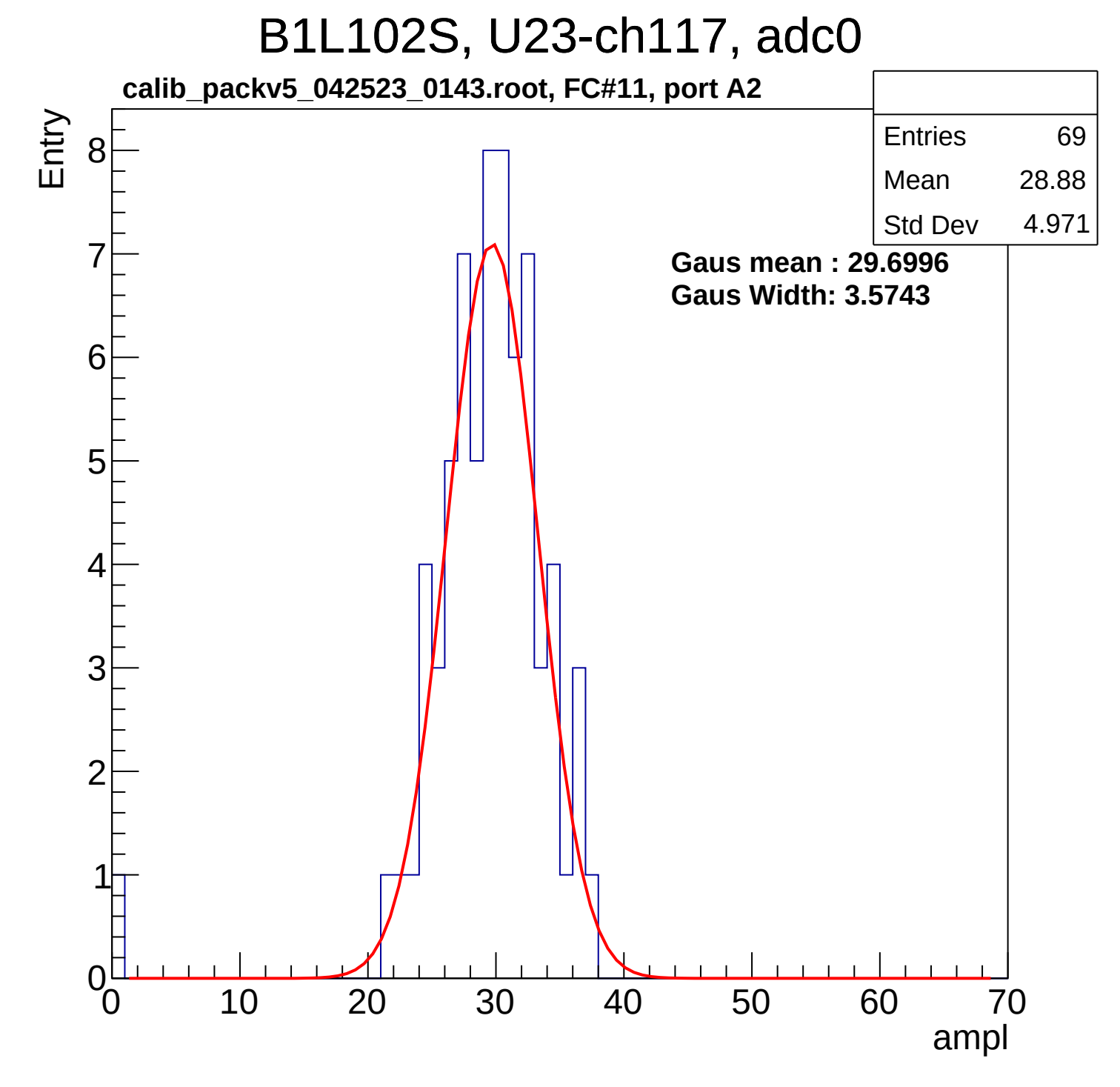

B1L102S, U23-ch116, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 48.78
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

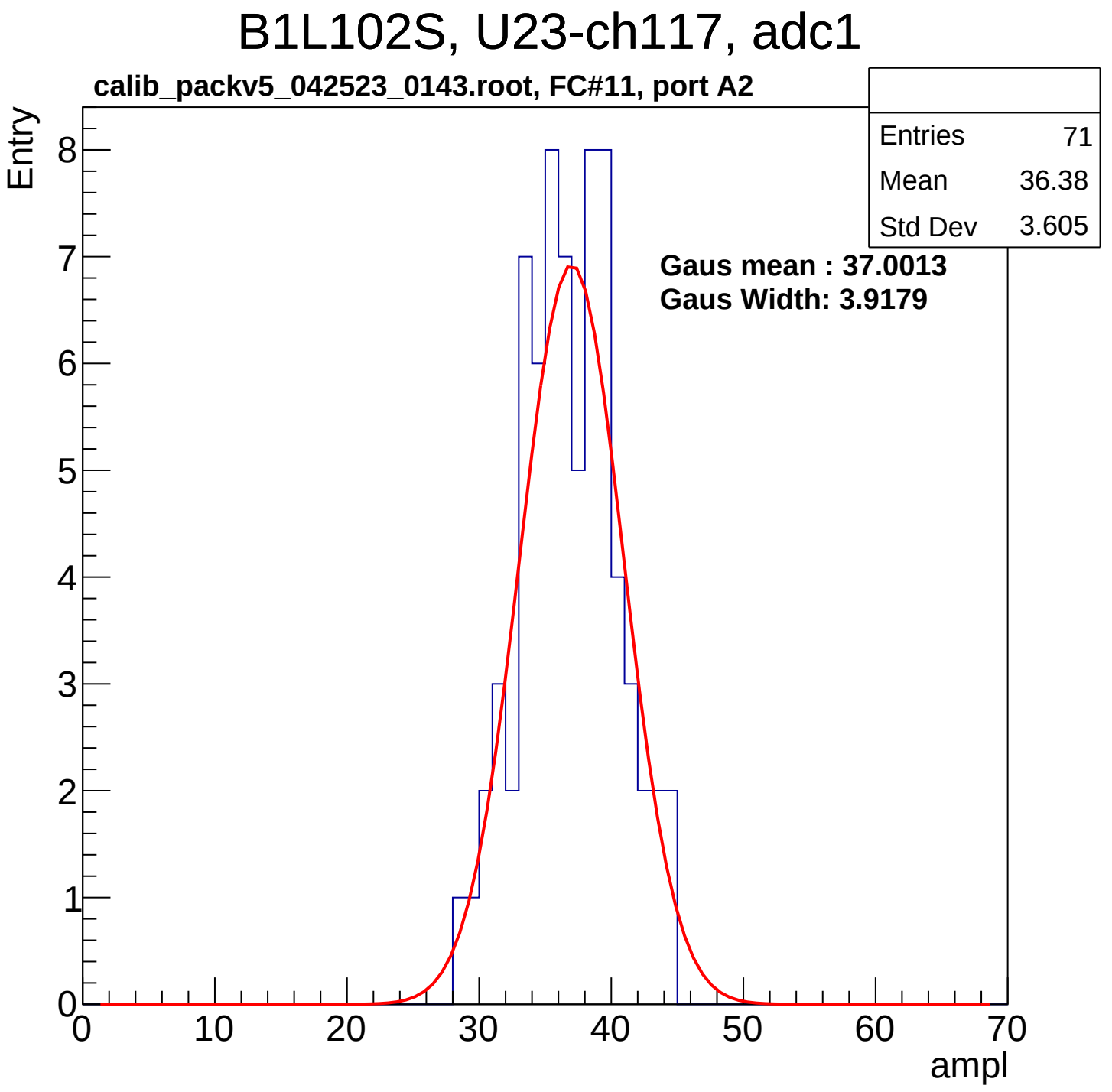

B1L102S, U23-ch116, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 55.57
Std Dev 3.222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

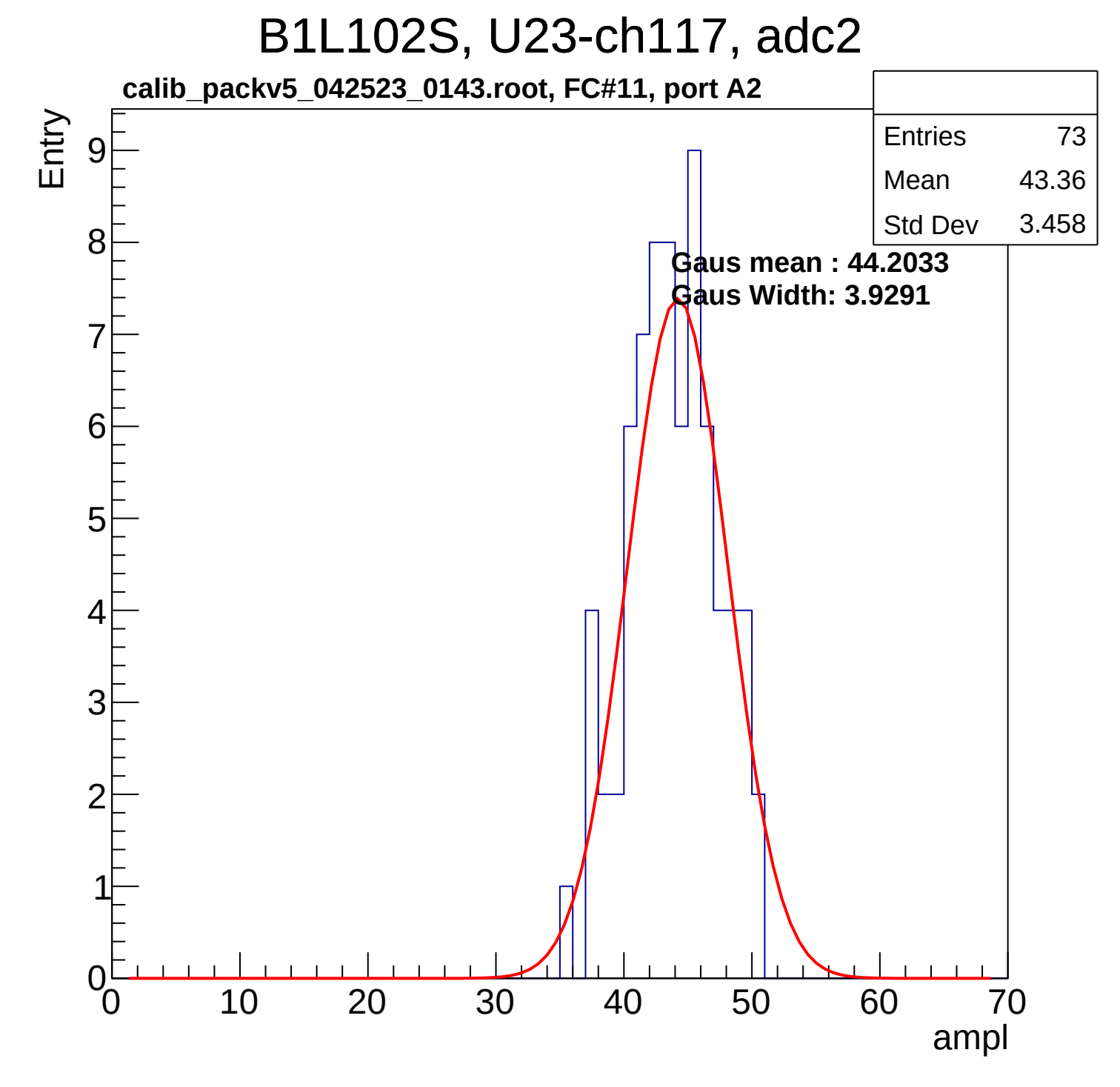

B1L102S, U23-ch116, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 59.85
Std Dev 2.385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

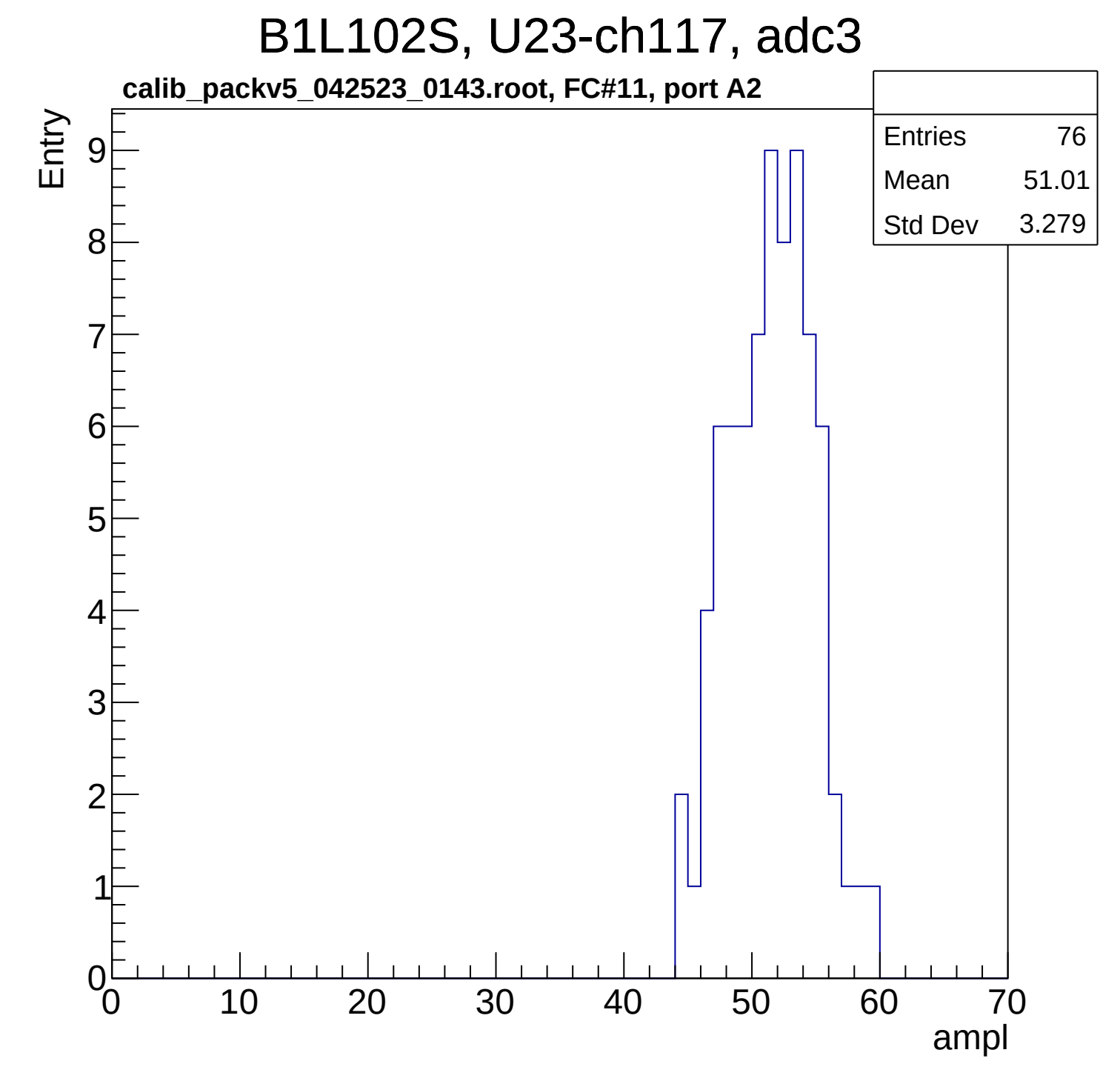

B1L102S, U23-ch116, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 55.44
Std Dev 19.62
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

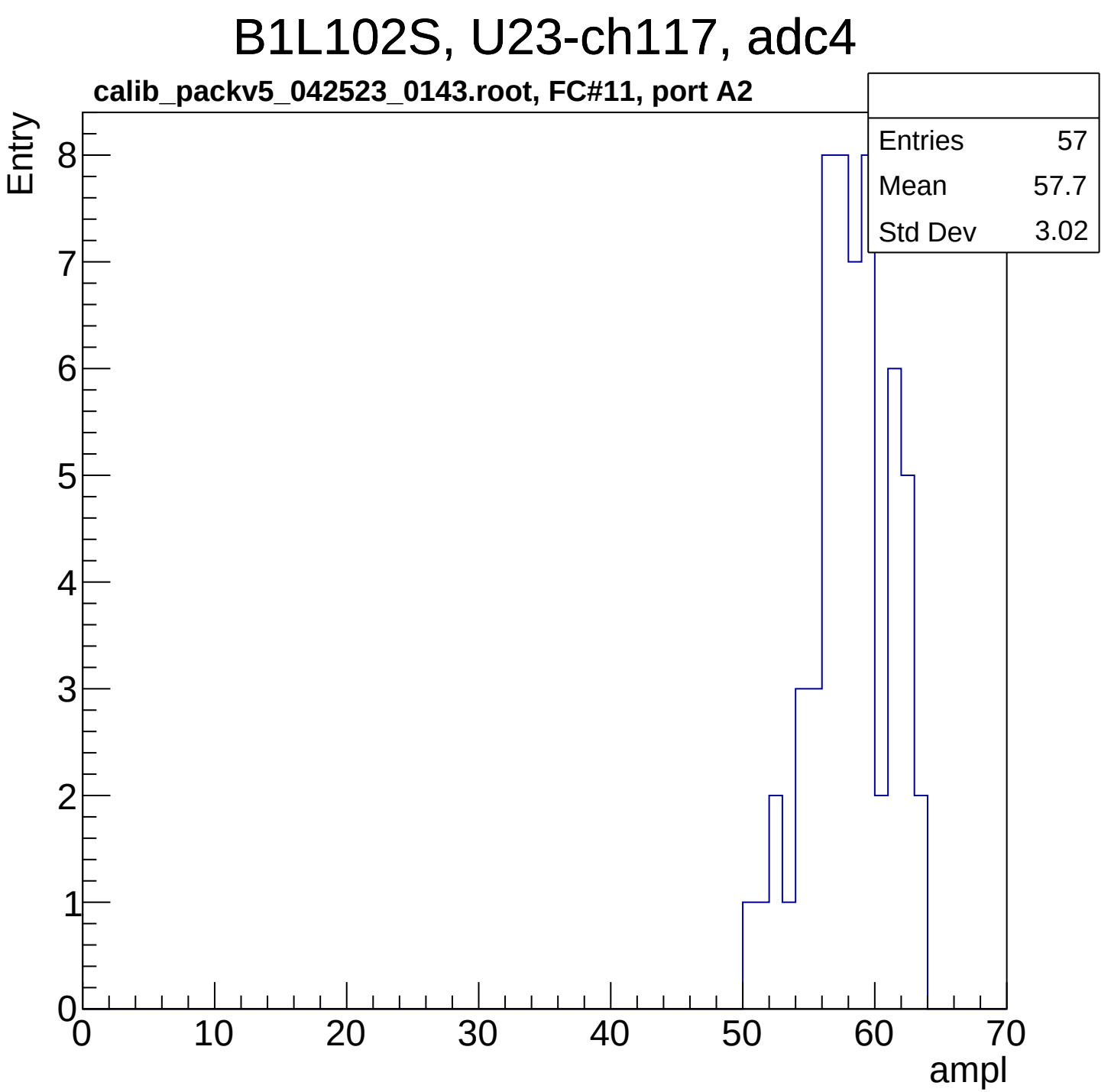

B1L102S, U23-ch116, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

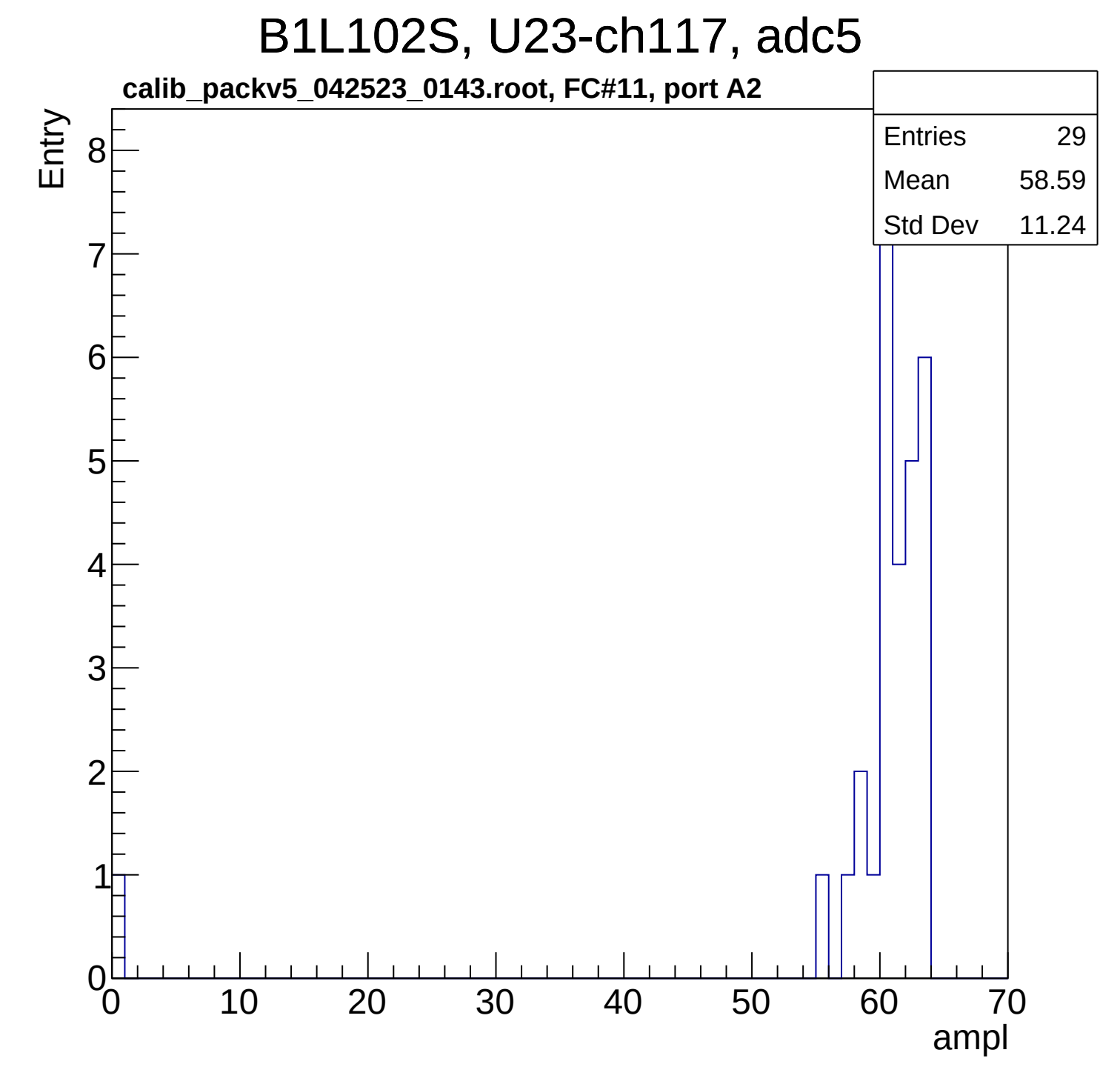

B1L102S, U23-ch117, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.88
Std Dev 4.971
Gaus mean : 29.6996
Gaus Width: 3.5743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

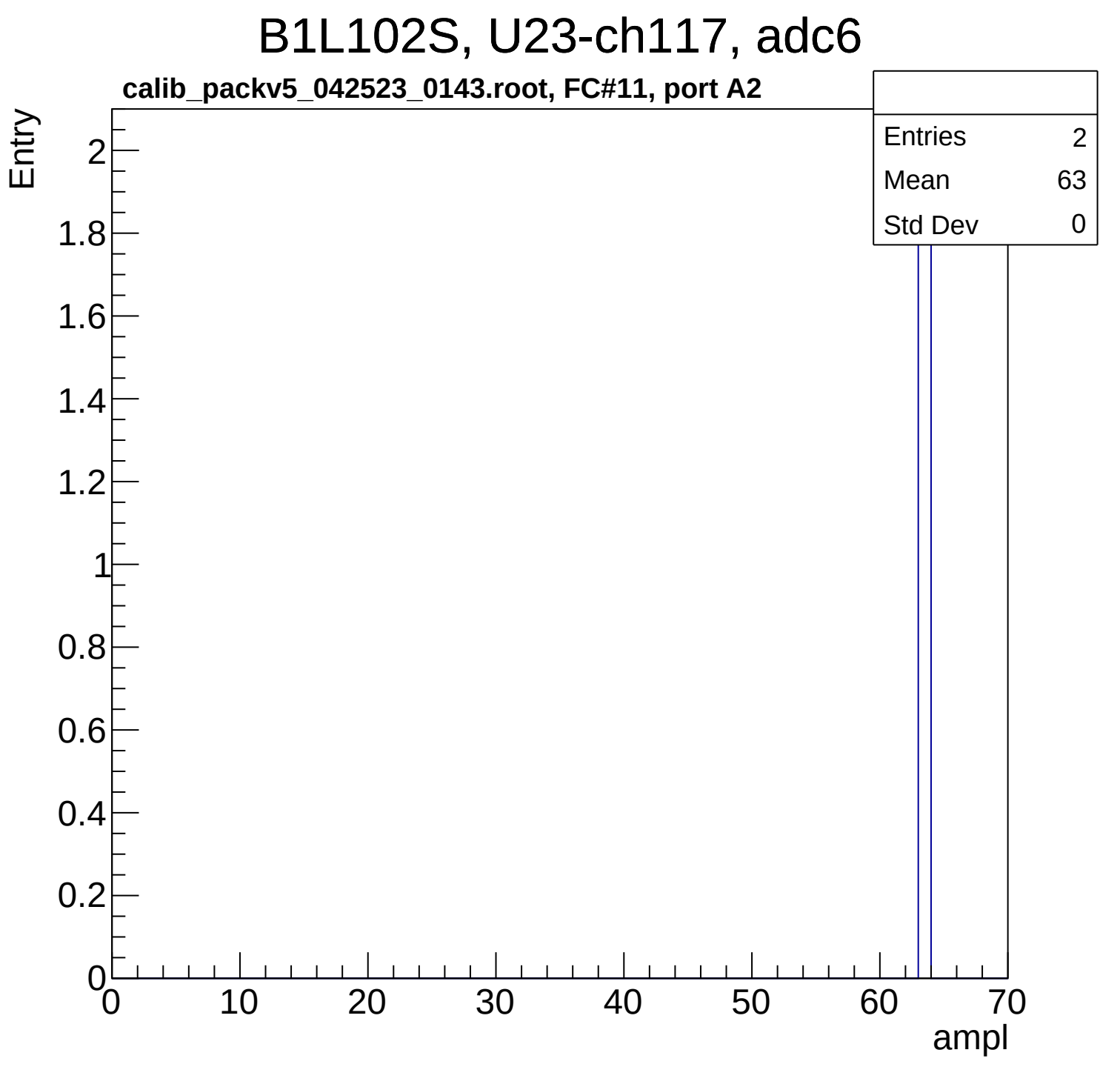

B1L102S, U23-ch117, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 36.38
Std Dev 3.605
Gaus mean : 37.0013
Gaus Width: 3.9179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

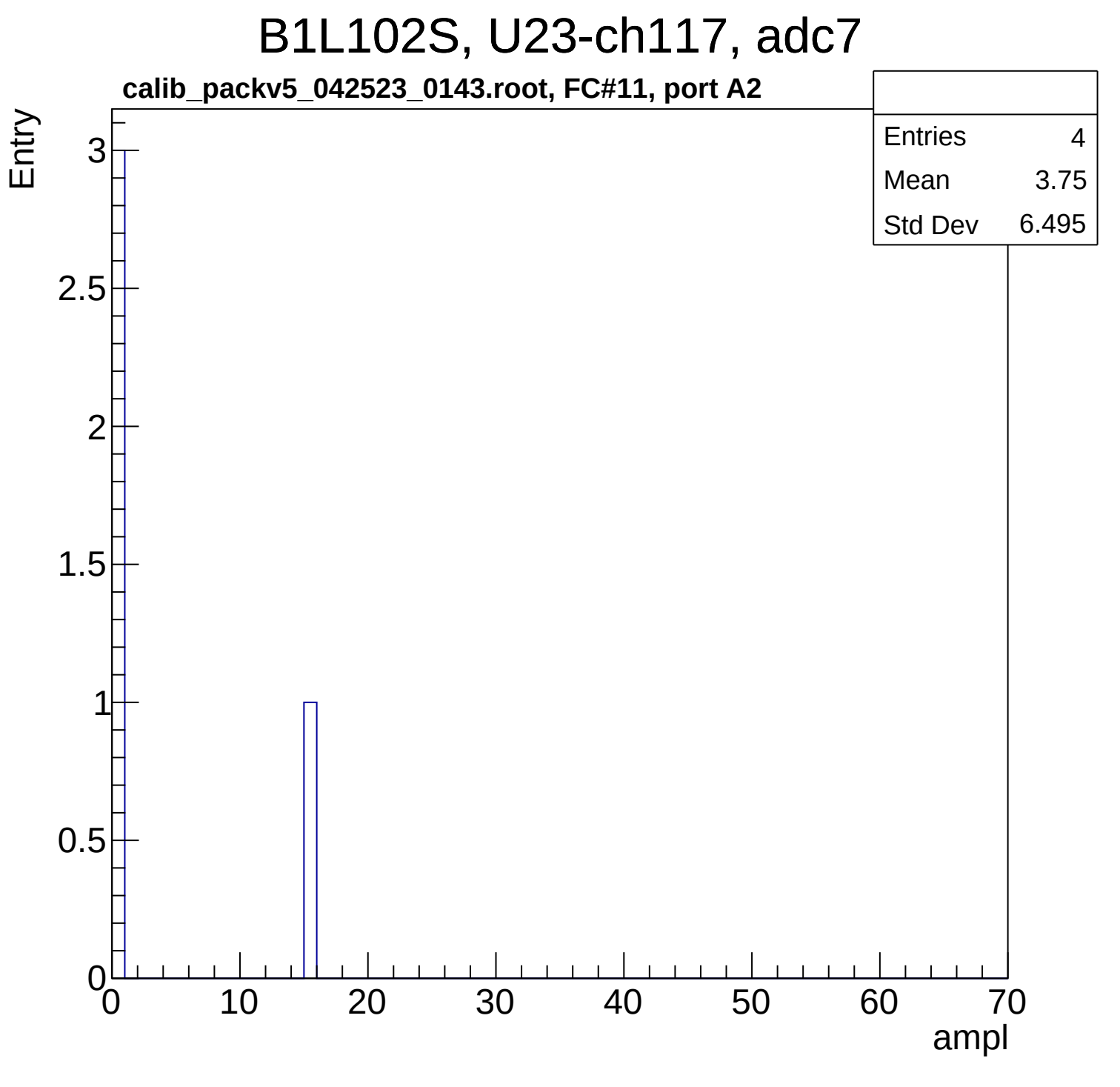

B1L102S, U23-ch117, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 43.36
Std Dev 3.458
Gaus mean : 44.2033
Gaus Width: 3.9291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

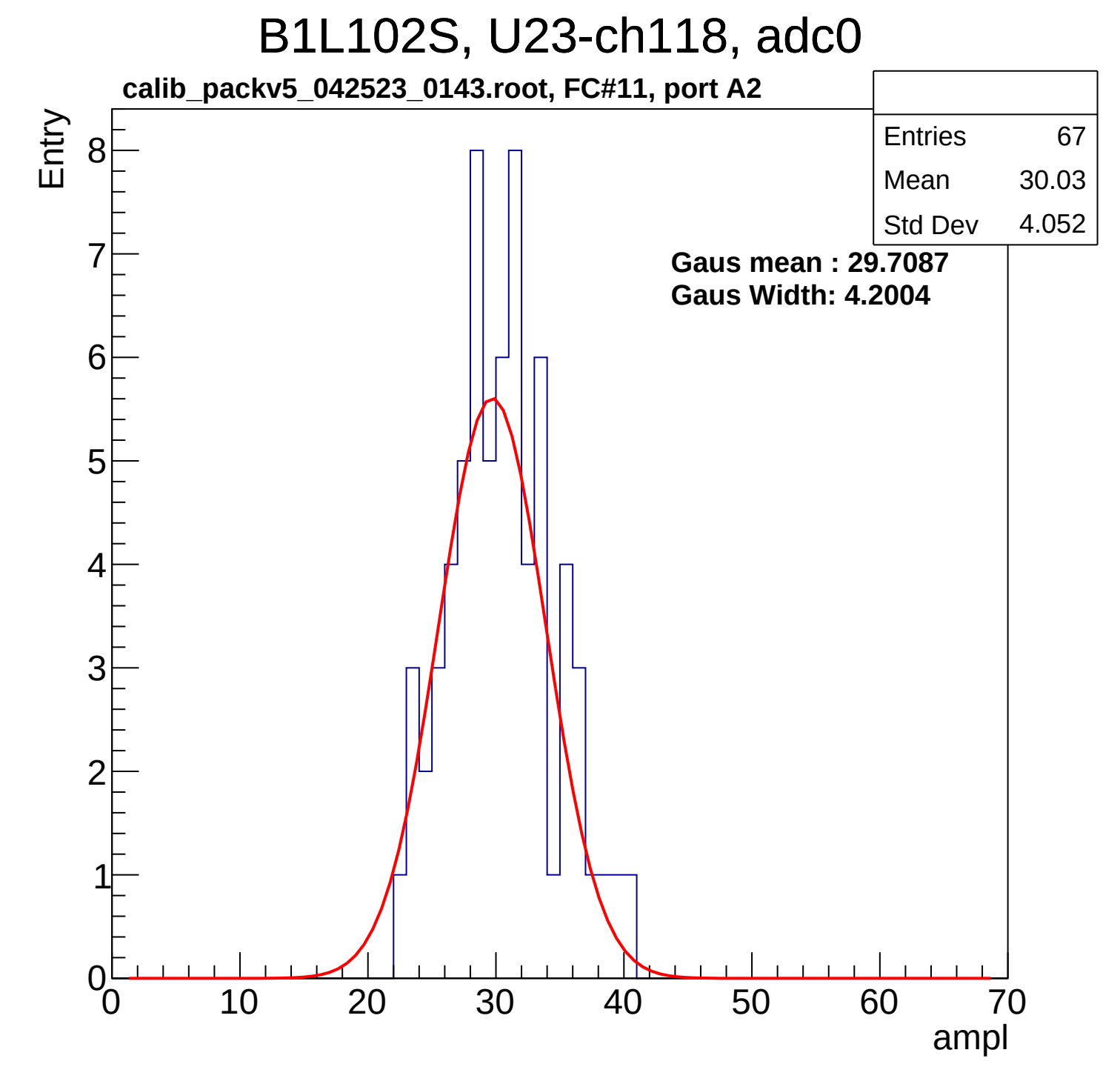

B1L102S, U23-ch117, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 51.01
Std Dev 3.279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

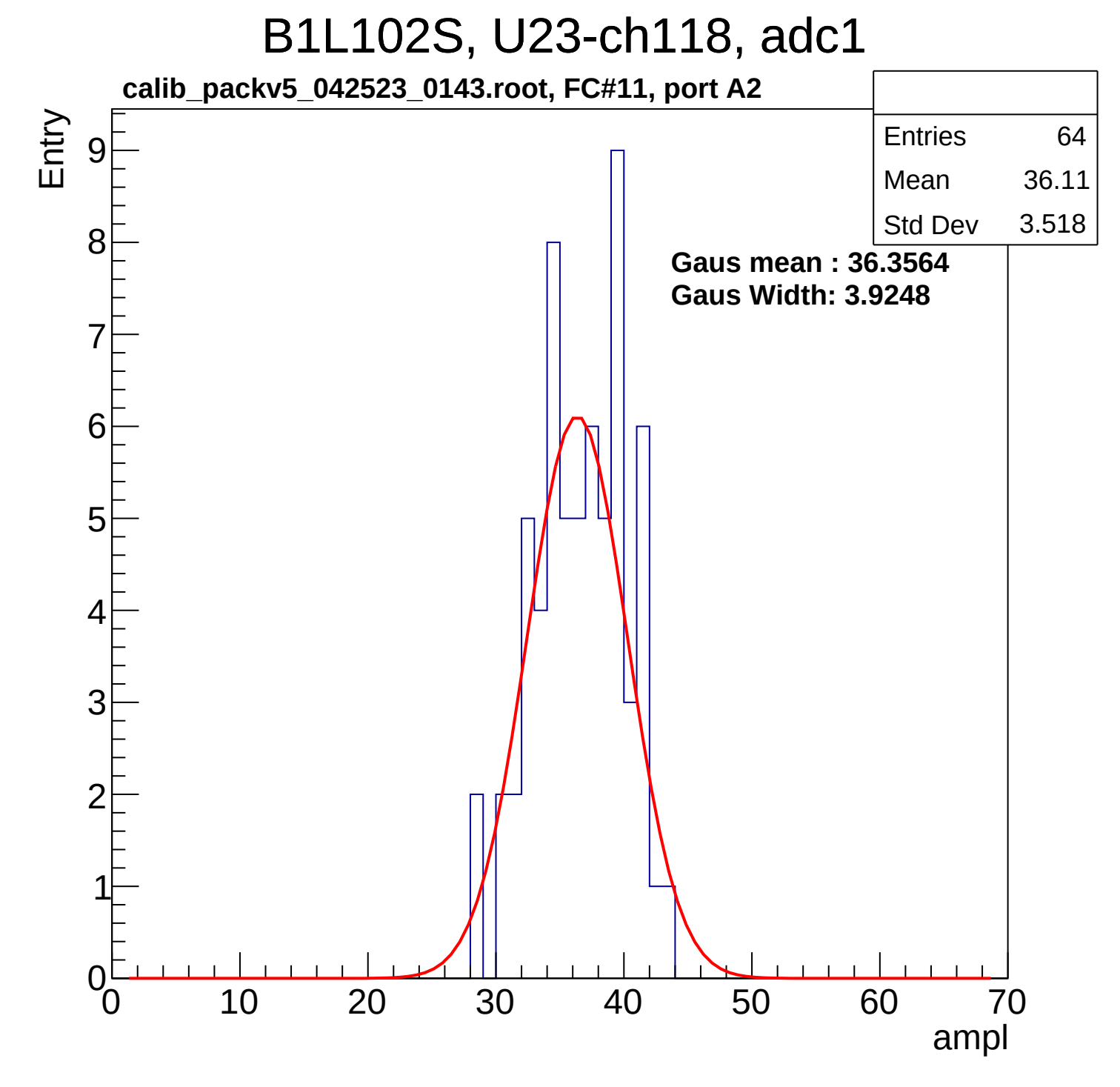

B1L102S, U23-ch117, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 57.7
Std Dev 3.02
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

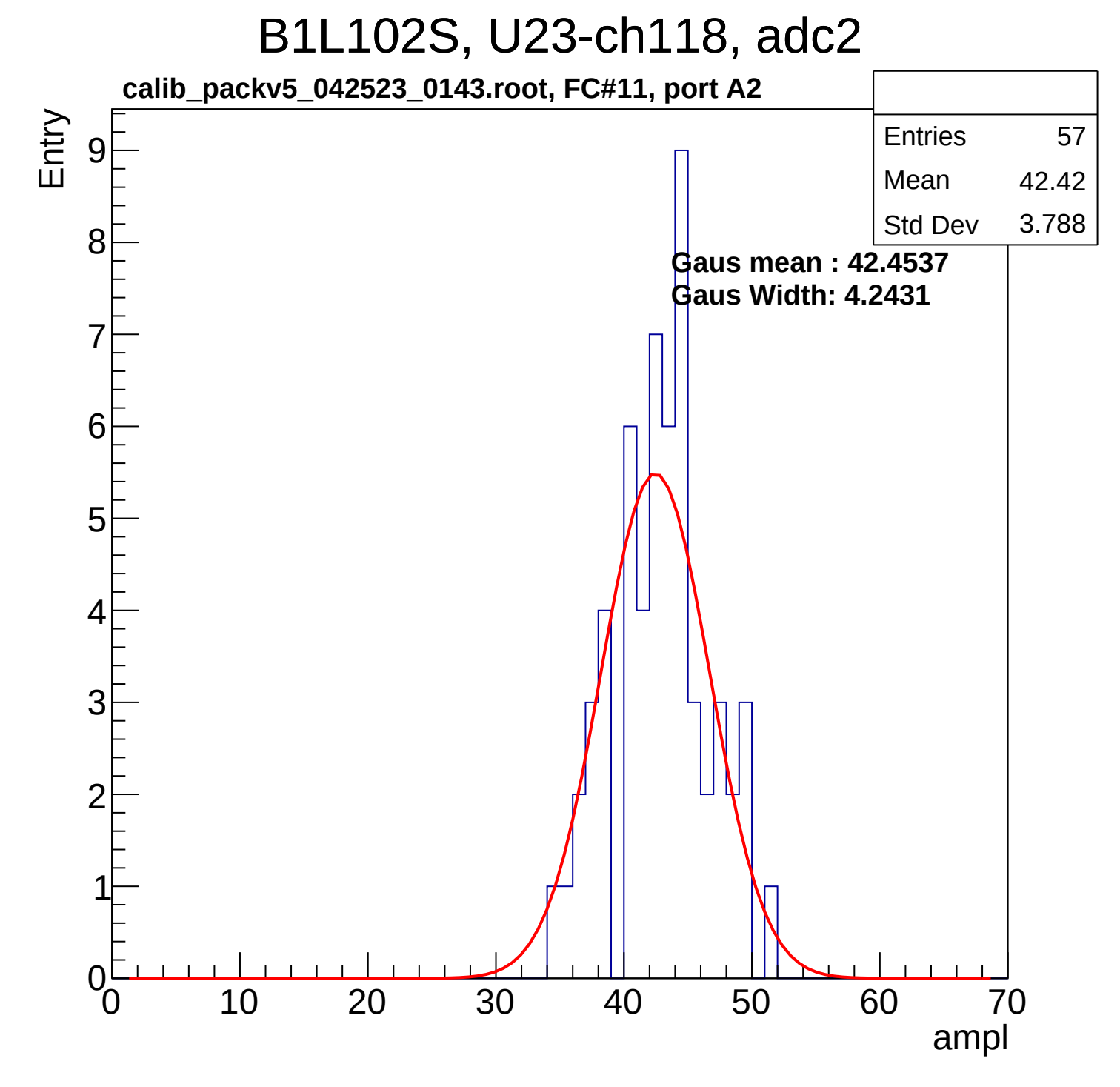

B1L102S, U23-ch117, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 29
Mean 58.59
Std Dev 11.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

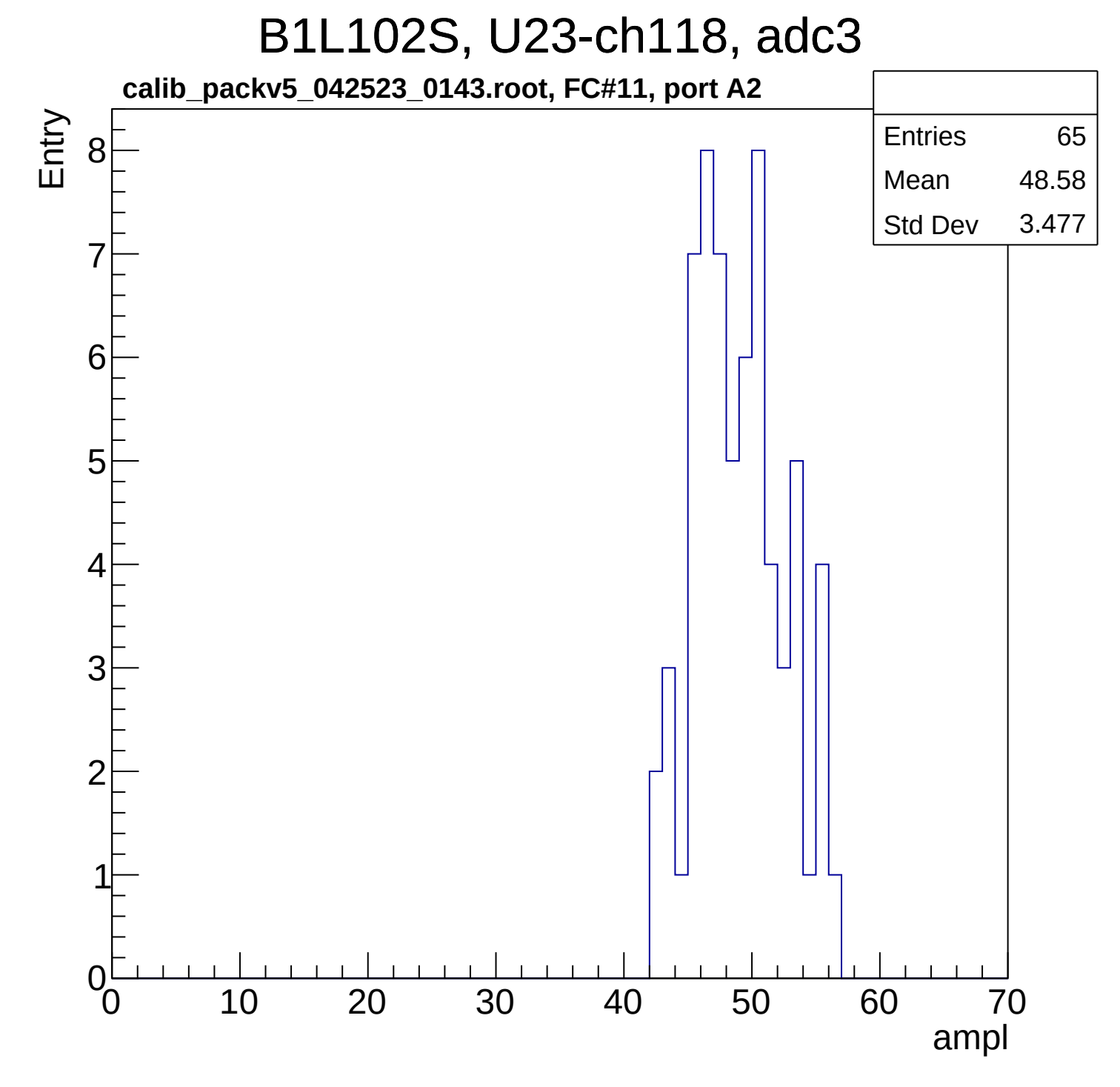

B1L102S, U23-ch117, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

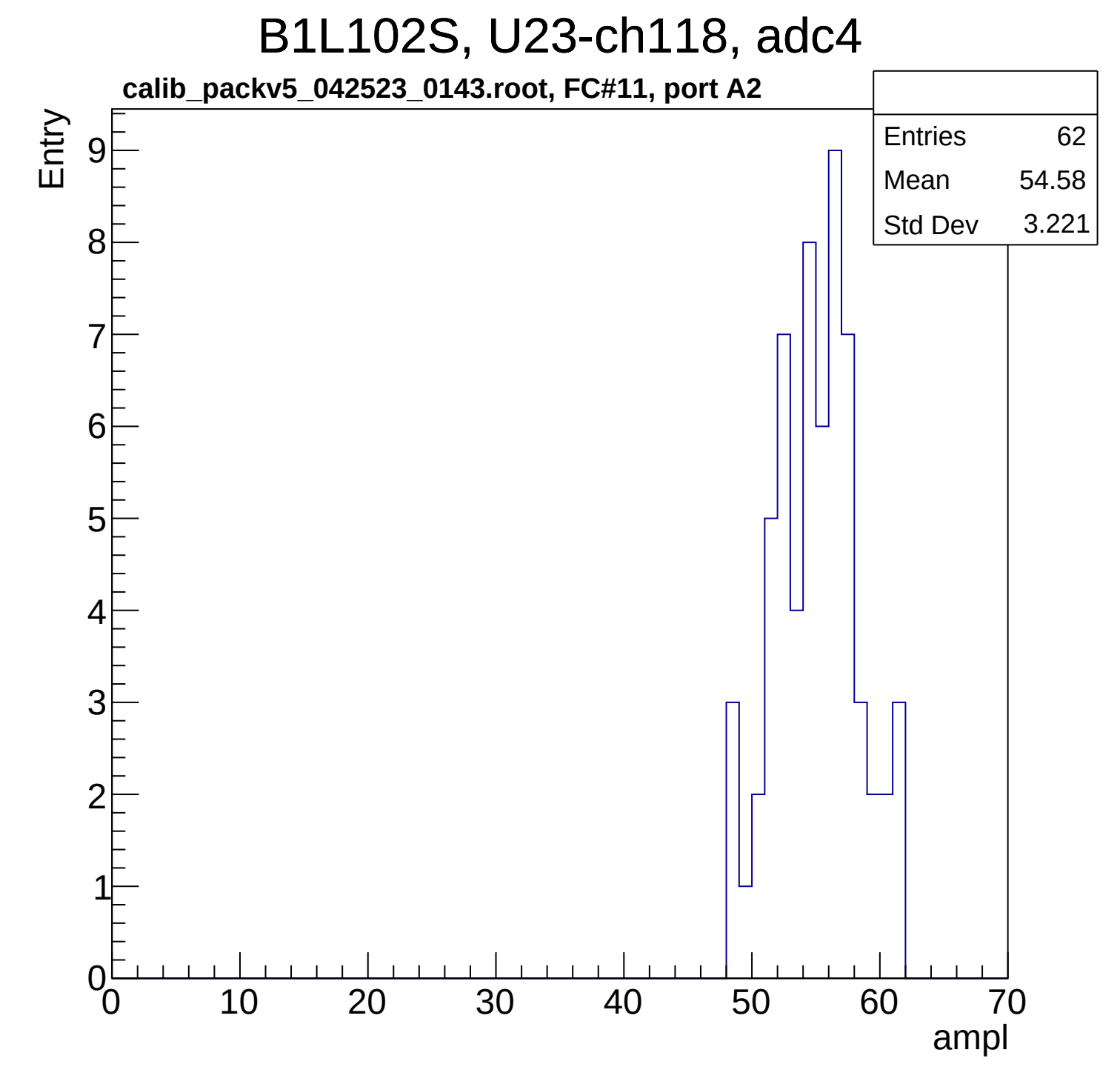

B1L102S, U23-ch117, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 3.75
Std Dev 6.495
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

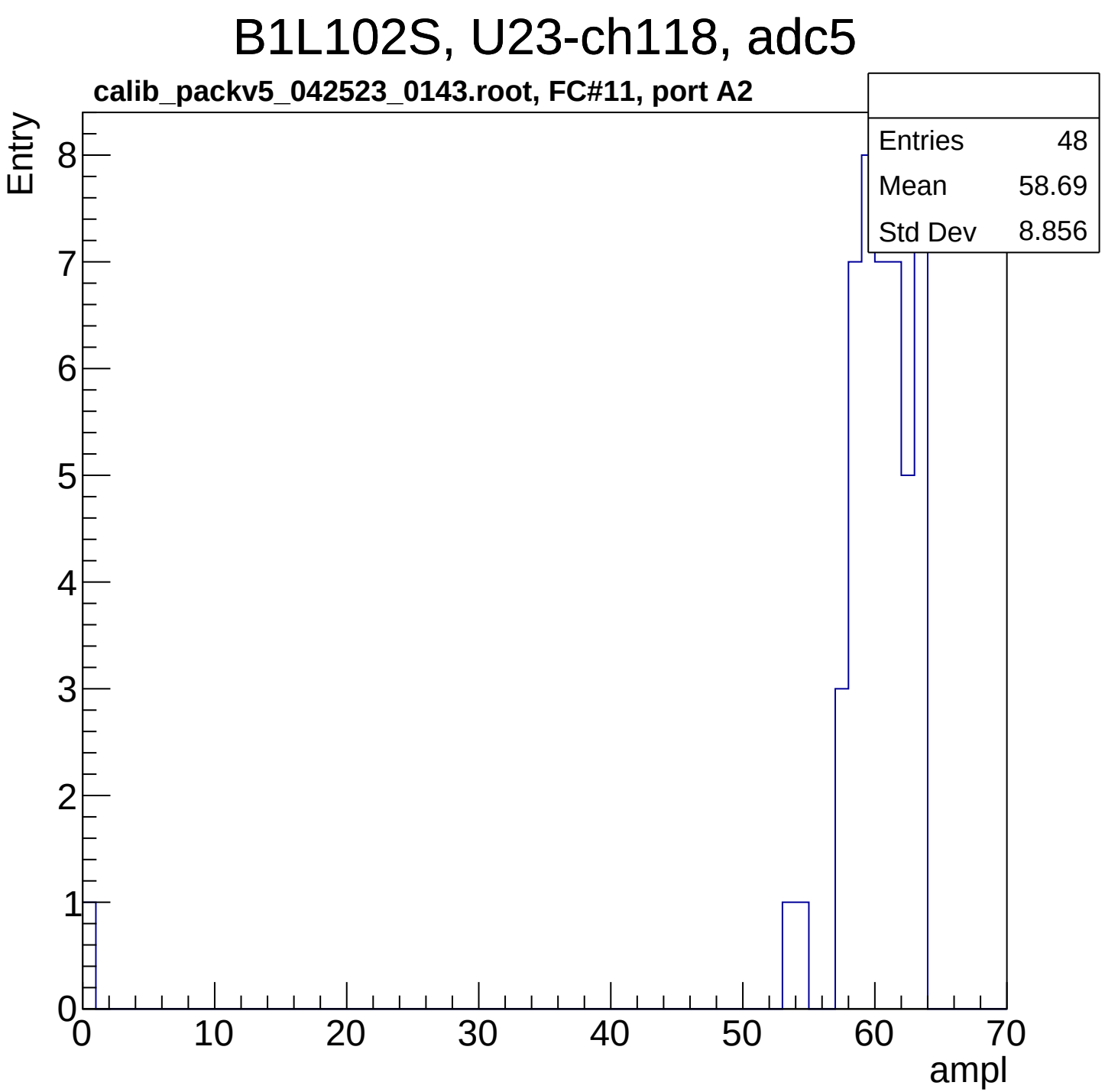

B1L102S, U23-ch118, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 30.03
Std Dev 4.052
Gaus mean : 29.7087
Gaus Width: 4.2004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

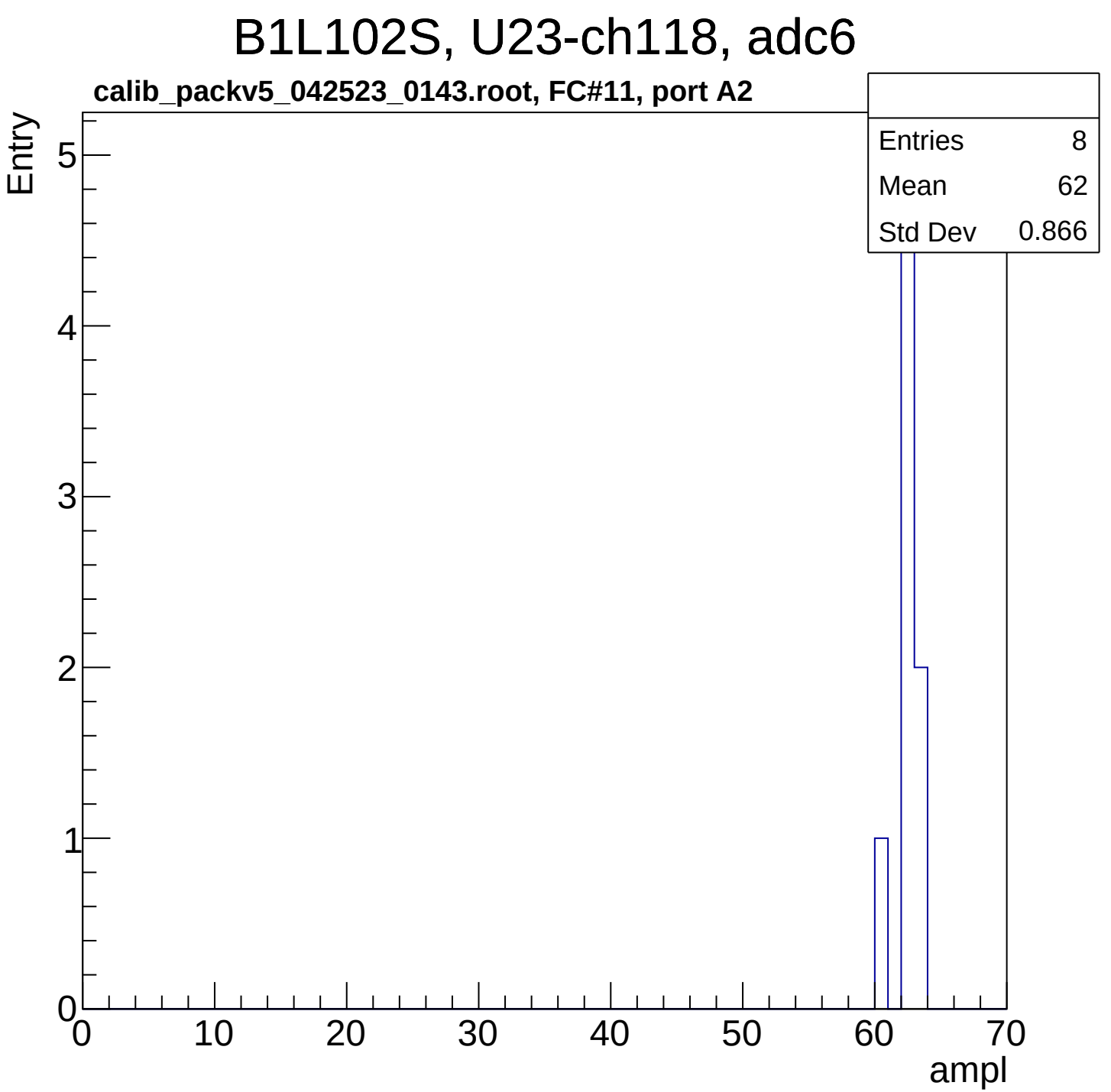

B1L102S, U23-ch118, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 36.11
Std Dev 3.518
Gaus mean : 36.3564
Gaus Width: 3.9248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

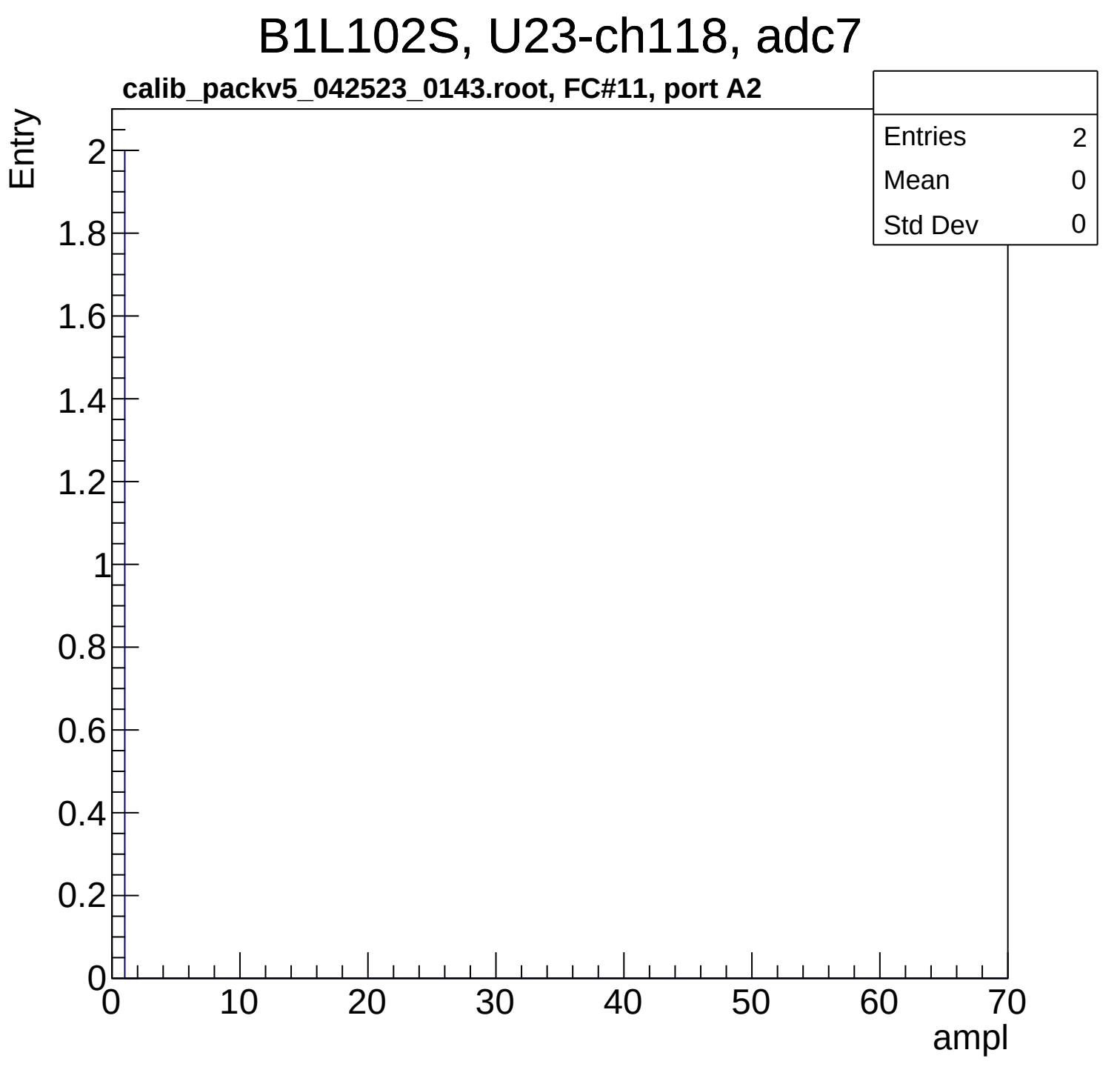

B1L102S, U23-ch118, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 42.42
Std Dev 3.788
Gaus mean : 42.4537
Gaus Width: 4.2431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

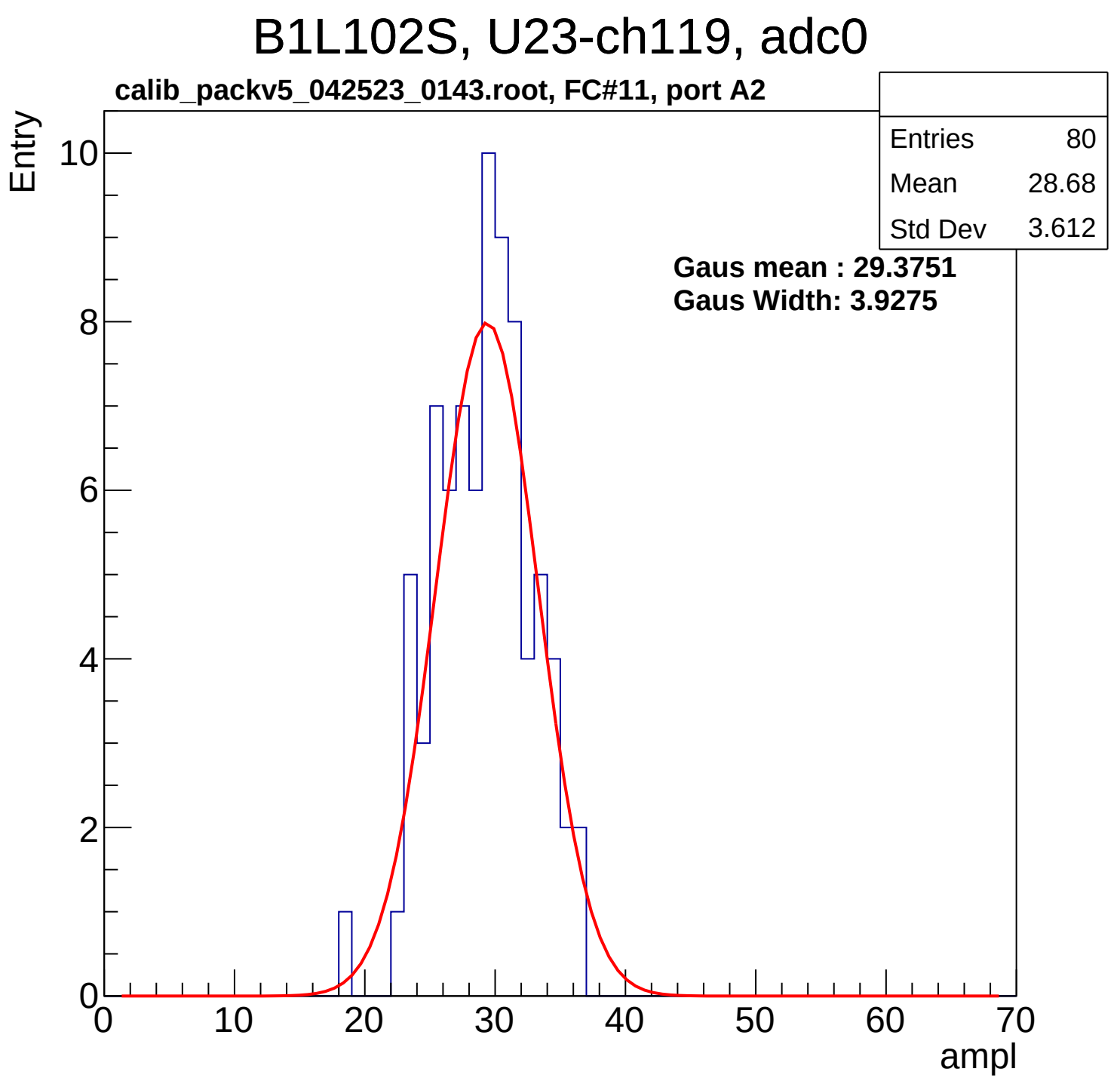

B1L102S, U23-ch118, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 48.58
Std Dev 3.477
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

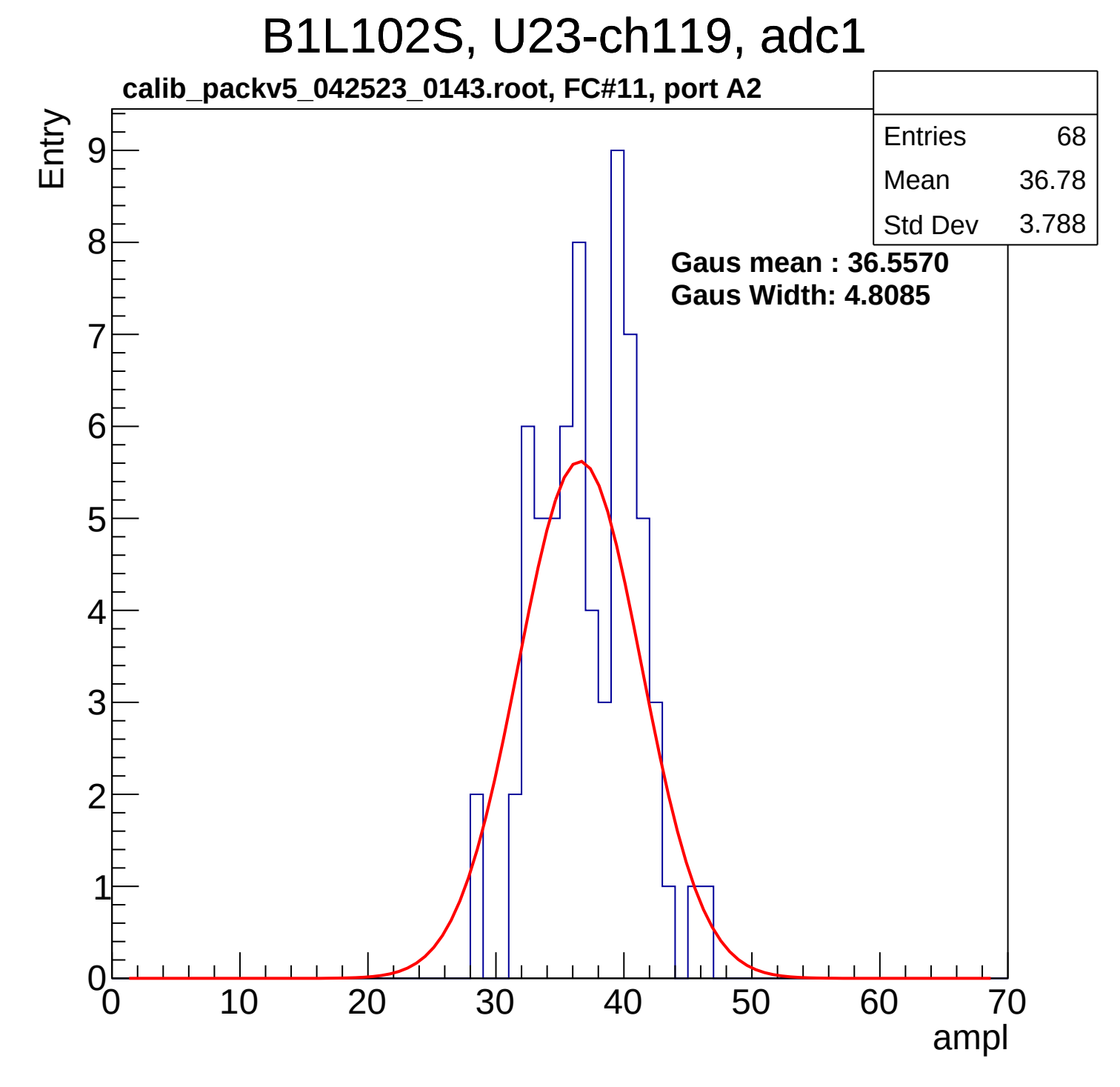

B1L102S, U23-ch118, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 54.58
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

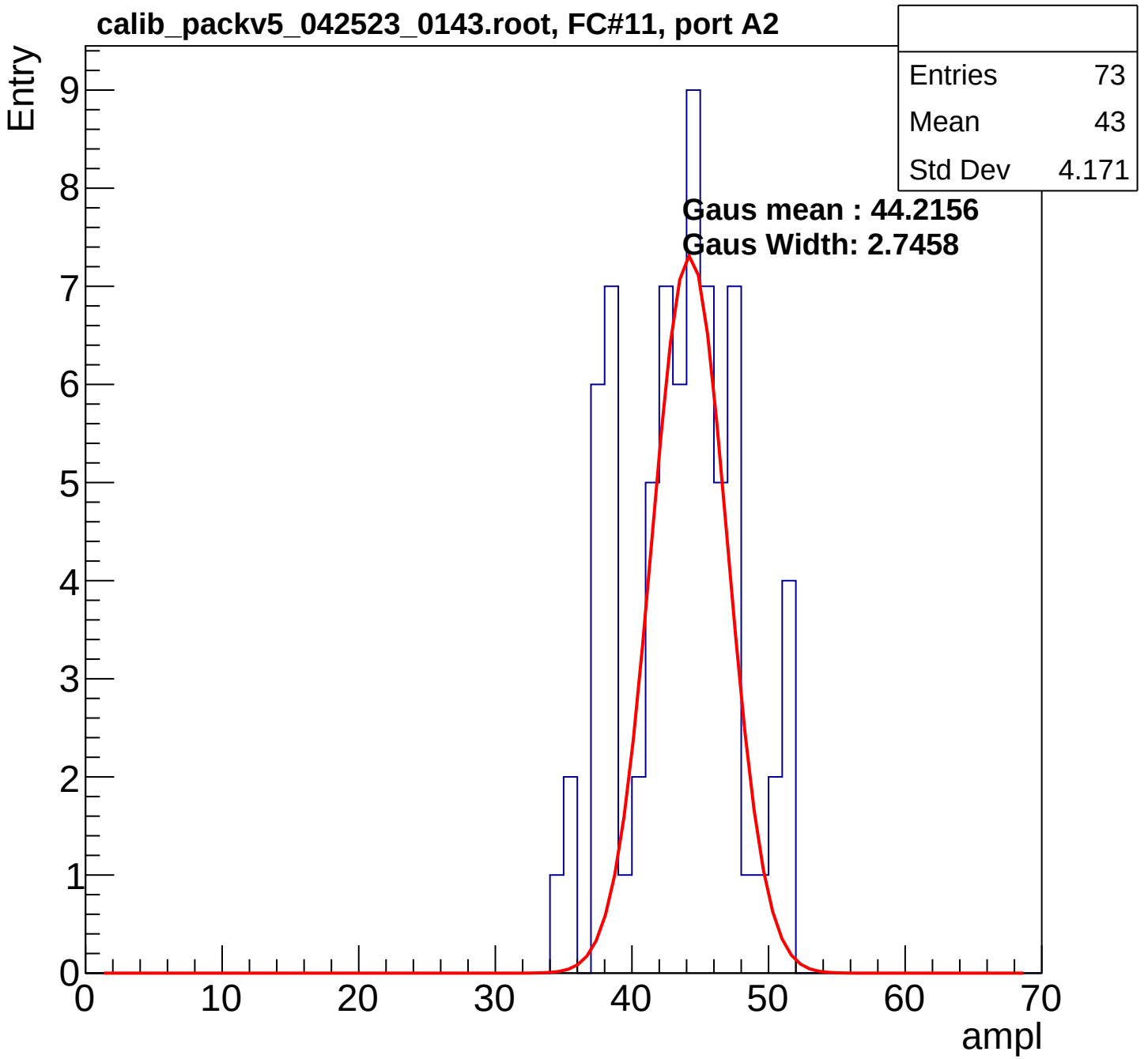

B1L102S, U23-ch118, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 58.69
Std Dev 8.856
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

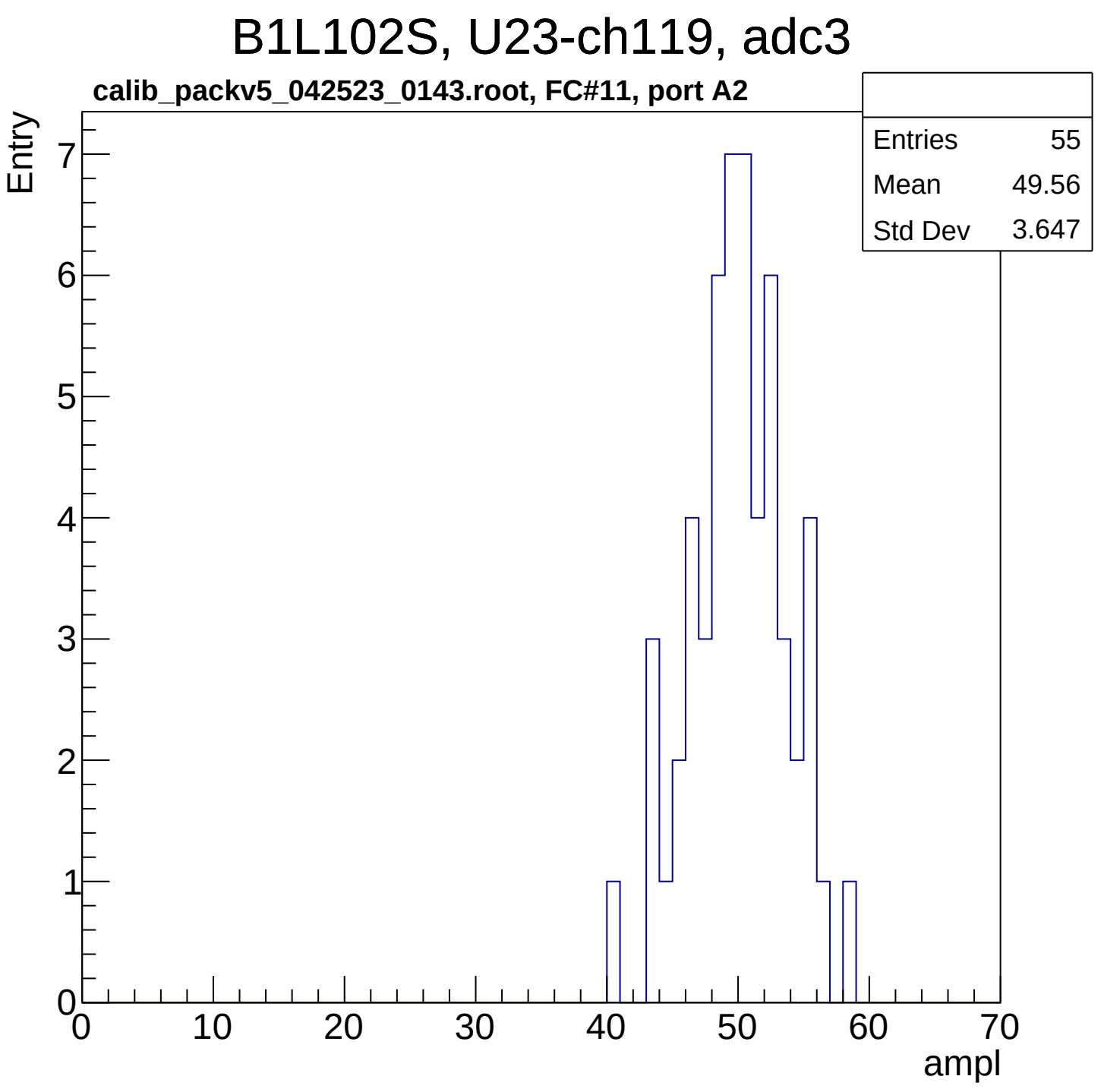

B1L102S, U23-ch118, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 62
Std Dev 0.866
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

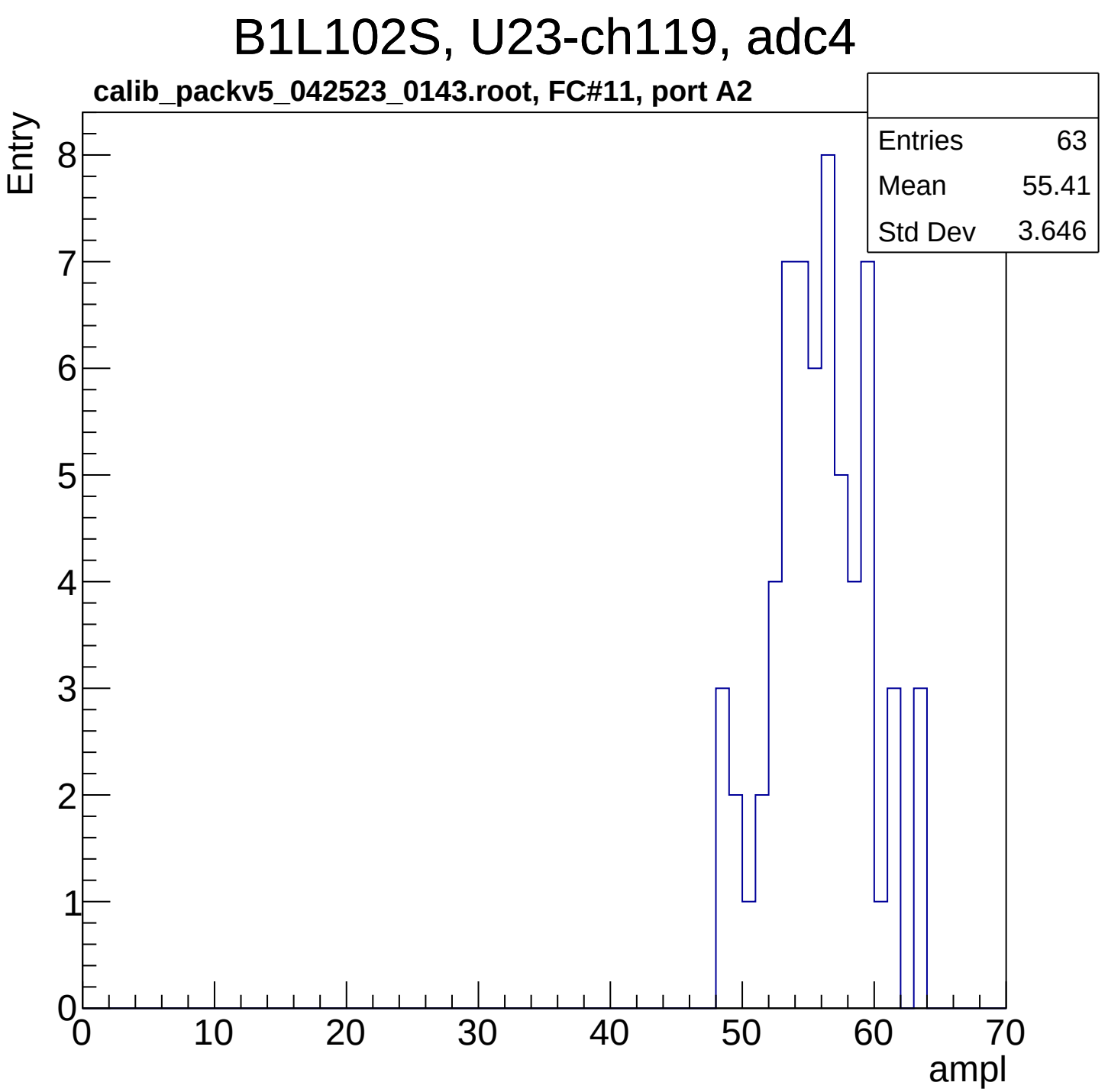

B1L102S, U23-ch118, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

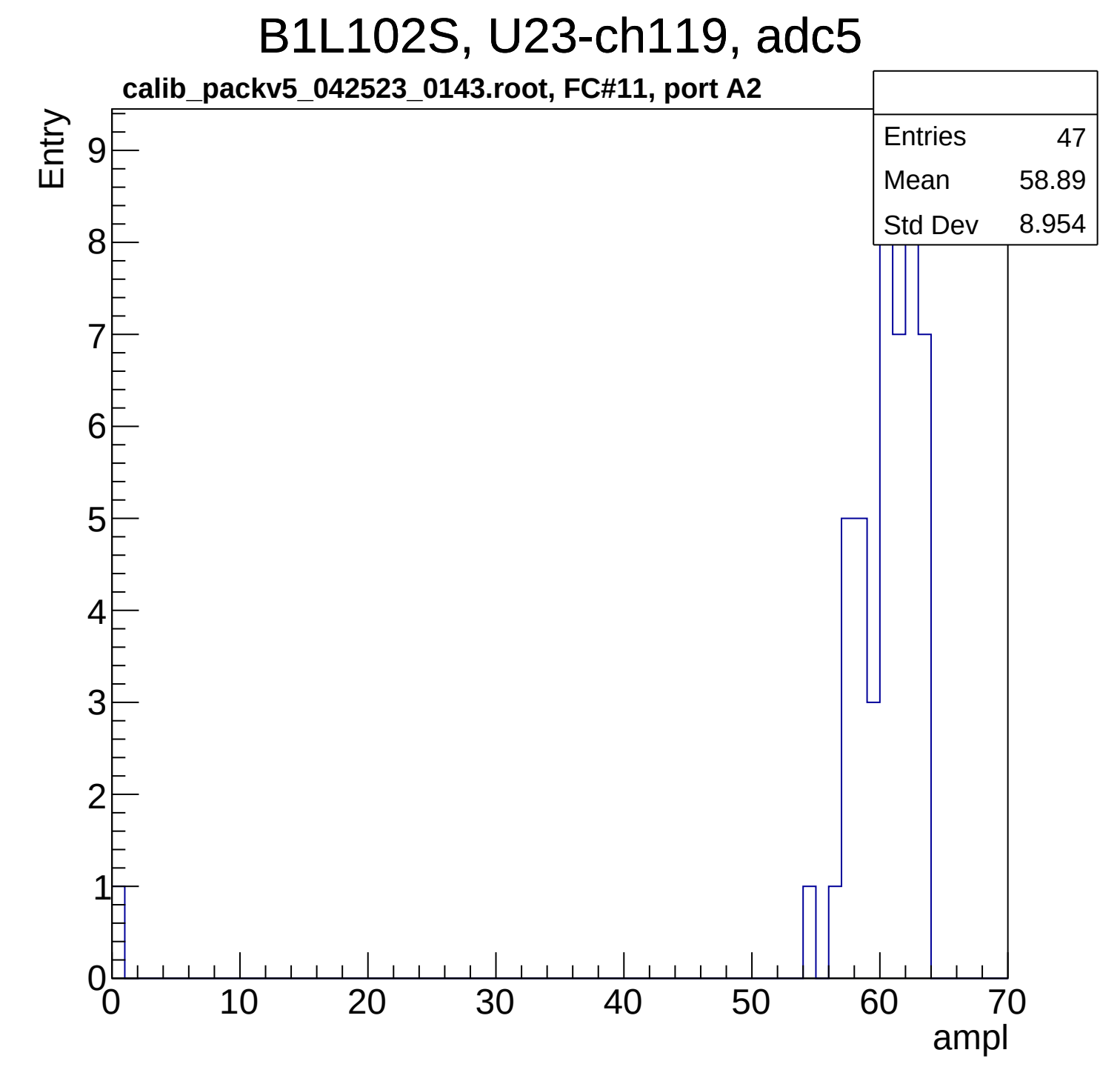

B1L102S, U23-ch119, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 28.68
Std Dev 3.612
Gaus mean : 29.3751
Gaus Width: 3.9275
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

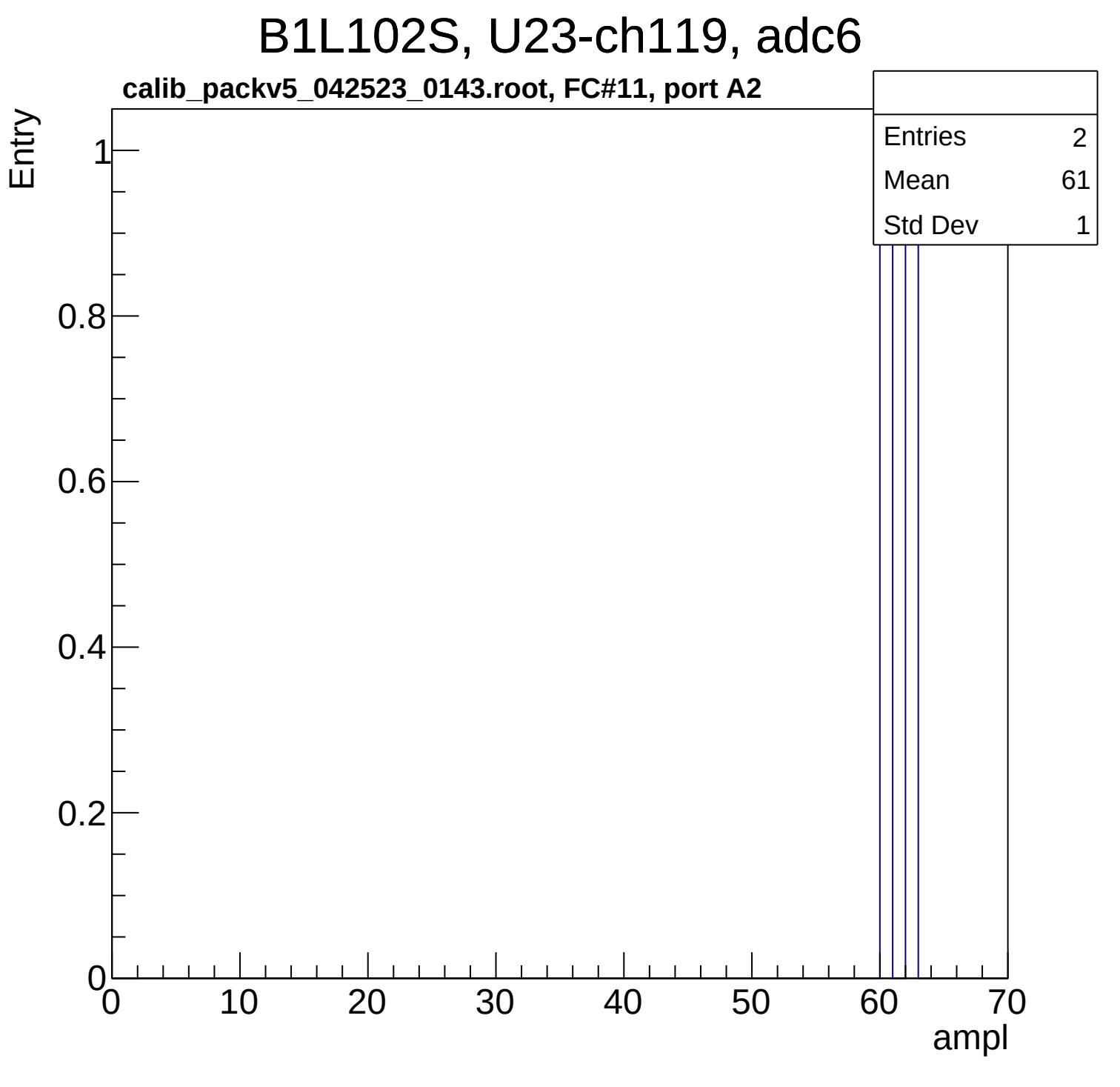

B1L102S, U23-ch119, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 36.78
Std Dev 3.788
Gaus mean : 36.5570
Gaus Width: 4.8085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

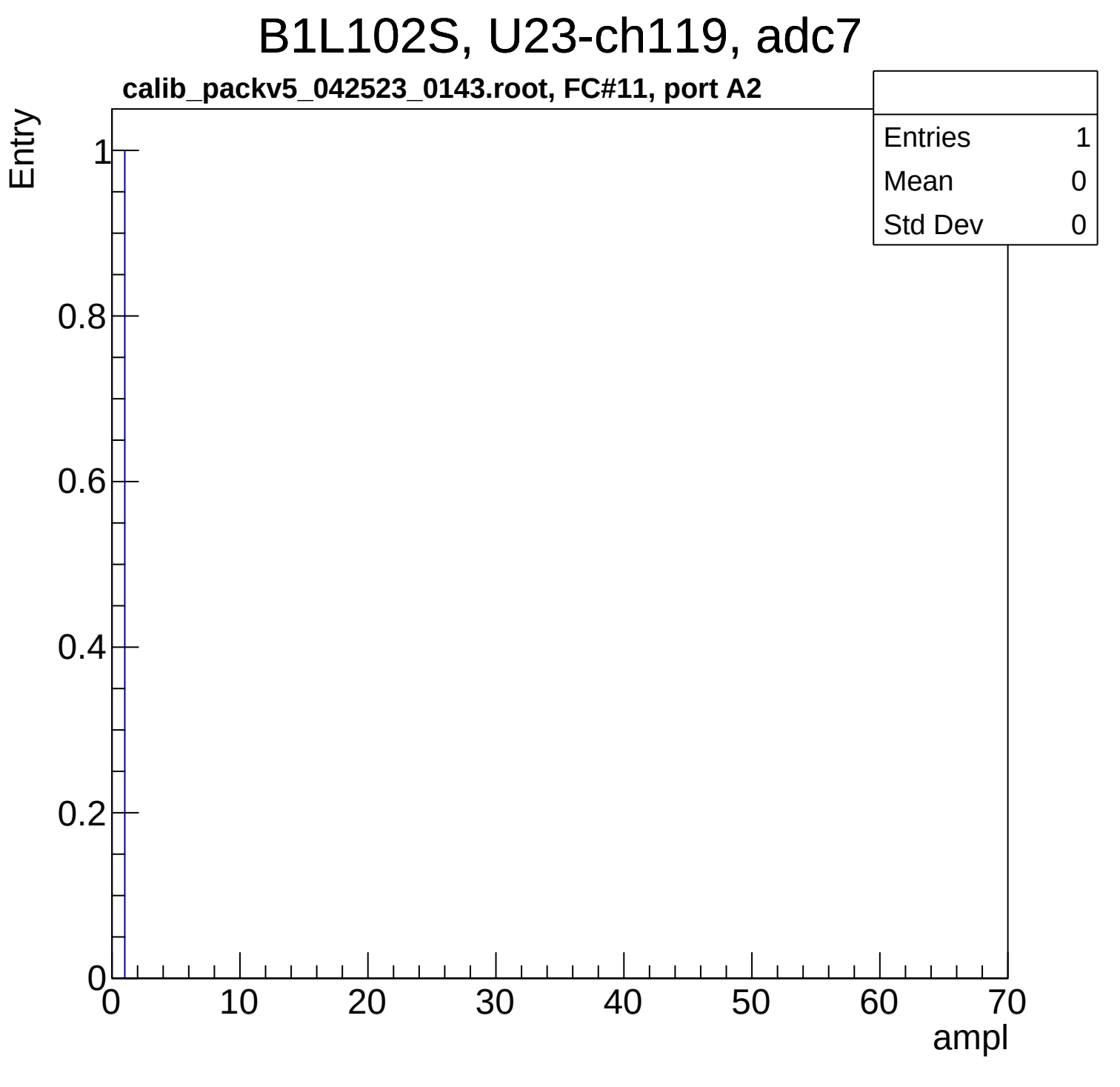

B1L102S, U23-ch119, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 43
Std Dev 4.171
Gaus mean : 44.2156
Gaus Width: 2.7458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

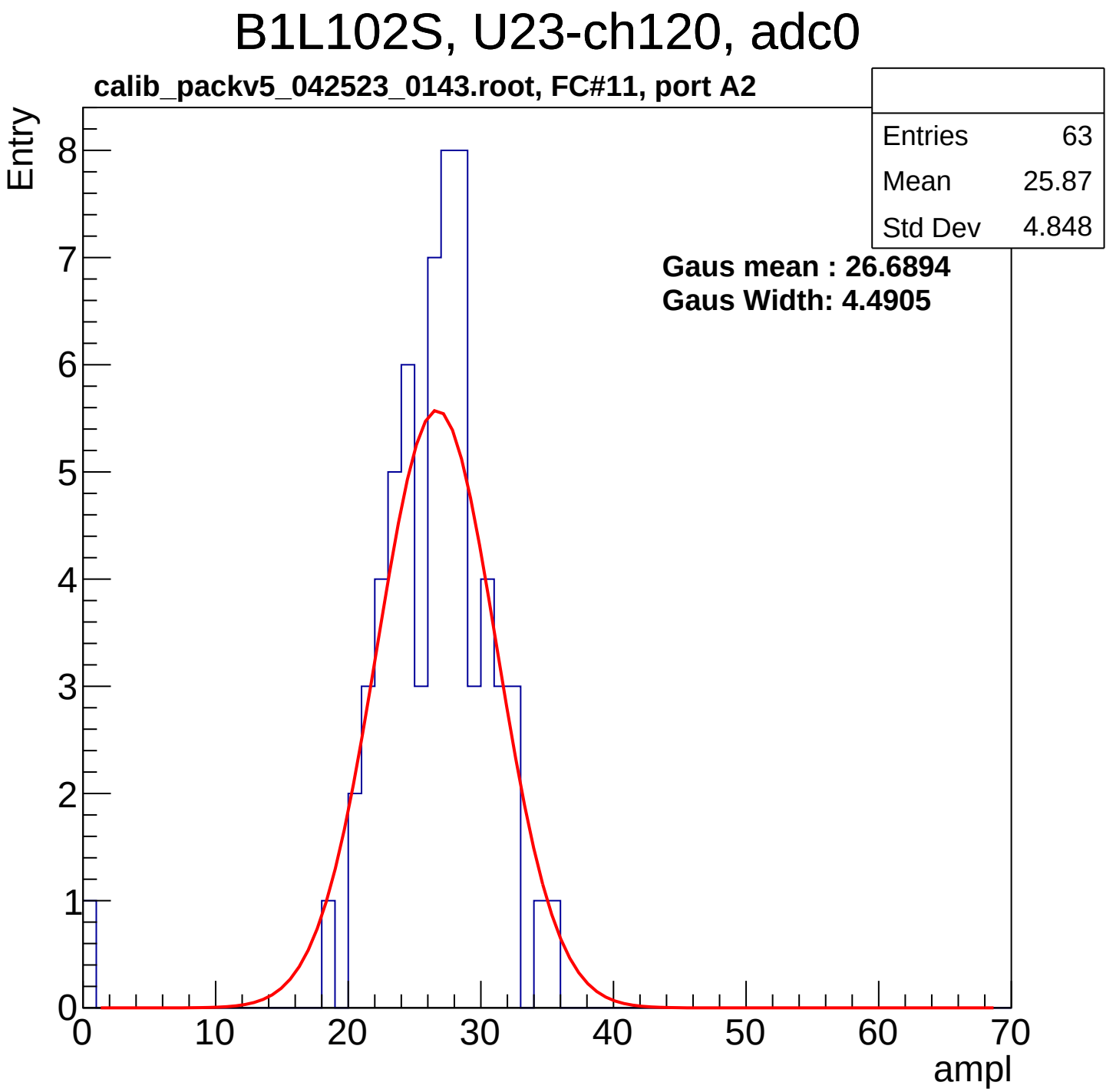

B1L102S, U23-ch119, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 49.56
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

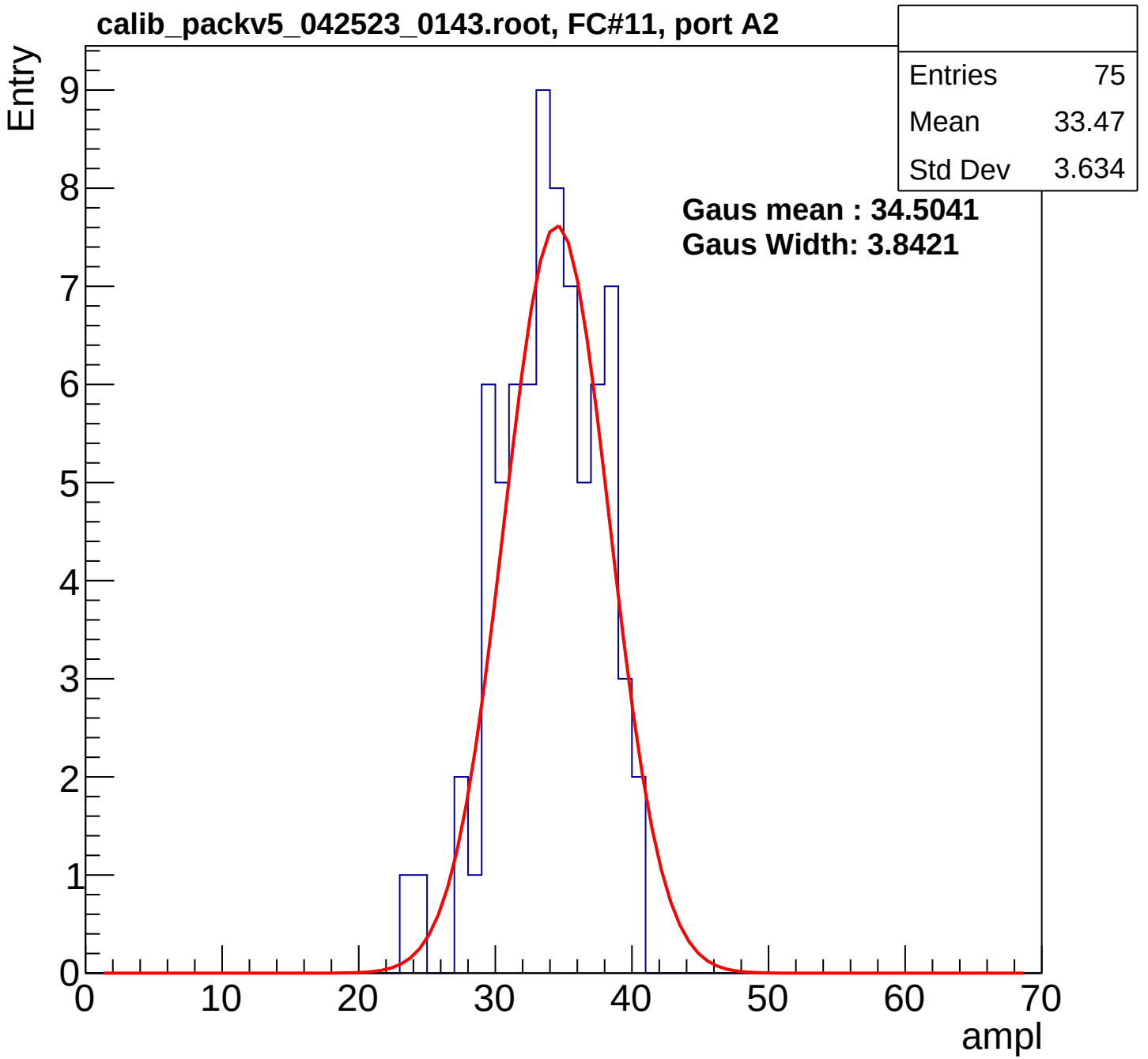

B1L102S, U23-ch119, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 55.41
Std Dev 3.646
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

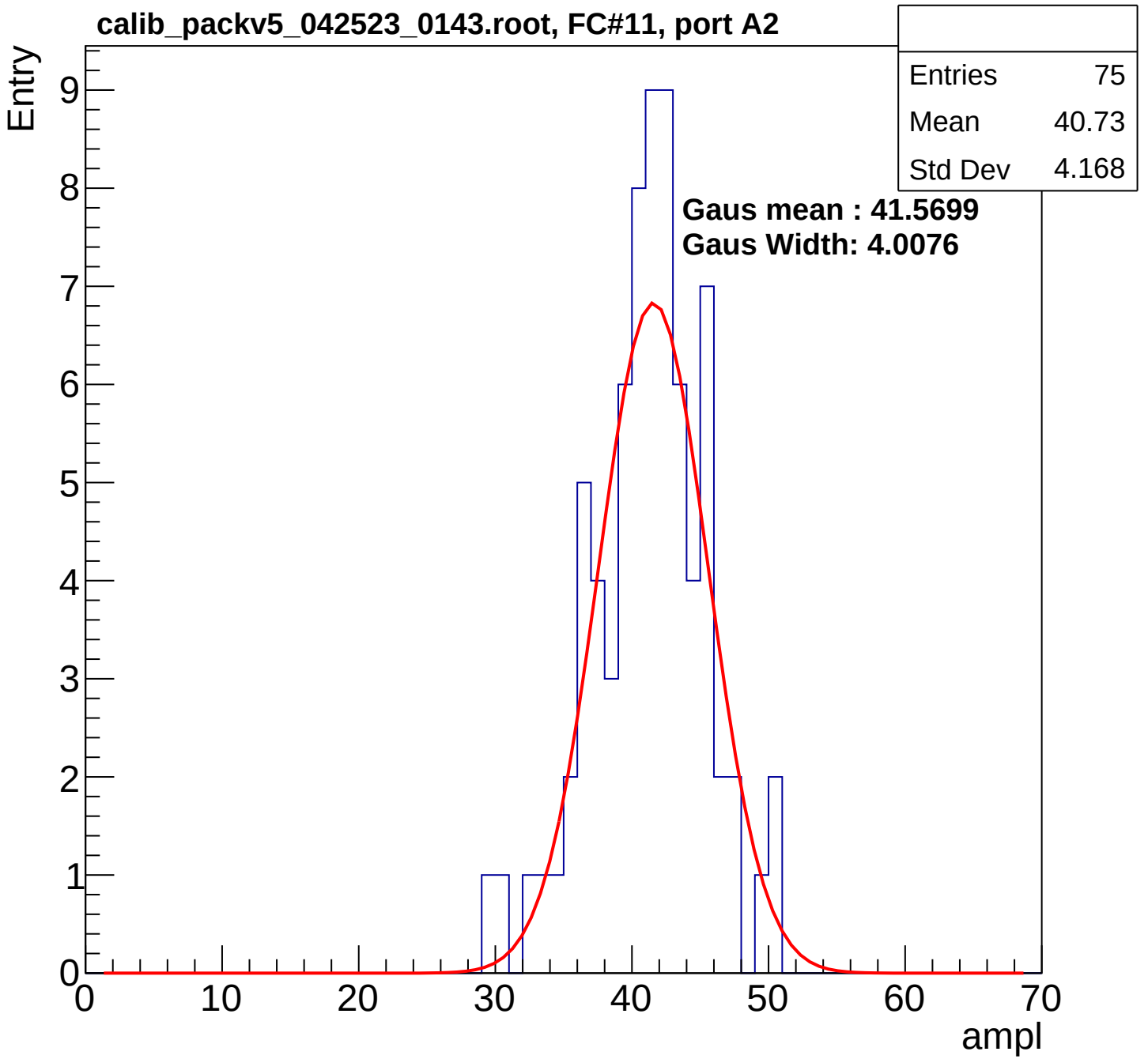

B1L102S, U23-ch119, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.89
Std Dev 8.954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

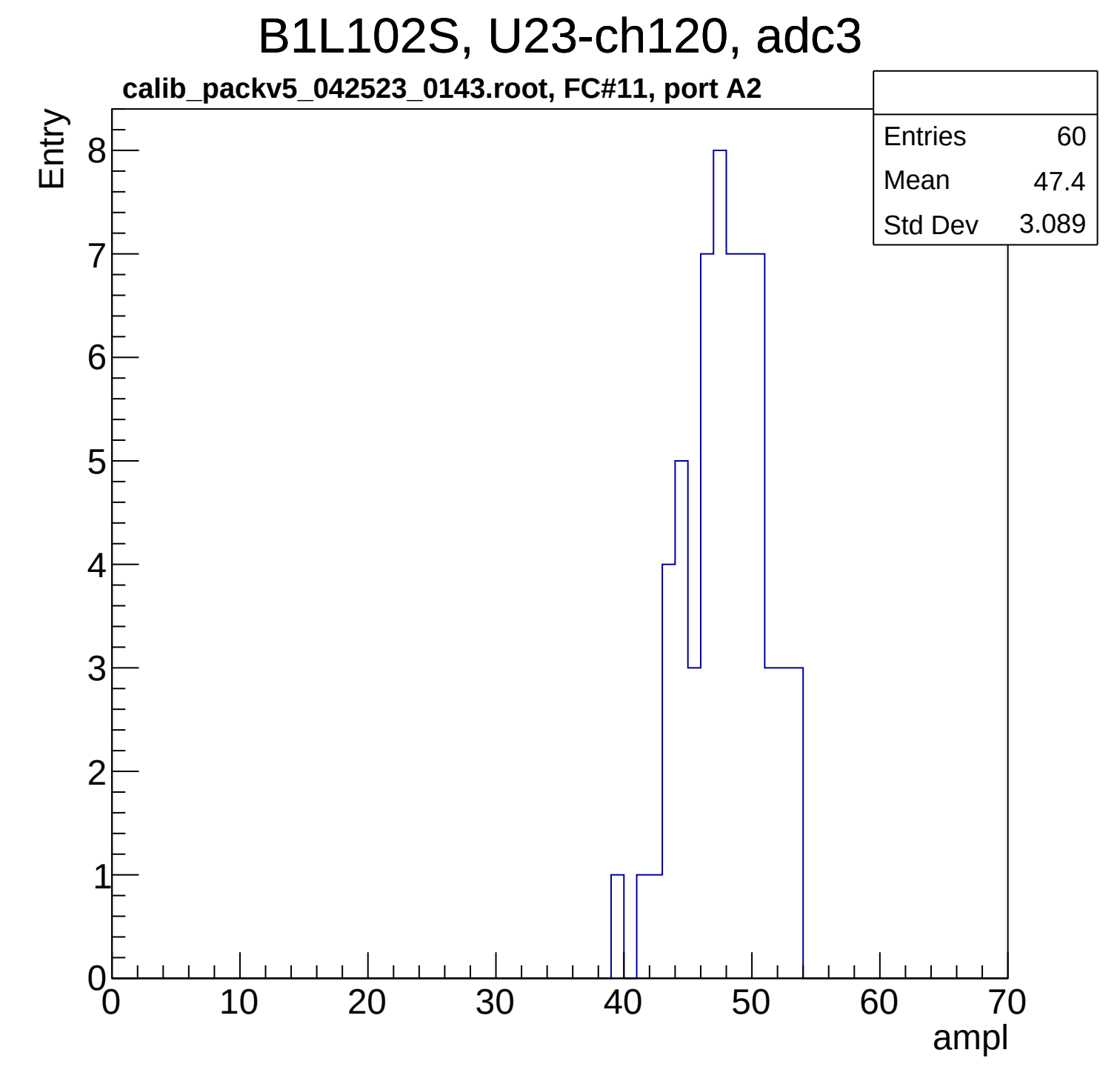

B1L102S, U23-ch119, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 61
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

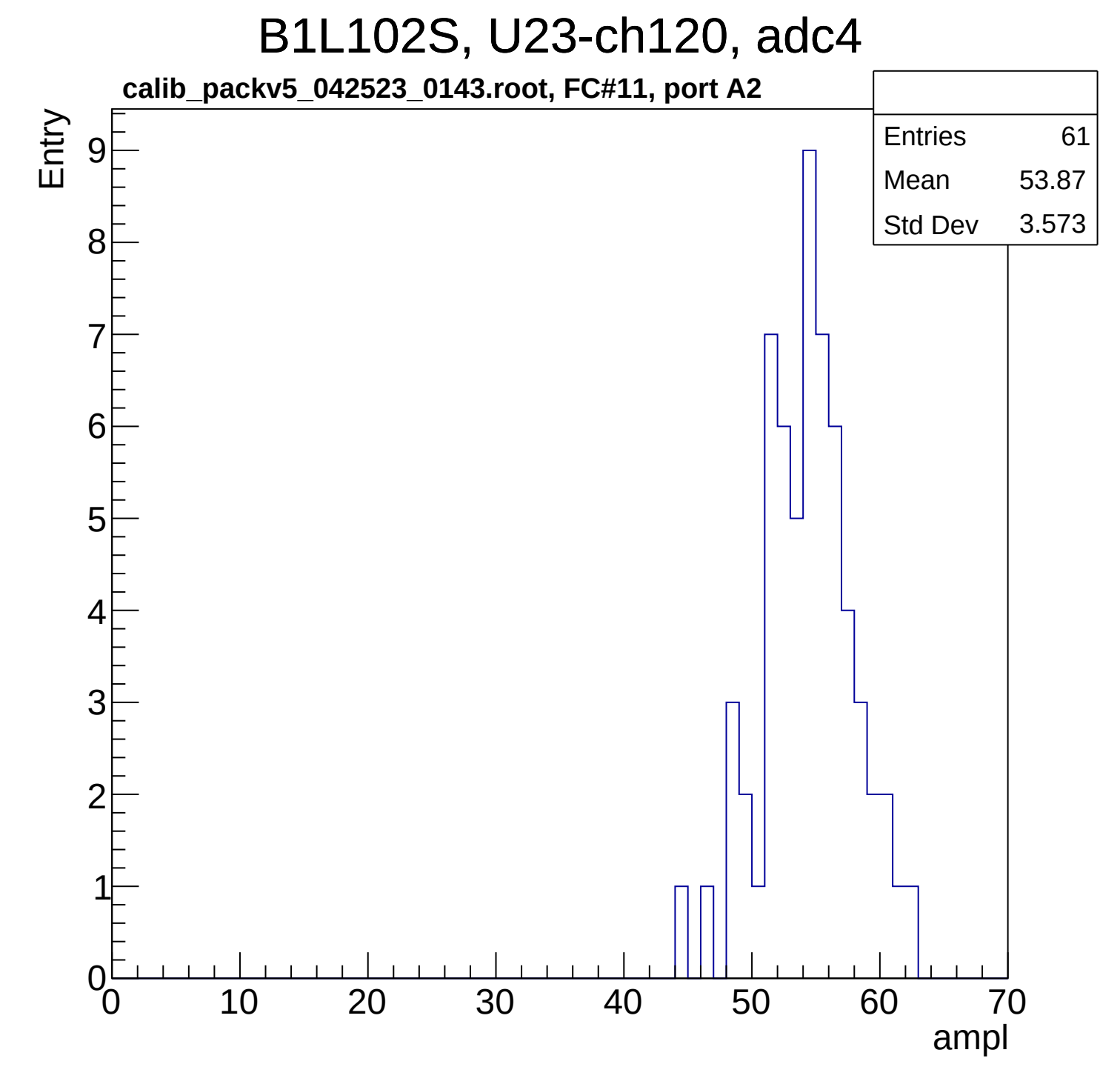

B1L102S, U23-ch119, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

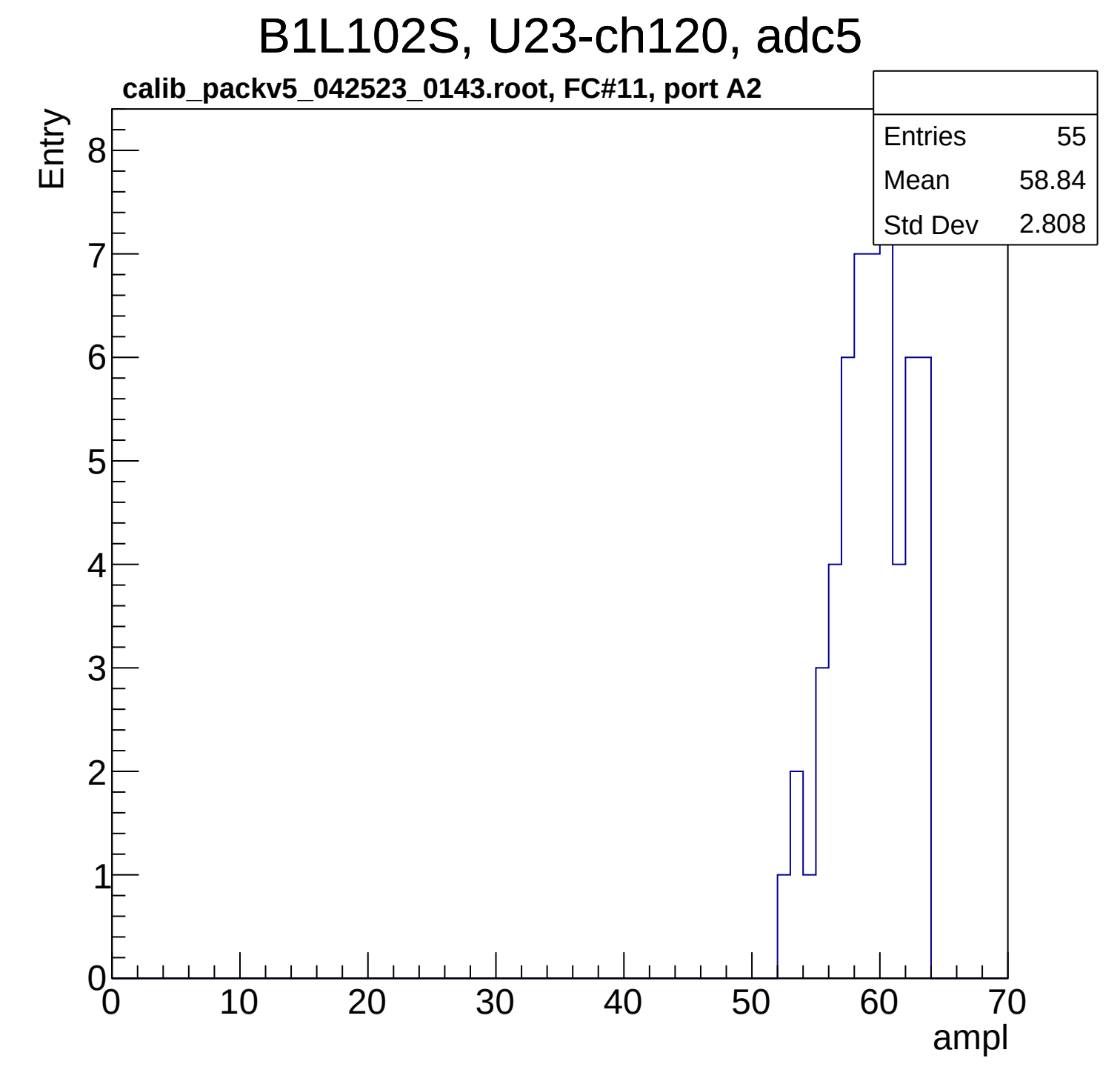

B1L102S, U23-ch120, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 25.87
Std Dev 4.848
Gaus mean : 26.6894
Gaus Width: 4.4905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

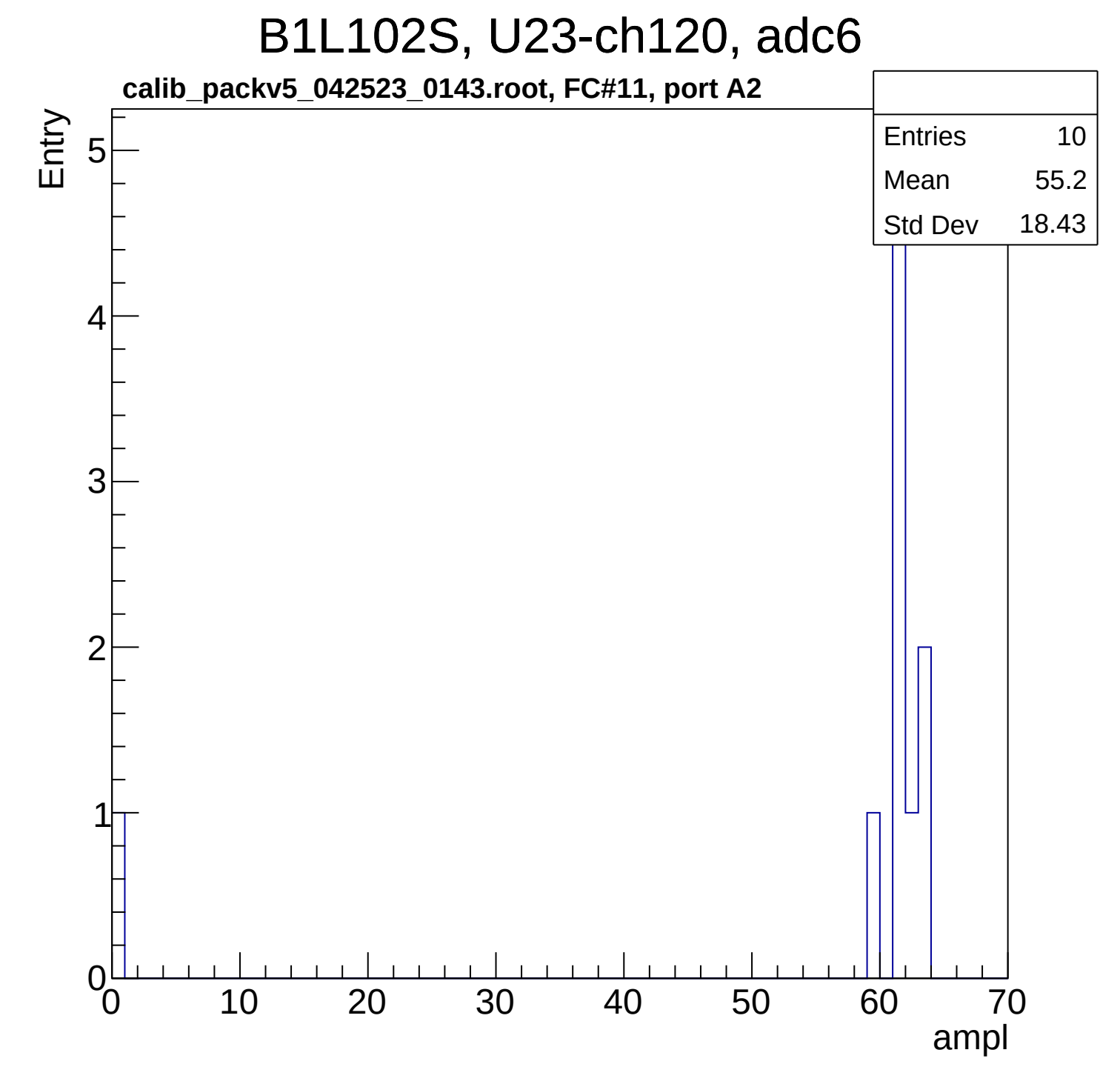

B1L102S, U23-ch120, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 33.47
Std Dev 3.634
Gaus mean : 34.5041
Gaus Width: 3.8421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

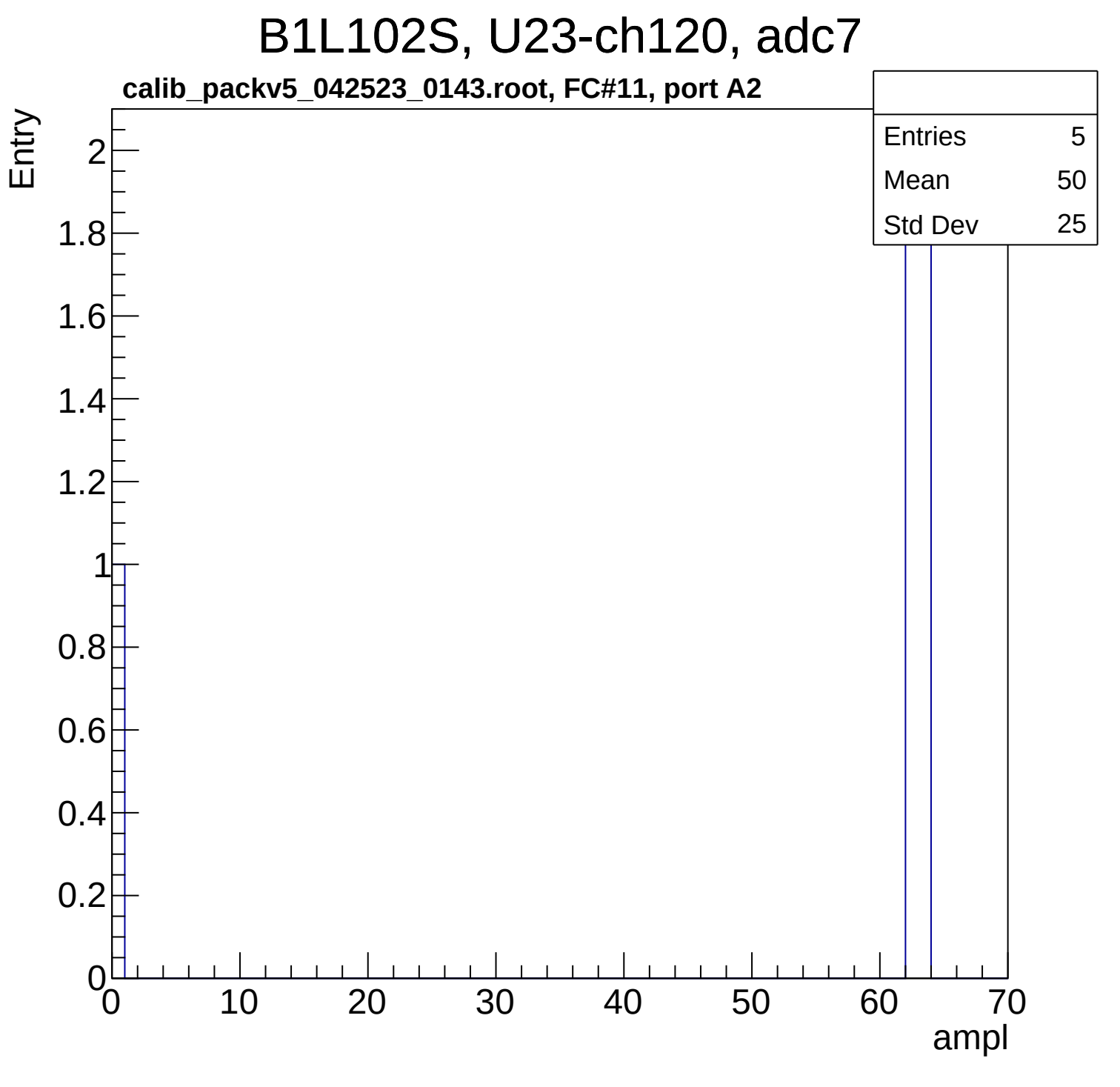

B1L102S, U23-ch120, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 40.73
Std Dev 4.168
Gaus mean : 41.5699
Gaus Width: 4.0076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

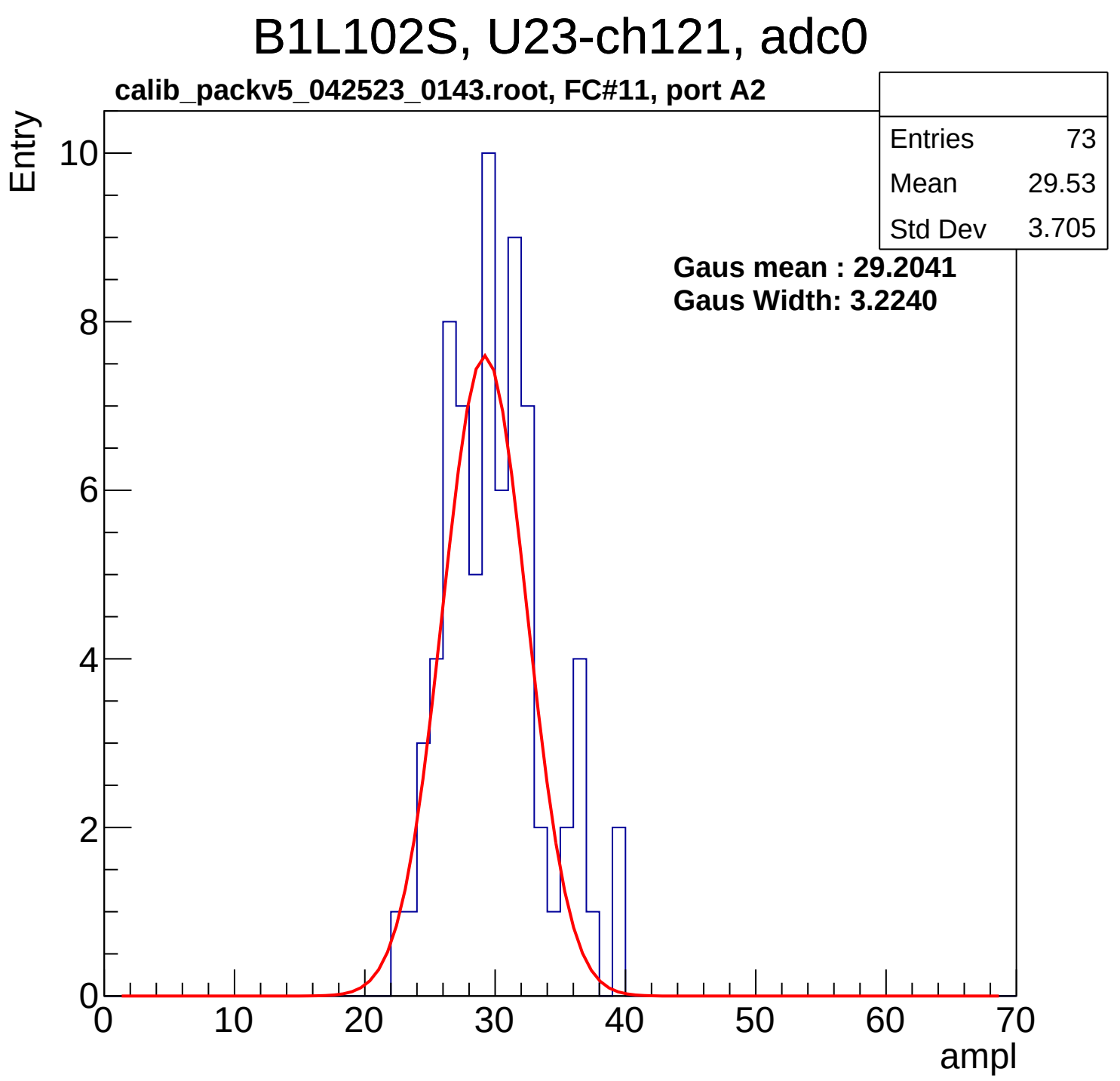

B1L102S, U23-ch120, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 47.4
Std Dev 3.089
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

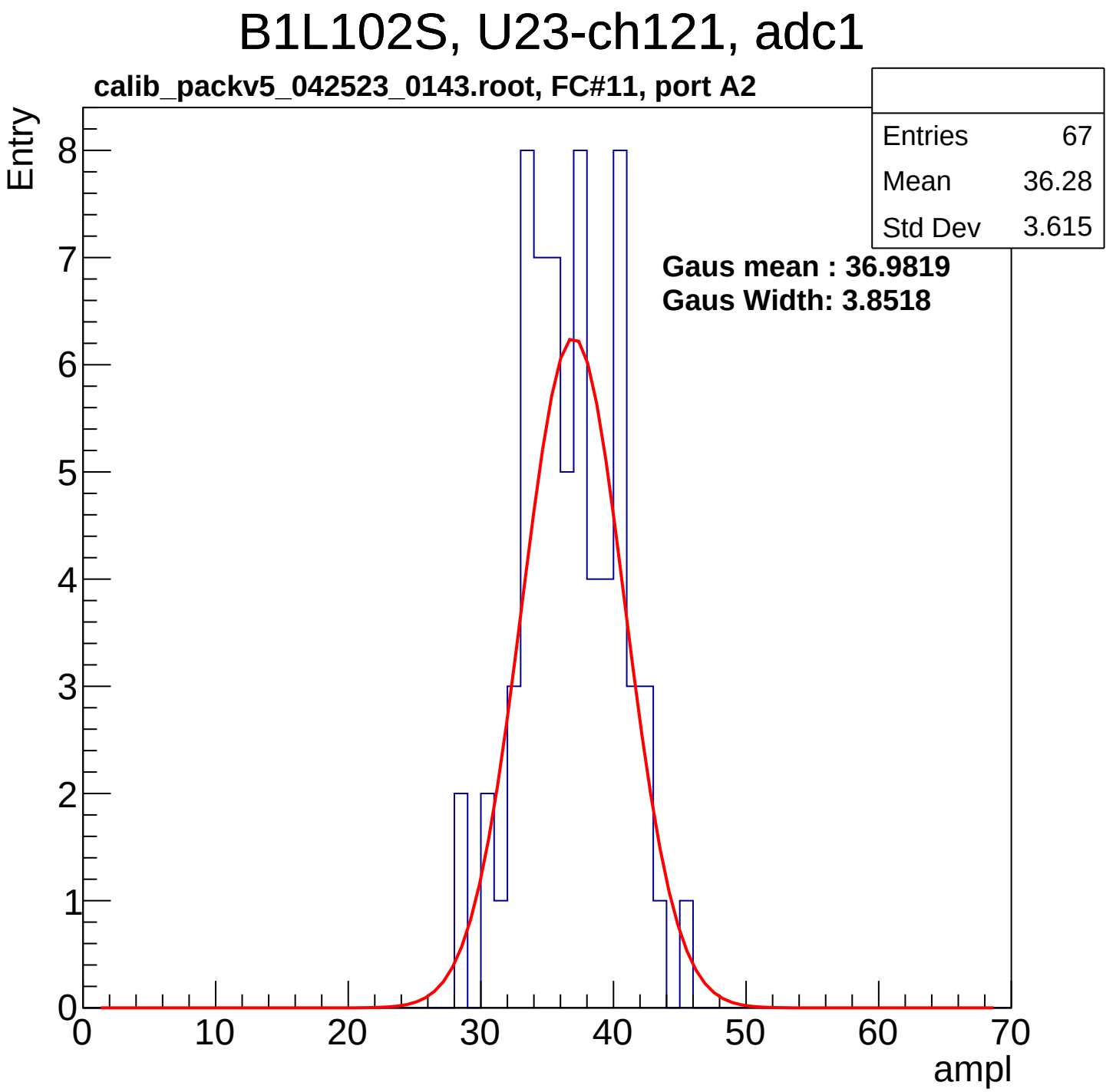

B1L102S, U23-ch120, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 53.87
Std Dev 3.573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

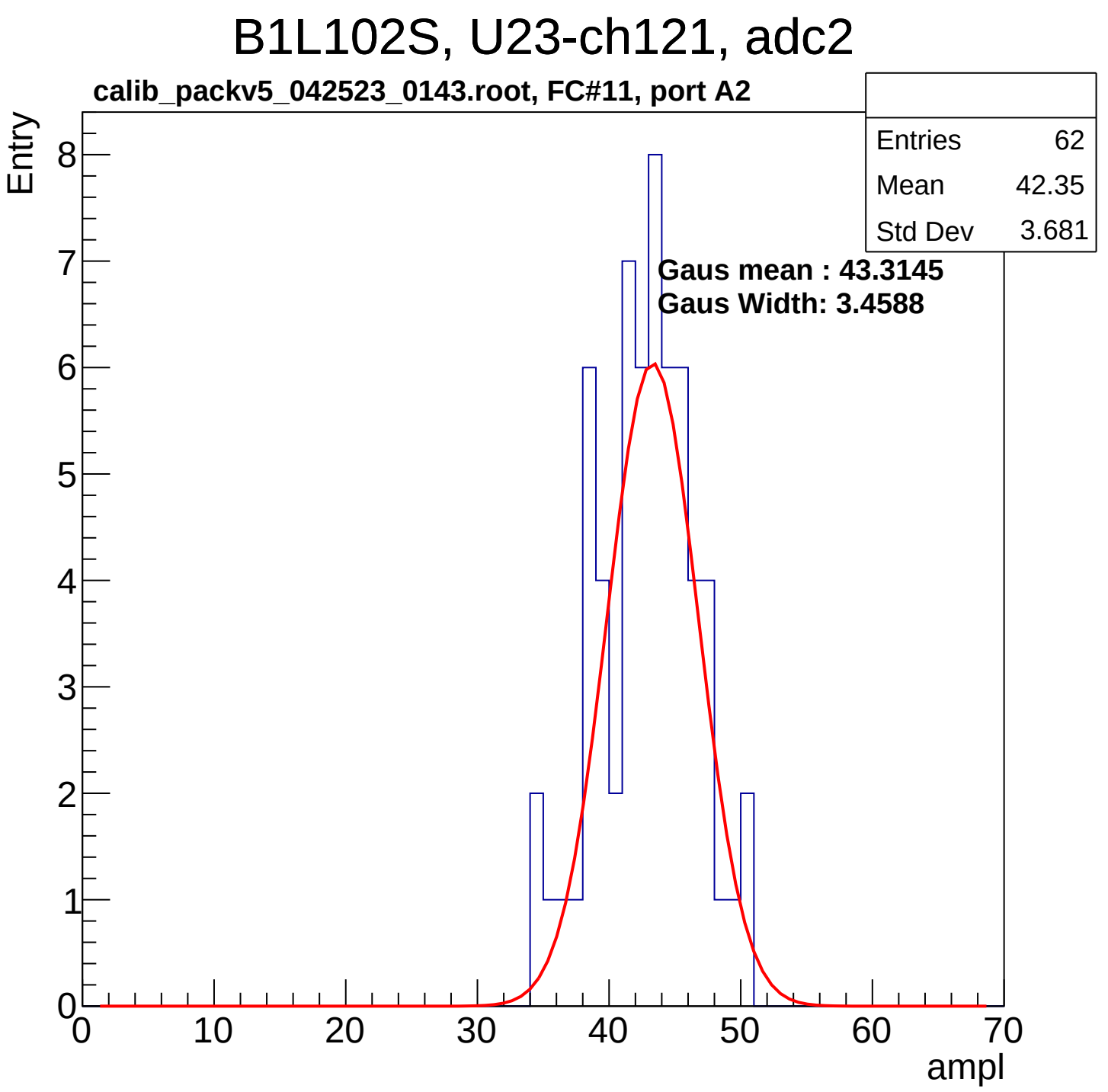

B1L102S, U23-ch120, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 58.84
Std Dev 2.808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

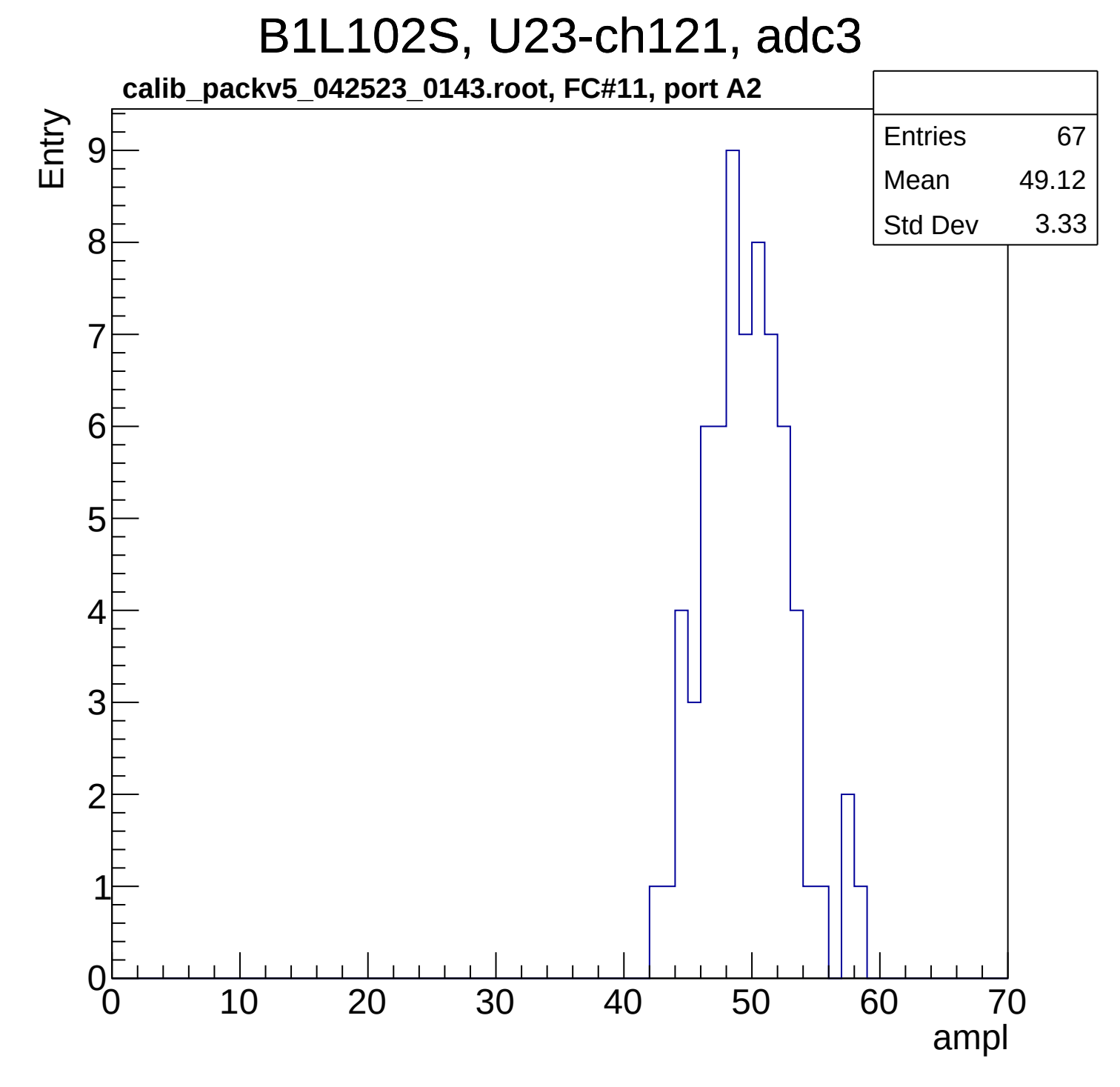

B1L102S, U23-ch120, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 55.2
Std Dev 18.43
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

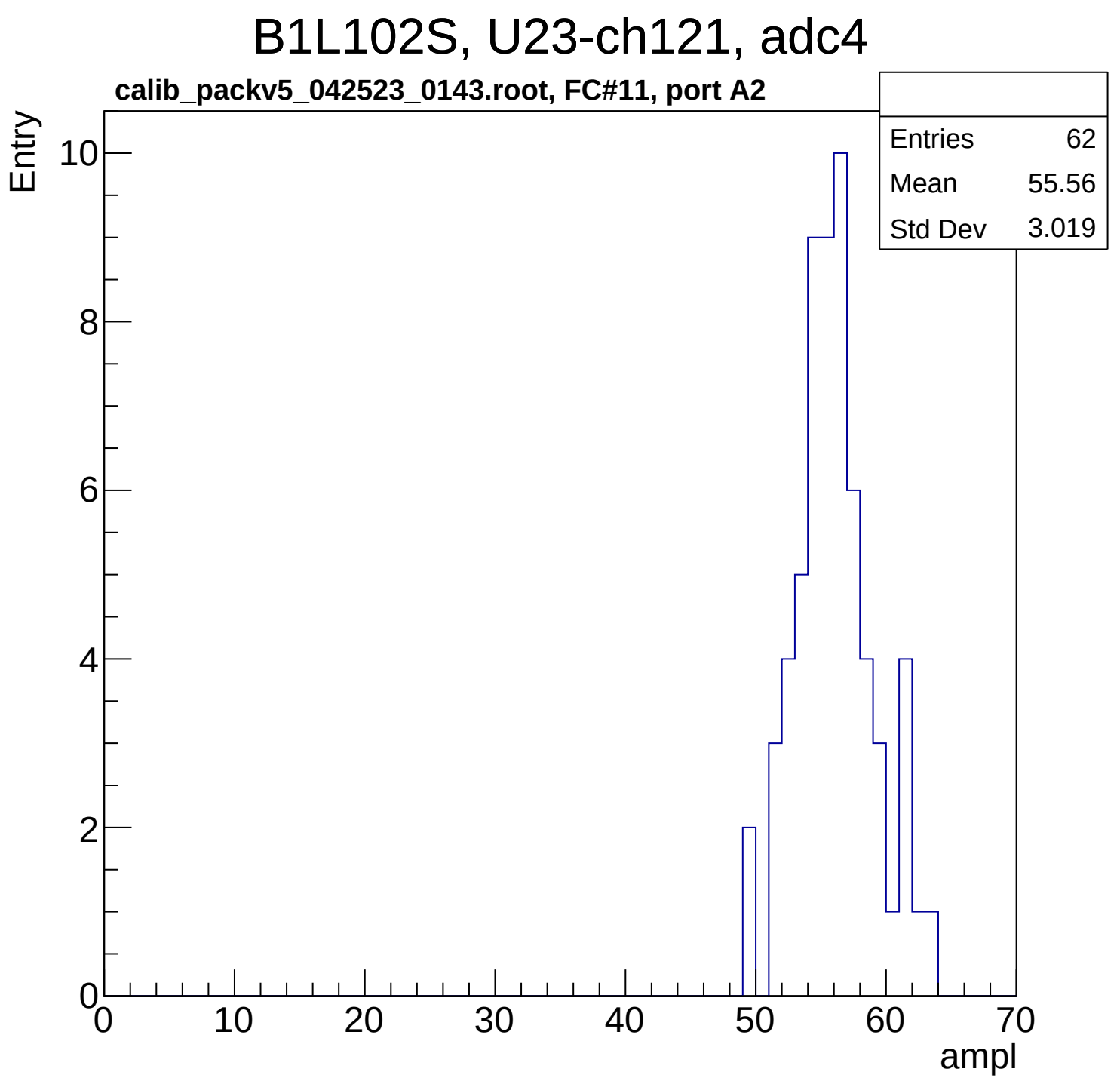

B1L102S, U23-ch120, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

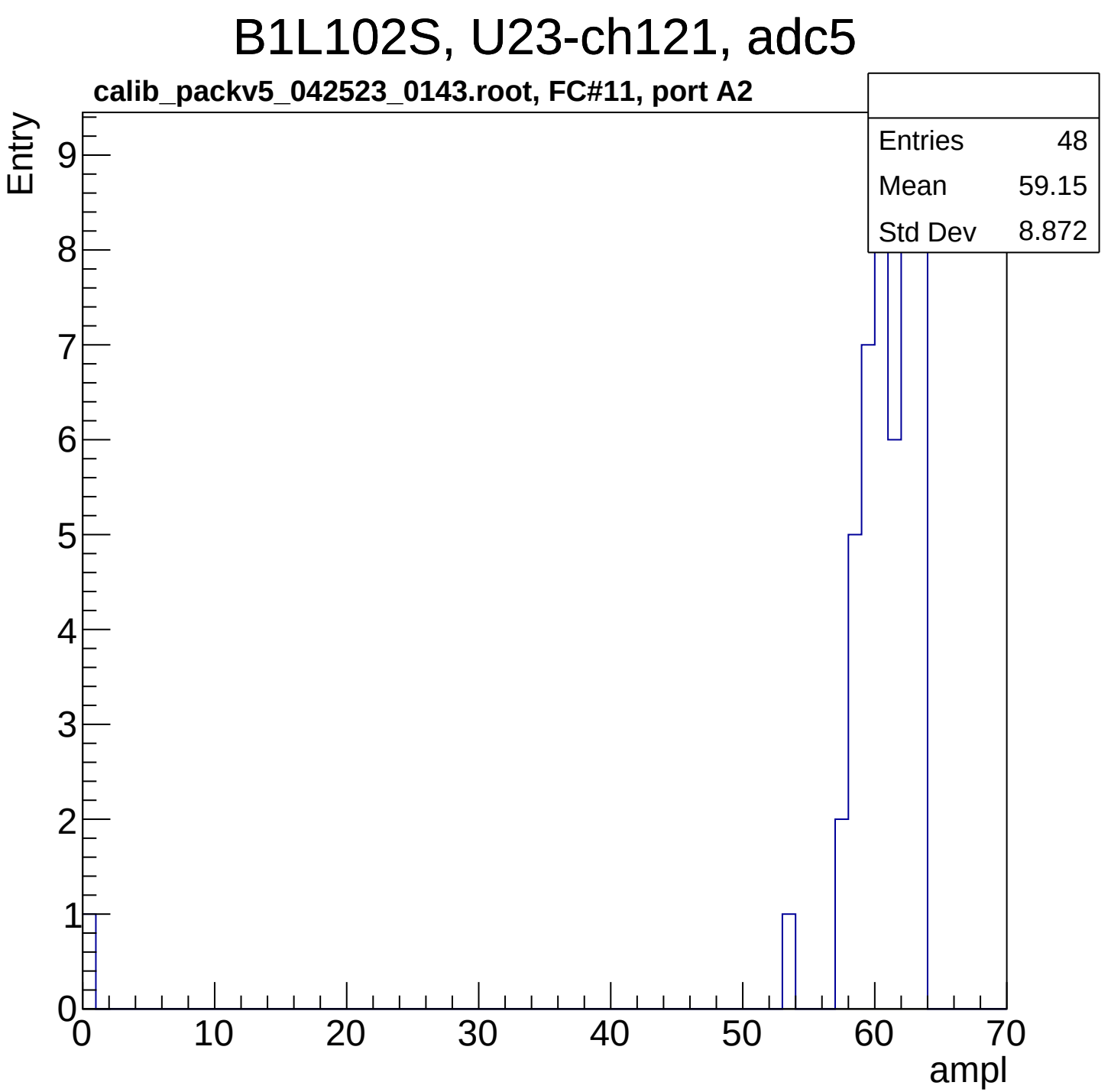

B1L102S, U23-ch121, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 29.53
Std Dev 3.705
Gaus mean : 29.2041
Gaus Width: 3.2240
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

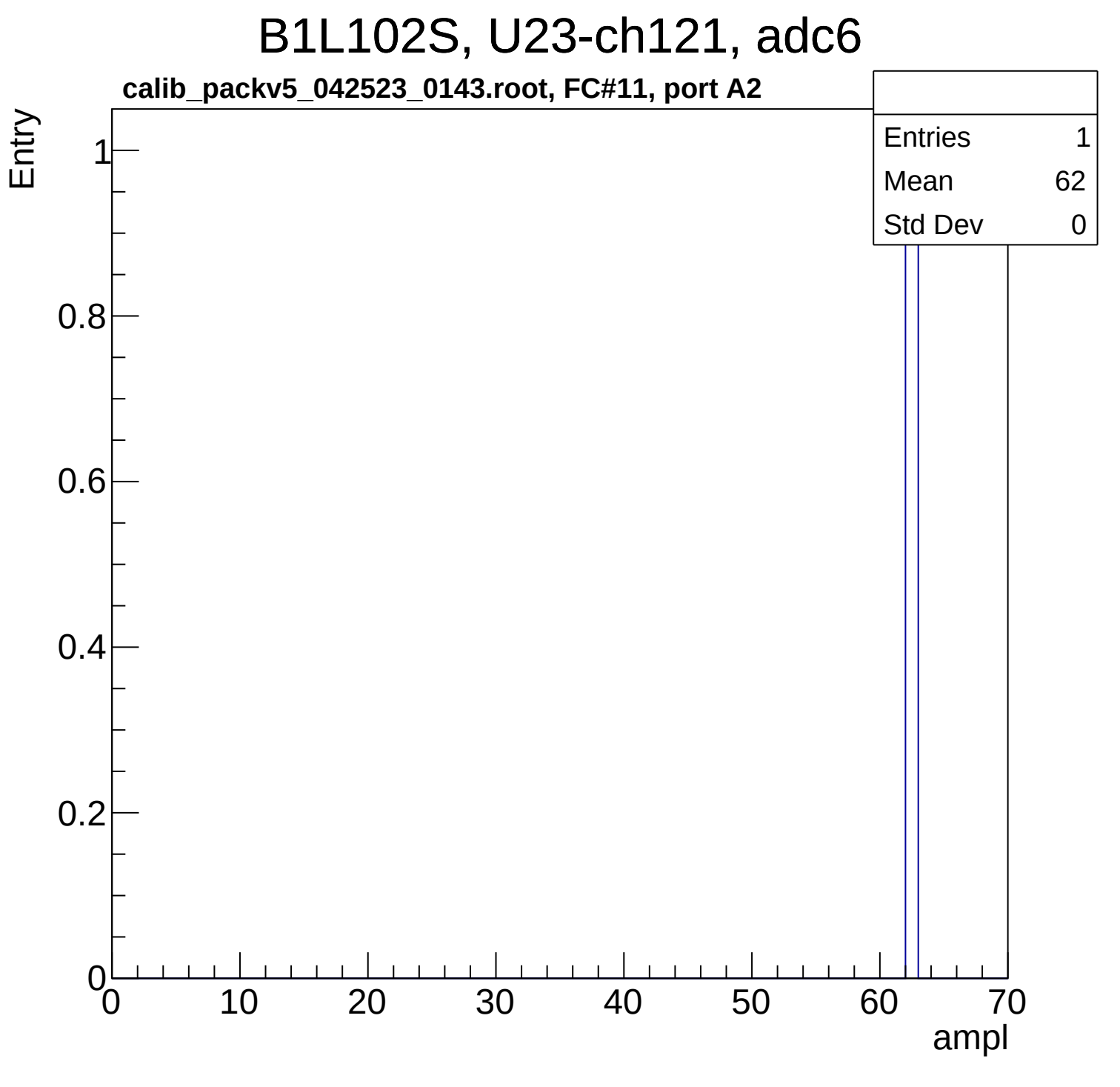

B1L102S, U23-ch121, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 36.28
Std Dev 3.615
Gaus mean : 36.9819
Gaus Width: 3.8518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

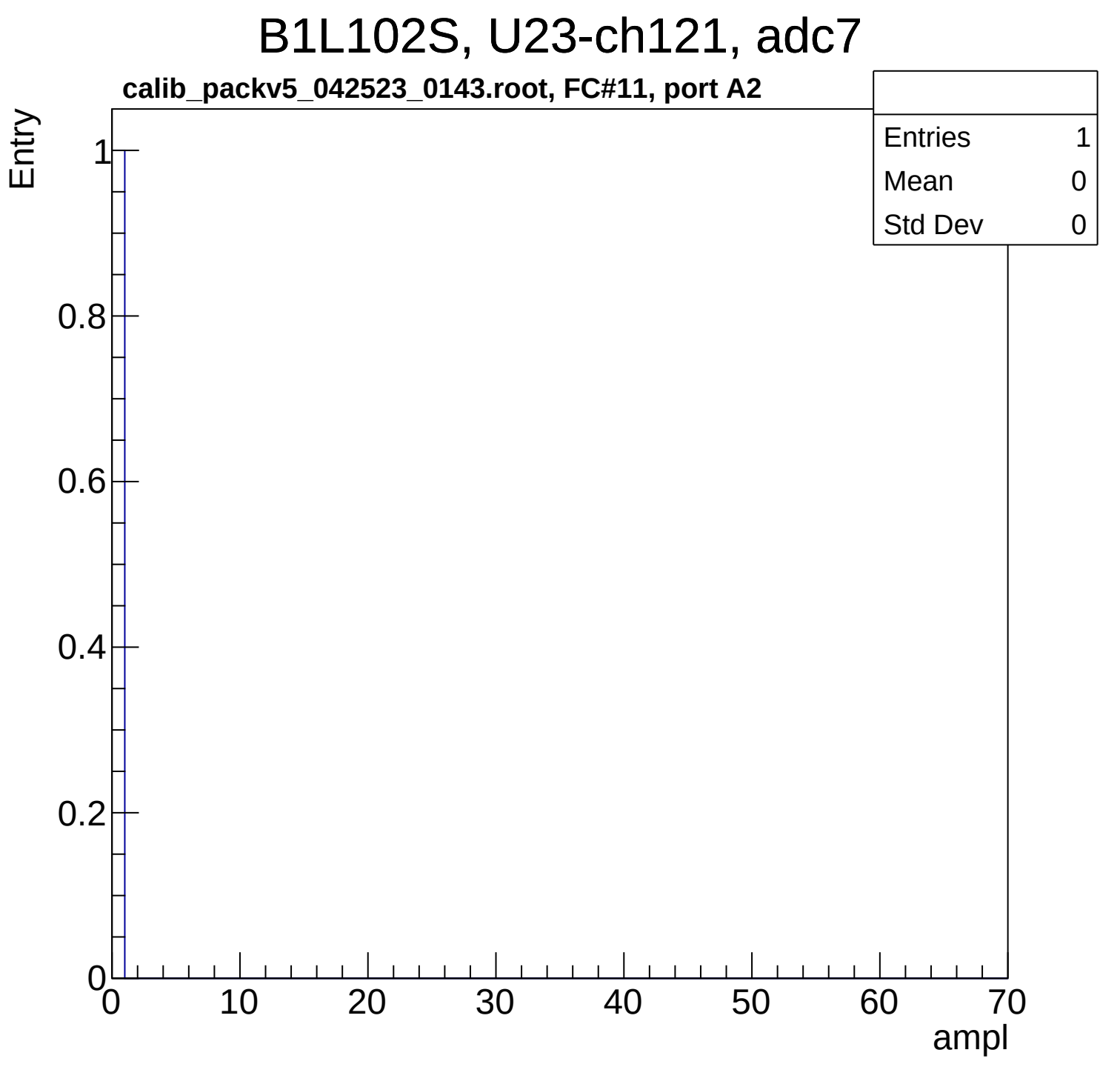

B1L102S, U23-ch121, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 42.35
Std Dev 3.681
Gaus mean : 43.3145
Gaus Width: 3.4588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

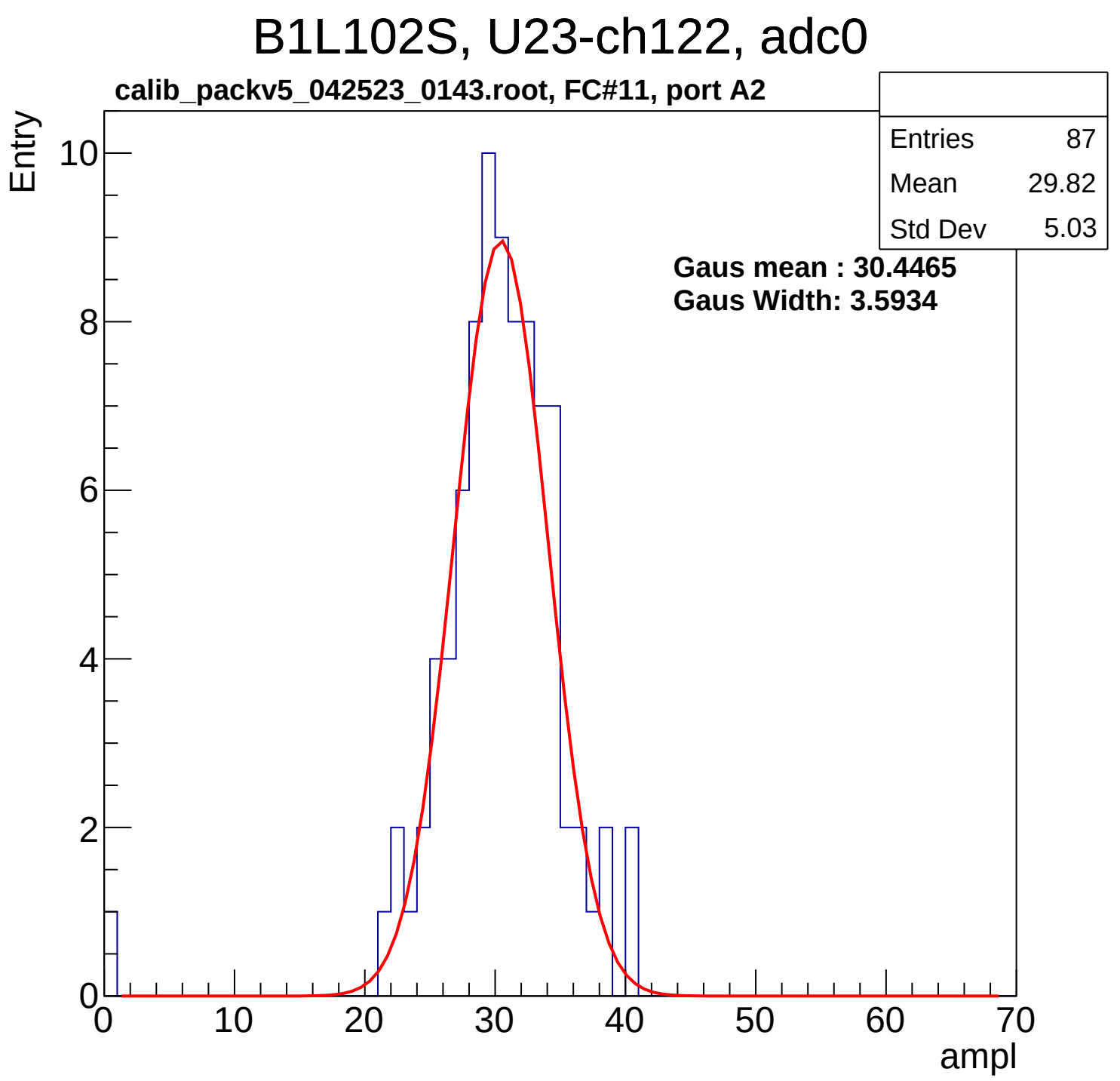

B1L102S, U23-ch121, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 49.12
Std Dev 3.33
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

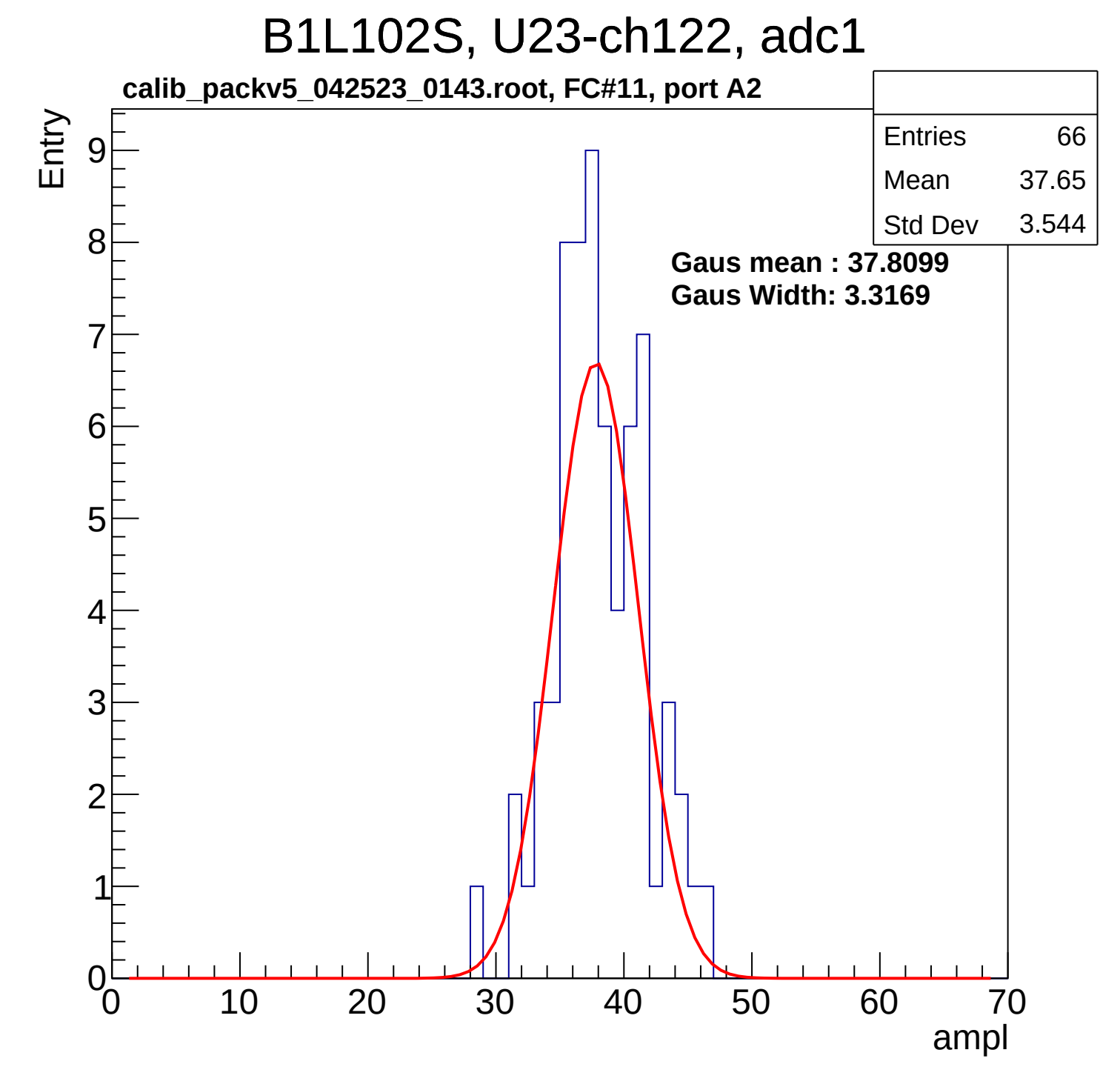

B1L102S, U23-ch121, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 55.56
Std Dev 3.019
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

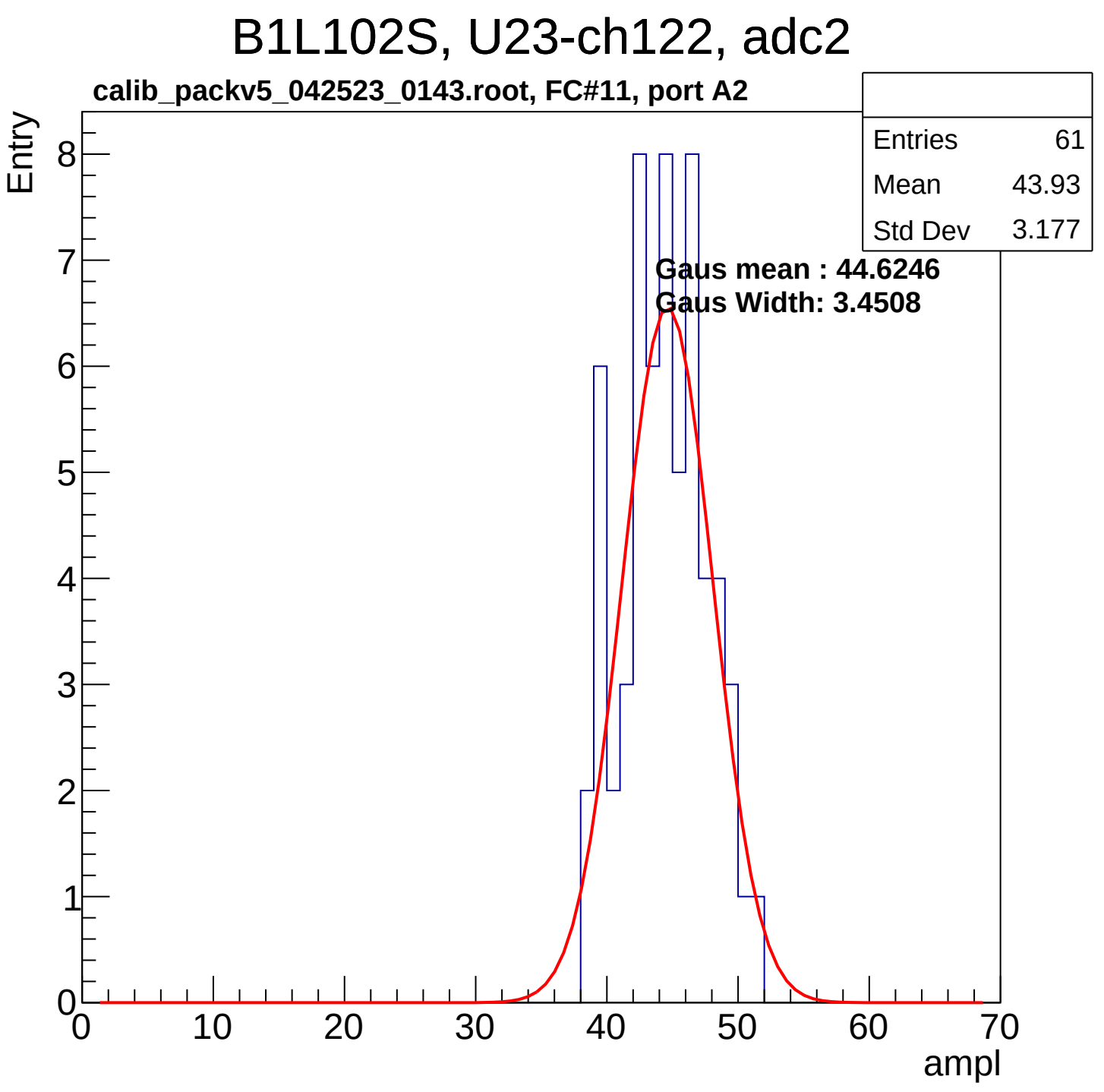

B1L102S, U23-ch121, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 59.15
Std Dev 8.872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

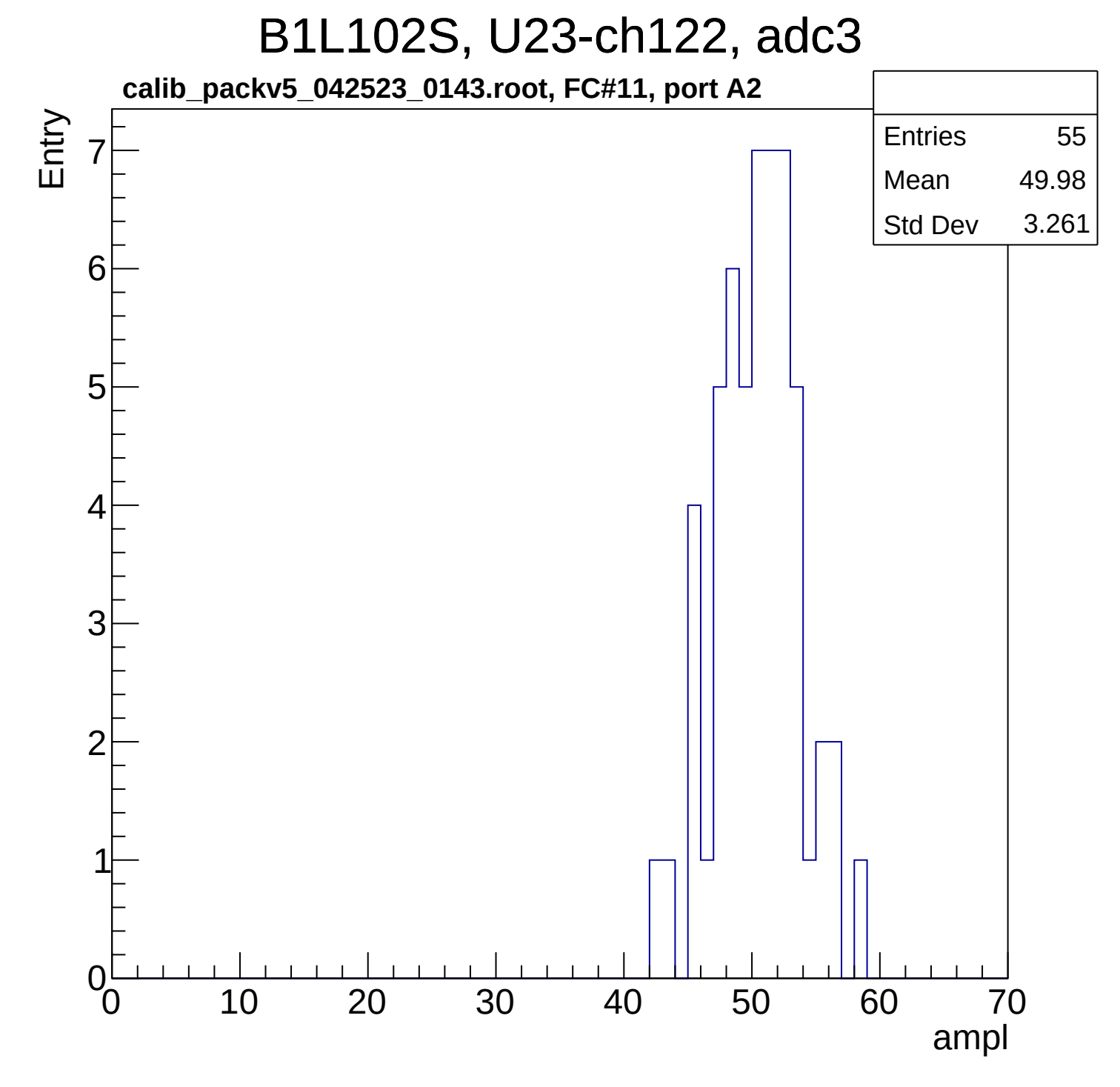

B1L102S, U23-ch121, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

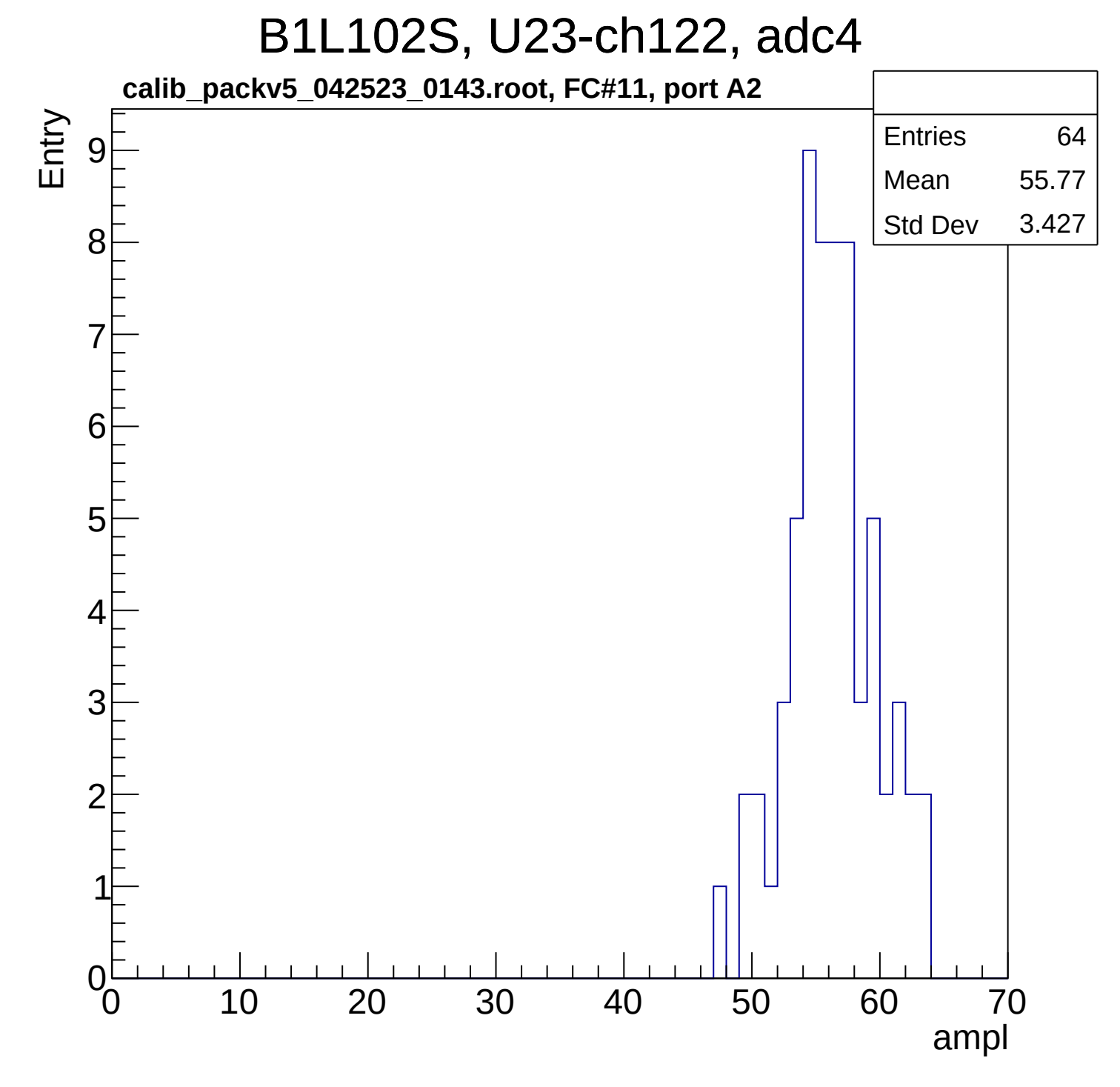

B1L102S, U23-ch121, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

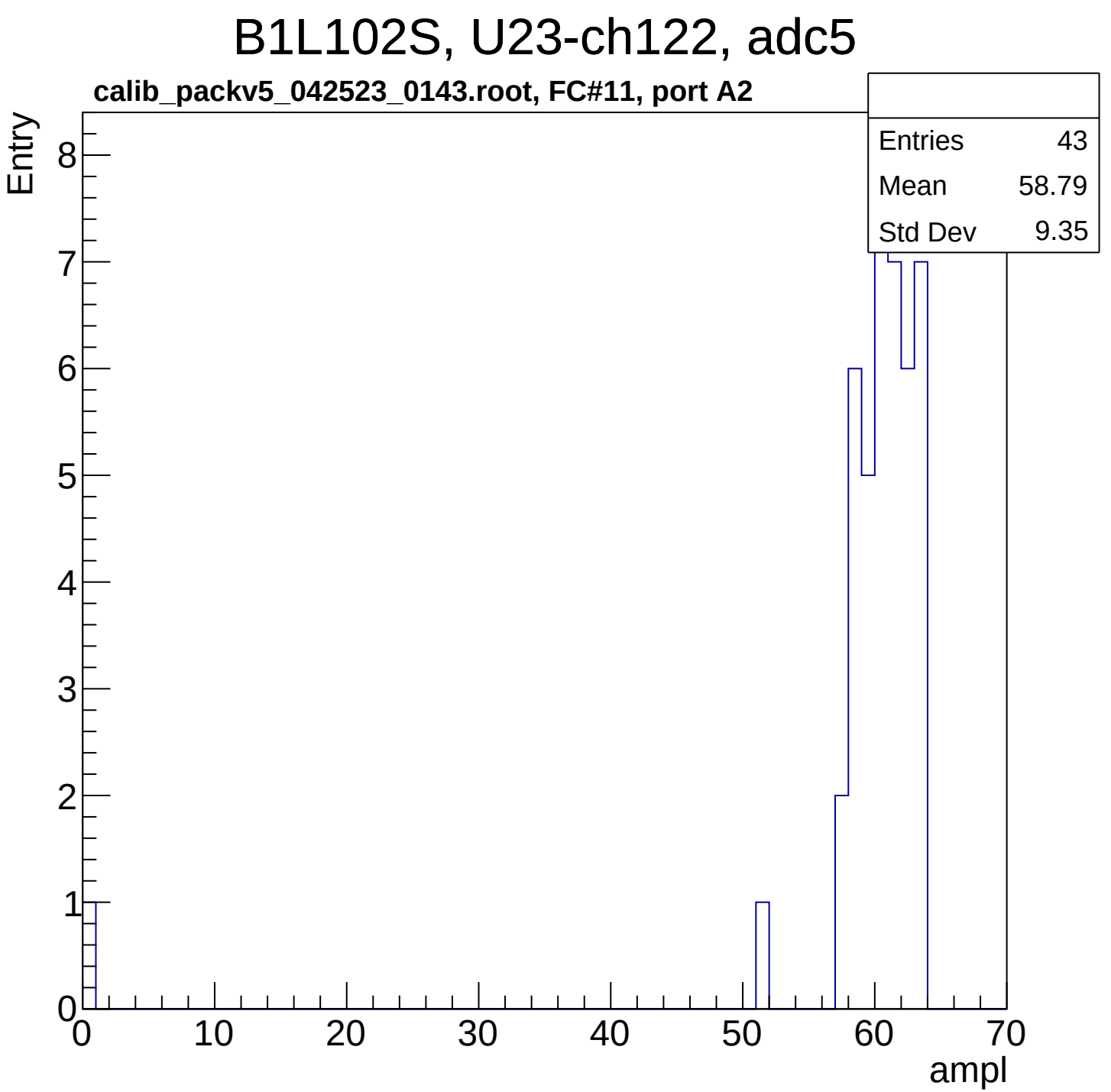

B1L102S, U23-ch122, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 87
Mean 29.82
Std Dev 5.03
Gaus mean : 30.4465
Gaus Width: 3.5934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

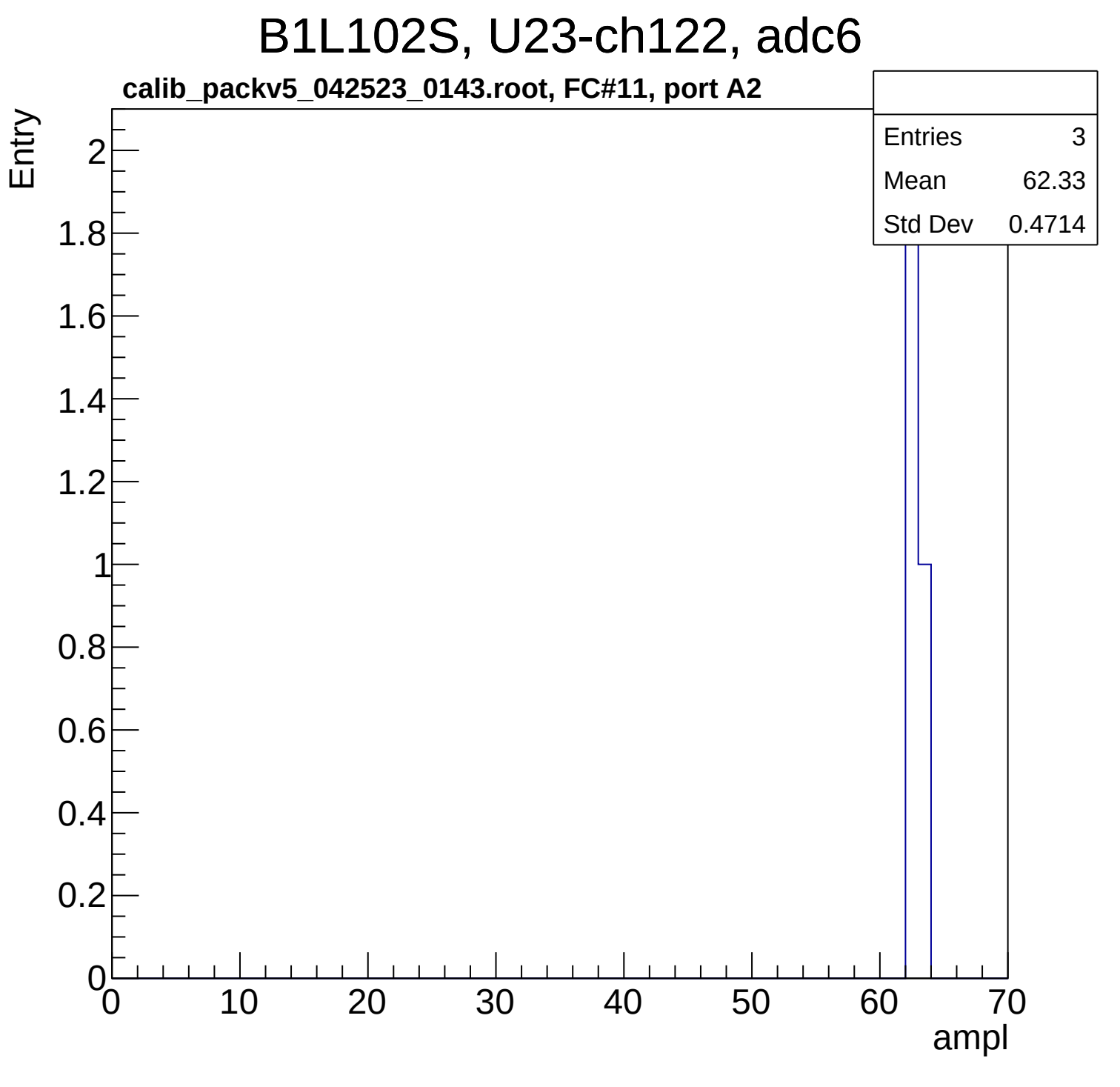

B1L102S, U23-ch122, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 37.65
Std Dev 3.544
Gaus mean : 37.8099
Gaus Width: 3.3169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

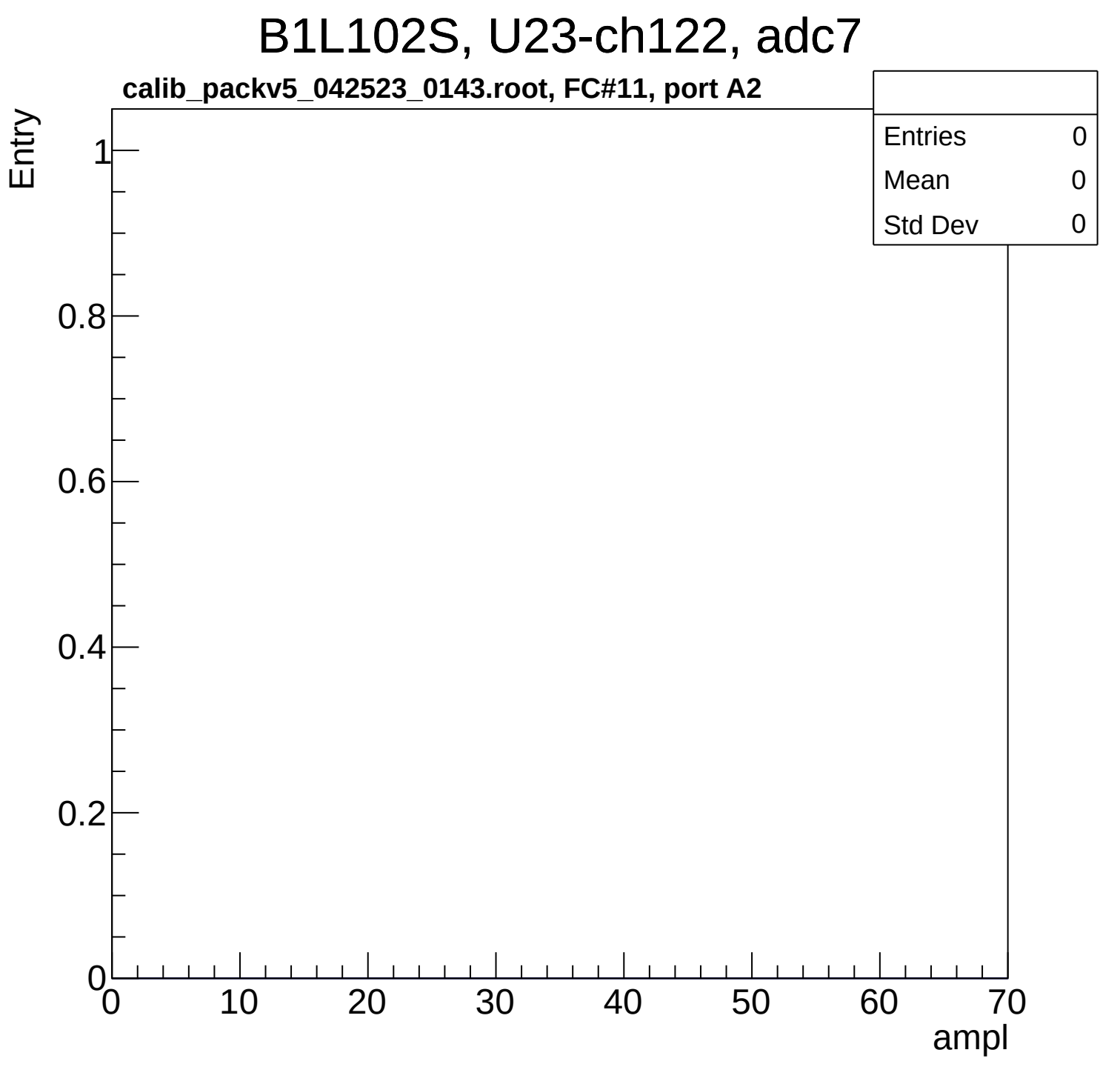

B1L102S, U23-ch122, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 43.93
Std Dev 3.177
Gaus mean : 44.6246
Gaus Width: 3.4508
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

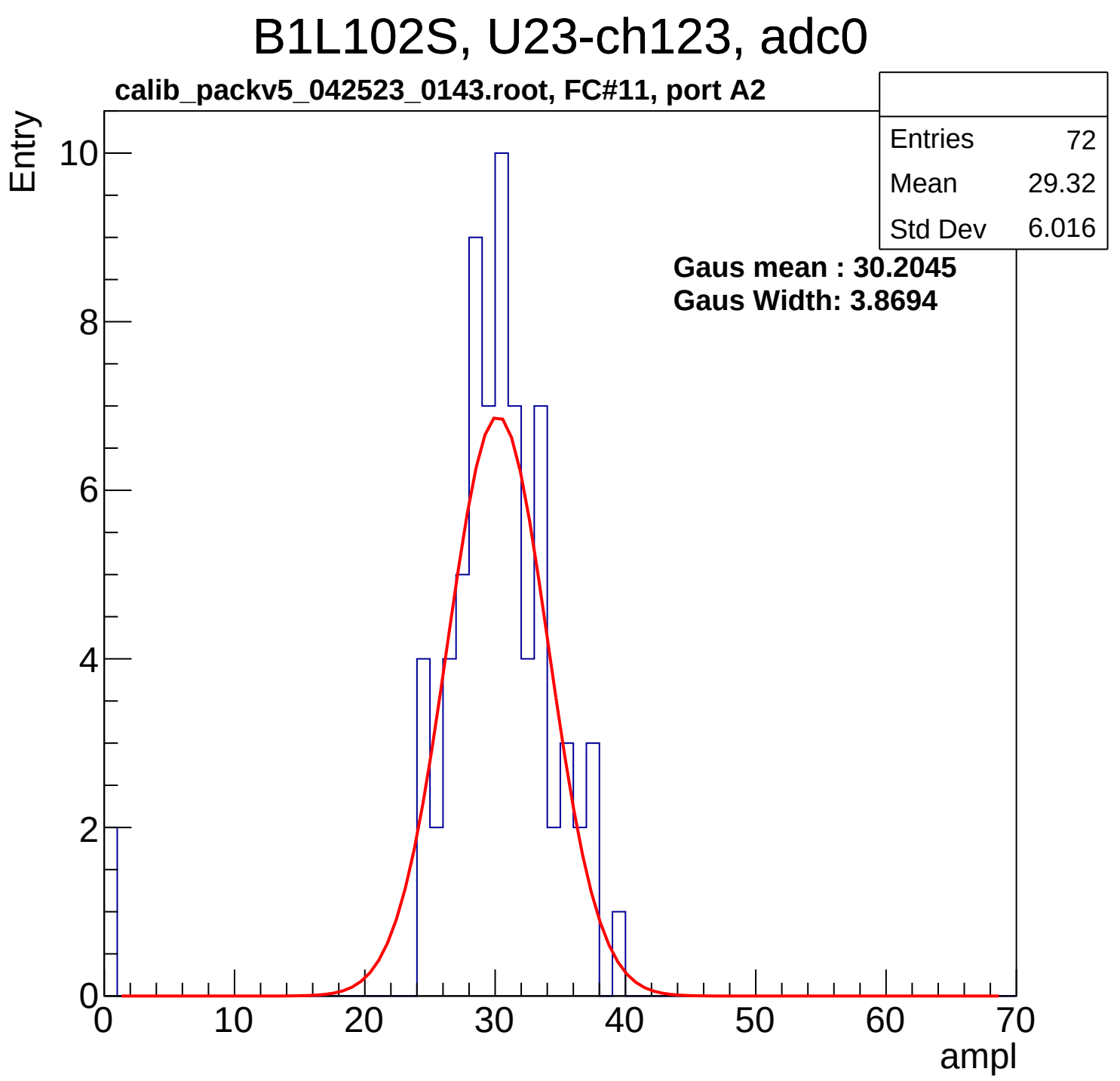

B1L102S, U23-ch122, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 49.98
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

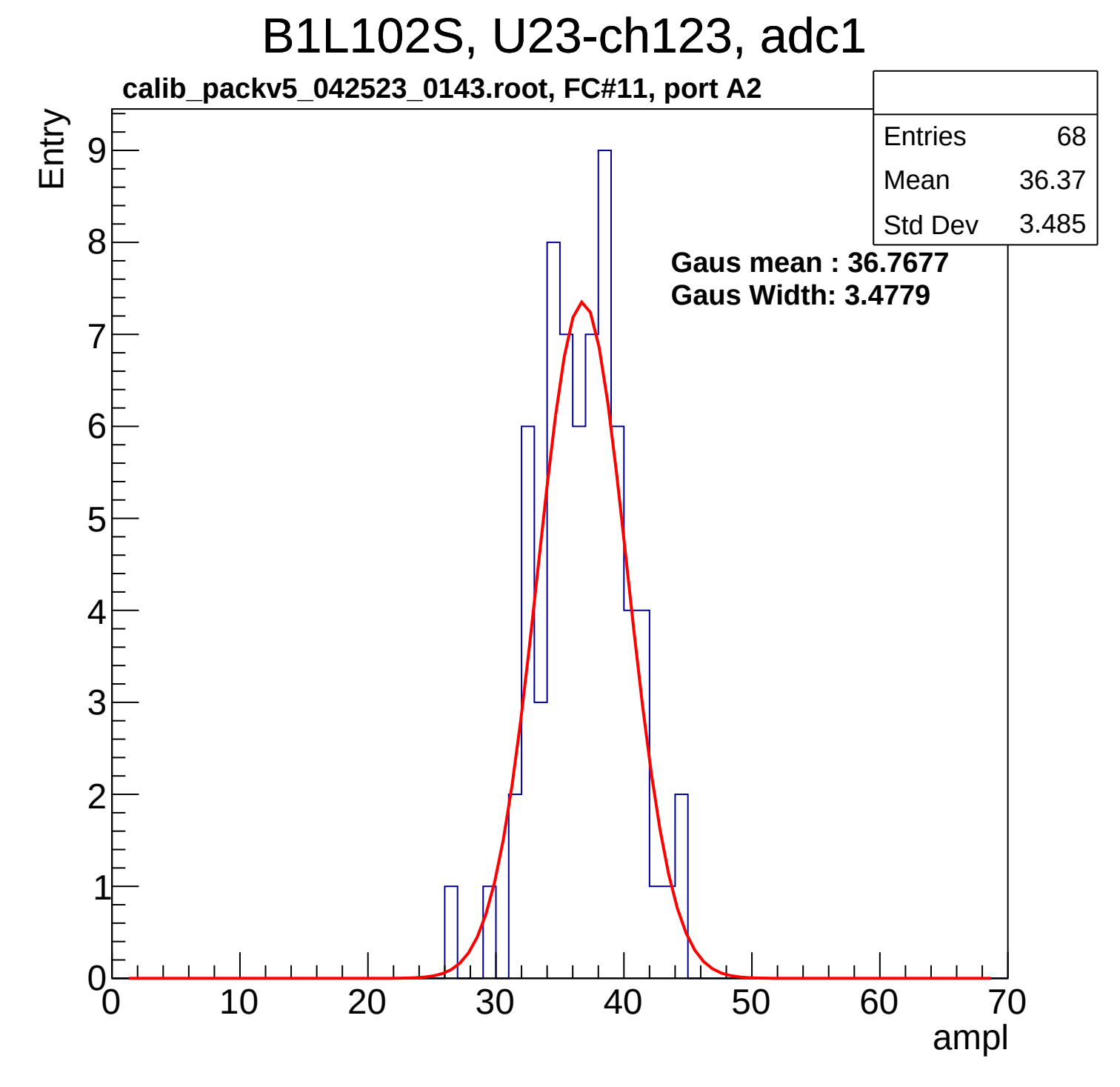

B1L102S, U23-ch122, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 55.77
Std Dev 3.427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

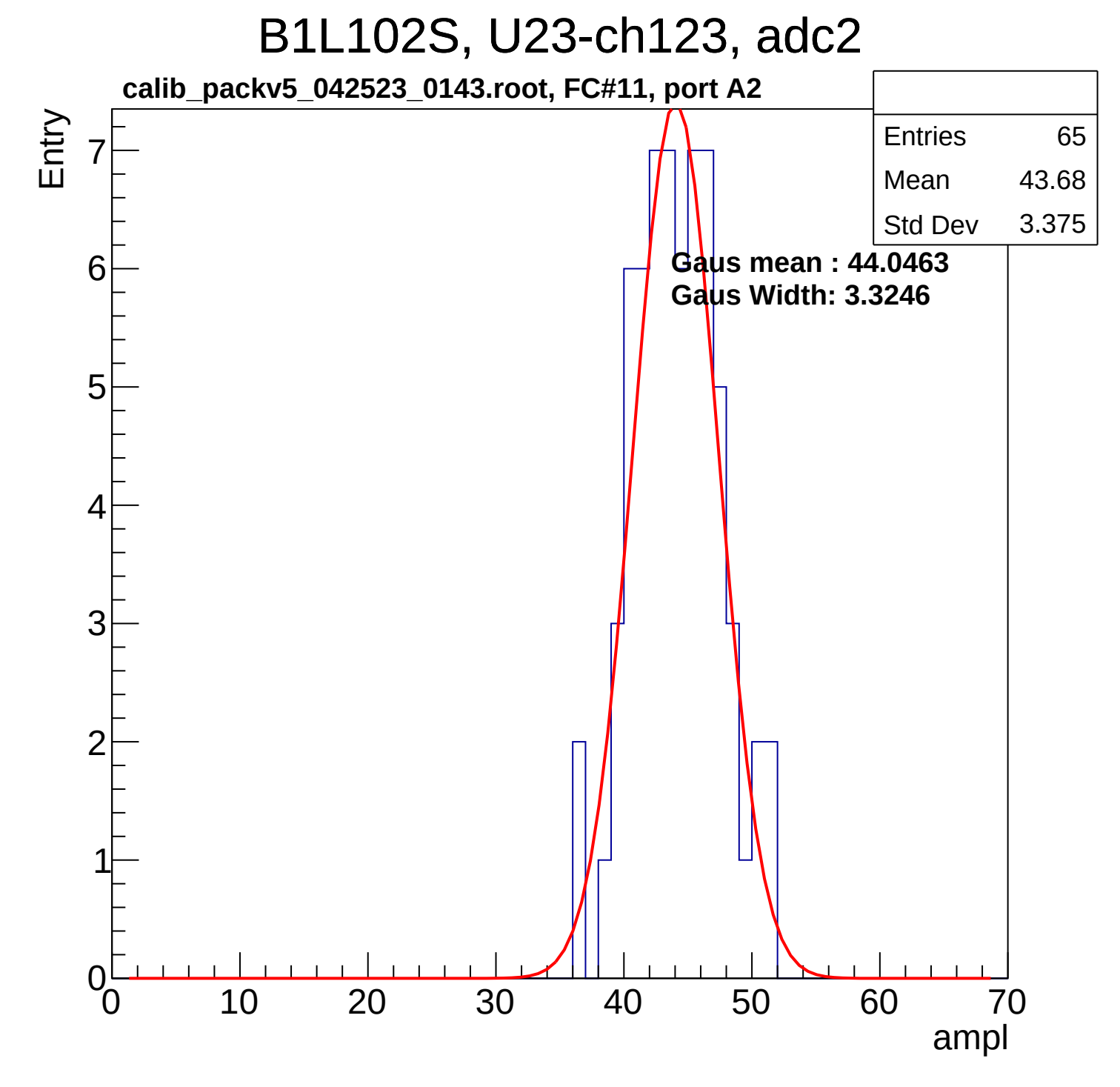

B1L102S, U23-ch122, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 58.79
Std Dev 9.35
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

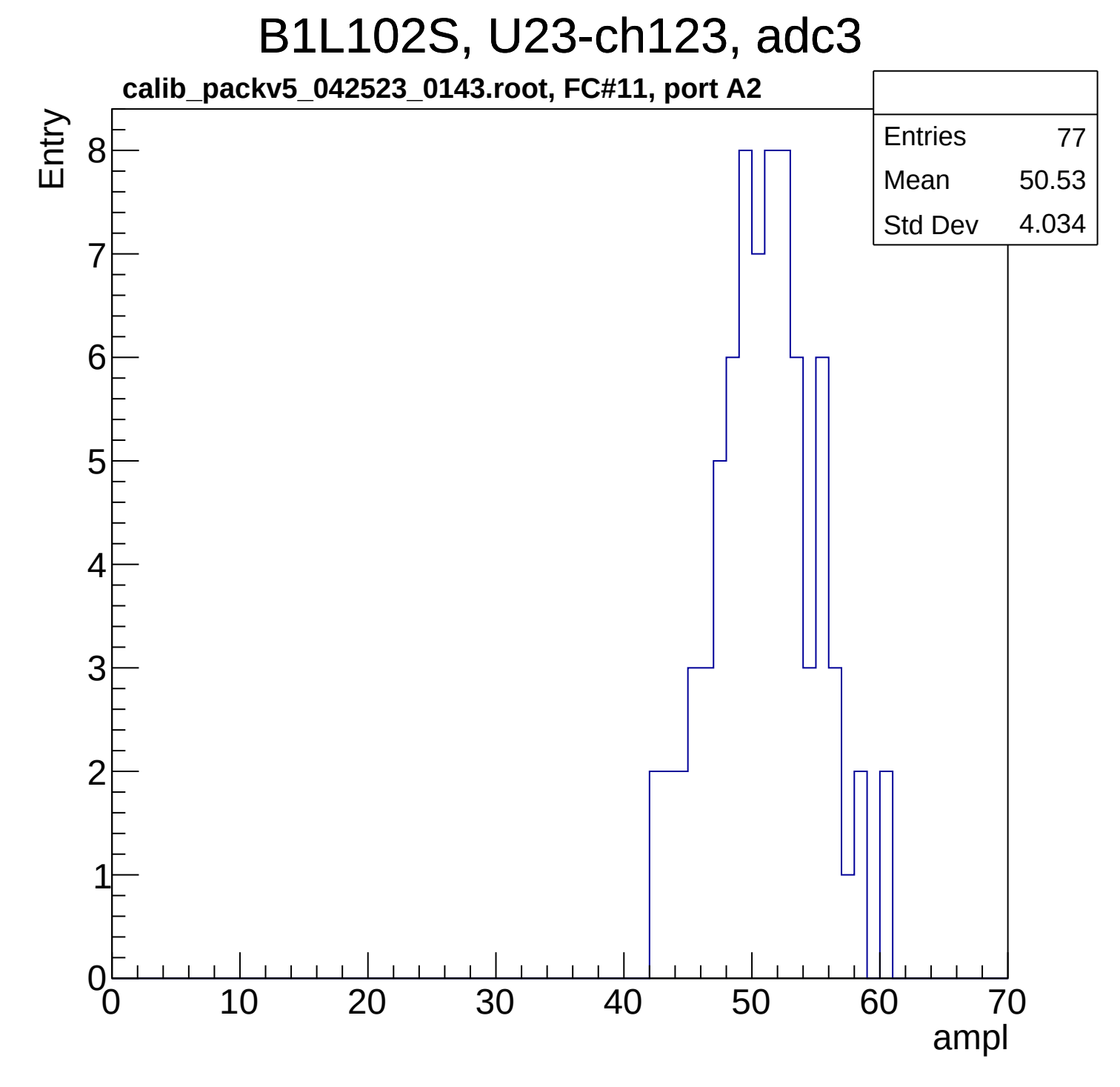

B1L102S, U23-ch122, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

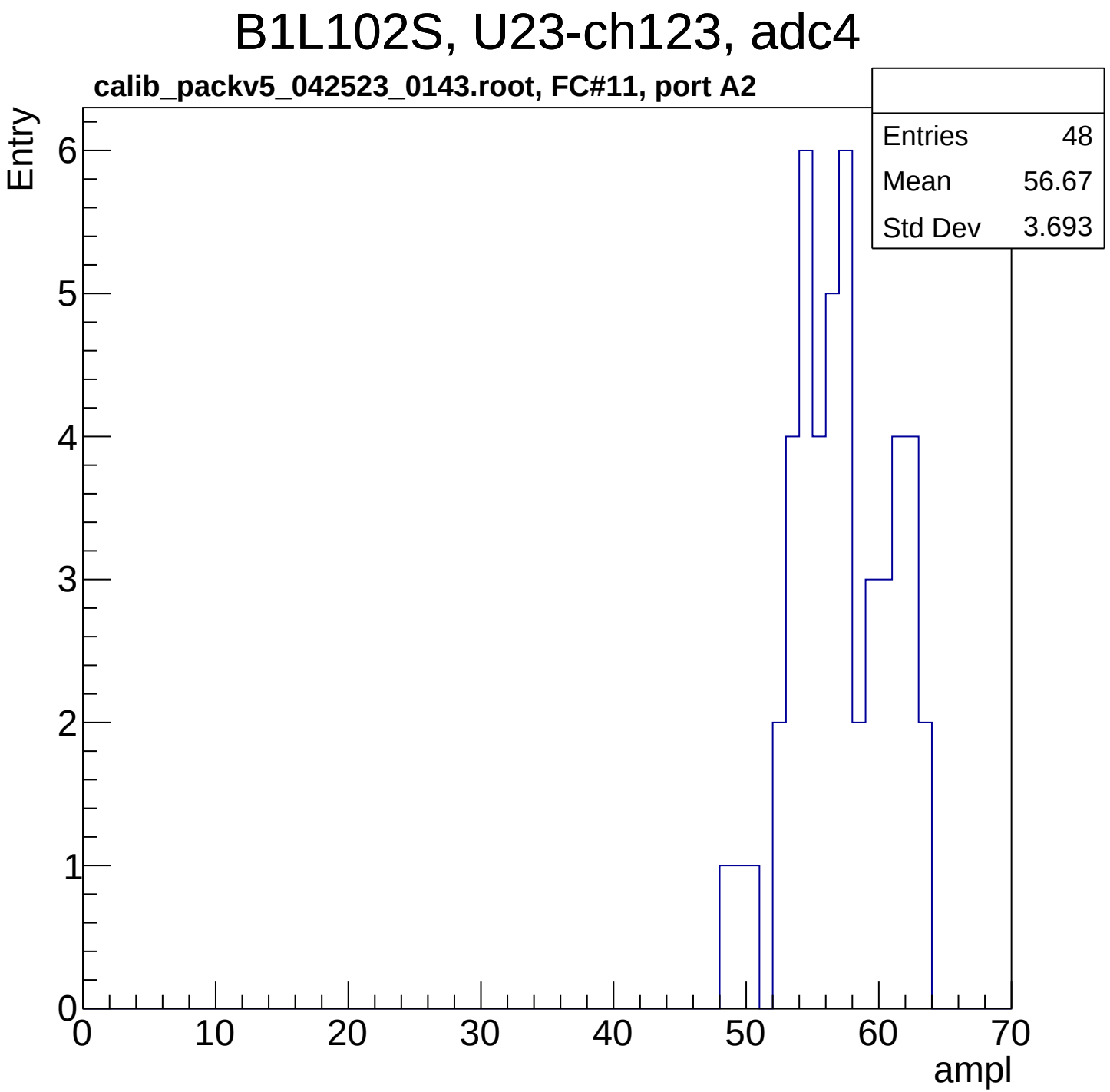

B1L102S, U23-ch122, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

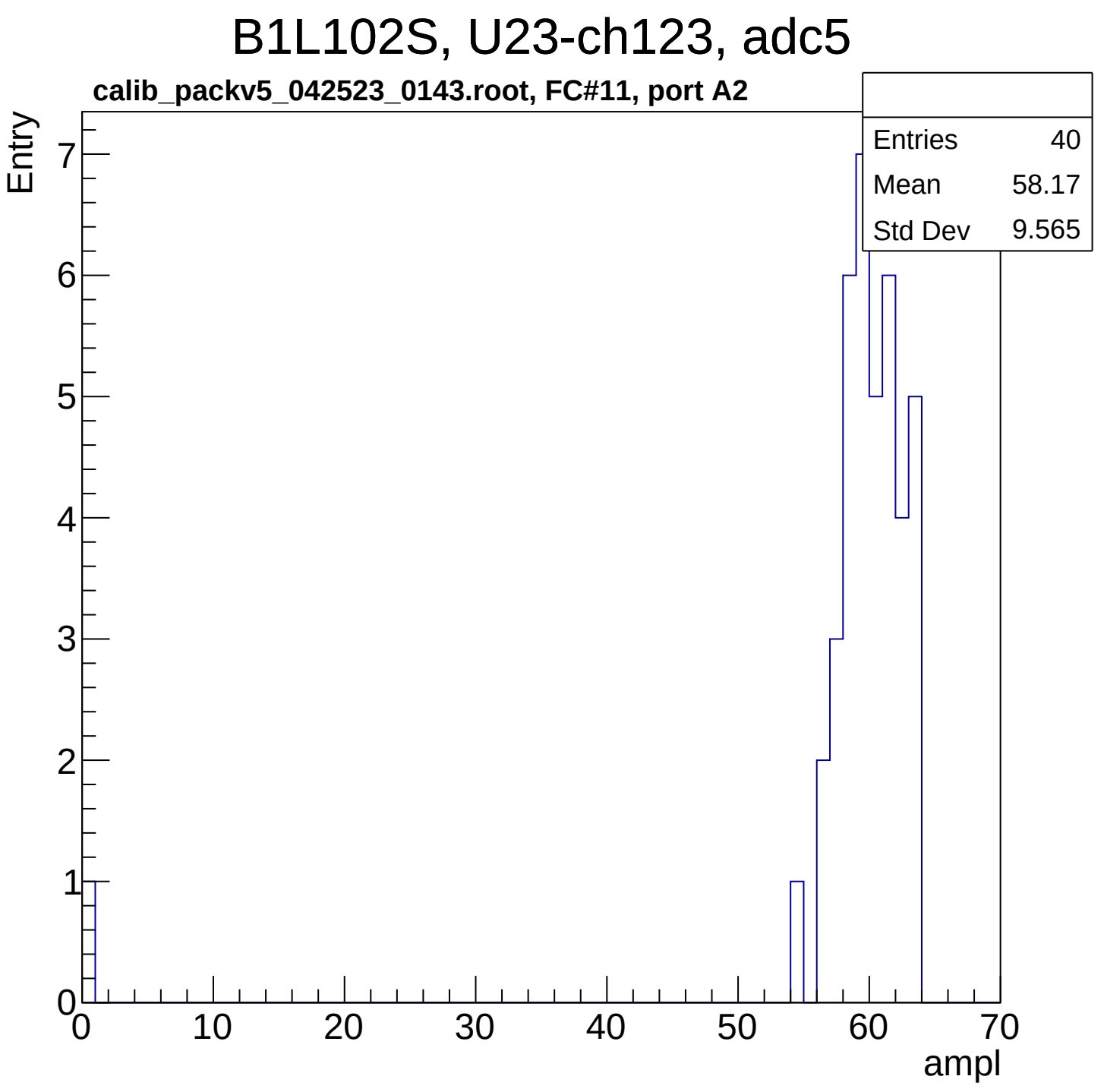

B1L102S, U23-ch123, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 29.32
Std Dev 6.016
Gaus mean : 30.2045
Gaus Width: 3.8694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

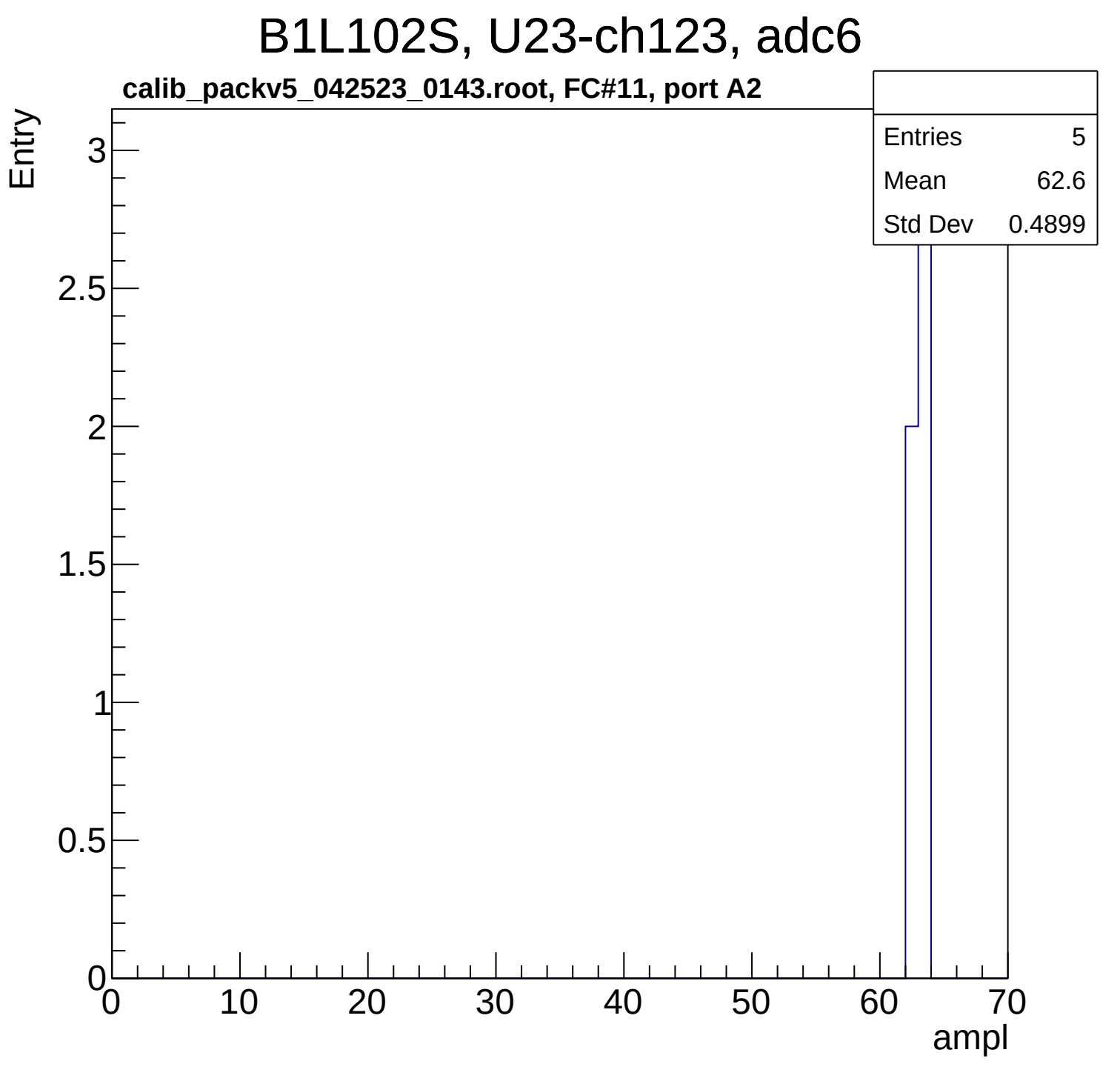

B1L102S, U23-ch123, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 36.37
Std Dev 3.485
Gaus mean : 36.7677
Gaus Width: 3.4779
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

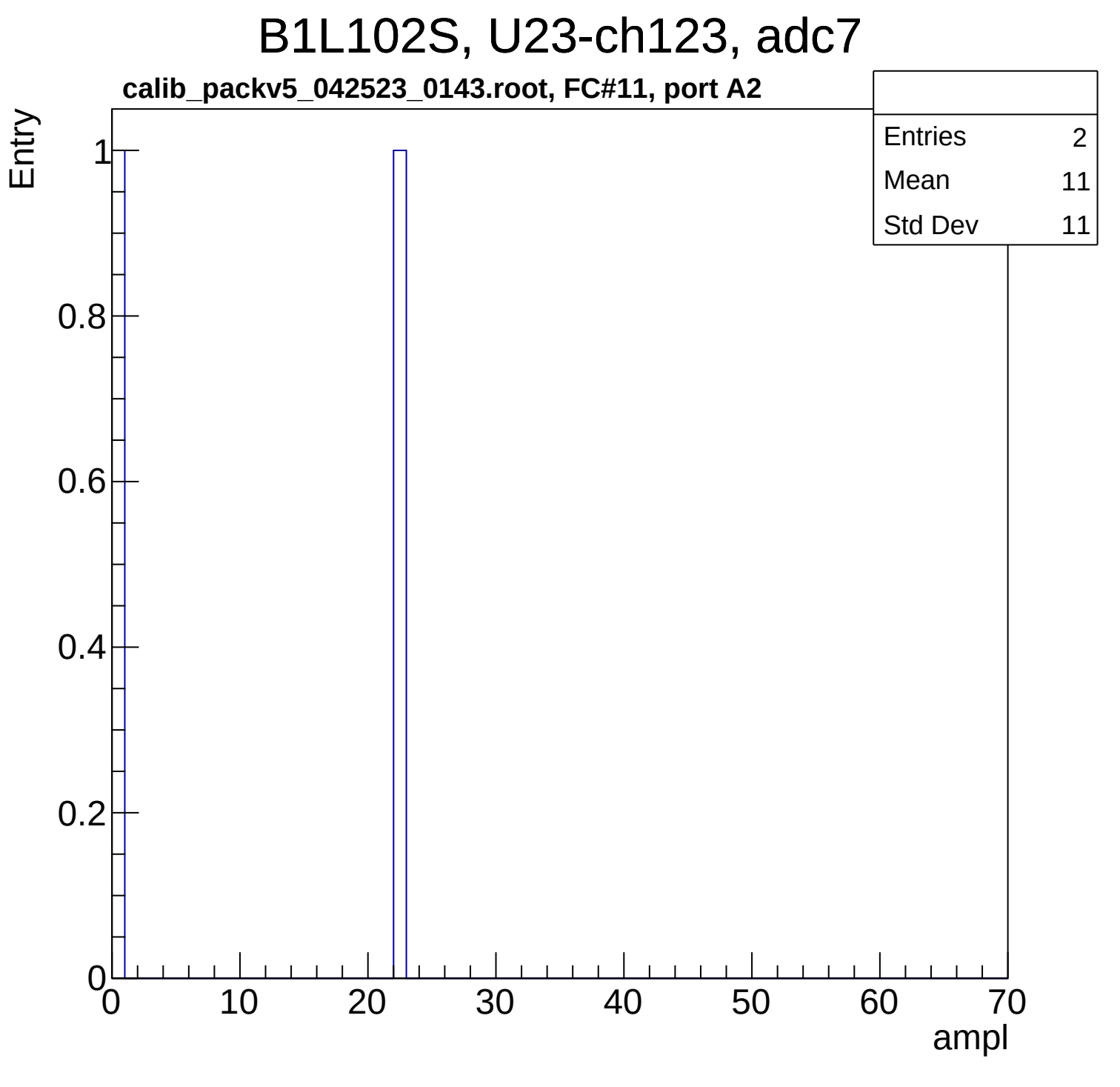

B1L102S, U23-ch123, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 43.68
Std Dev 3.375
Gaus mean : 44.0463
Gaus Width: 3.3246
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

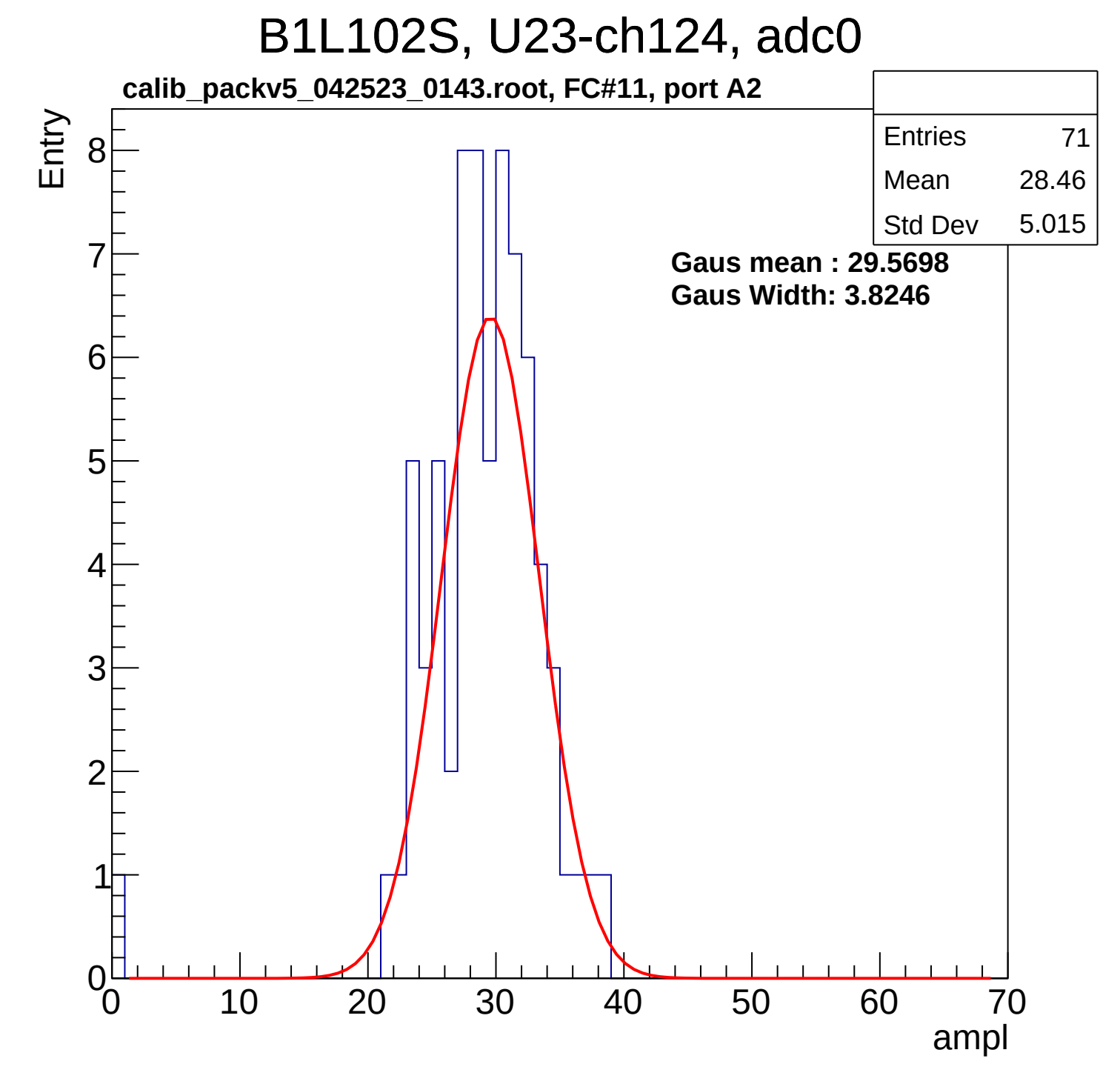

B1L102S, U23-ch123, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 50.53
Std Dev 4.034
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

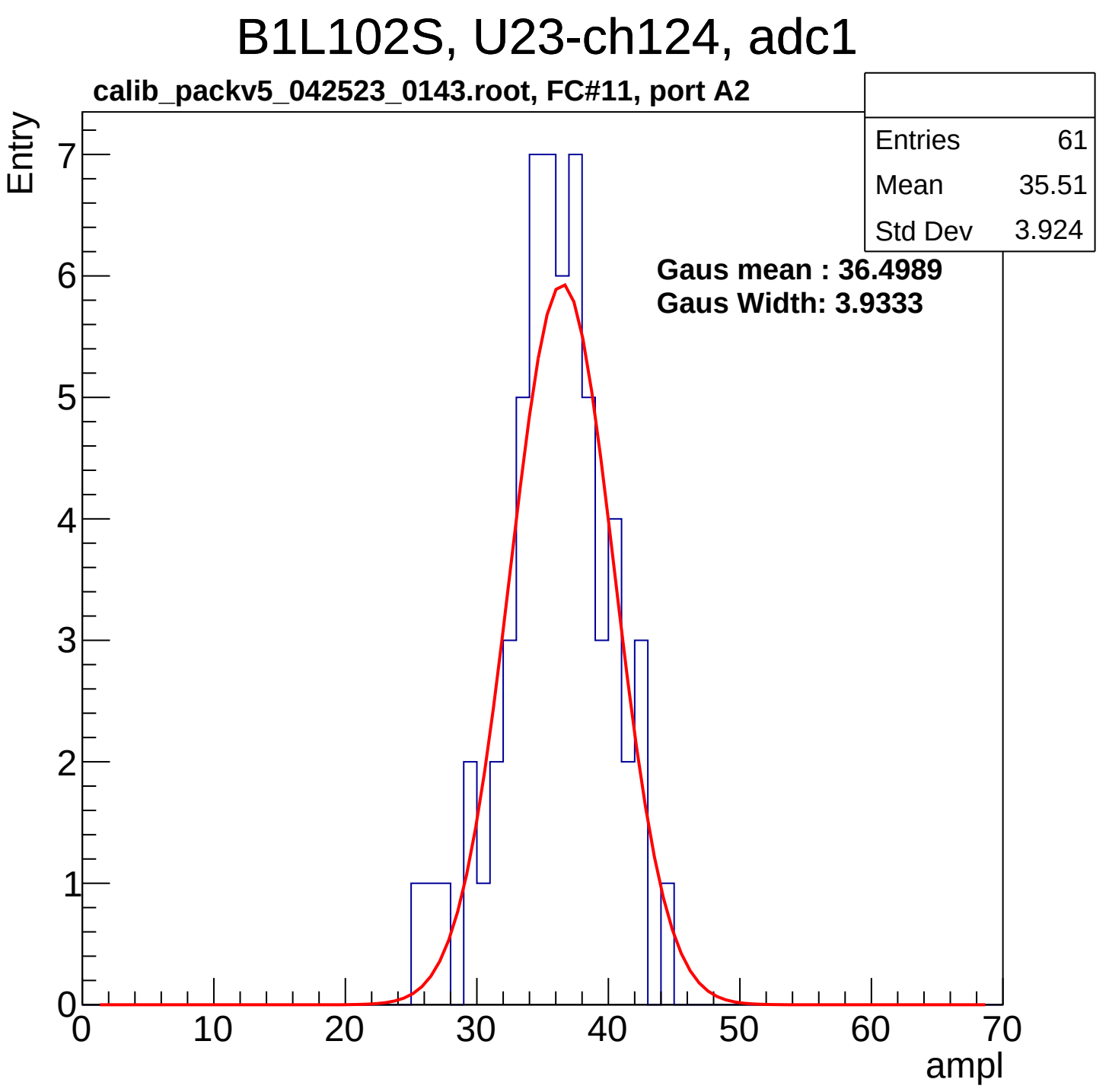

B1L102S, U23-ch123, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 56.67
Std Dev 3.693
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

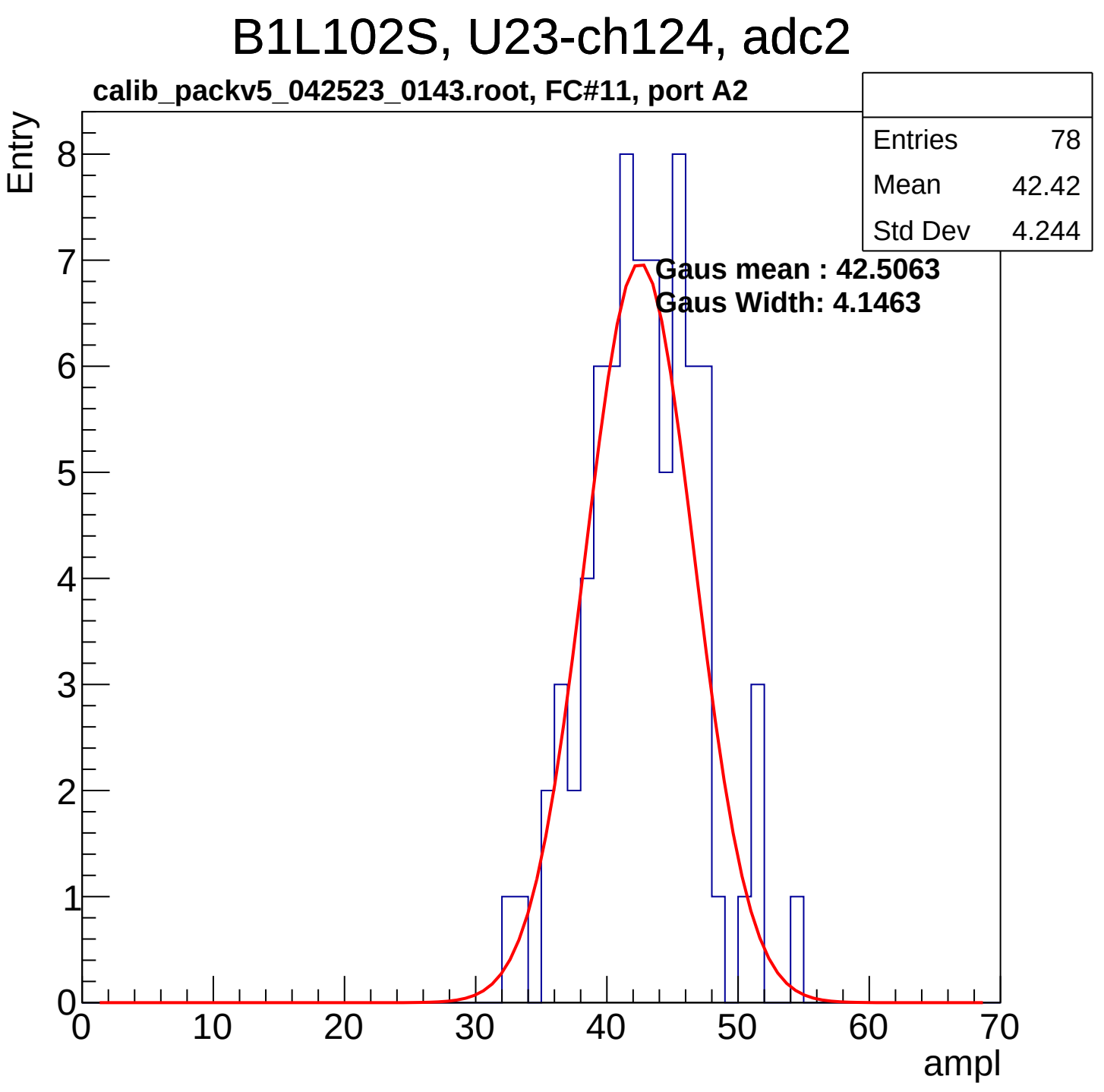

B1L102S, U23-ch123, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.17
Std Dev 9.565
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

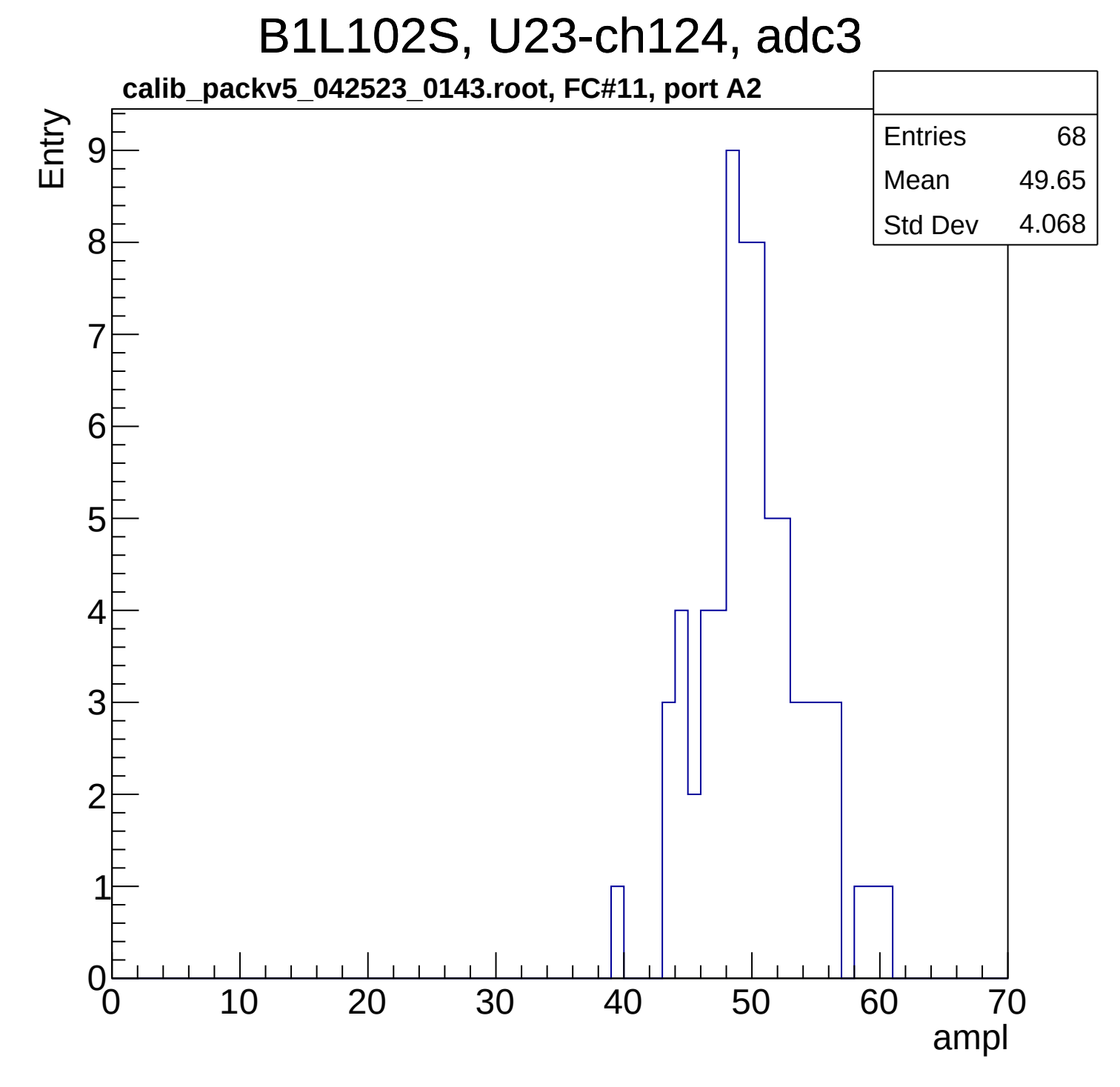

B1L102S, U23-ch123, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

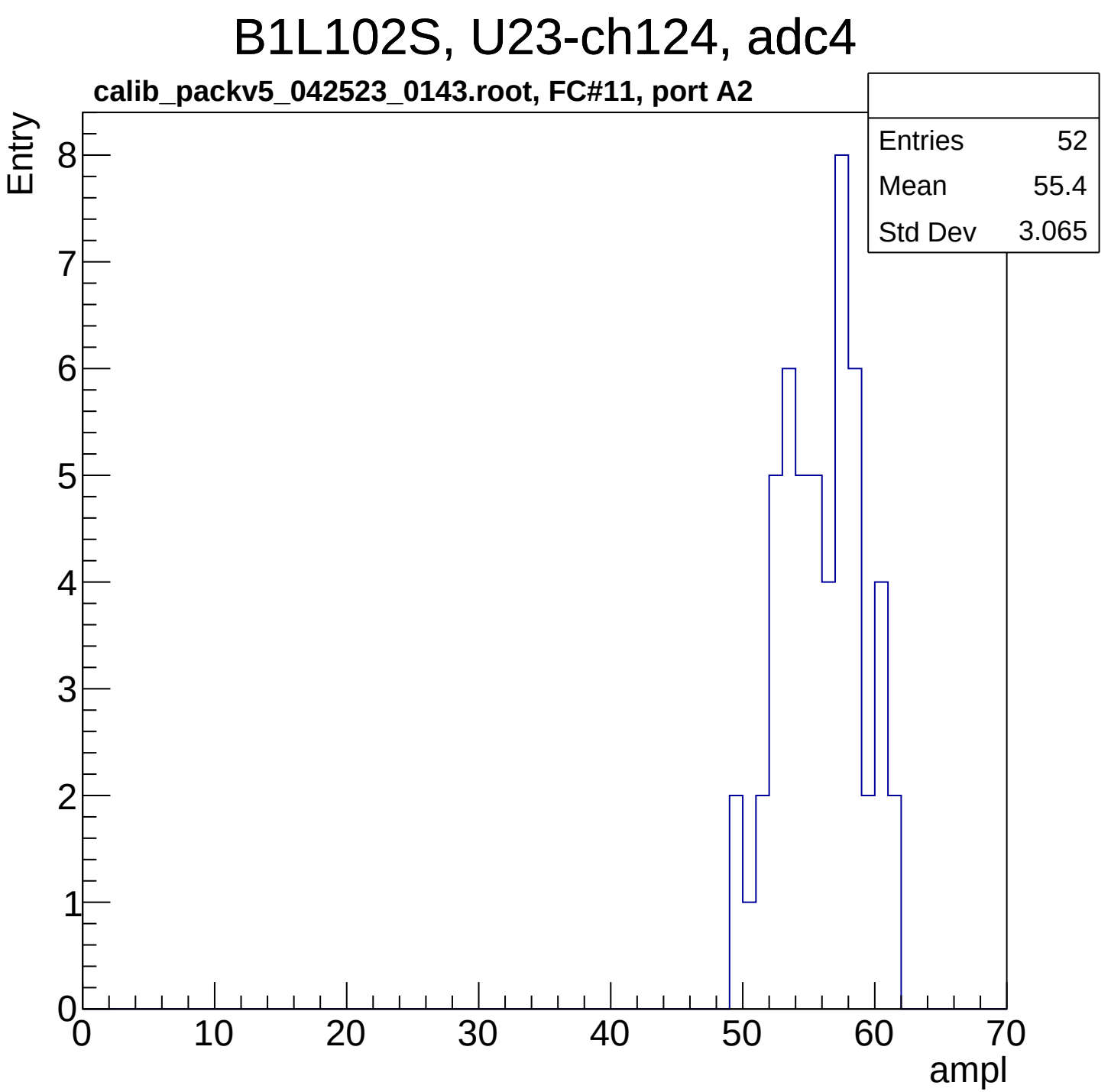

B1L102S, U23-ch123, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

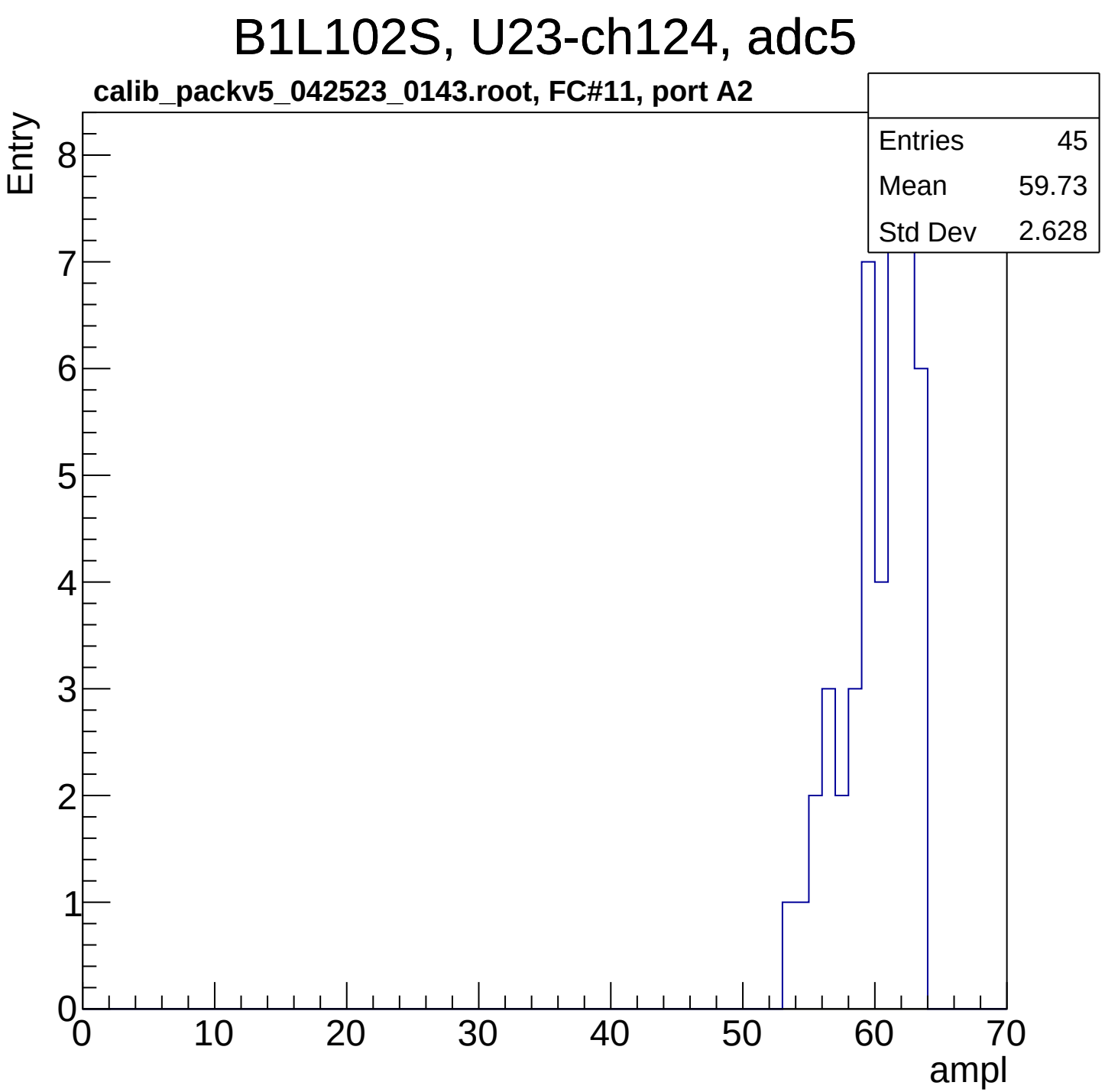

B1L102S, U23-ch124, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.46
Std Dev 5.015
Gaus mean : 29.5698
Gaus Width: 3.8246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

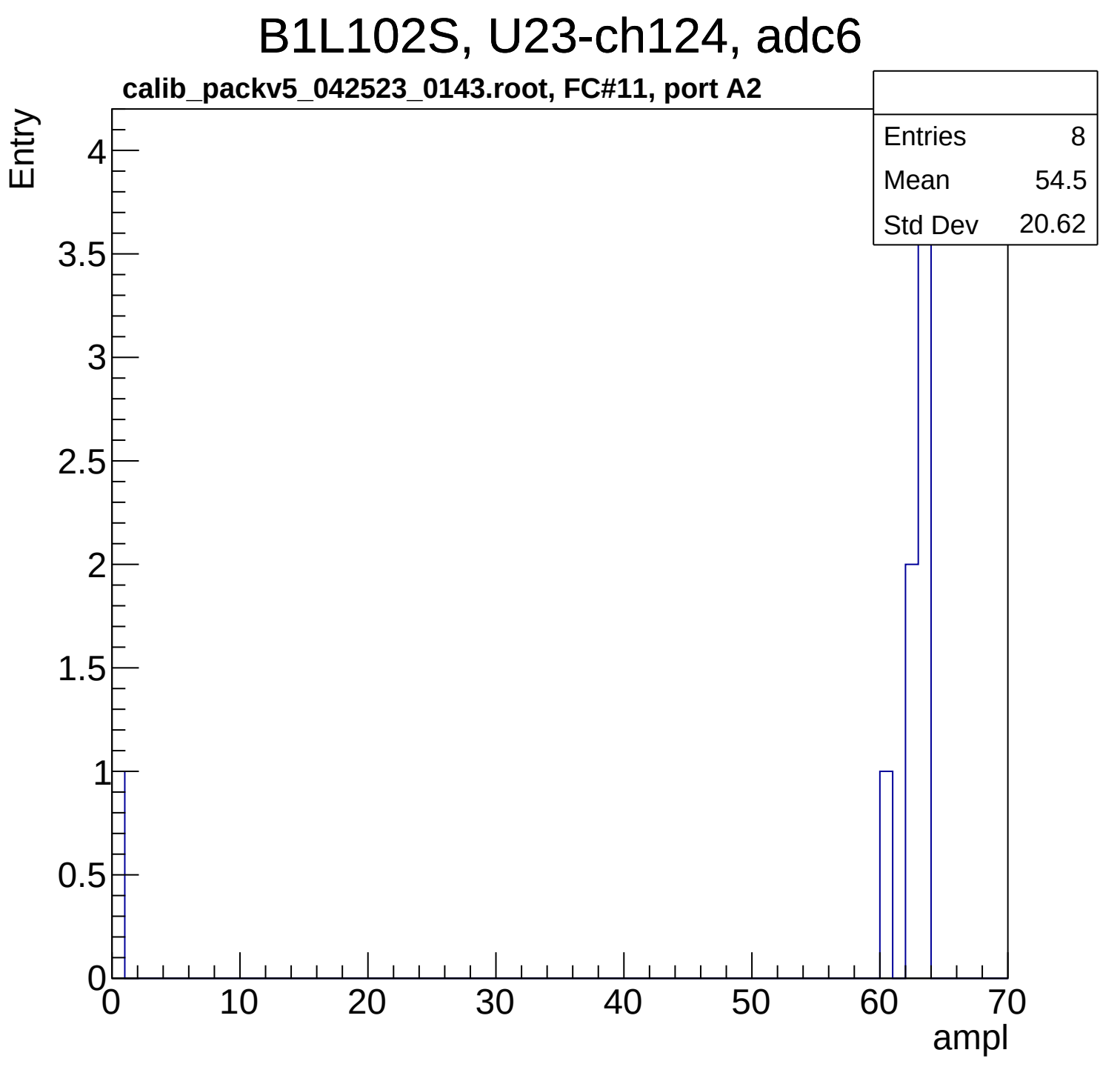

B1L102S, U23-ch124, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 35.51
Std Dev 3.924
Gaus mean : 36.4989
Gaus Width: 3.9333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

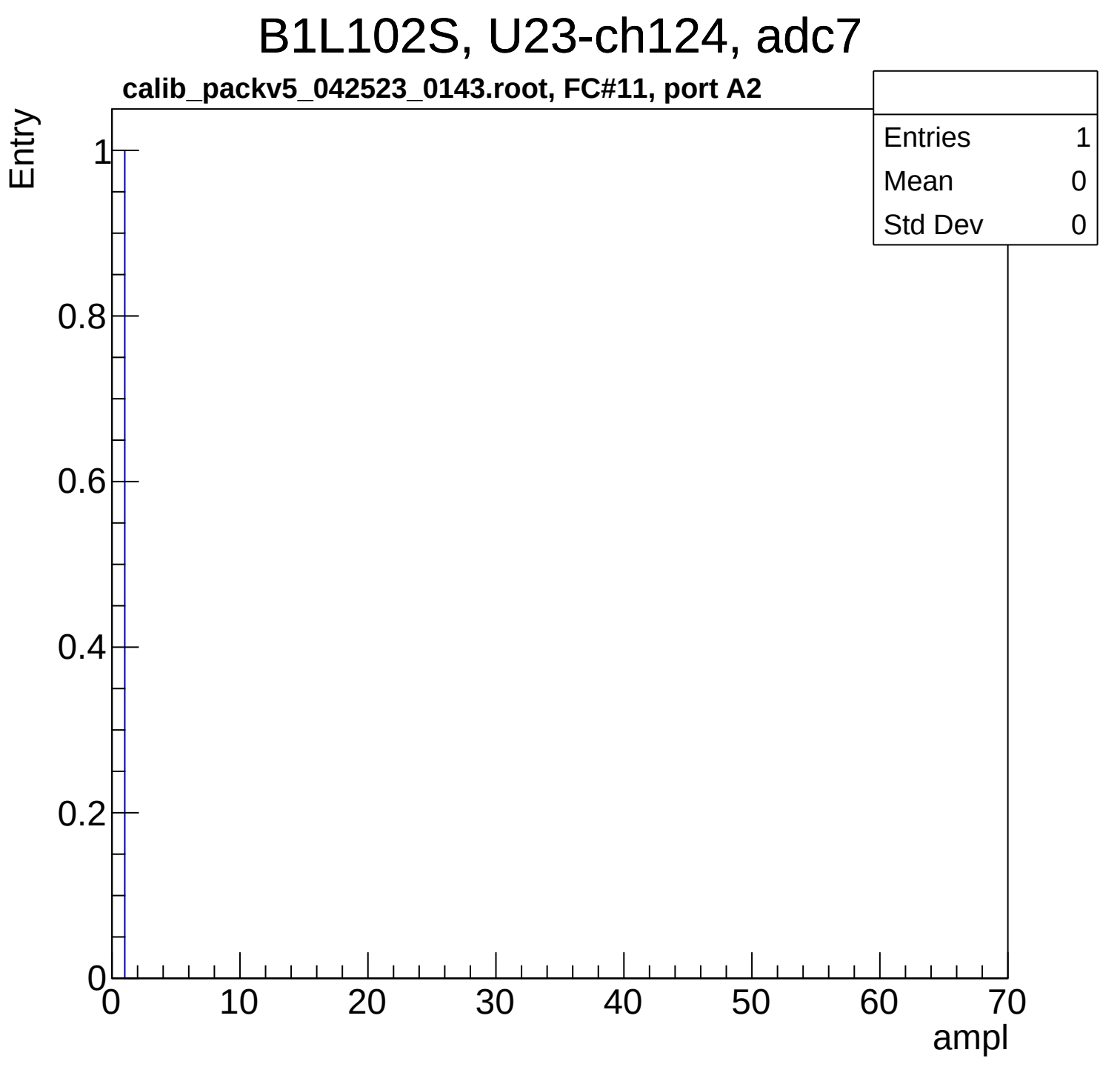

B1L102S, U23-ch124, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 42.42
Std Dev 4.244
Gaus mean : 42.5063
Gaus Width: 4.1463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

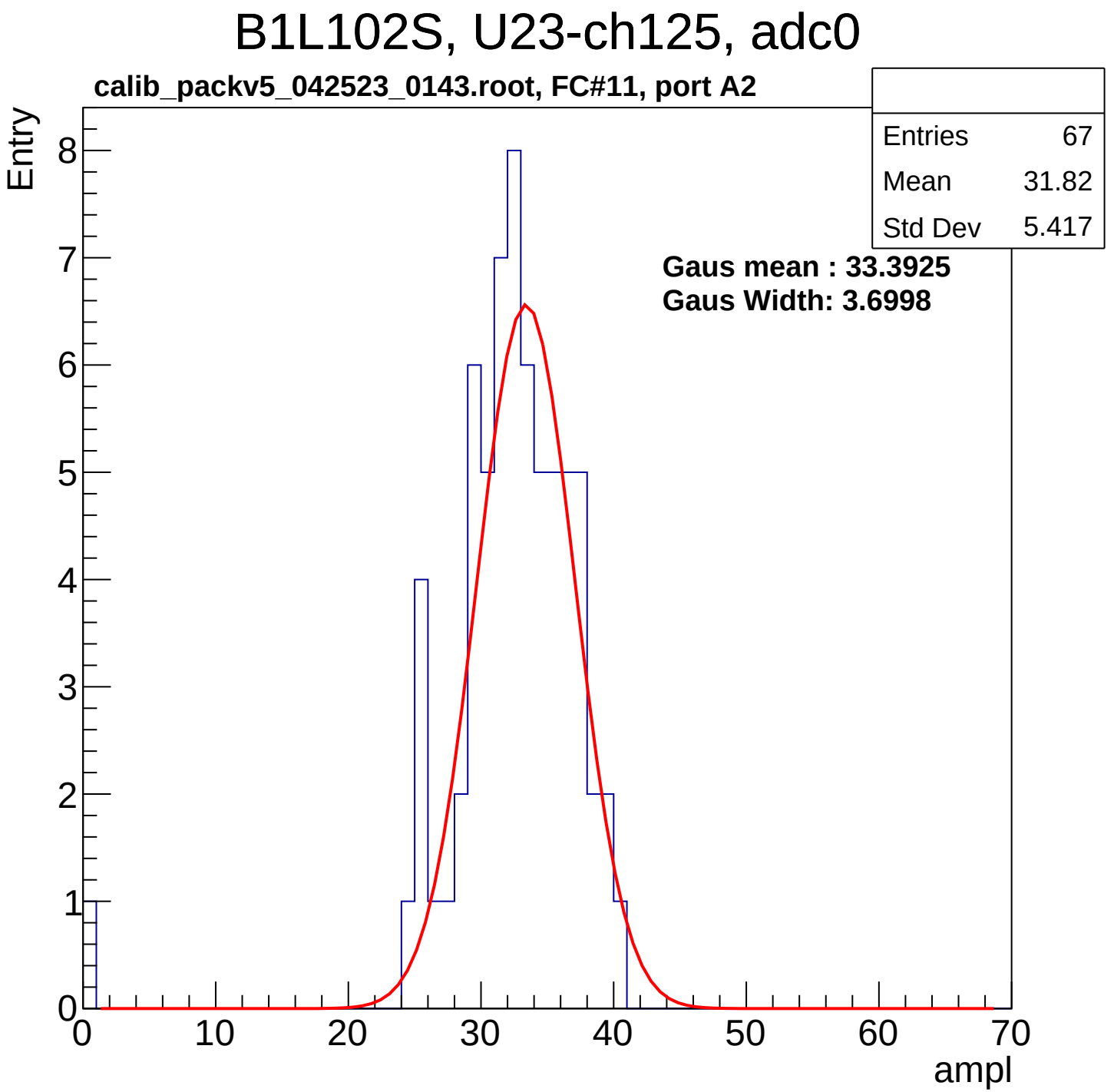

B1L102S, U23-ch124, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 49.65
Std Dev 4.068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

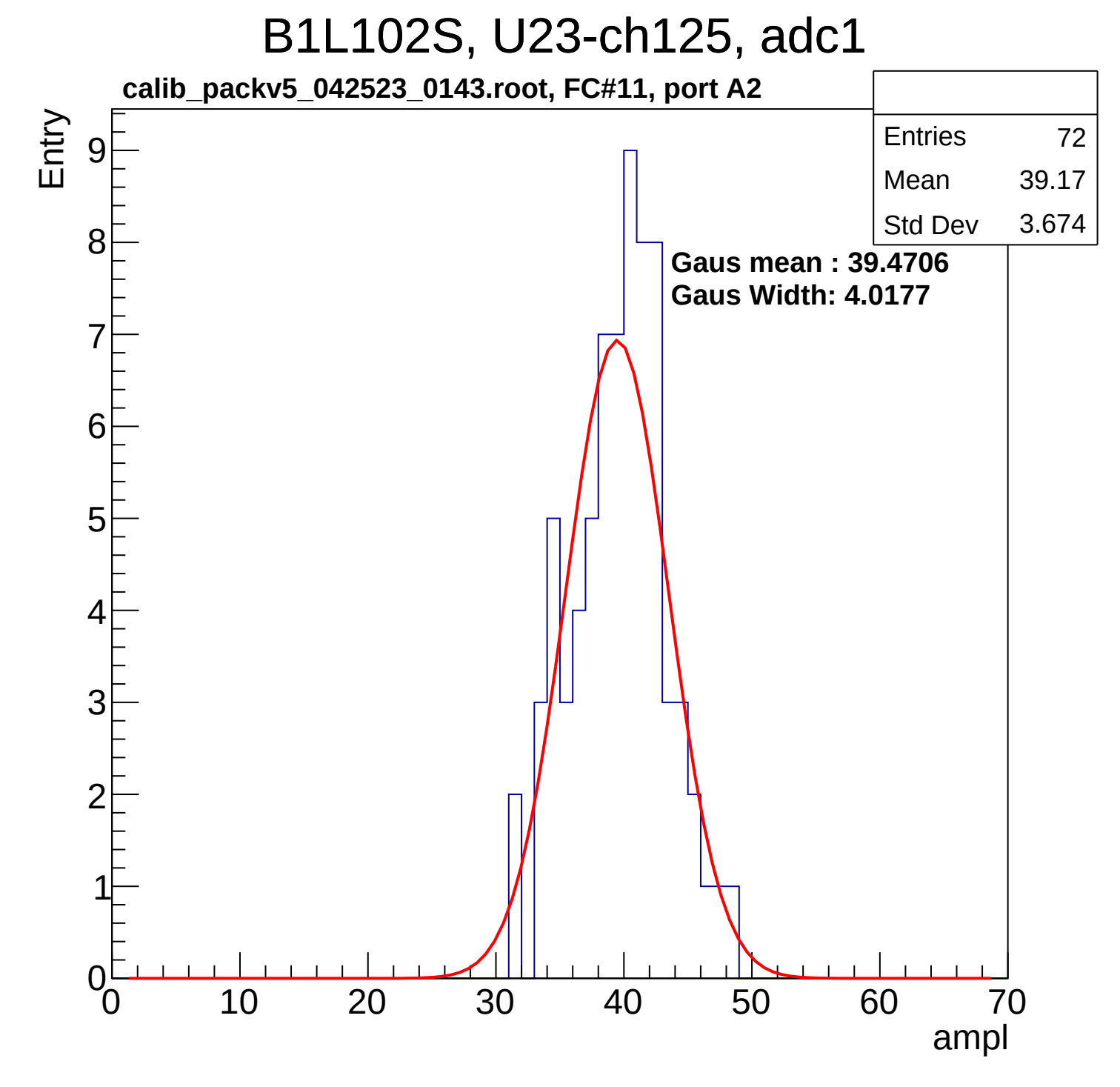

B1L102S, U23-ch124, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 55.4
Std Dev 3.065
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

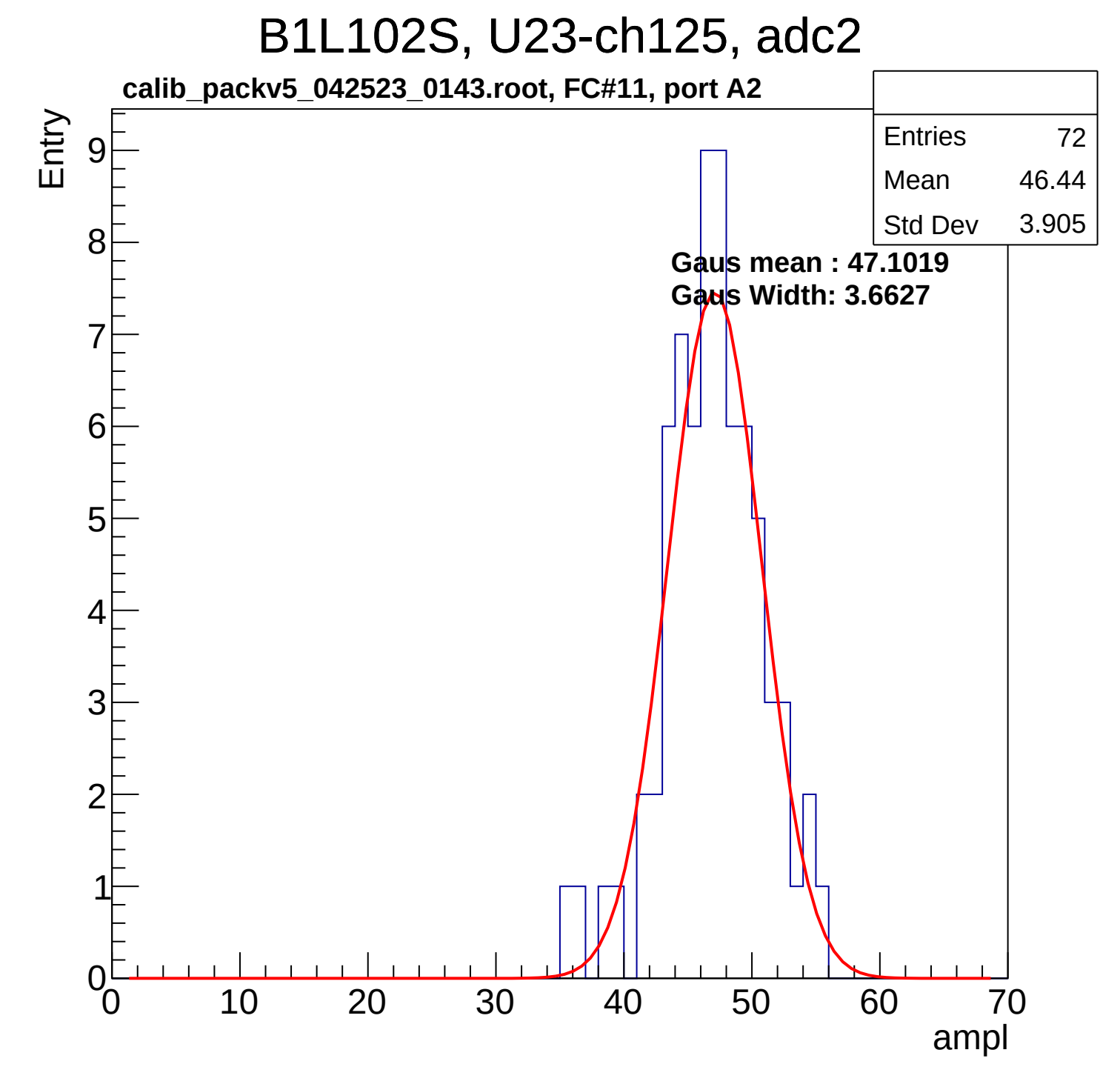

B1L102S, U23-ch124, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 59.73
Std Dev 2.628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

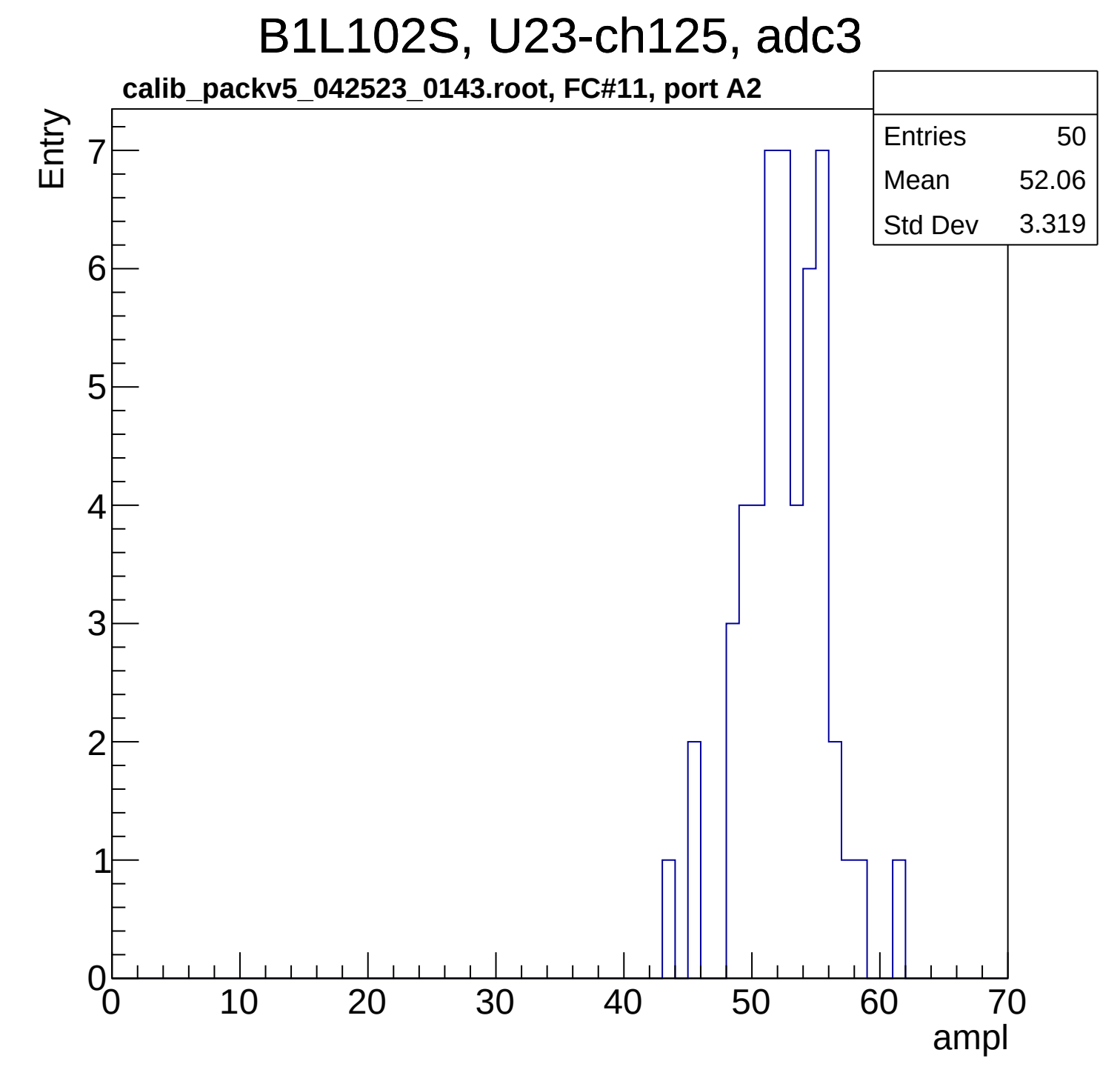

B1L102S, U23-ch124, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

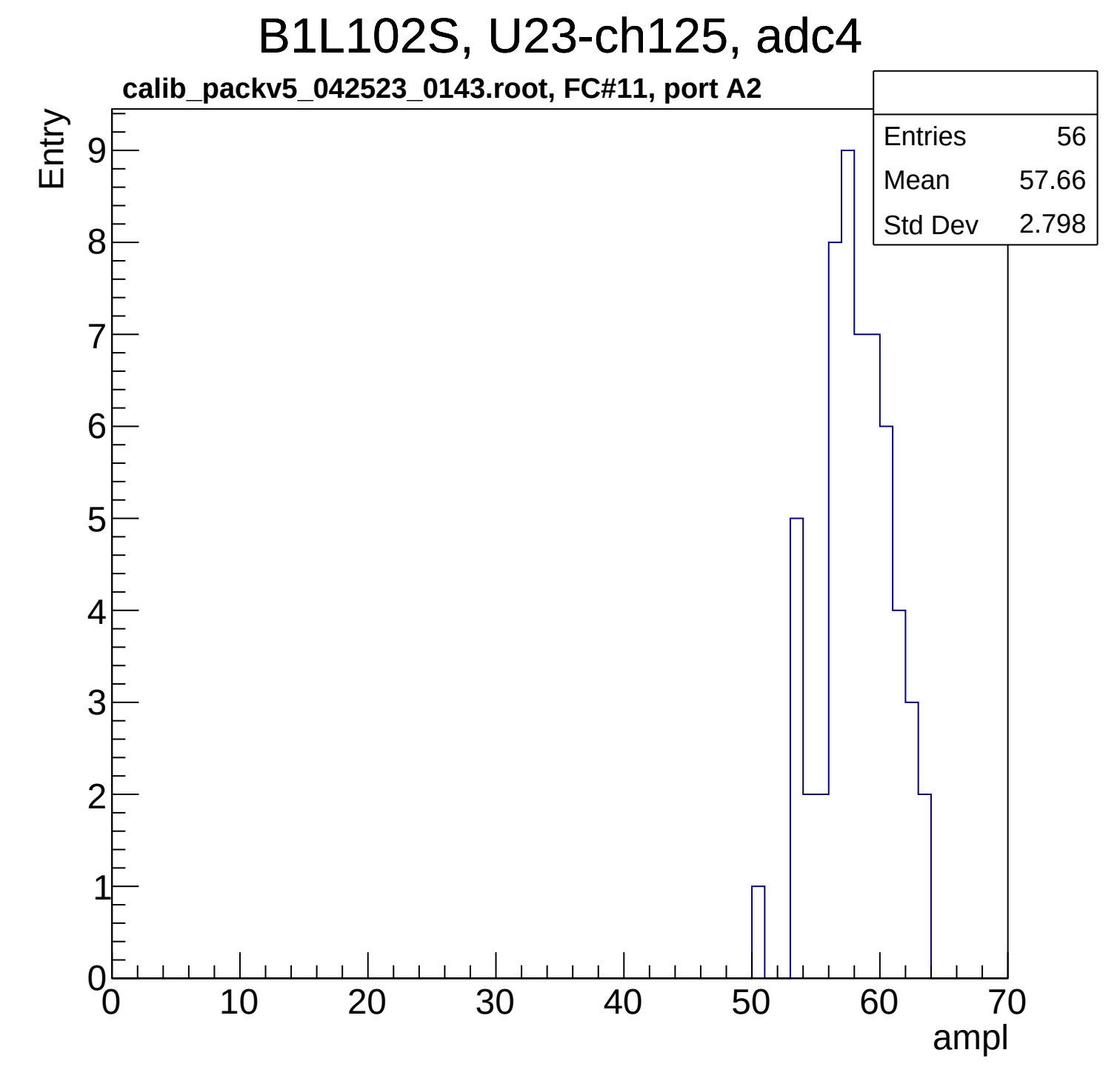

B1L102S, U23-ch124, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

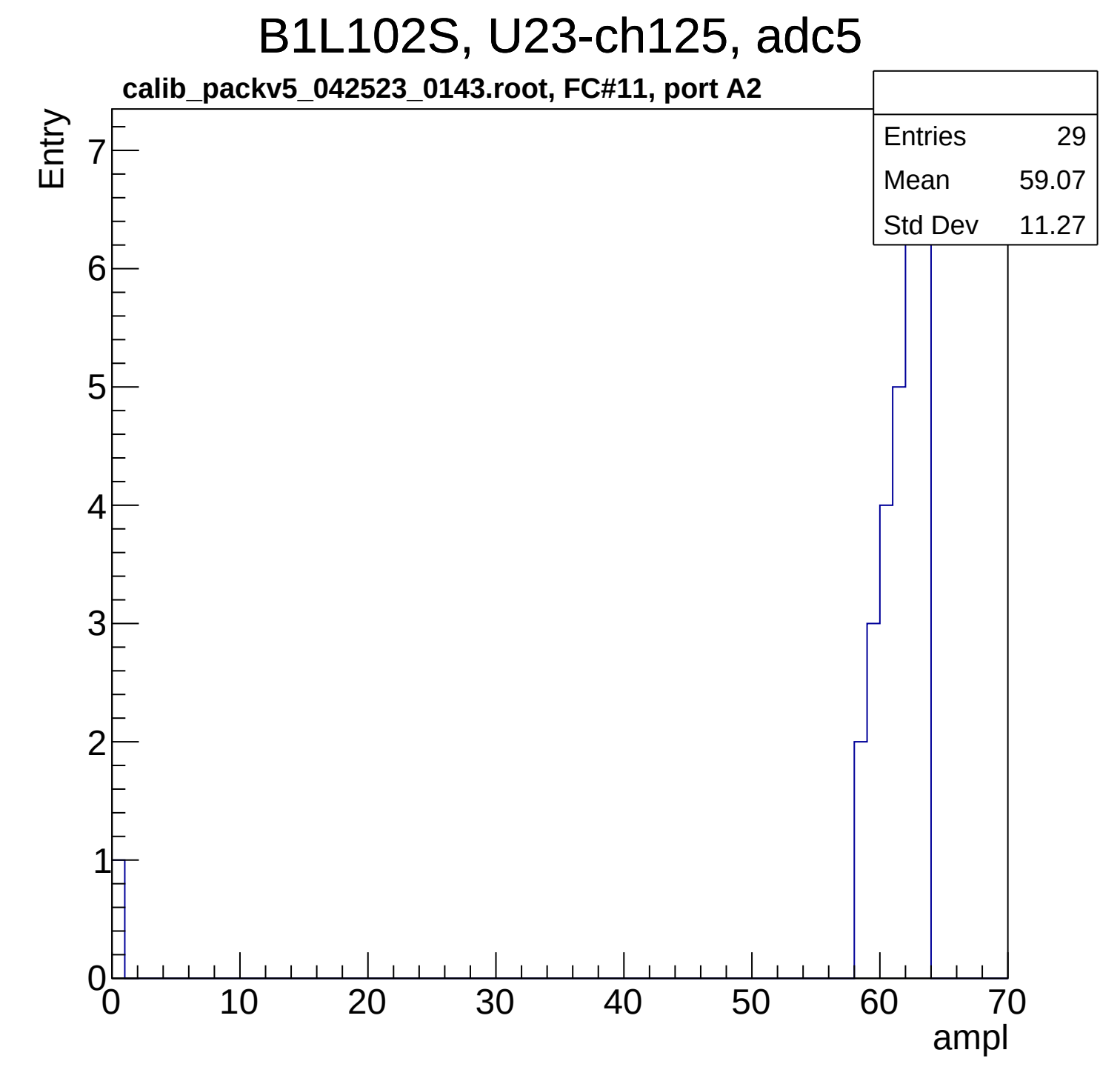

B1L102S, U23-ch125, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 31.82
Std Dev 5.417
Gaus mean : 33.3925
Gaus Width: 3.6998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

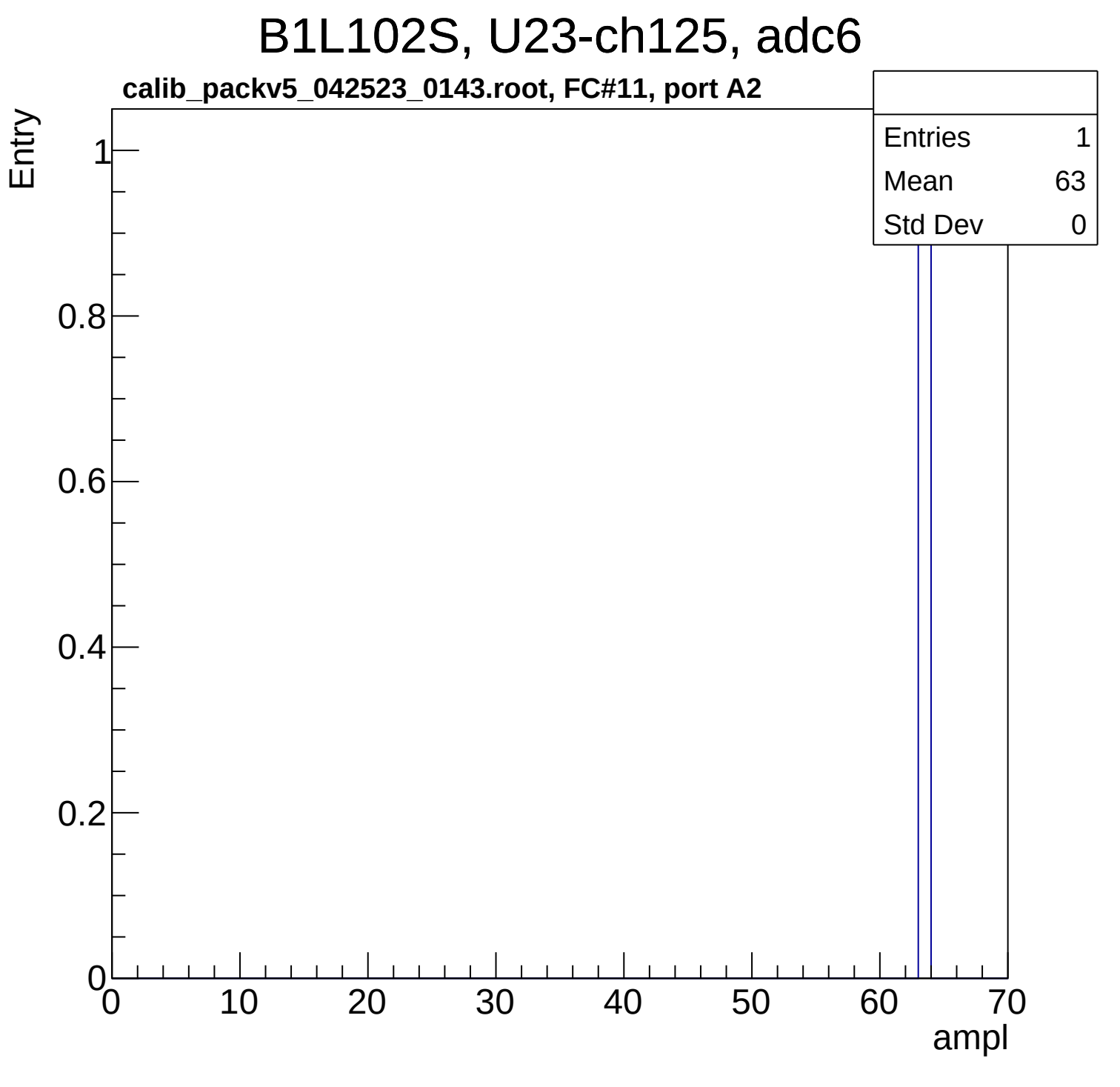

B1L102S, U23-ch125, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 39.17
Std Dev 3.674
Gaus mean : 39.4706
Gaus Width: 4.0177
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

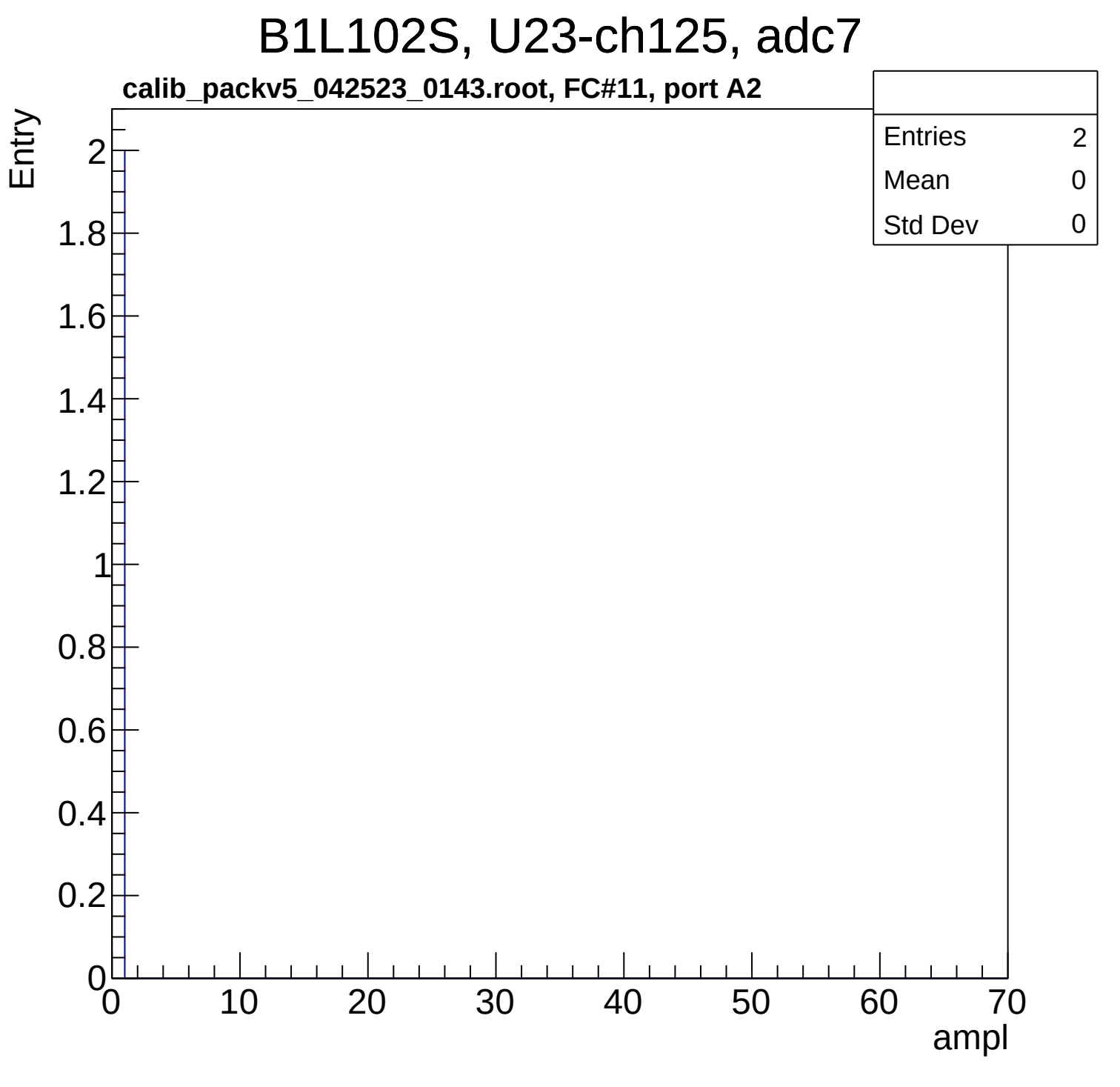

B1L102S, U23-ch125, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 46.44
Std Dev 3.905
Gaus mean : 47.1019
Gaus Width: 3.6627
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

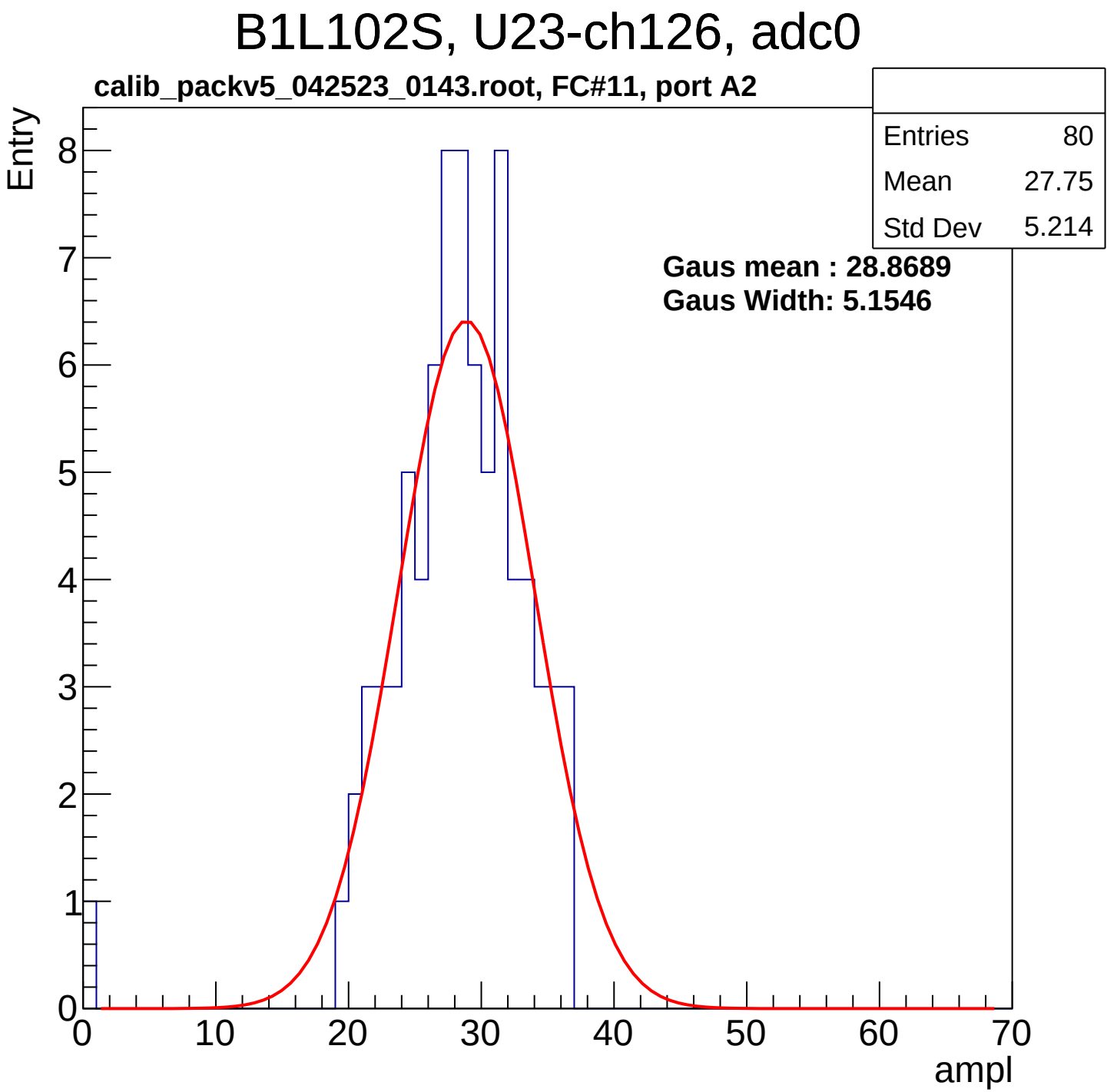

B1L102S, U23-ch125, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 52.06
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

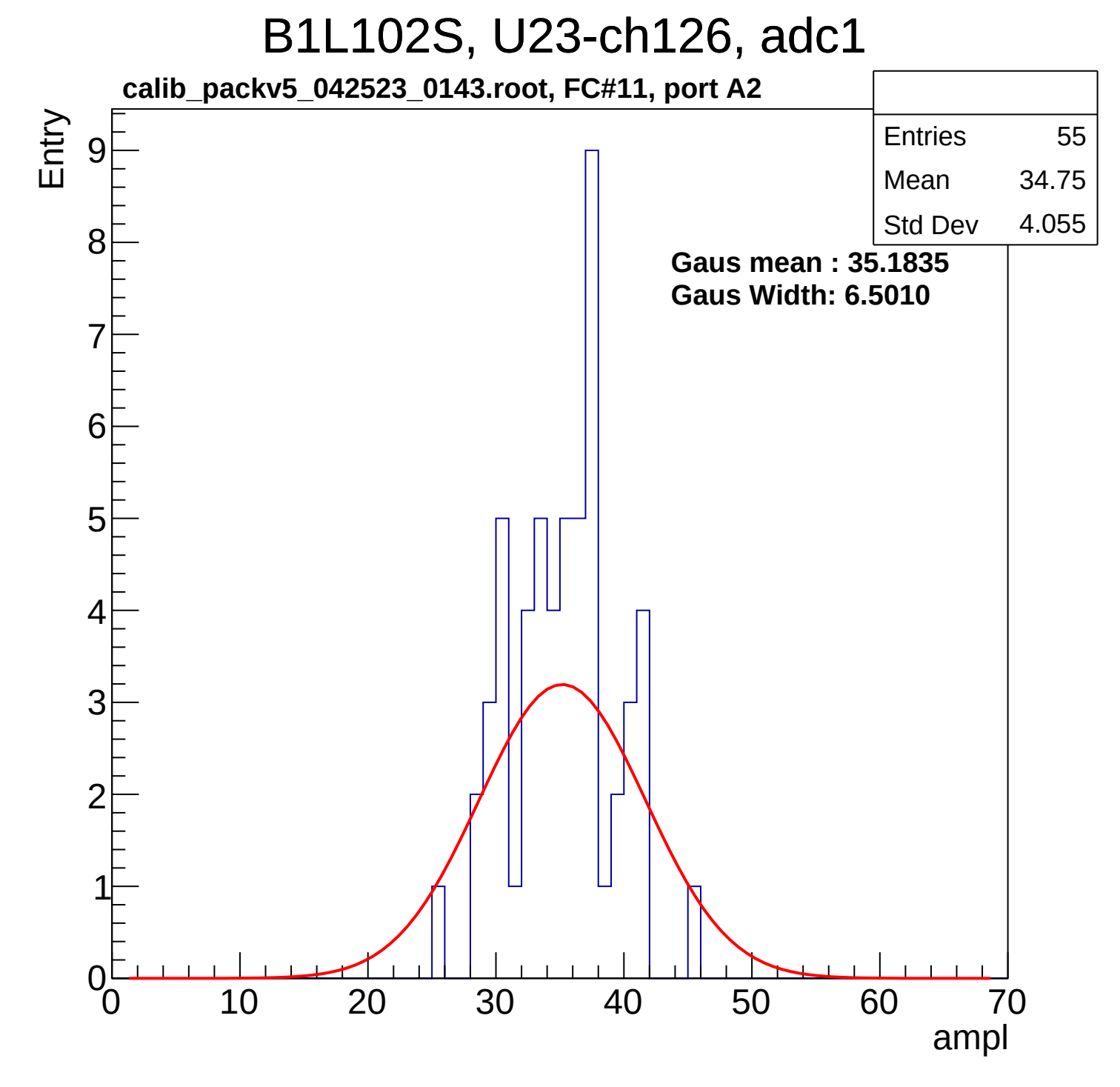

B1L102S, U23-ch125, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 57.66
Std Dev 2.798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

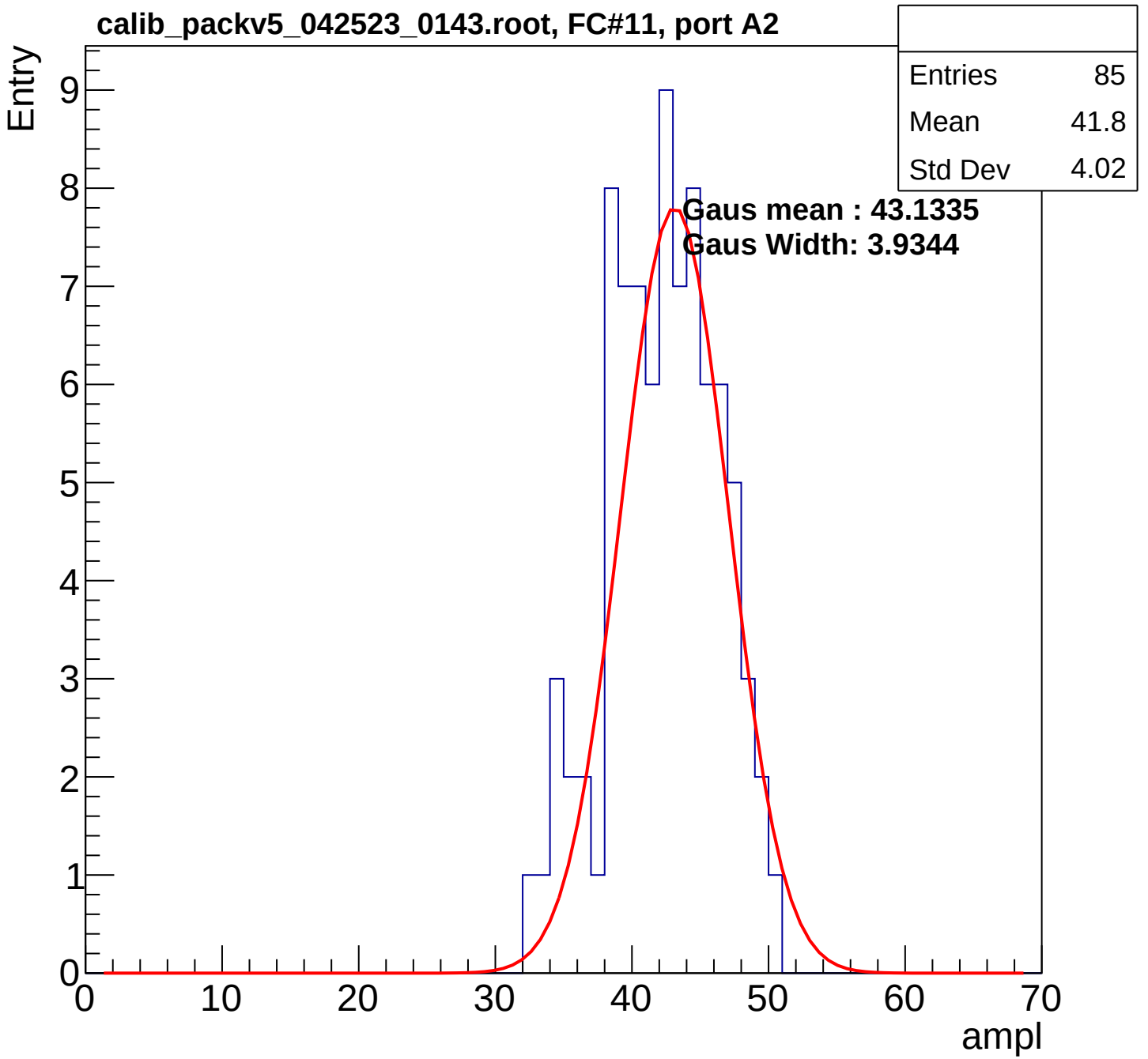

B1L102S, U23-ch125, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 29
Mean 59.07
Std Dev 11.27
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

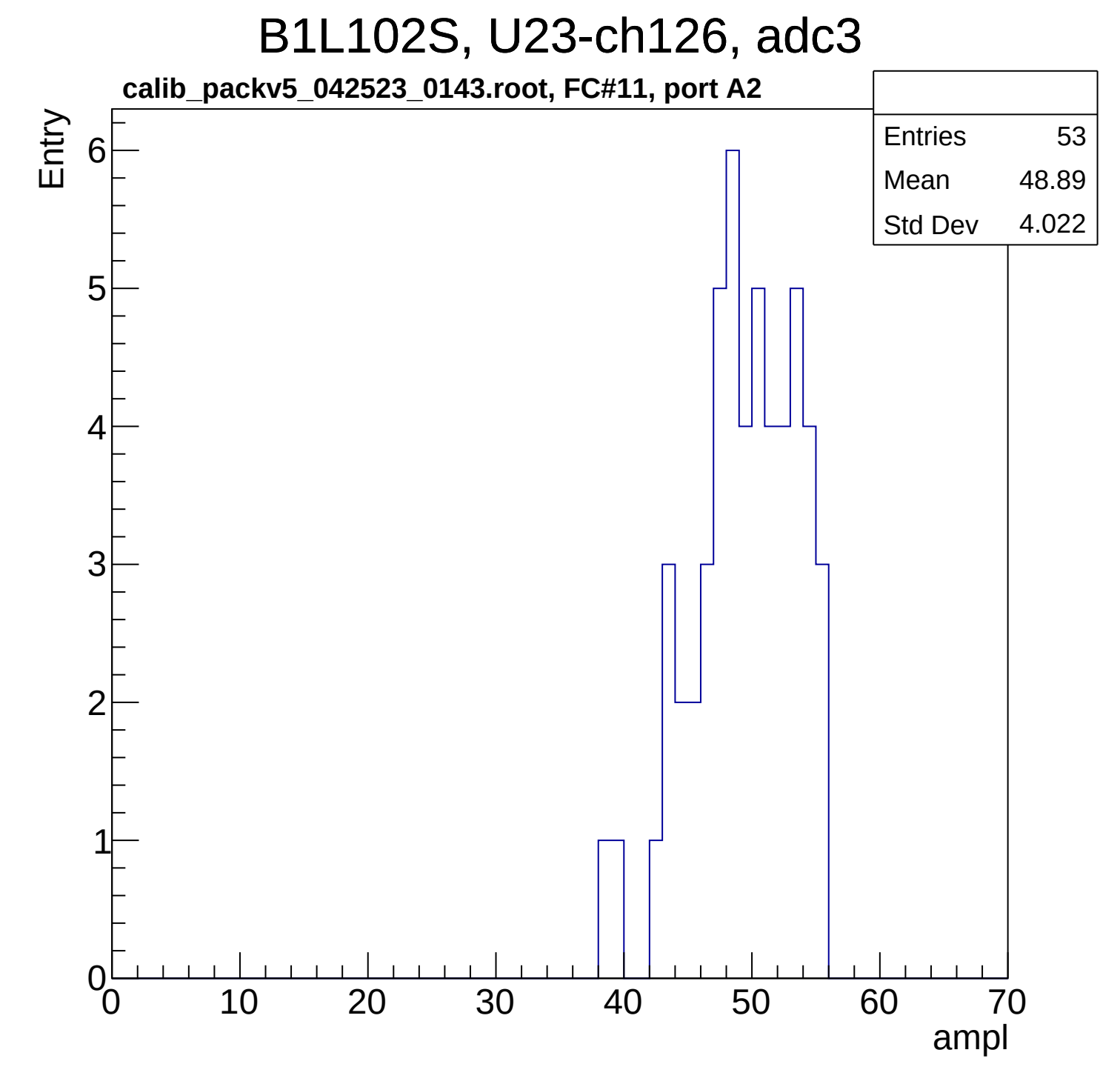

B1L102S, U23-ch125, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

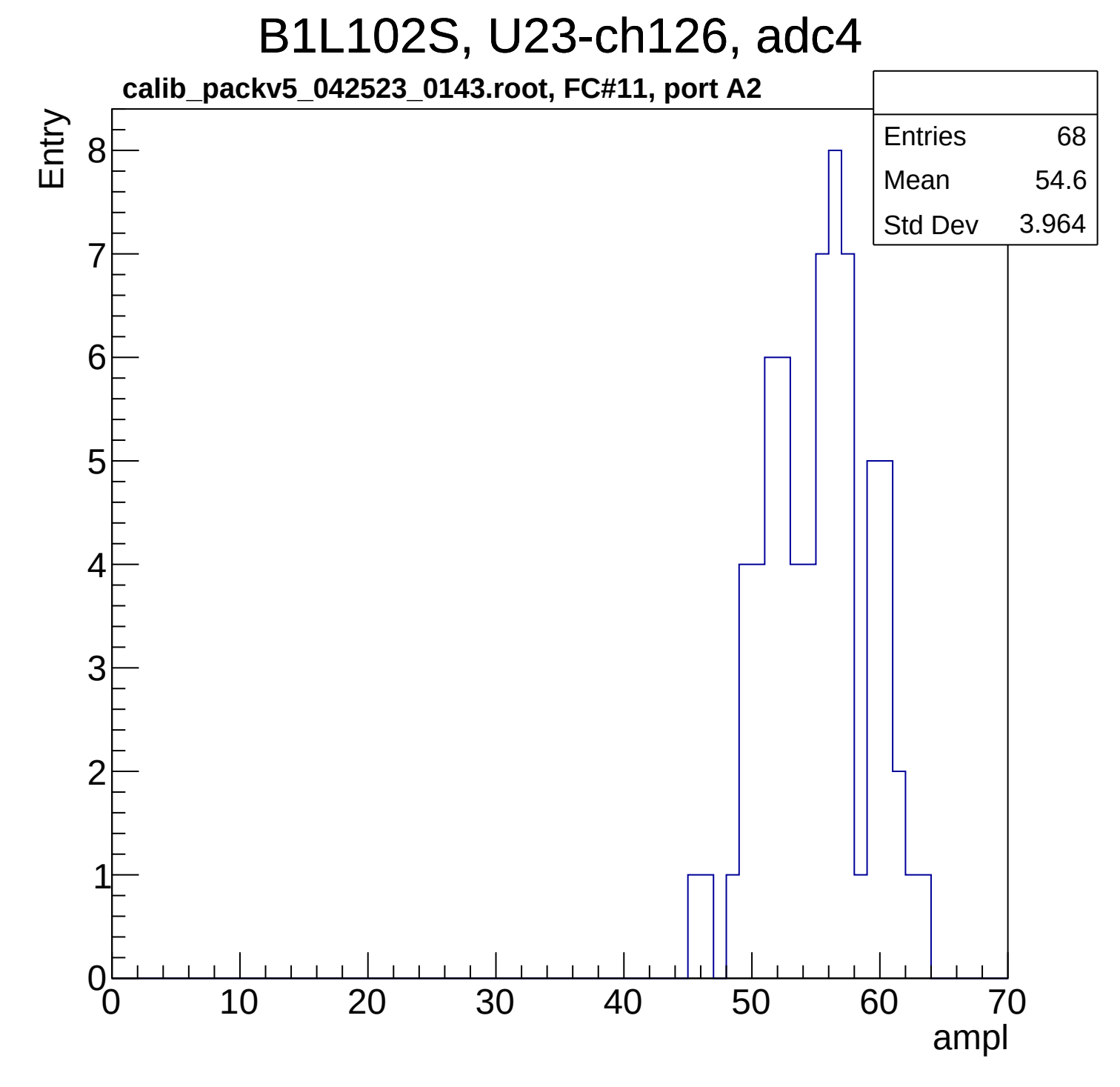

B1L102S, U23-ch125, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

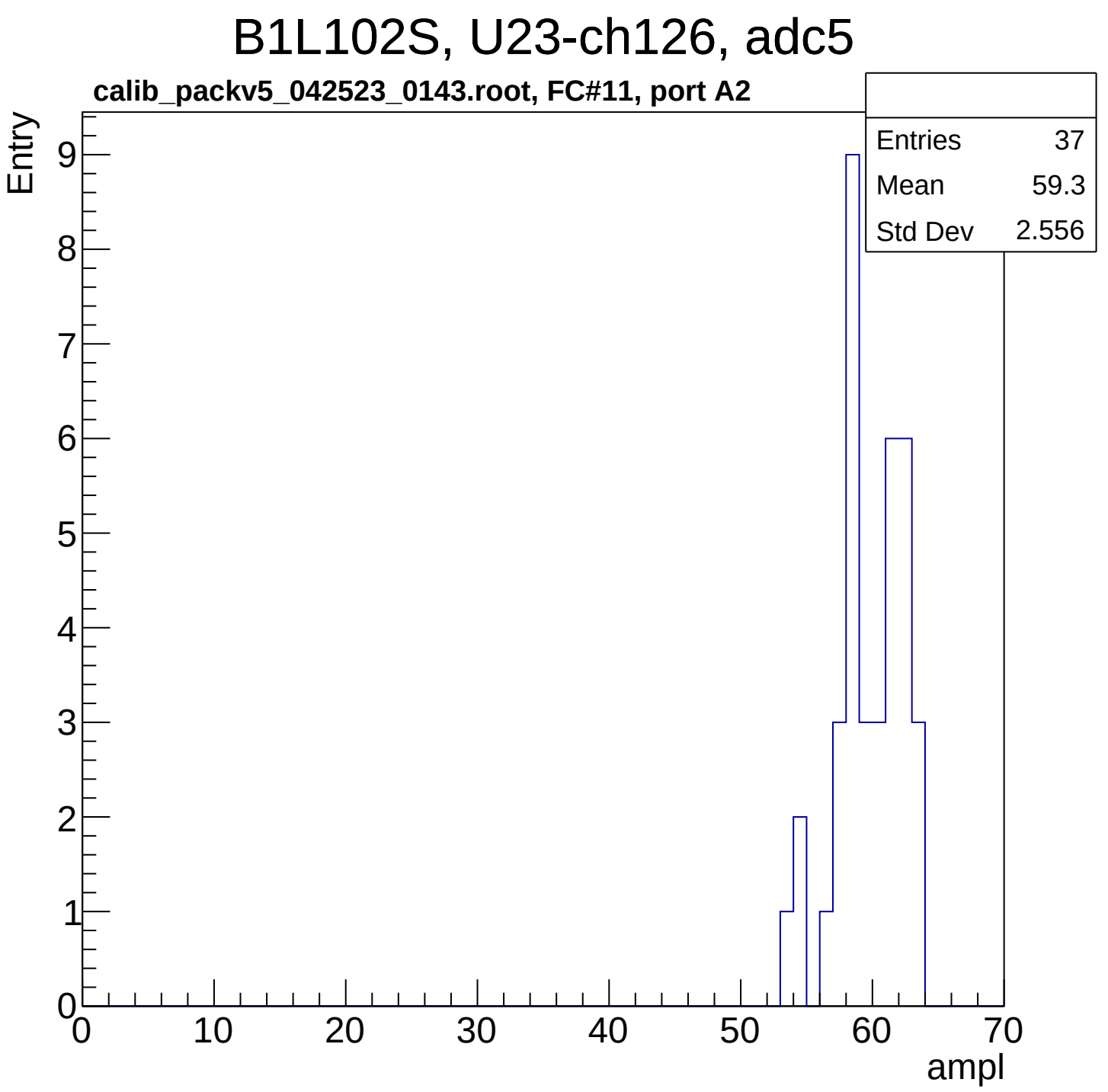

B1L102S, U23-ch126, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 27.75
Std Dev 5.214
Gaus mean : 28.8689
Gaus Width: 5.1546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

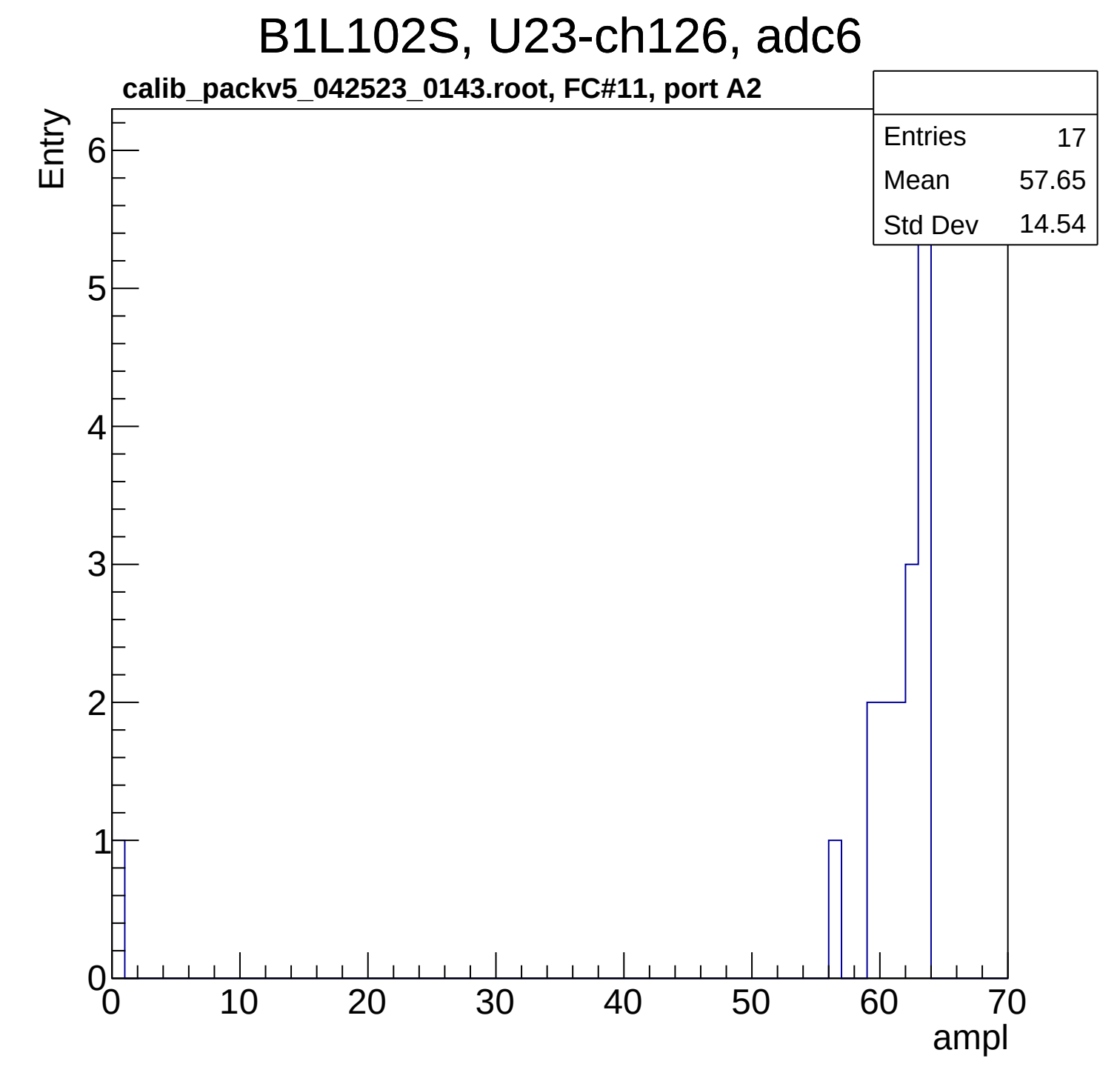

B1L102S, U23-ch126, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 34.75
Std Dev 4.055
Gaus mean : 35.1835
Gaus Width: 6.5010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

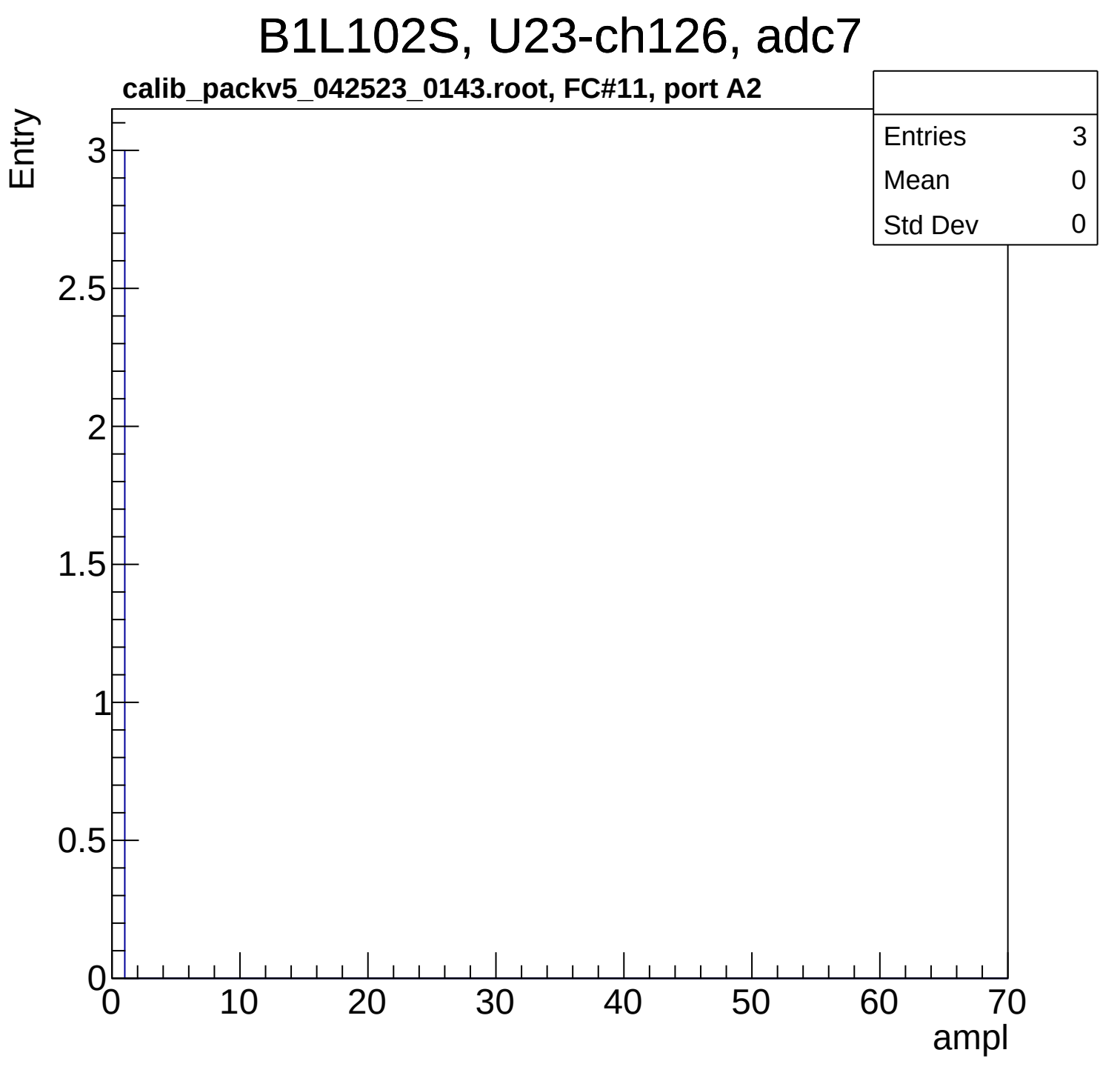

B1L102S, U23-ch126, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 41.8
Std Dev 4.02
Gaus mean : 43.1335
Gaus Width: 3.9344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

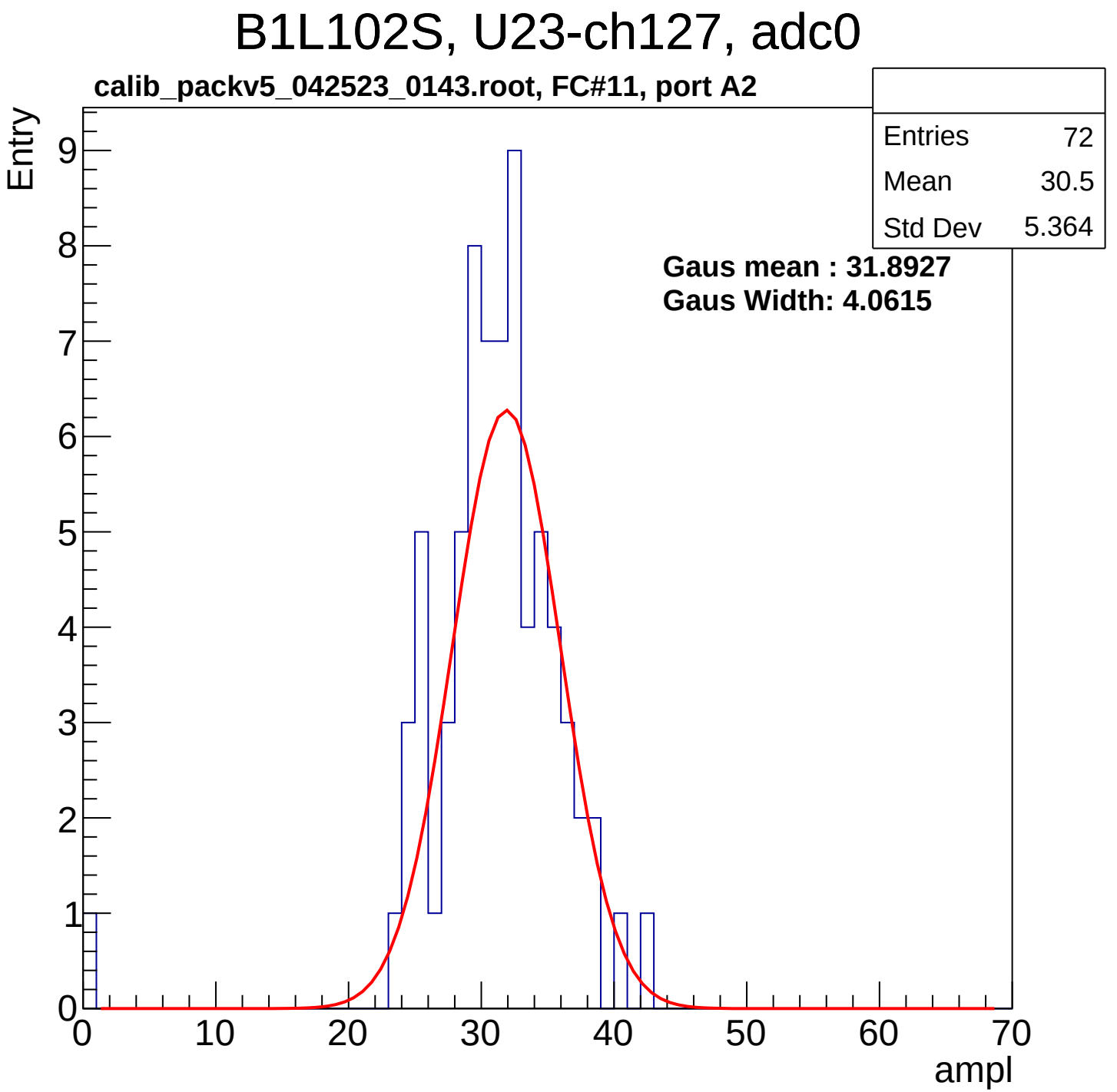

B1L102S, U23-ch126, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 48.89
Std Dev 4.022
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

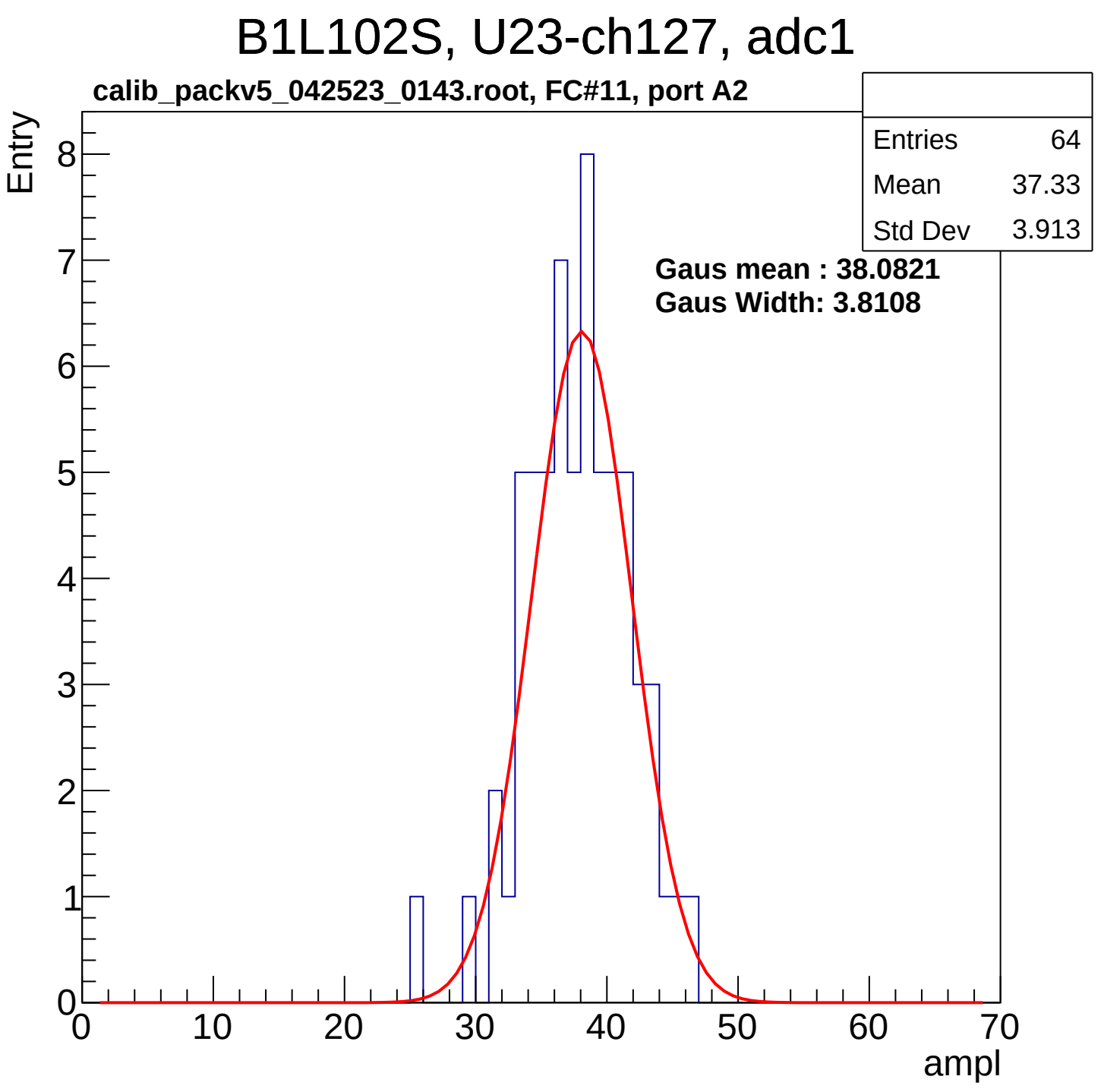

B1L102S, U23-ch126, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 54.6
Std Dev 3.964
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

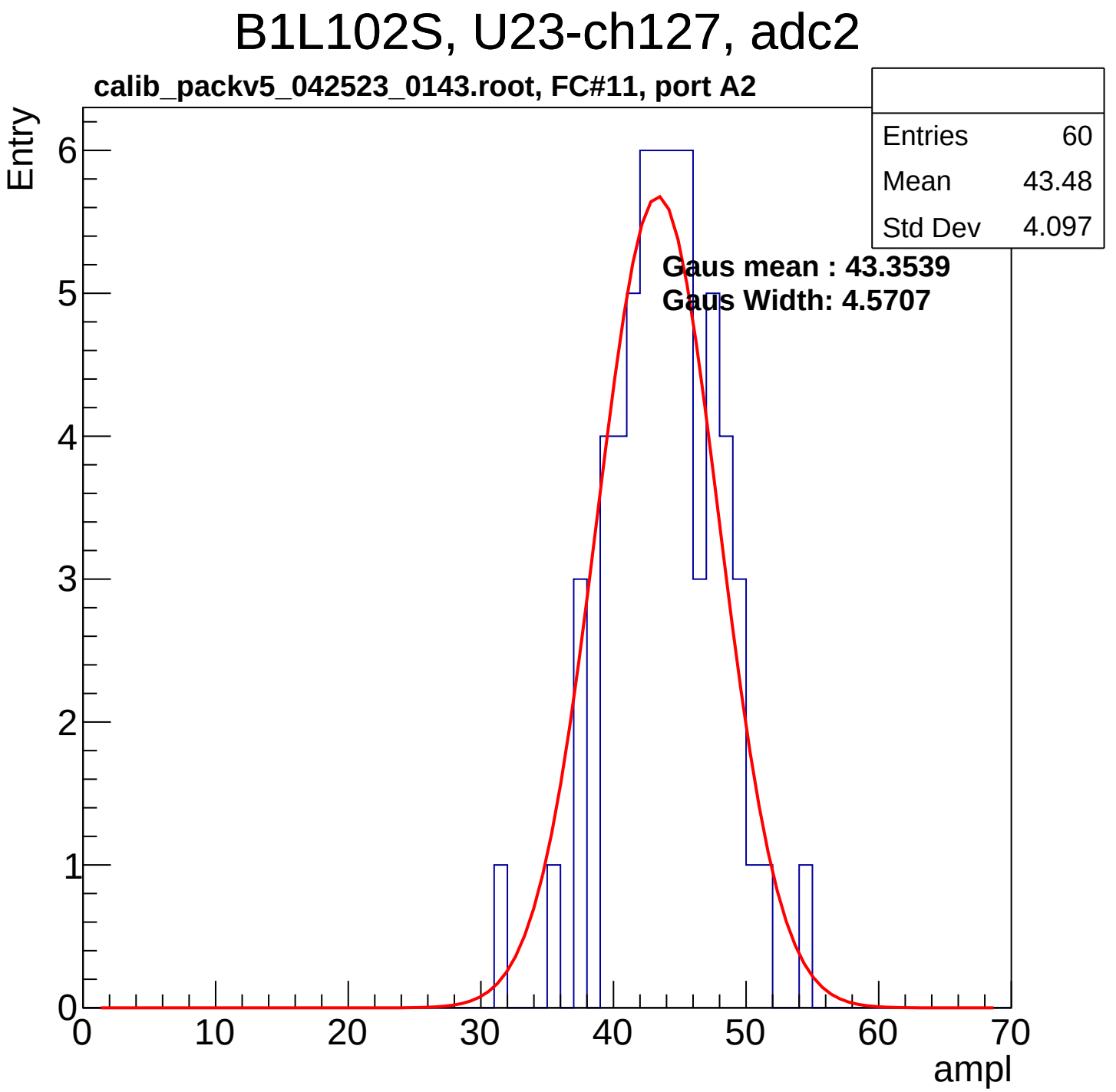

B1L102S, U23-ch126, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 59.3
Std Dev 2.556
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

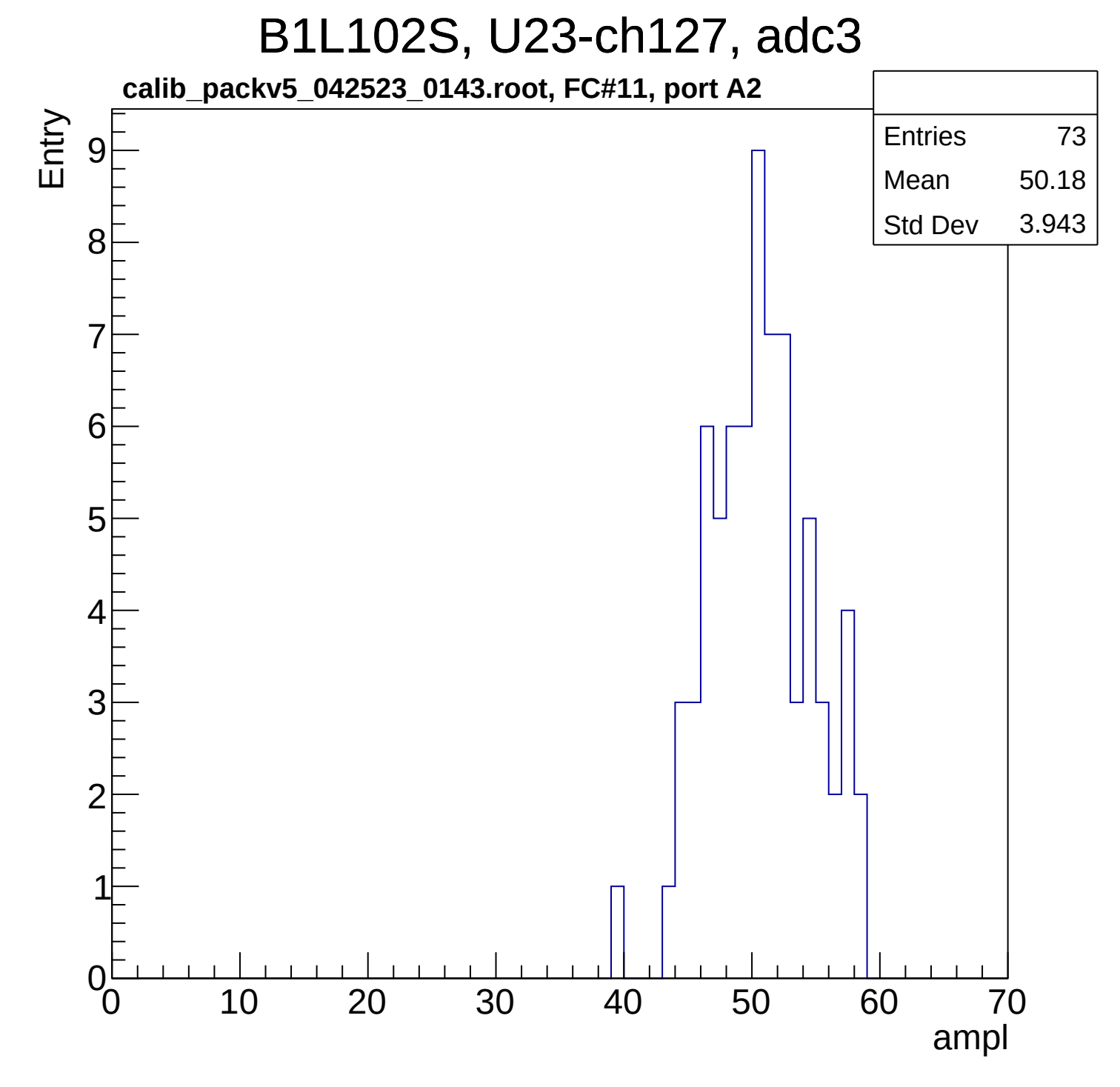

B1L102S, U23-ch126, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 17
Mean 57.65
Std Dev 14.54
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

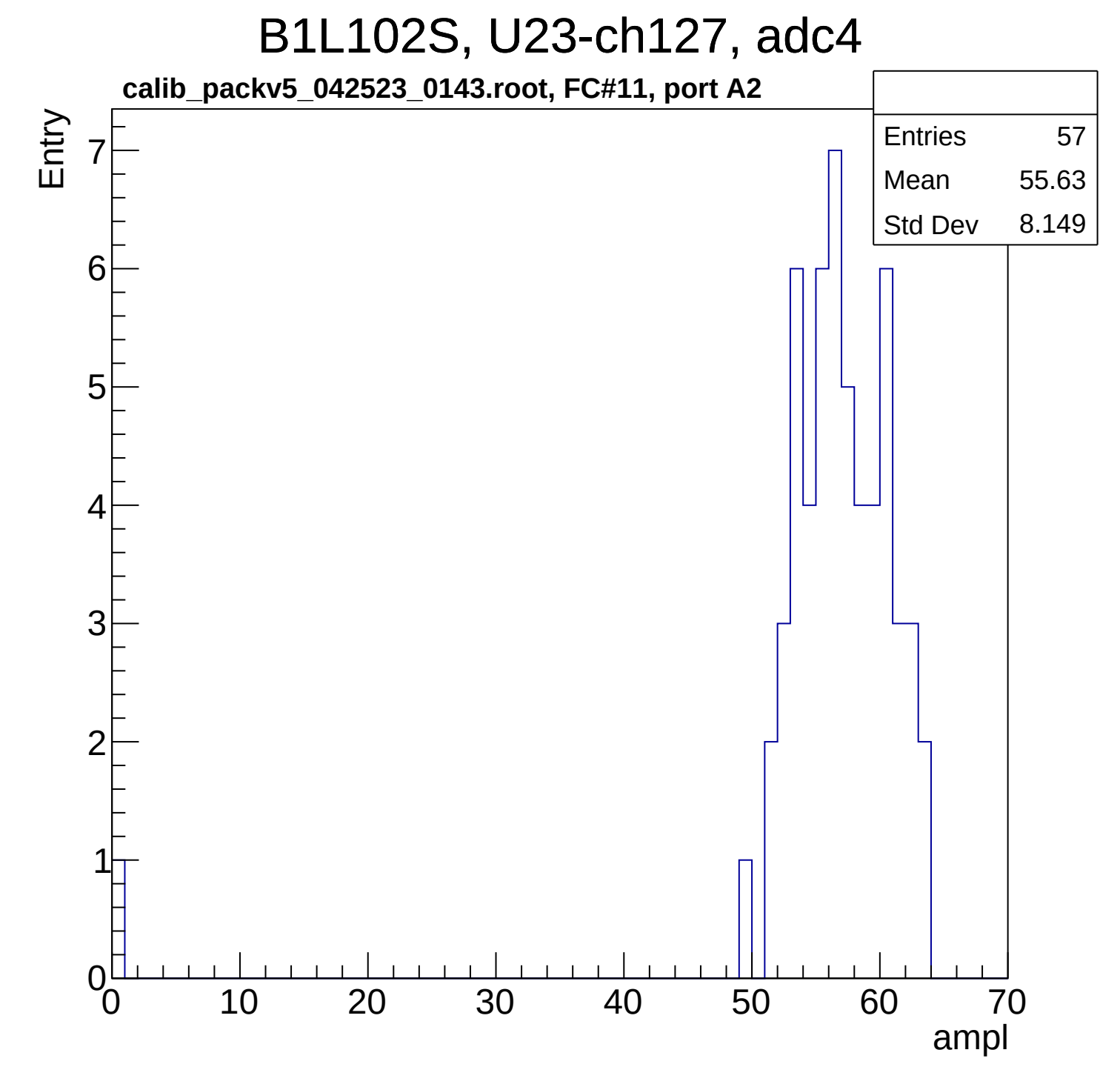

B1L102S, U23-ch126, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

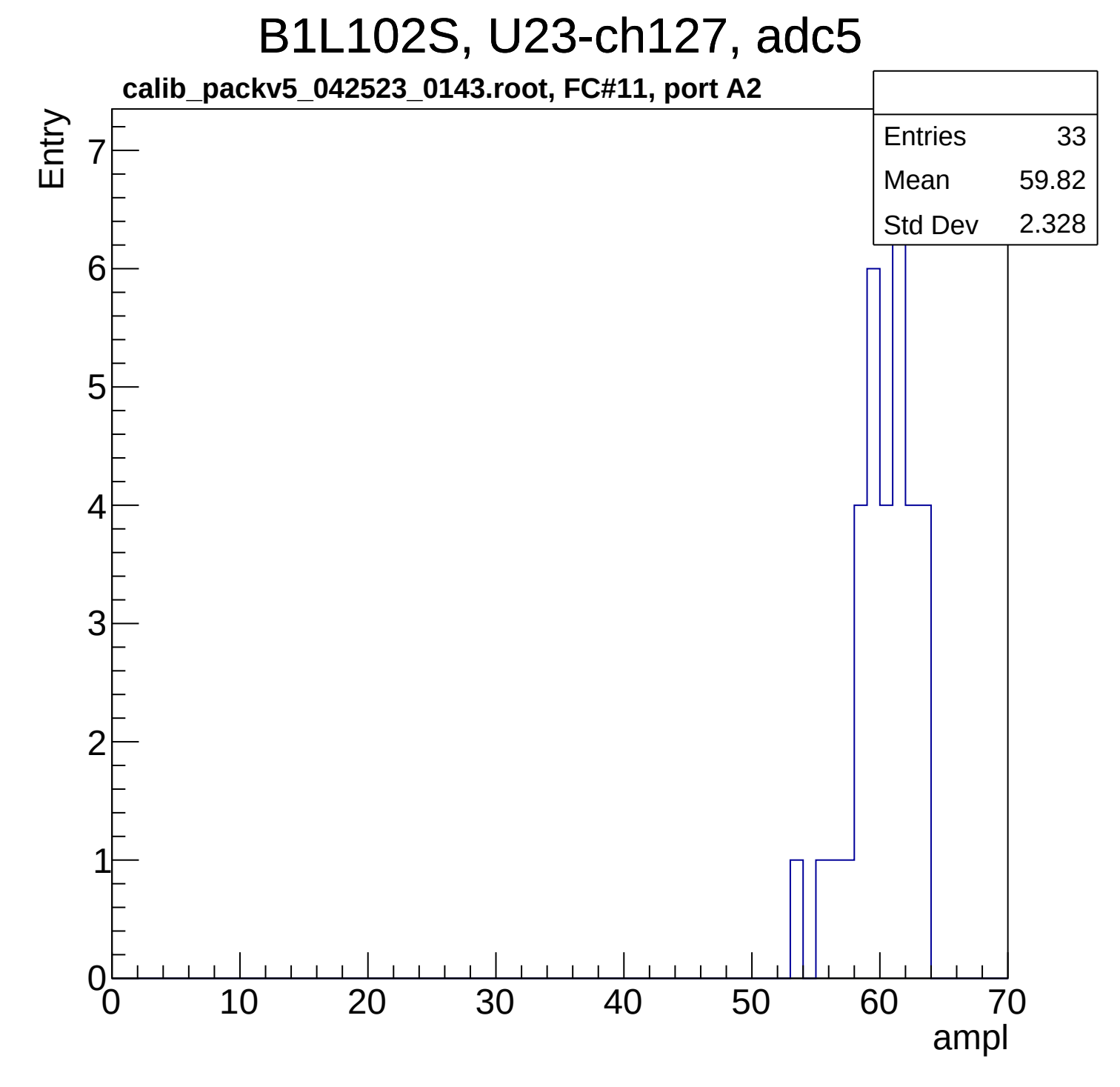

B1L102S, U23-ch127, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 30.5
Std Dev 5.364
Gaus mean : 31.8927
Gaus Width: 4.0615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

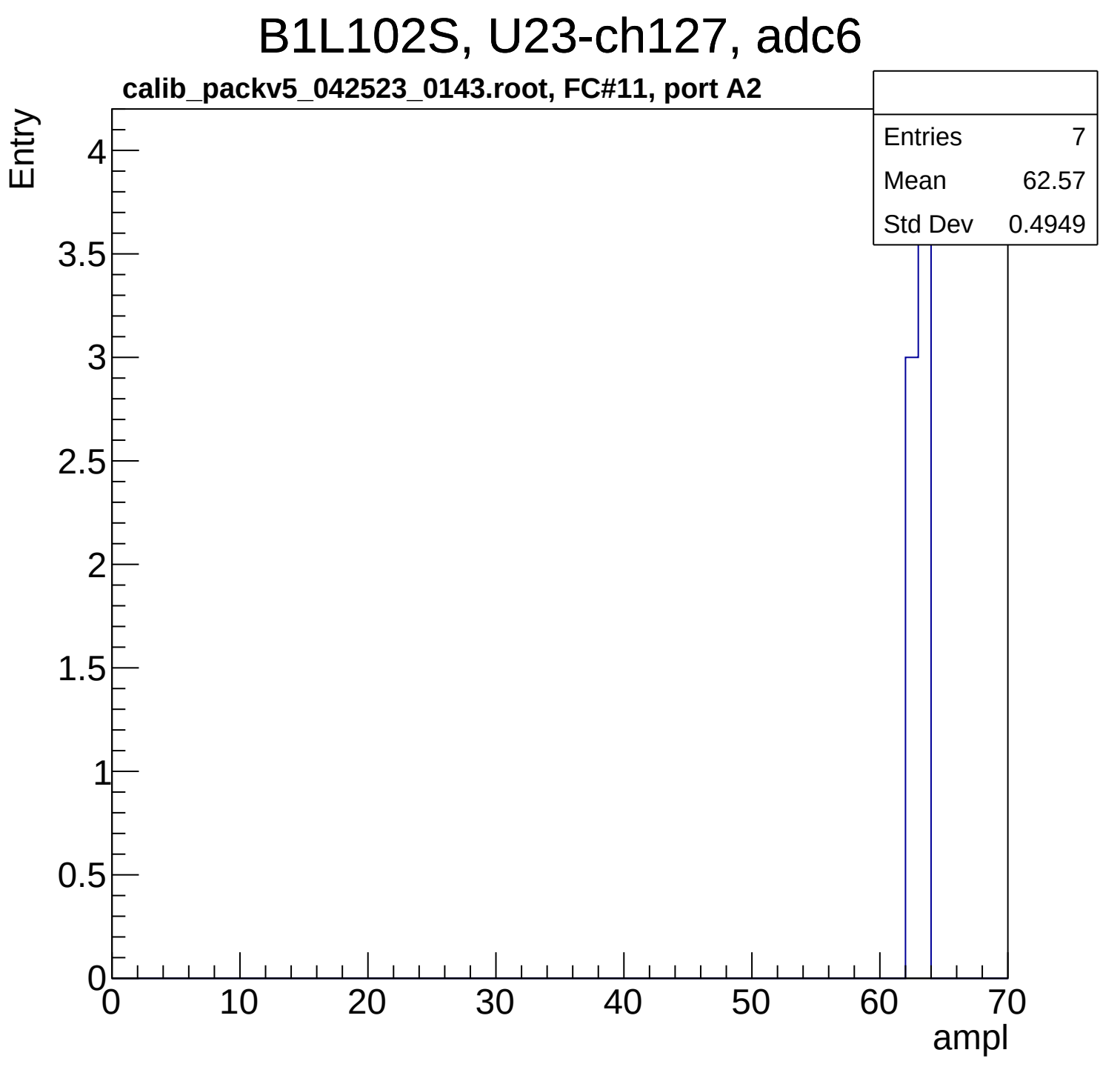

B1L102S, U23-ch127, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 37.33
Std Dev 3.913
Gaus mean : 38.0821
Gaus Width: 3.8108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

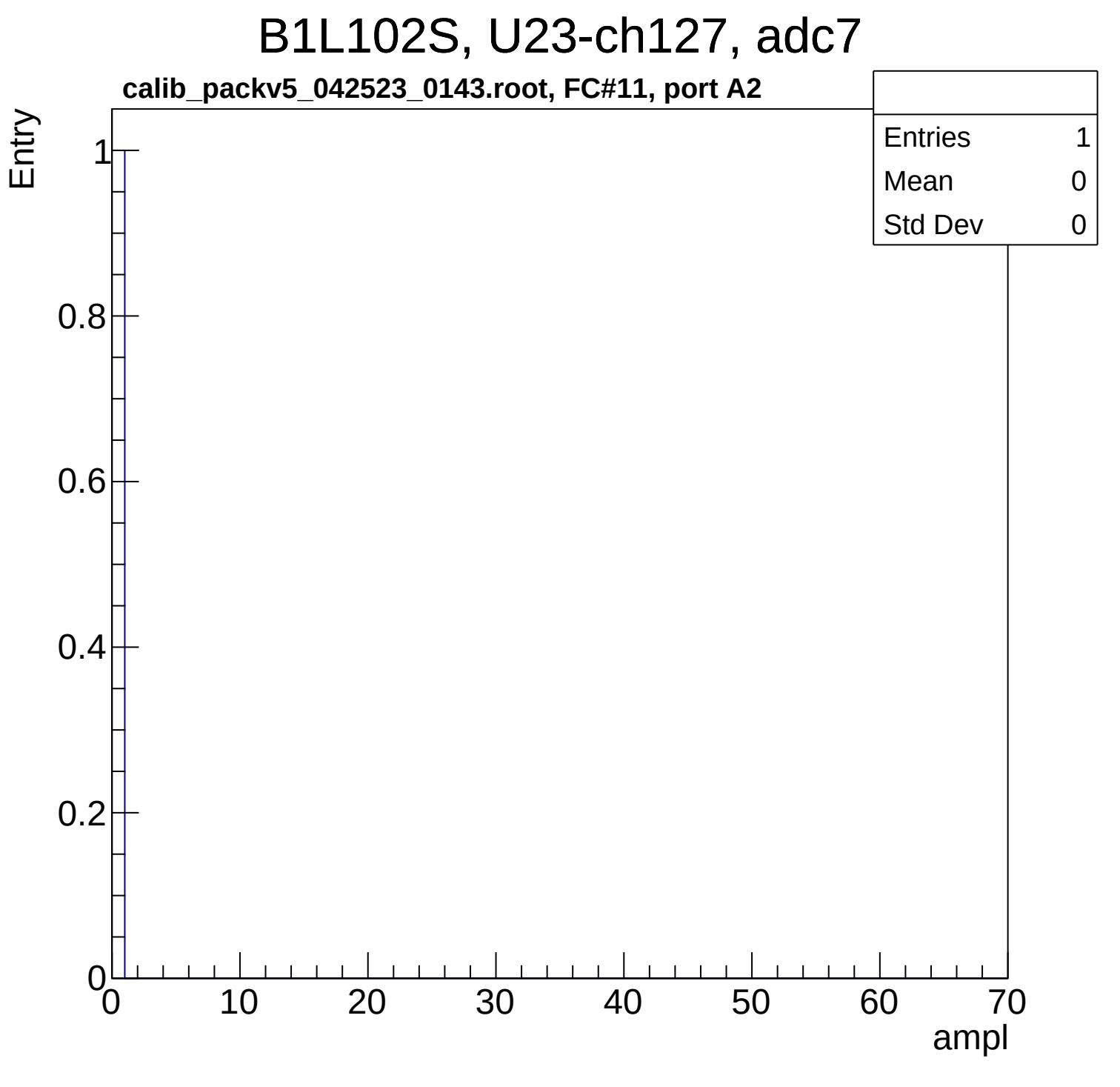

B1L102S, U23-ch127, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 43.48
Std Dev 4.097
Gaus mean : 43.3539
Gaus Width: 4.5707
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

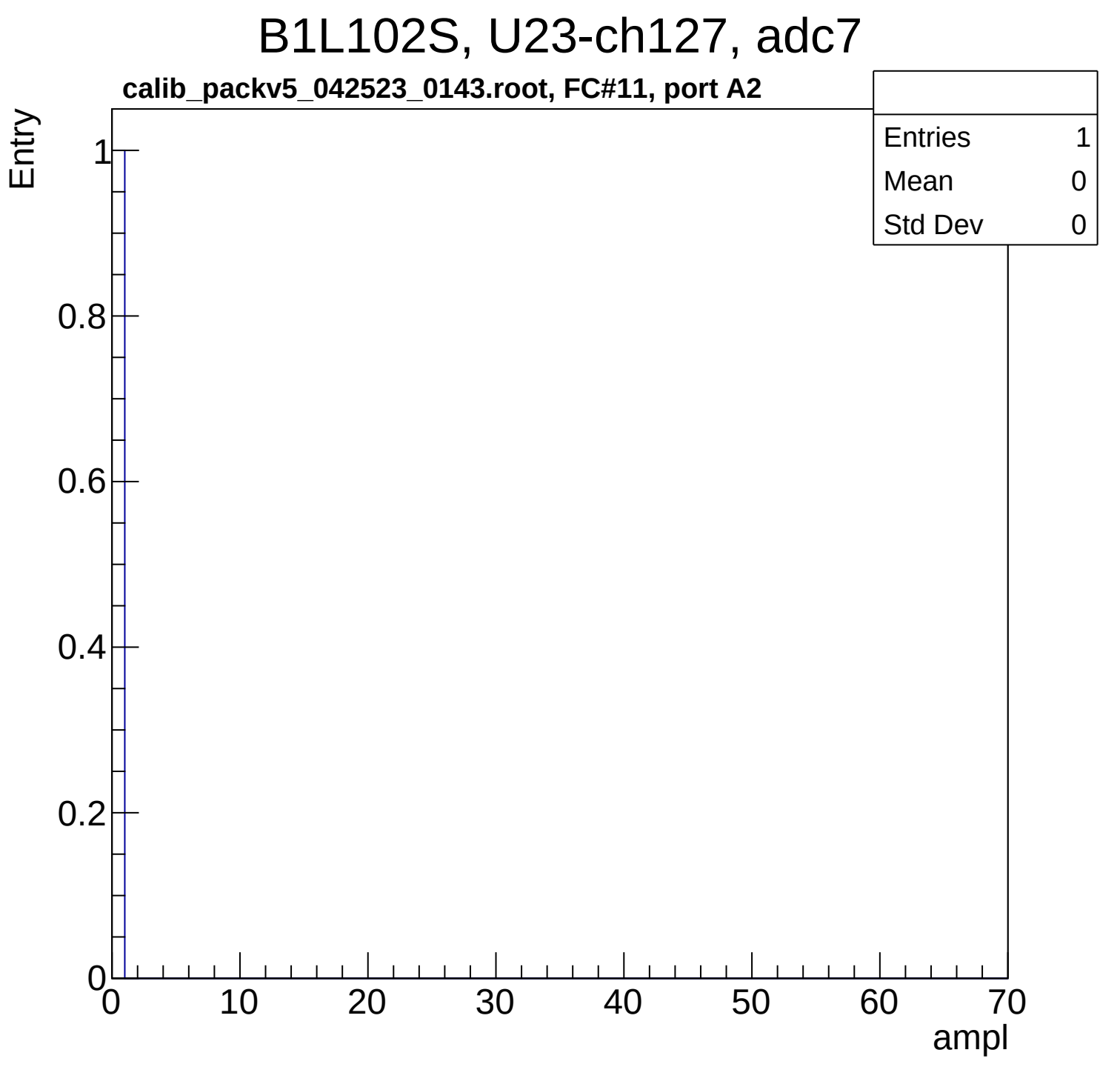

B1L102S, U23-ch127, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 50.18
Std Dev 3.943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U23-ch127, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.63
Std Dev 8.149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U23-ch127, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 33
Mean 59.82
Std Dev 2.328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U23-ch127, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl