



# B1L002S, U26-ch0, adc0

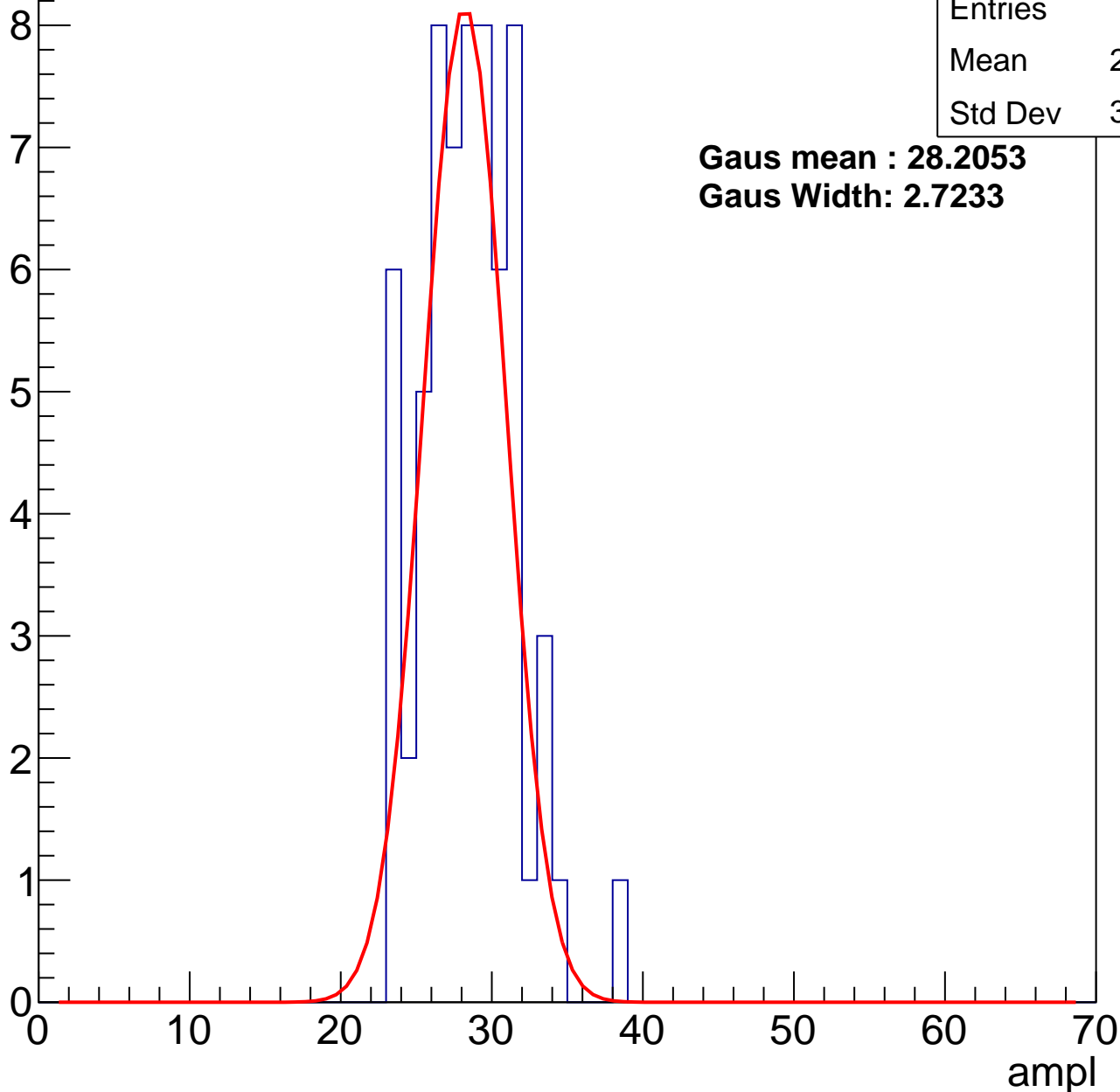
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	28.05
Std Dev	3.054

**Gaus mean : 28.2053**

**Gaus Width: 2.7233**



# B1L002S, U26-ch0, adc1

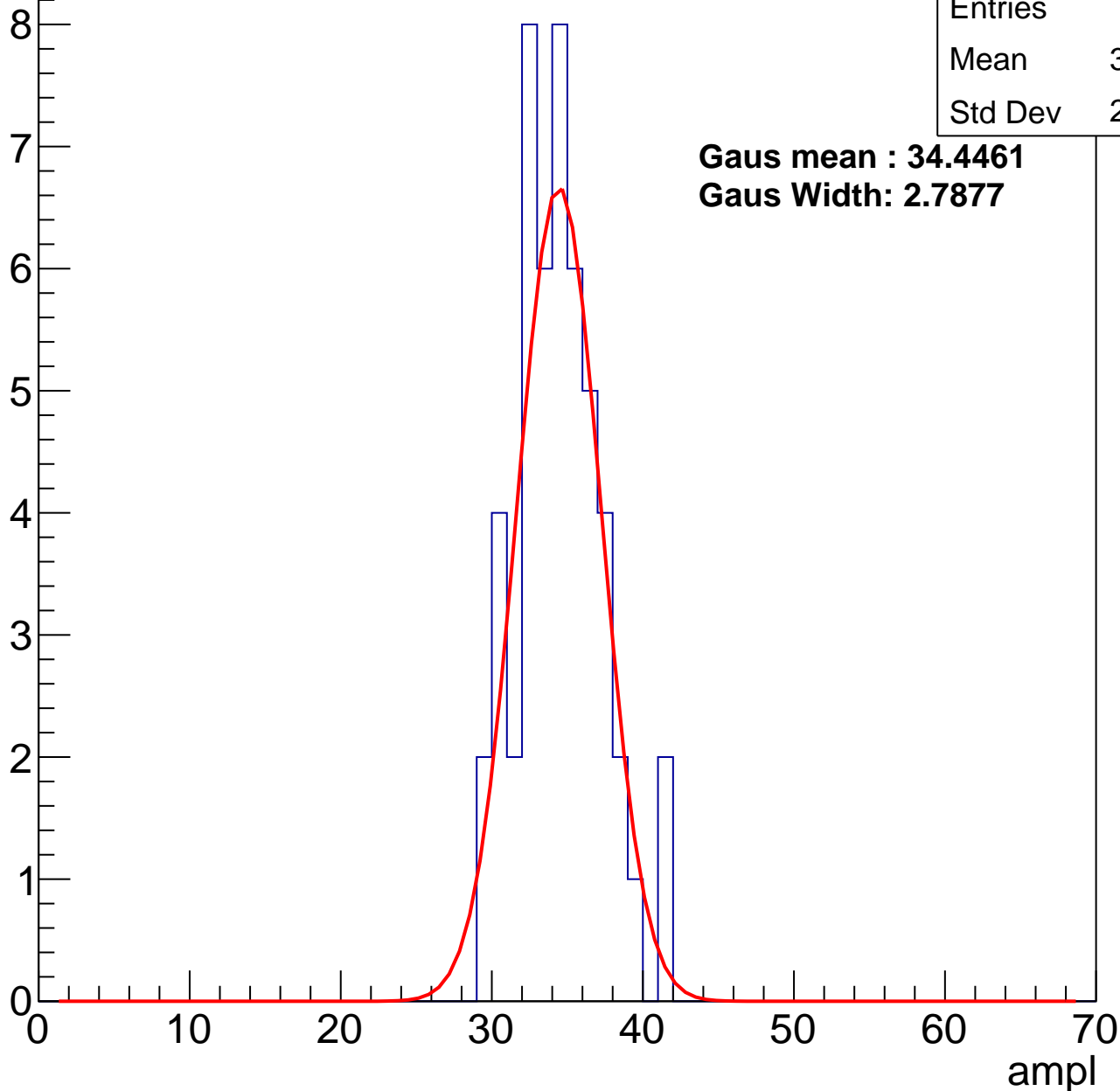
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	34.02
Std Dev	2.782

**Gaus mean : 34.4461**

**Gaus Width: 2.7877**



# B1L002S, U26-ch0, adc2

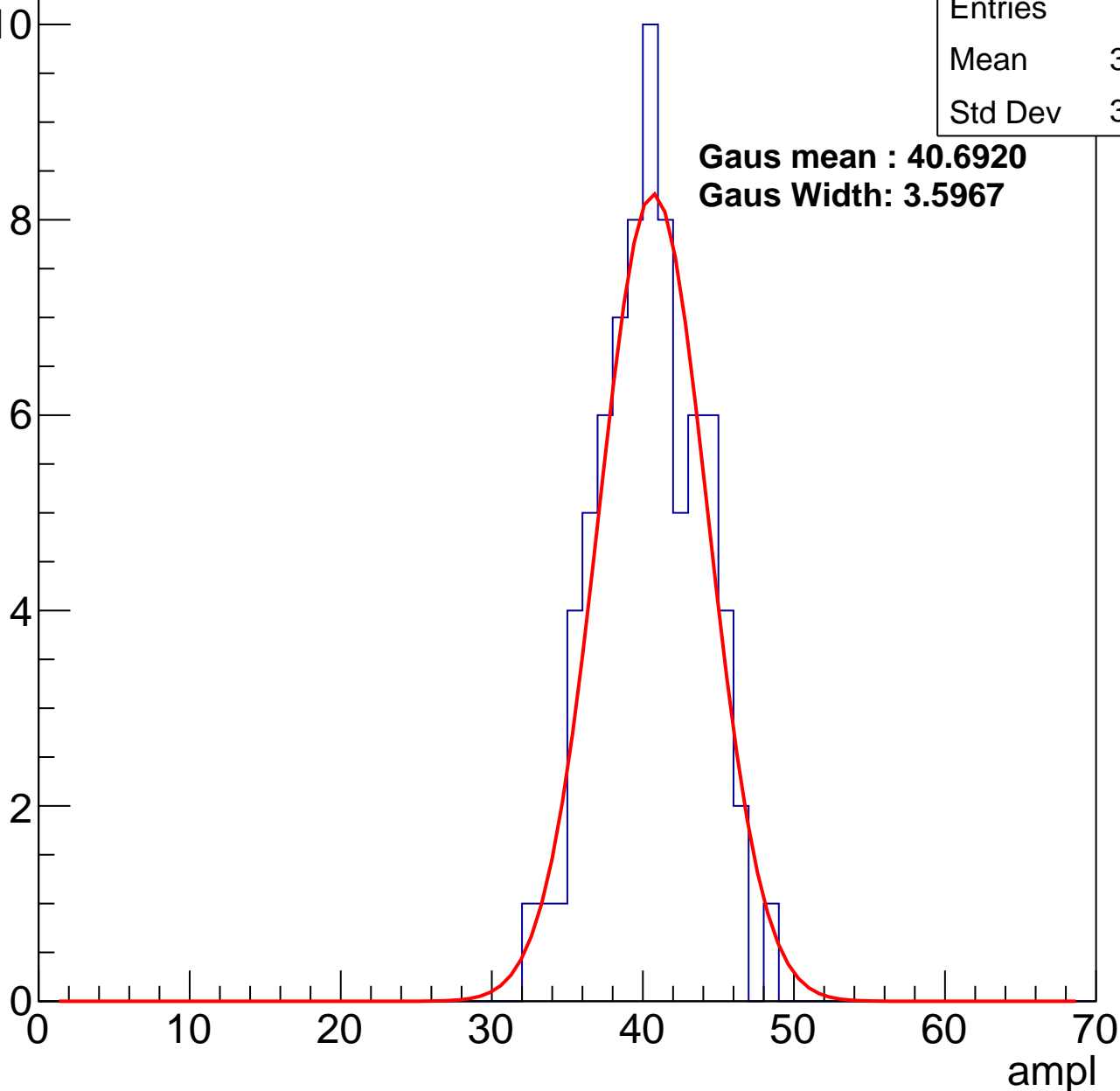
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	39.99
Std Dev	3.333

**Gaus mean : 40.6920**

**Gaus Width: 3.5967**

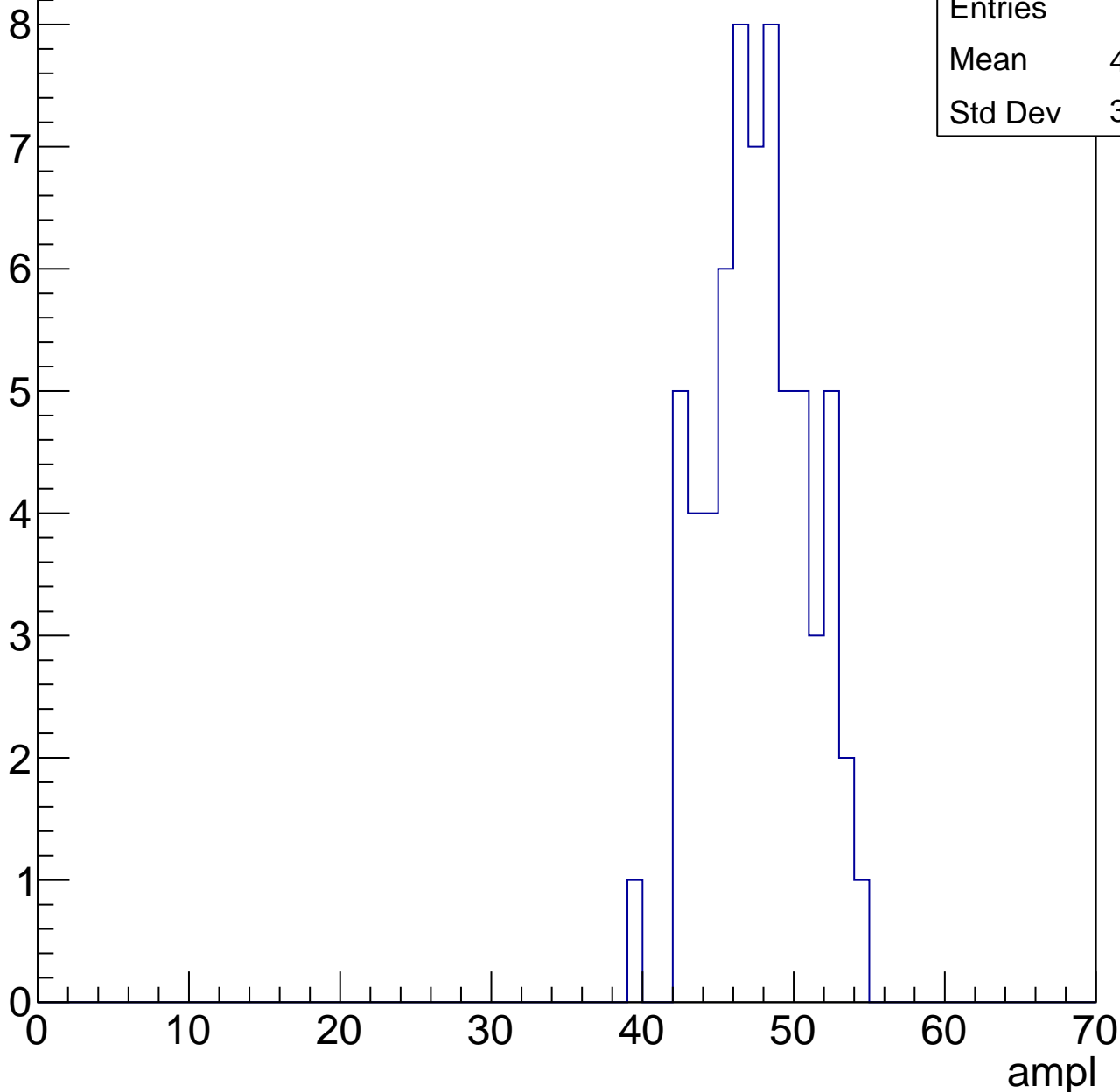


# B1L002S, U26-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

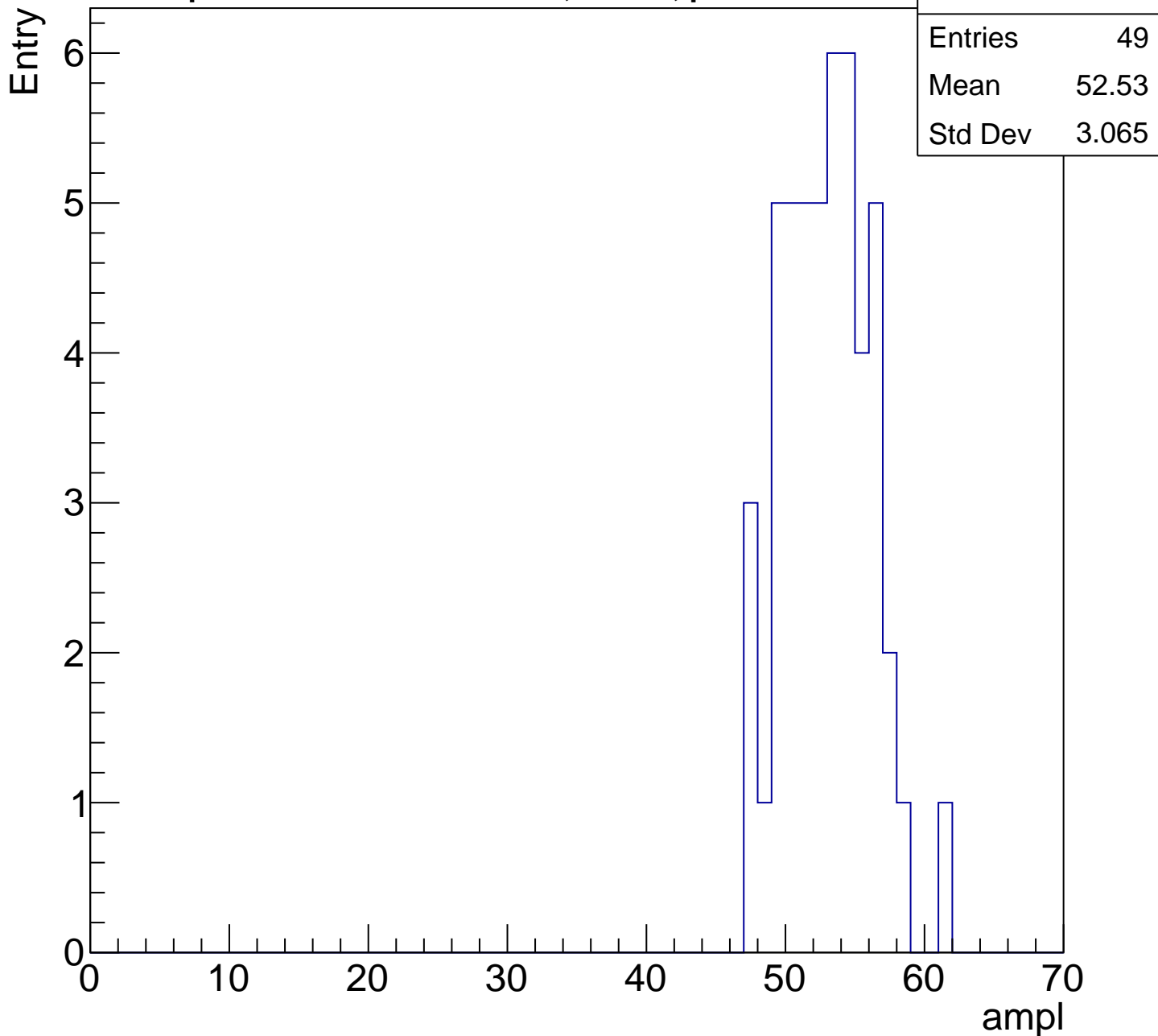
Entry

Entries	64
Mean	47.12
Std Dev	3.276



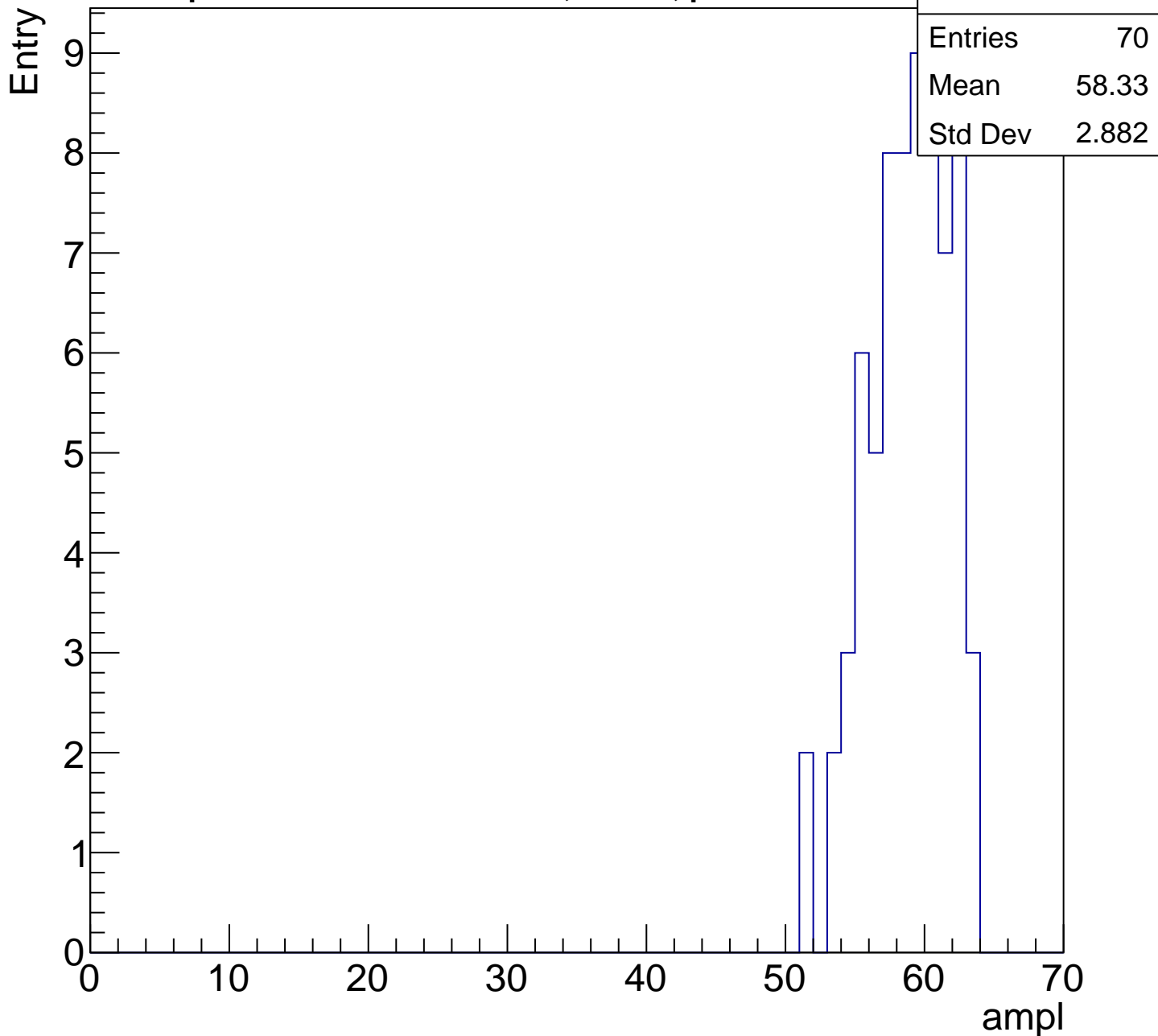
# B1L002S, U26-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

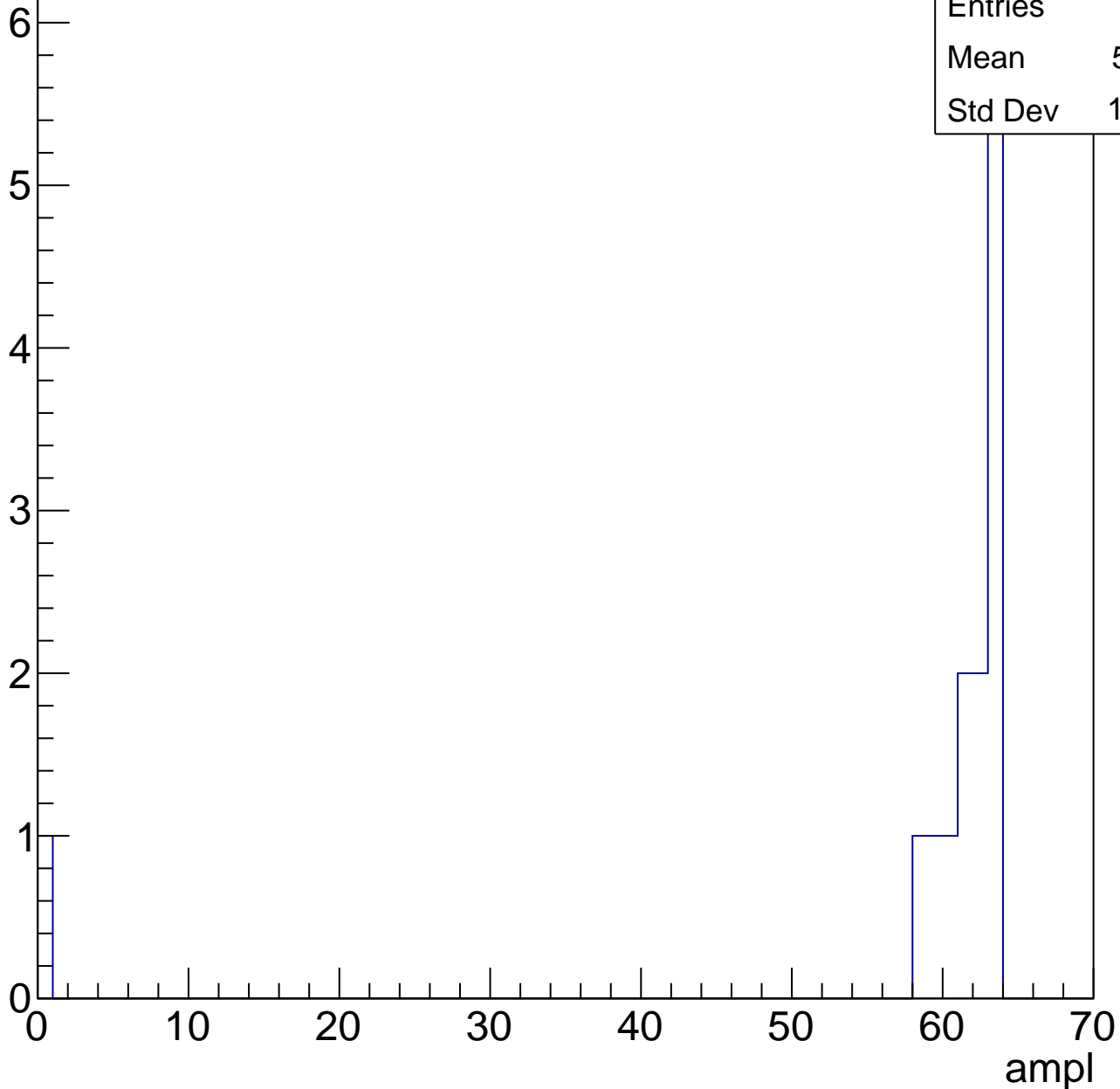


# B1L002S, U26-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.21
Std Dev	15.95





# B1L002S, U26-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L002S, U26-ch1, adc0

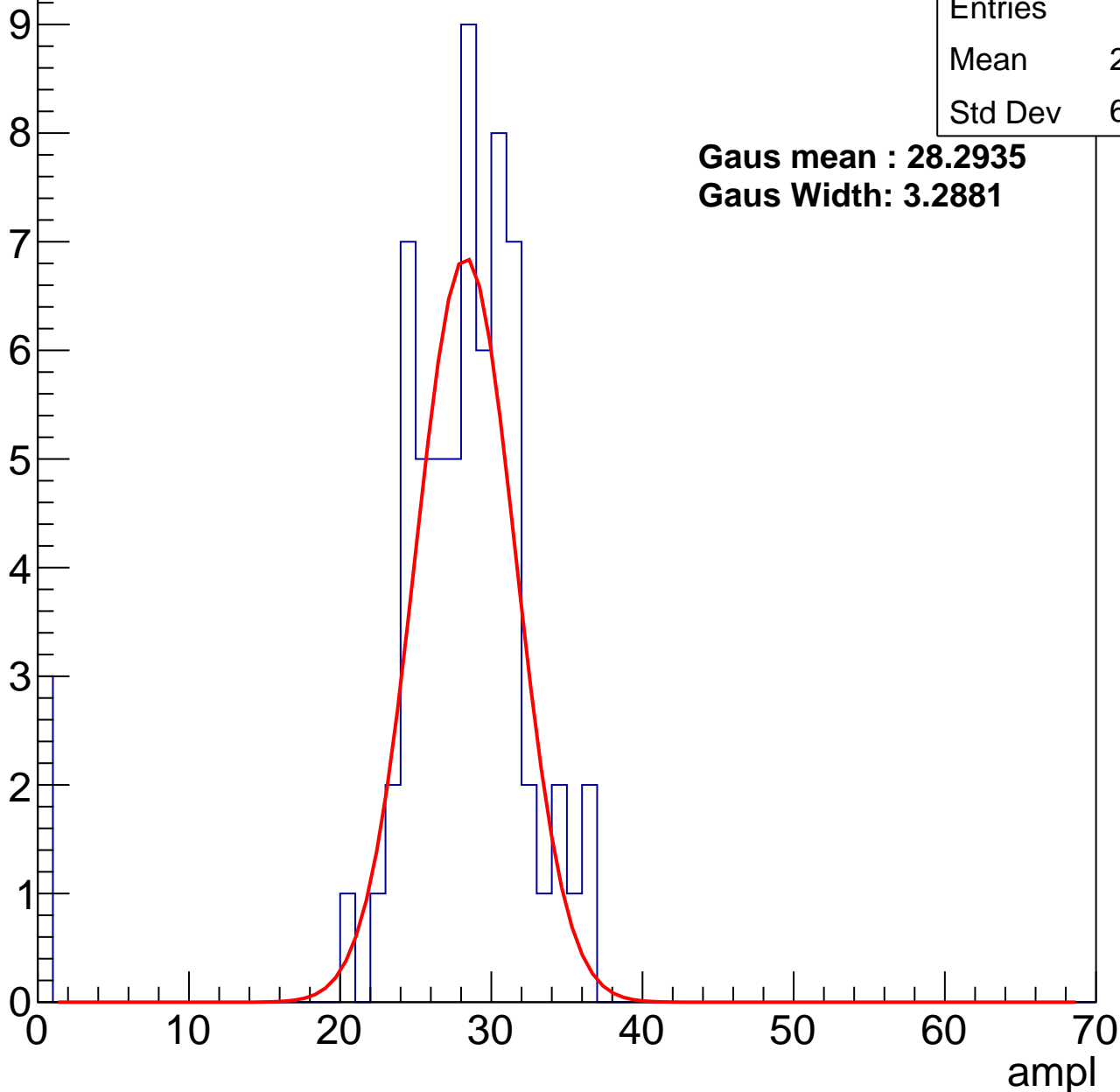
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	26.88
Std Dev	6.699

**Gaus mean : 28.2935**

**Gaus Width: 3.2881**



# B1L002S, U26-ch1, adc1

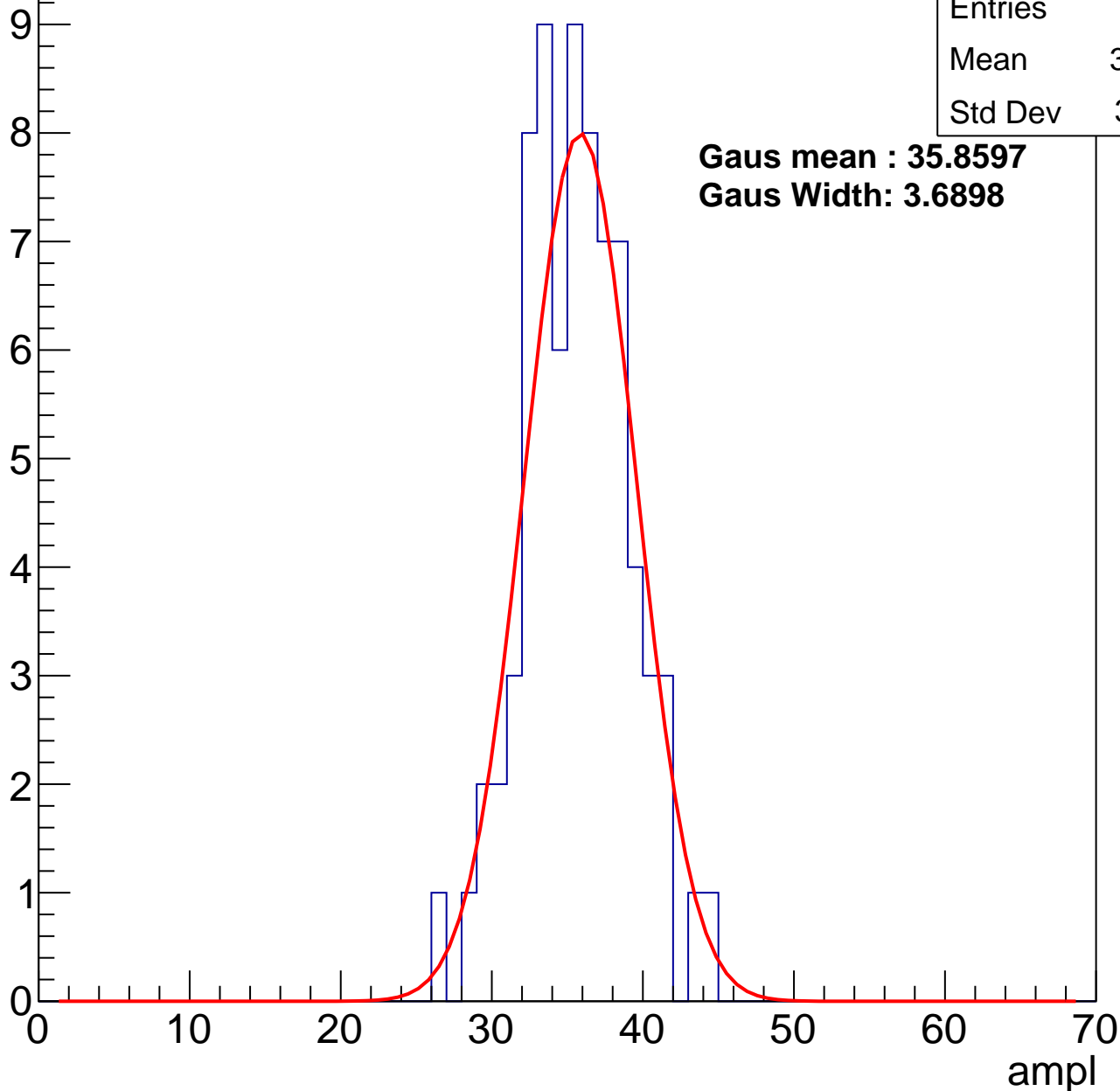
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	35.15
Std Dev	3.471

**Gaus mean : 35.8597**

**Gaus Width: 3.6898**



# B1L002S, U26-ch1, adc2

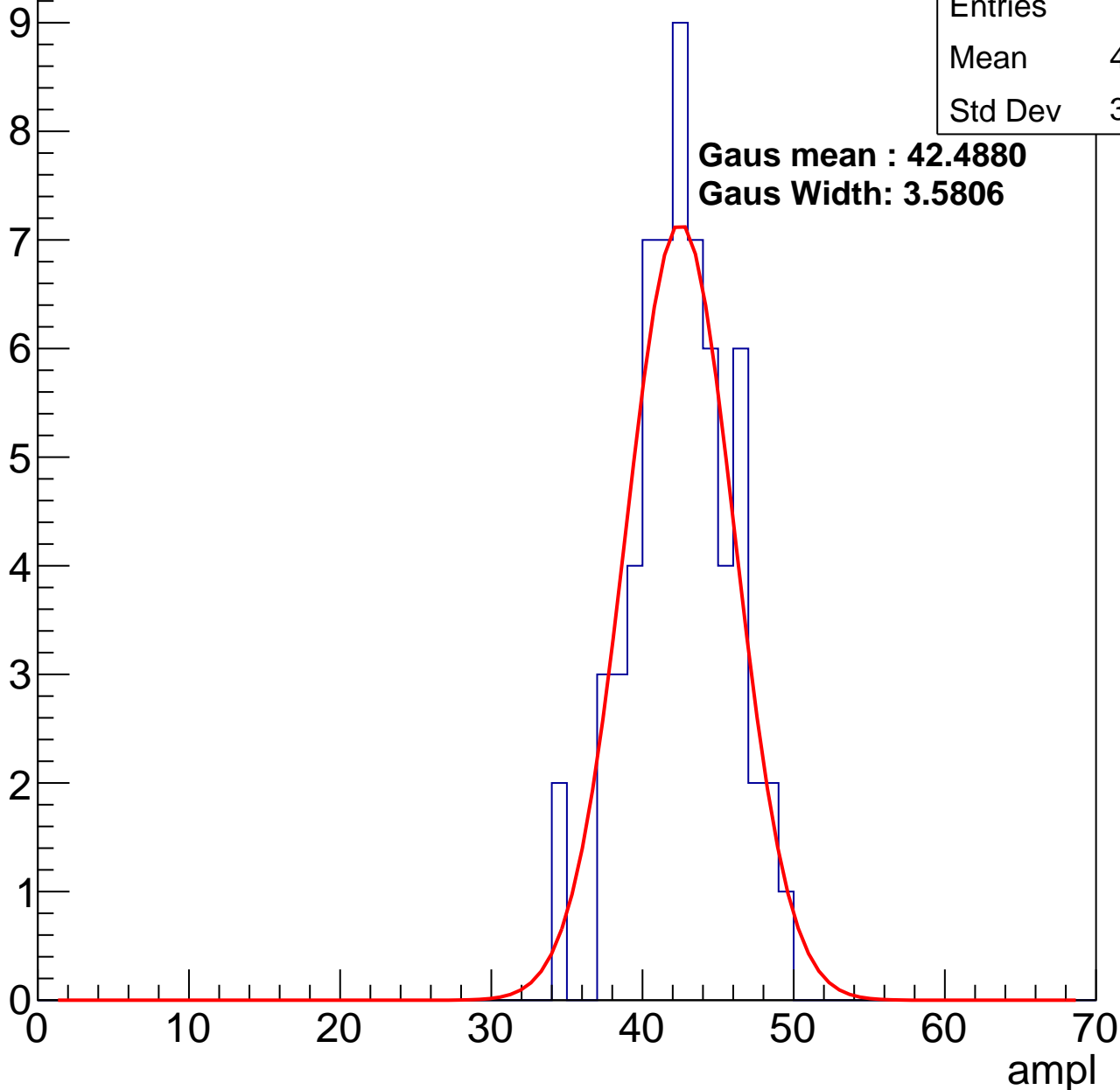
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	42.13
Std Dev	3.224

**Gaus mean : 42.4880**

**Gaus Width: 3.5806**

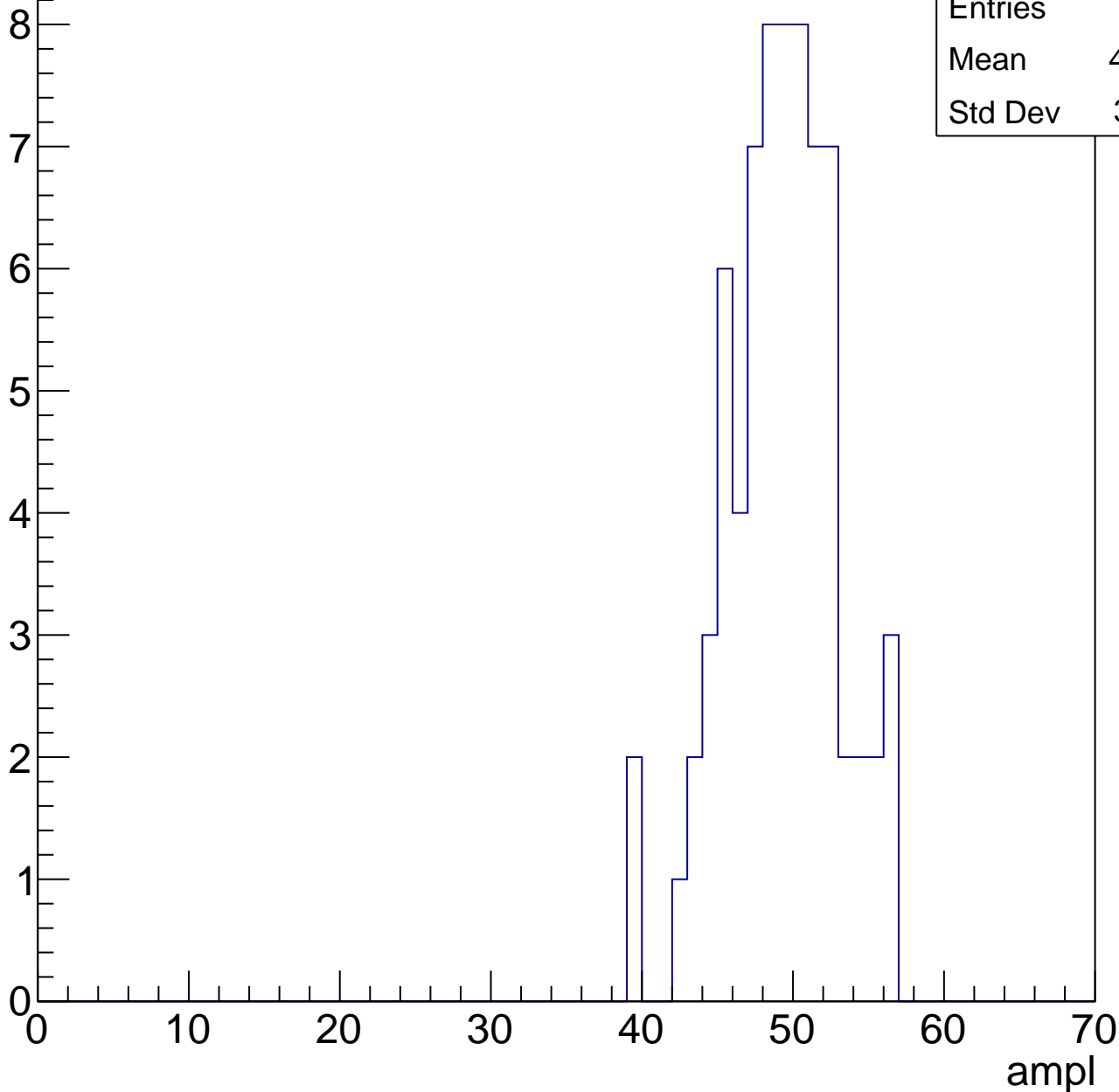


# B1L002S, U26-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

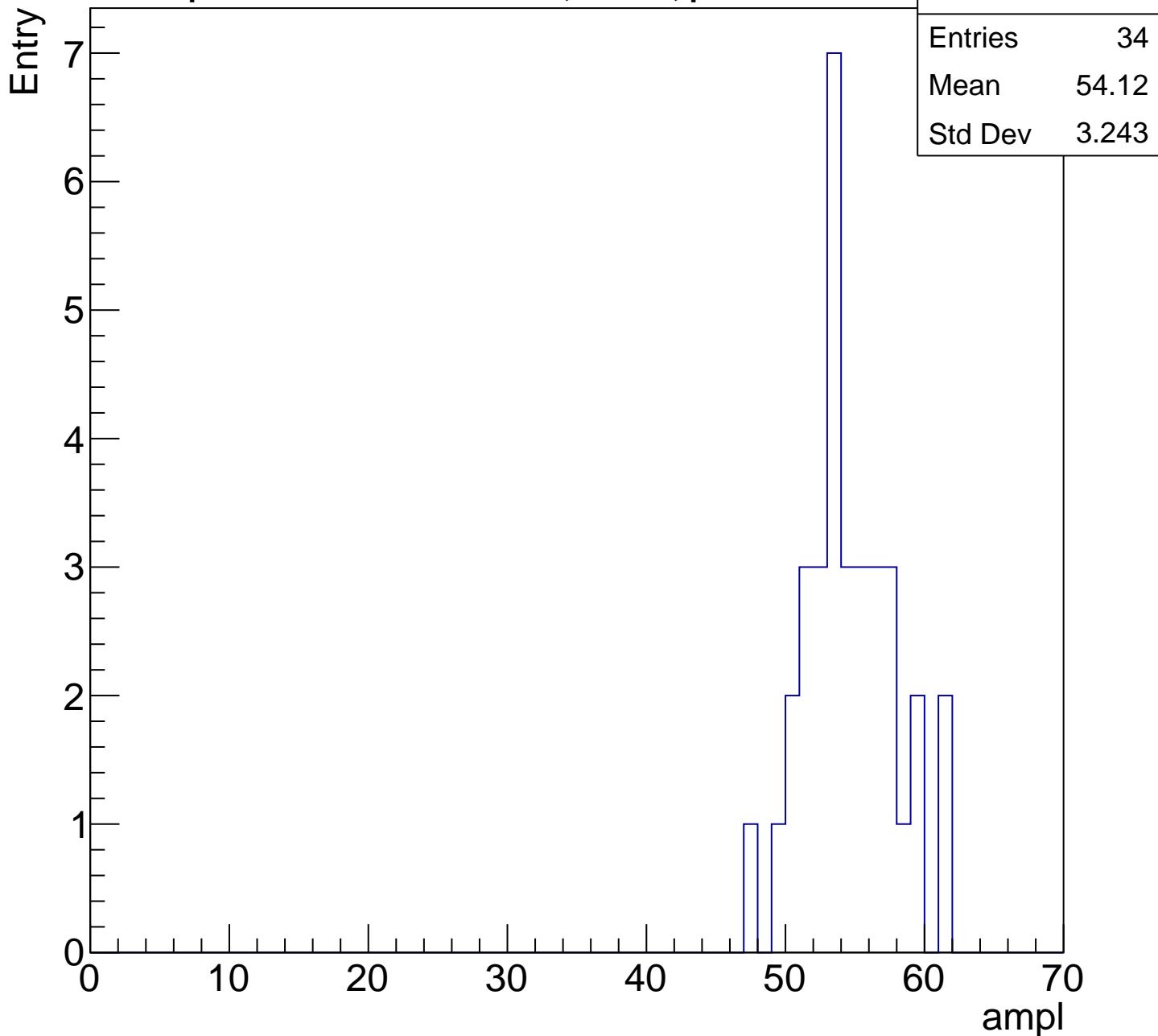
Entry

Entries	72
Mean	48.75
Std Dev	3.651



# B1L002S, U26-ch1, adc4

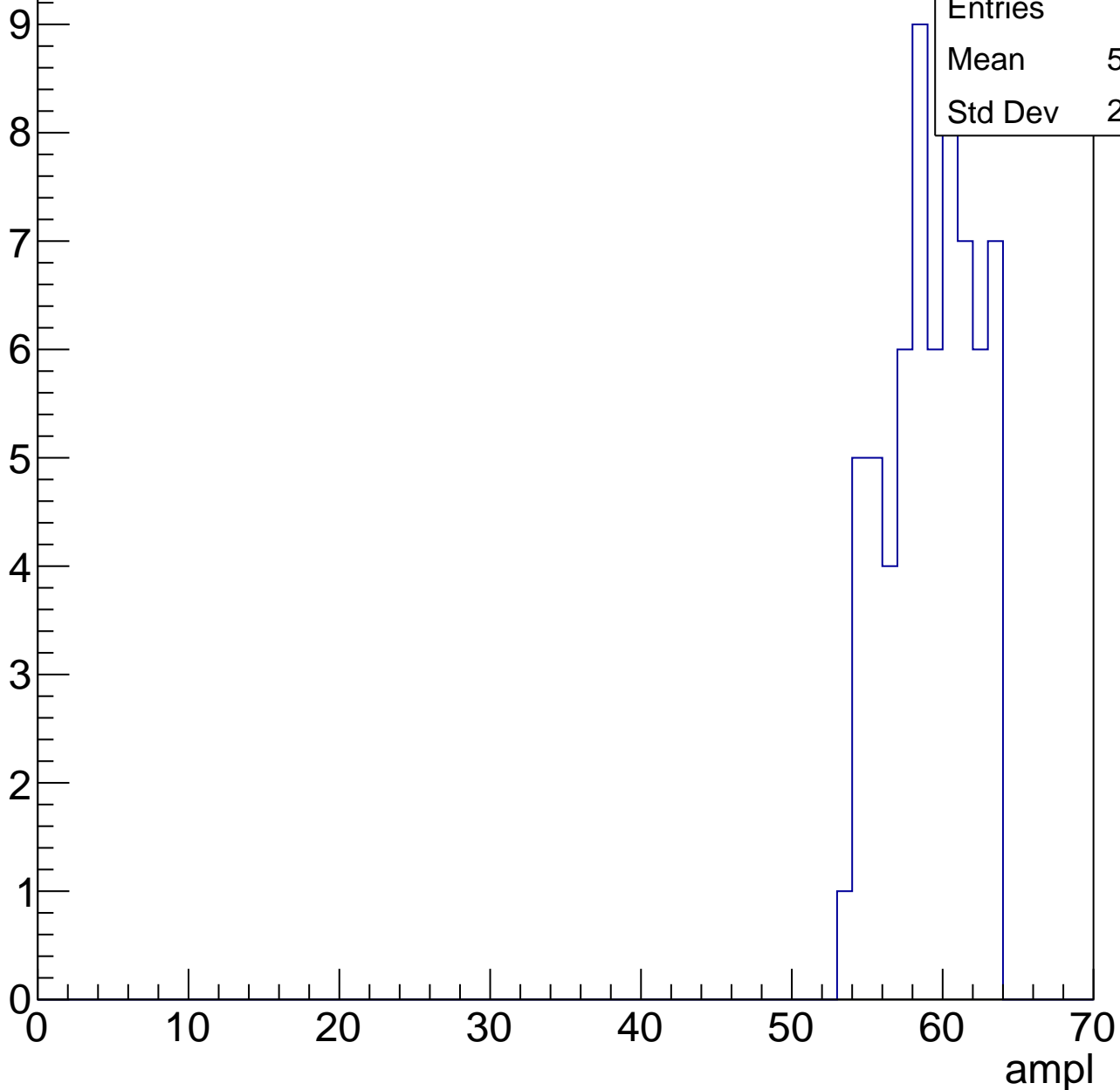
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

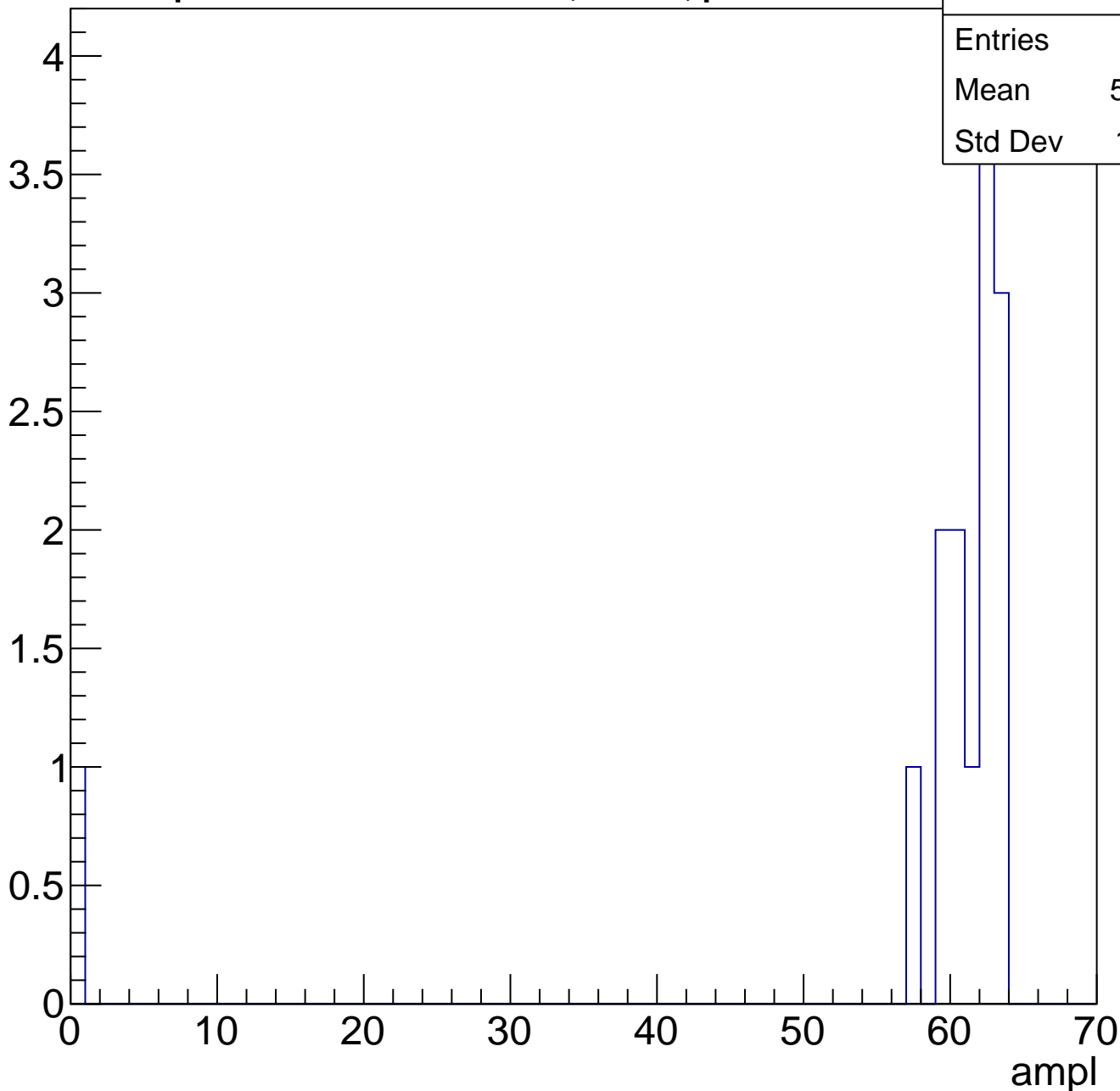


Entries	64
Mean	58.75
Std Dev	2.817

# B1L002S, U26-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U26-ch2, adc0

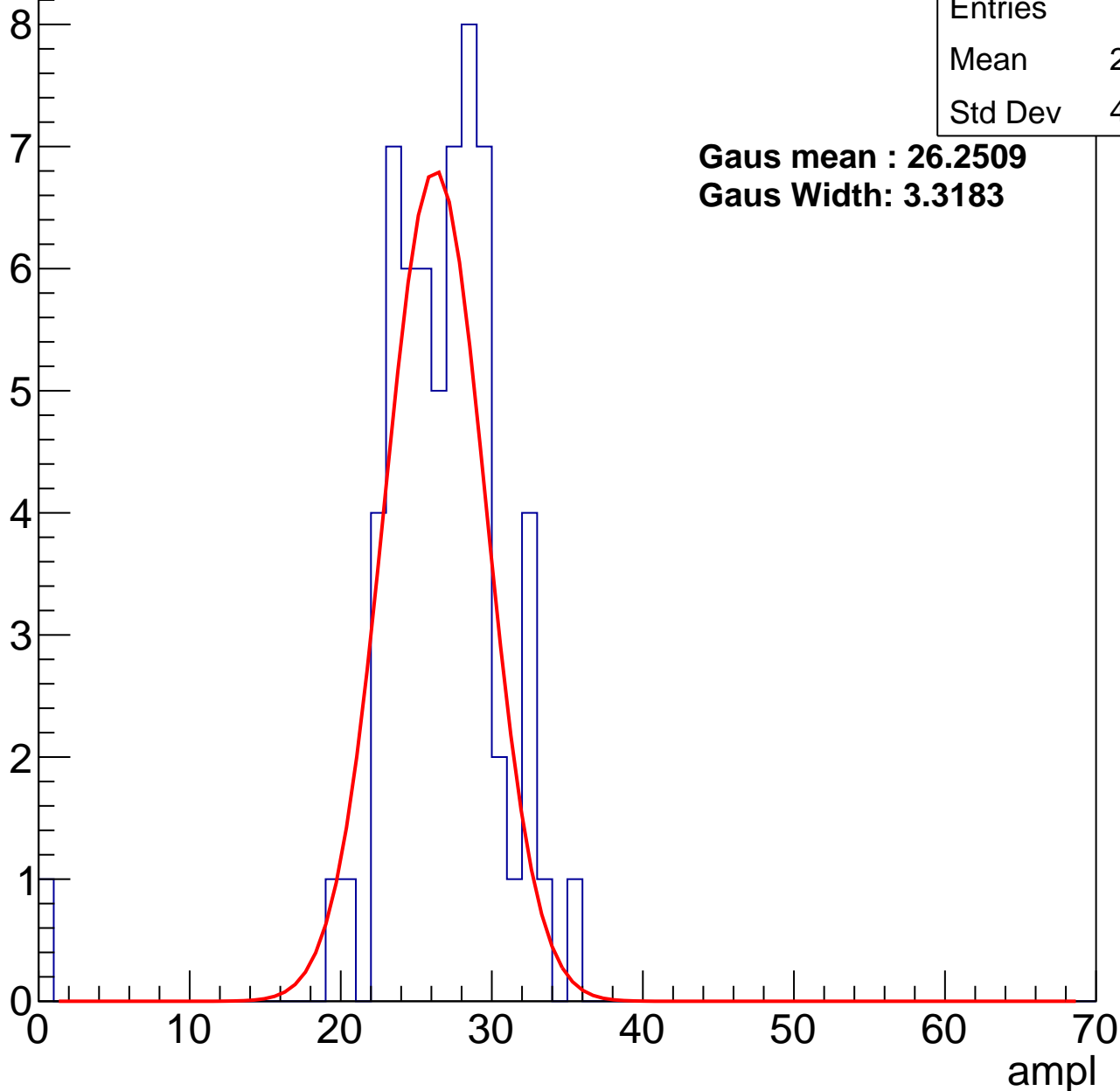
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	26.05
Std Dev	4.668

**Gaus mean : 26.2509**

**Gaus Width: 3.3183**



# B1L002S, U26-ch2, adc1

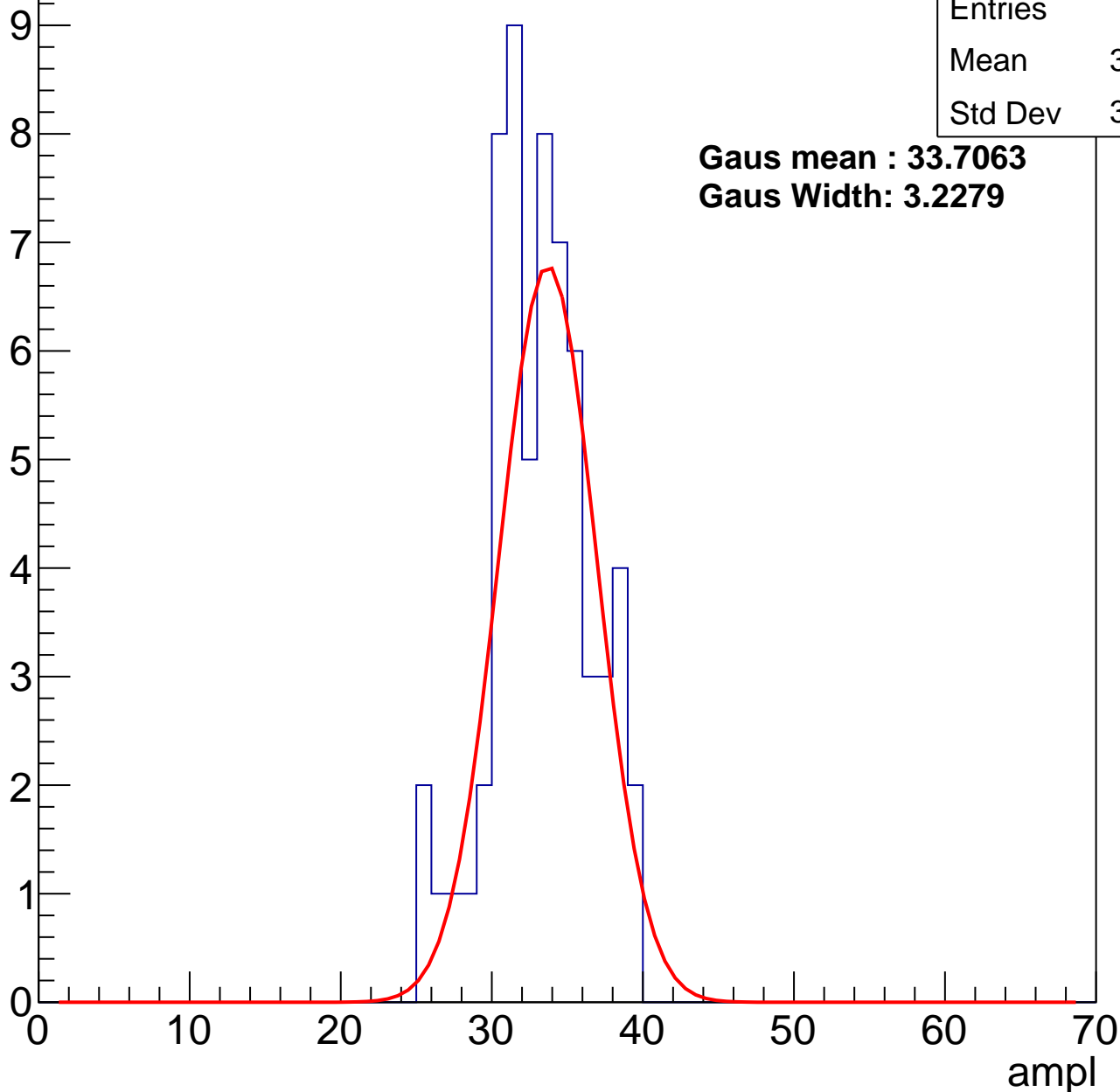
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	32.73
Std Dev	3.254

**Gaus mean : 33.7063**

**Gaus Width: 3.2279**



# B1L002S, U26-ch2, adc2

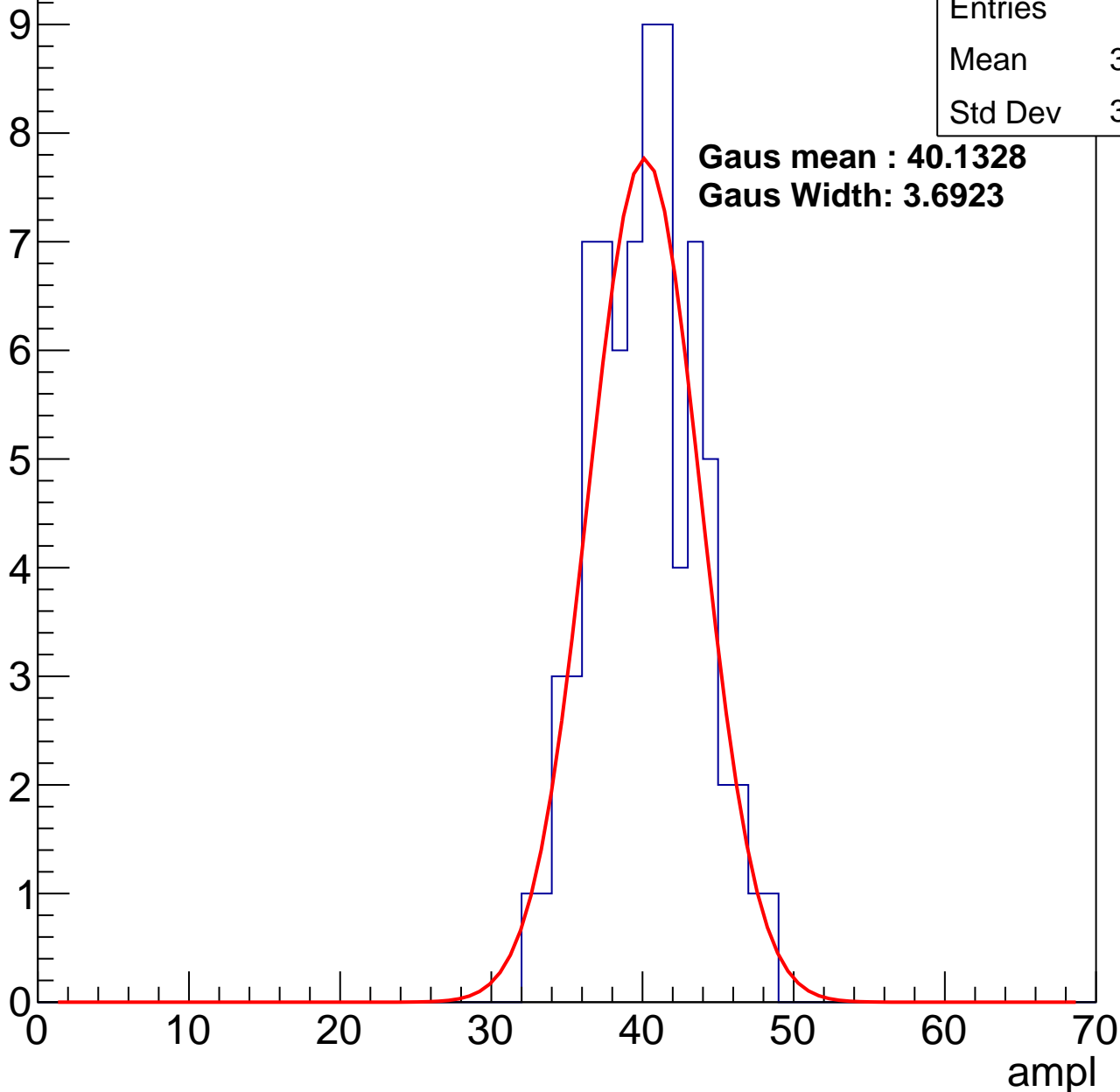
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	39.72
Std Dev	3.462

**Gaus mean : 40.1328**

**Gaus Width: 3.6923**

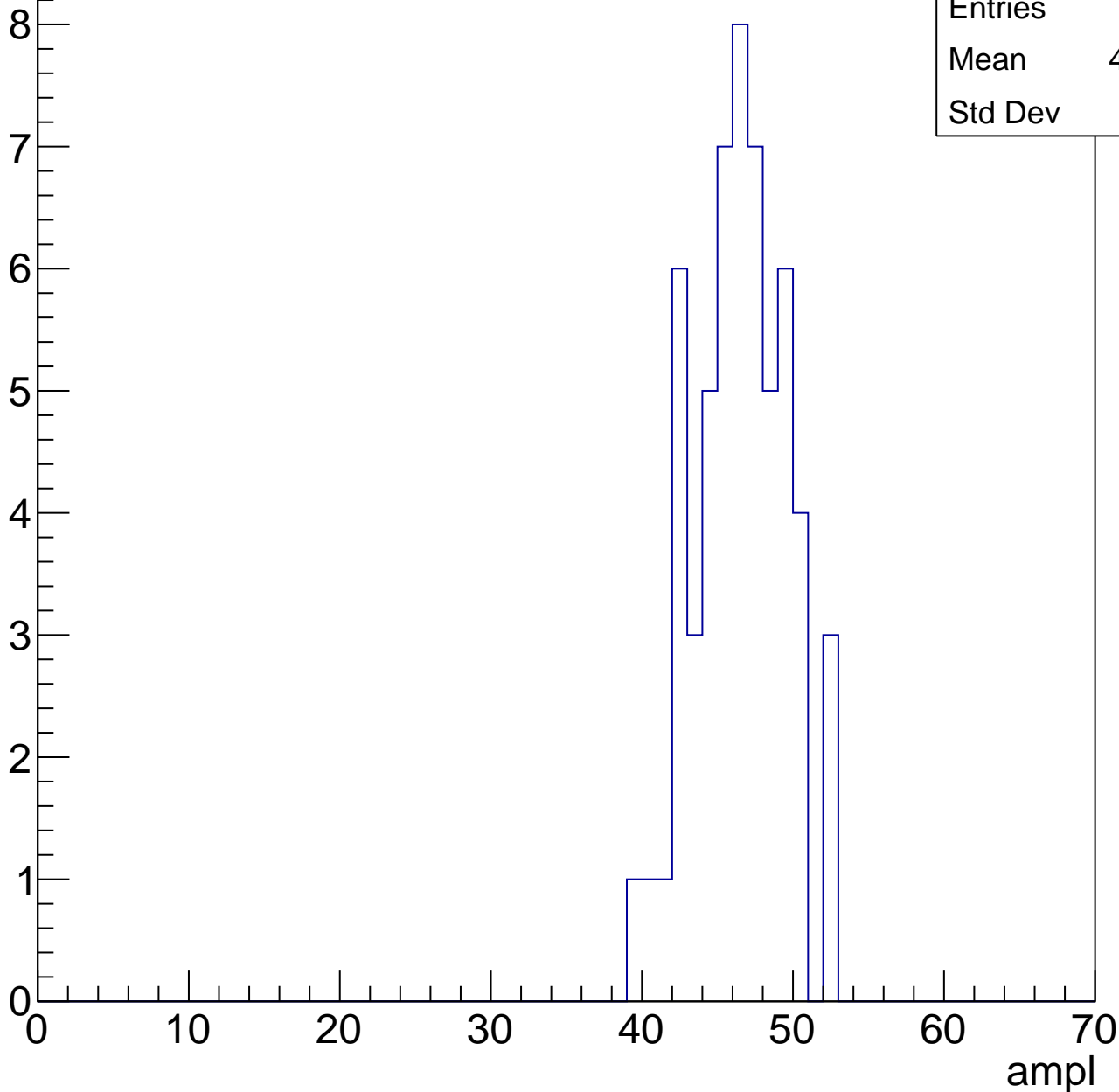


# B1L002S, U26-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.02
Std Dev	3

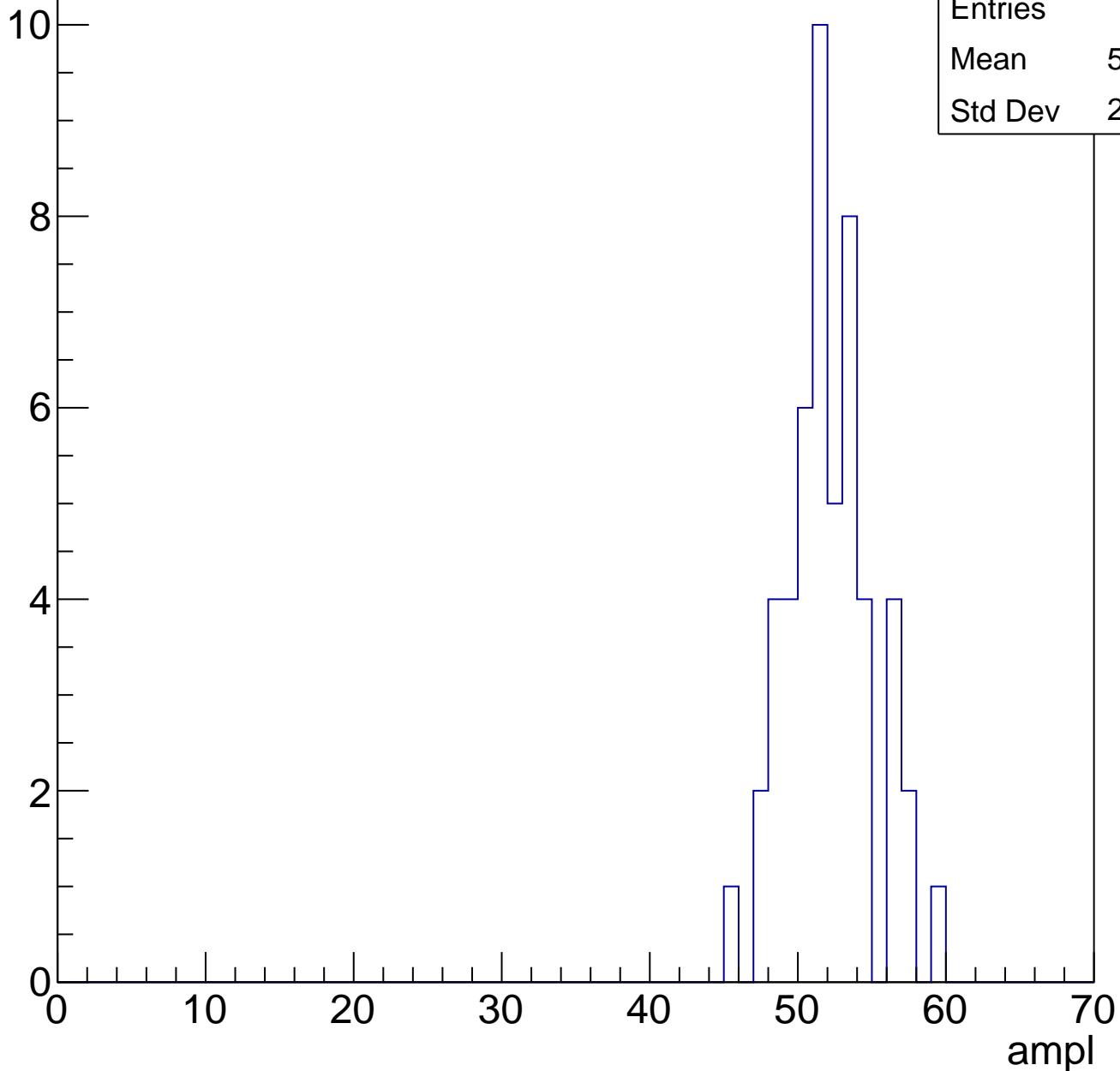


# B1L002S, U26-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	51
Mean	51.65
Std Dev	2.848

Entry

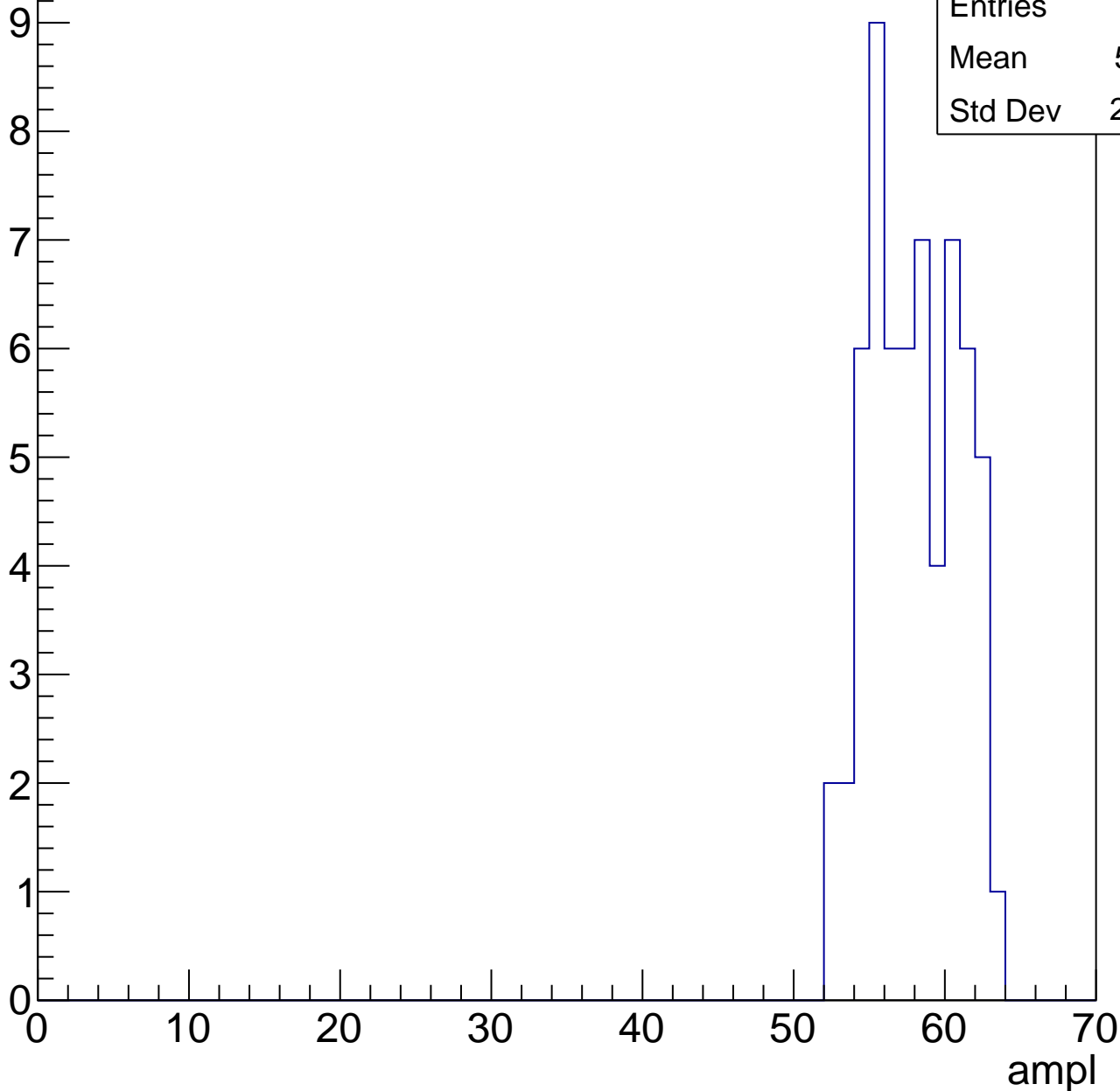


# B1L002S, U26-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.51
Std Dev	2.878

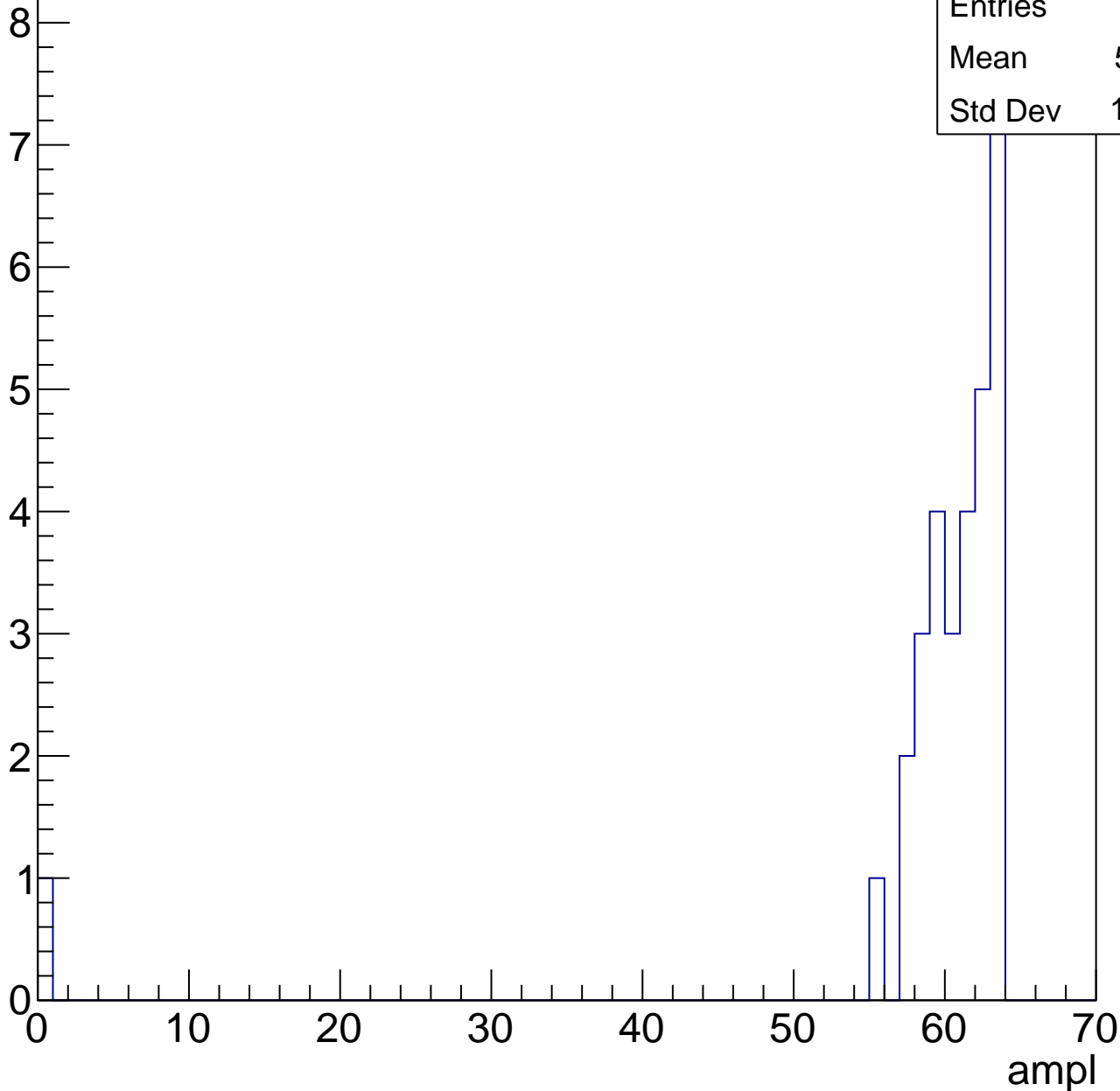


# B1L002S, U26-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.61
Std Dev	10.92





# B1L002S, U26-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

61

Std Dev

2

# B1L002S, U26-ch3, adc0

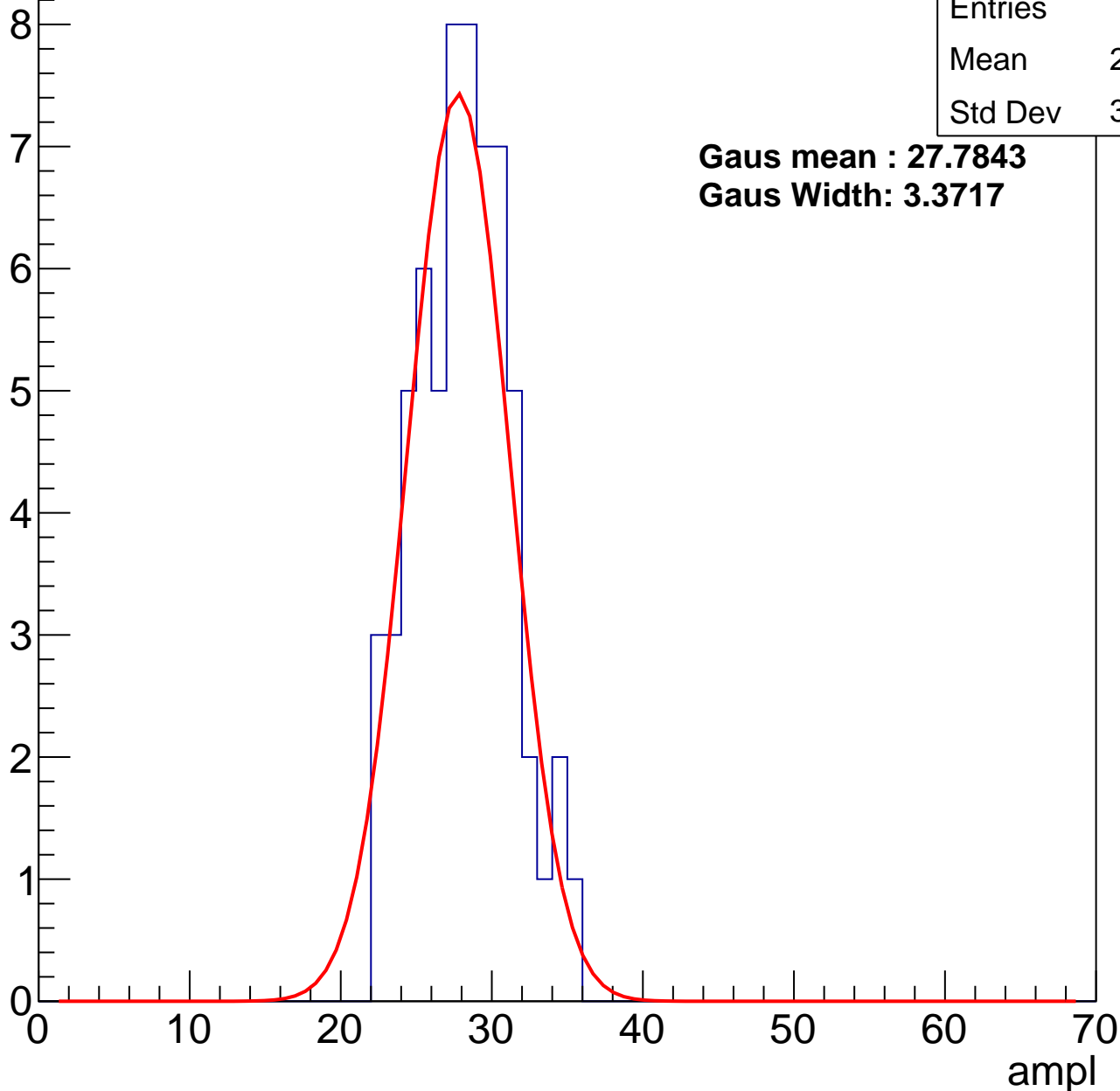
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.67
Std Dev	3.076

**Gaus mean : 27.7843**

**Gaus Width: 3.3717**



# B1L002S, U26-ch3, adc1

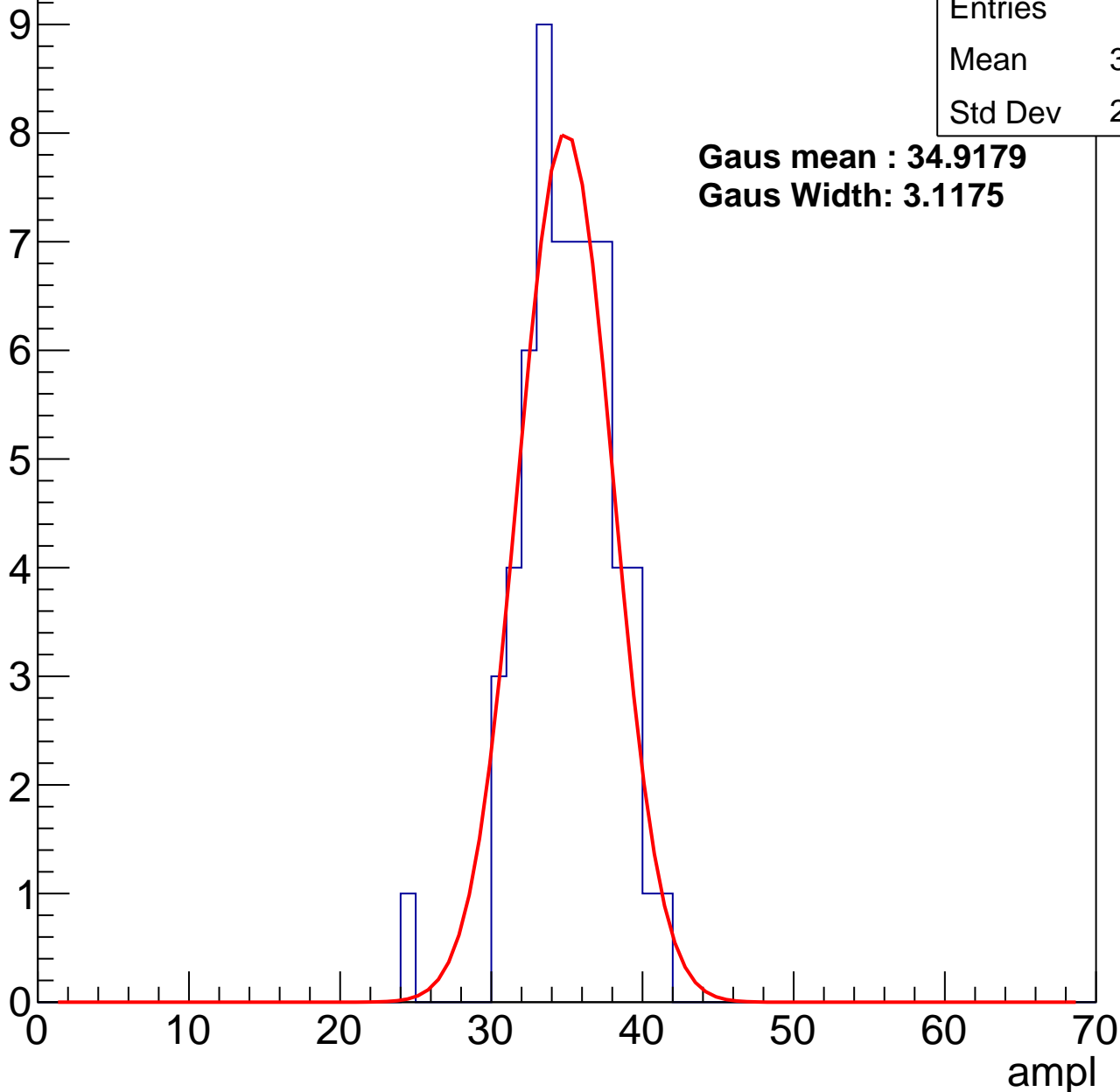
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.59
Std Dev	2.983

**Gaus mean : 34.9179**

**Gaus Width: 3.1175**



# B1L002S, U26-ch3, adc2

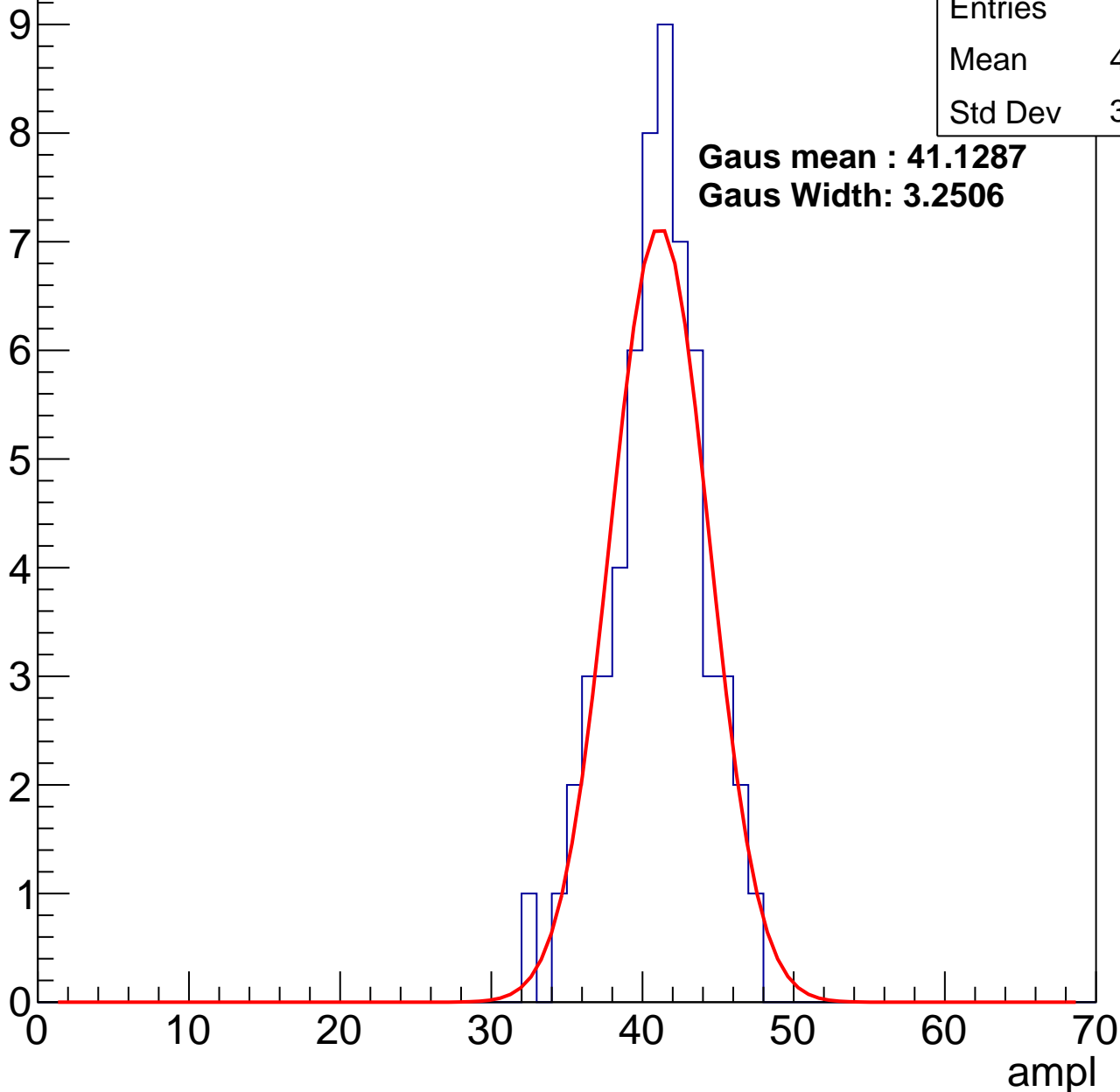
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.47
Std Dev	3.105

**Gaus mean : 41.1287**

**Gaus Width: 3.2506**



# B1L002S, U26-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

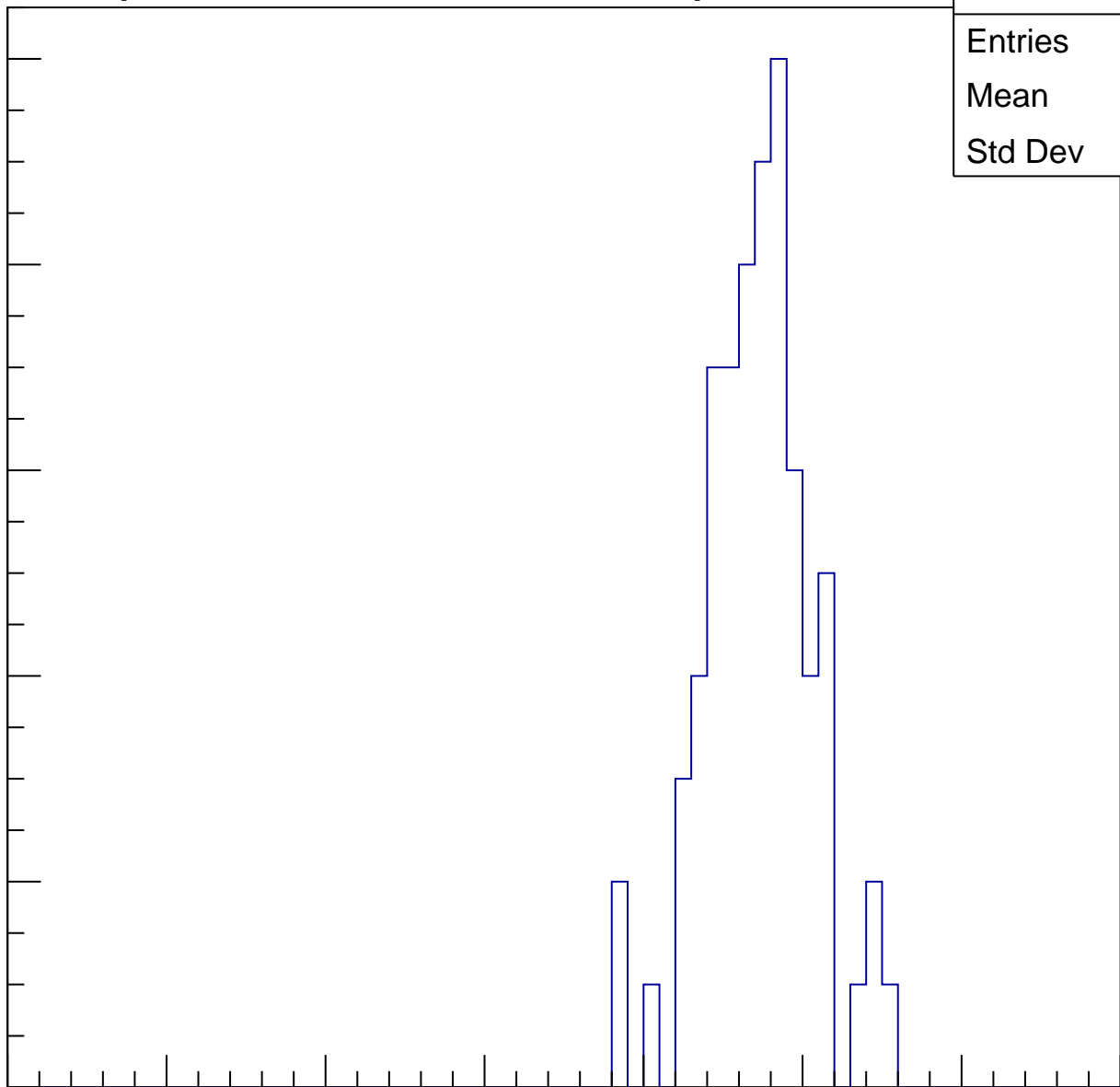
Entries	70
Mean	46.76
Std Dev	3.366

Entry

10  
8  
6  
4  
2  
0

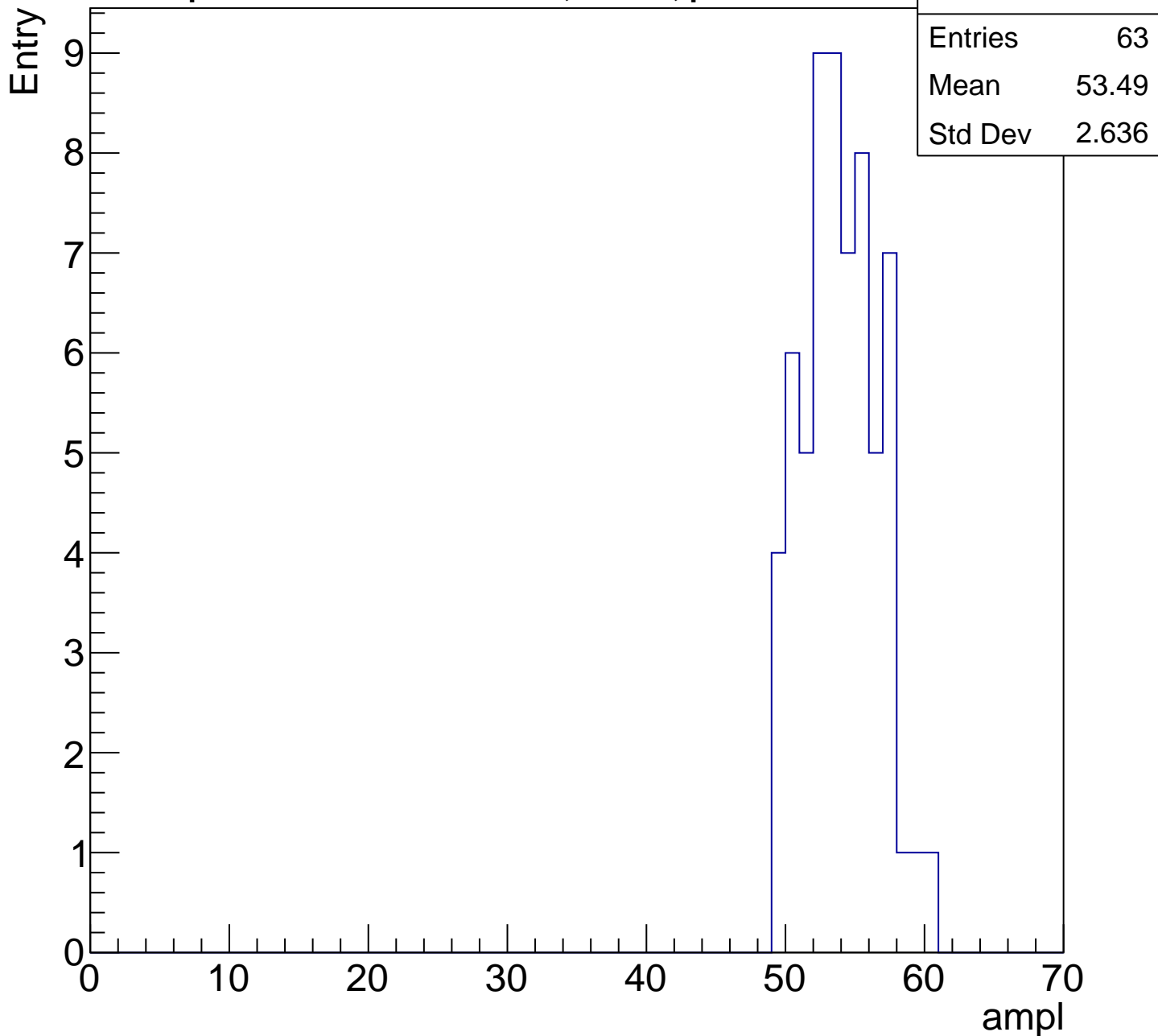
ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

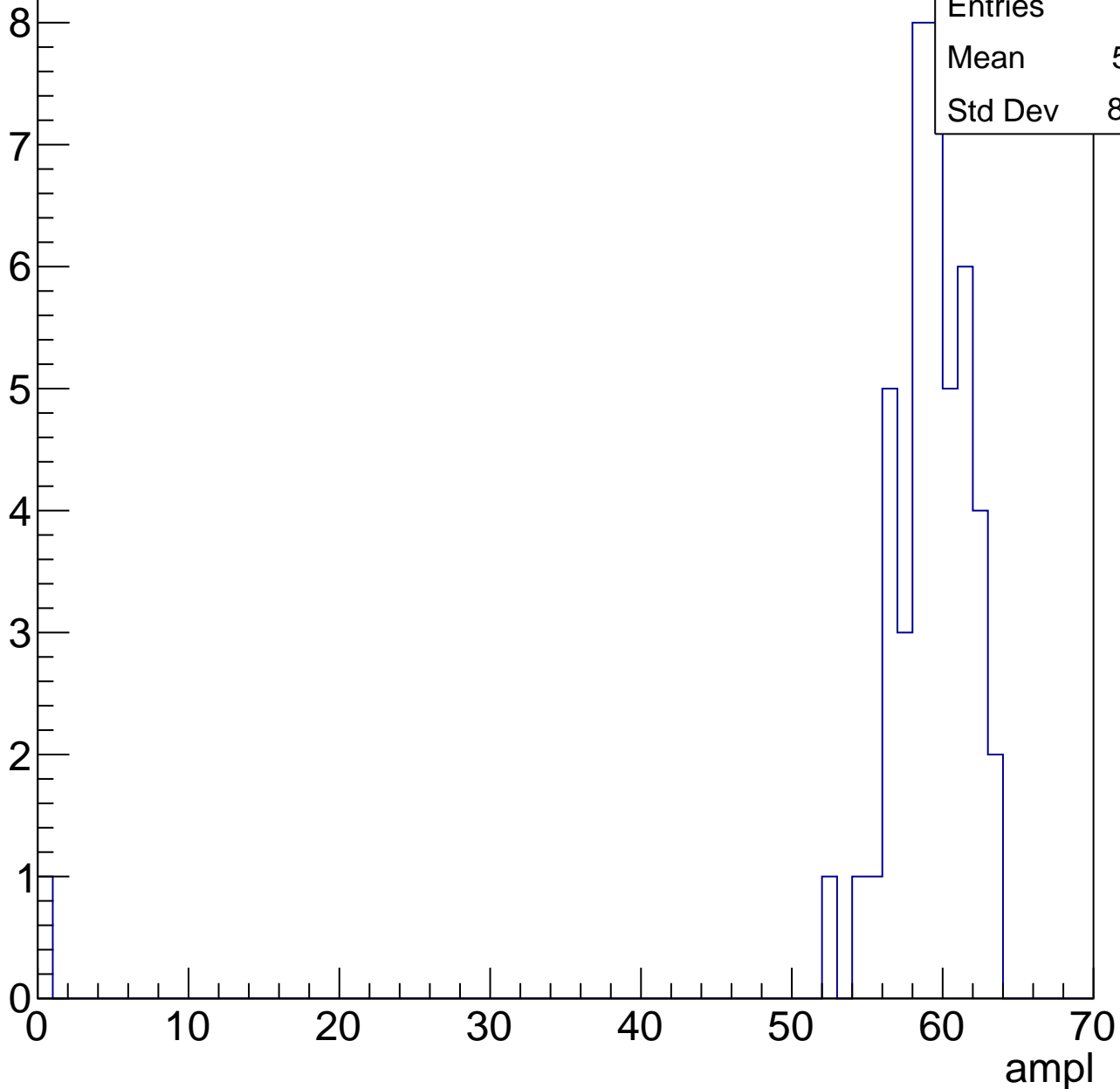


# B1L002S, U26-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

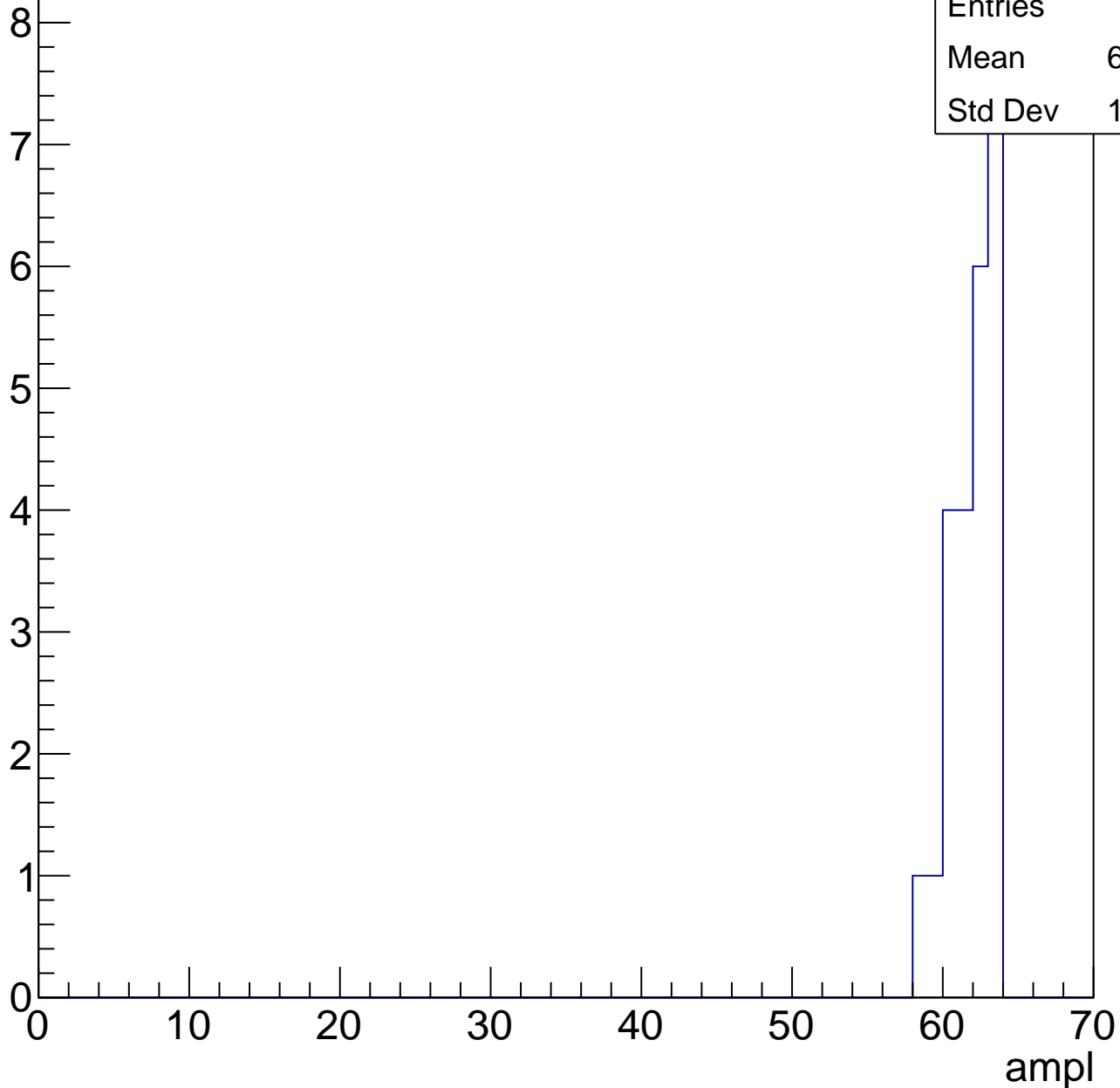
Entries	45
Mean	57.51
Std Dev	8.985



# B1L002S, U26-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L002S, U26-ch4, adc0

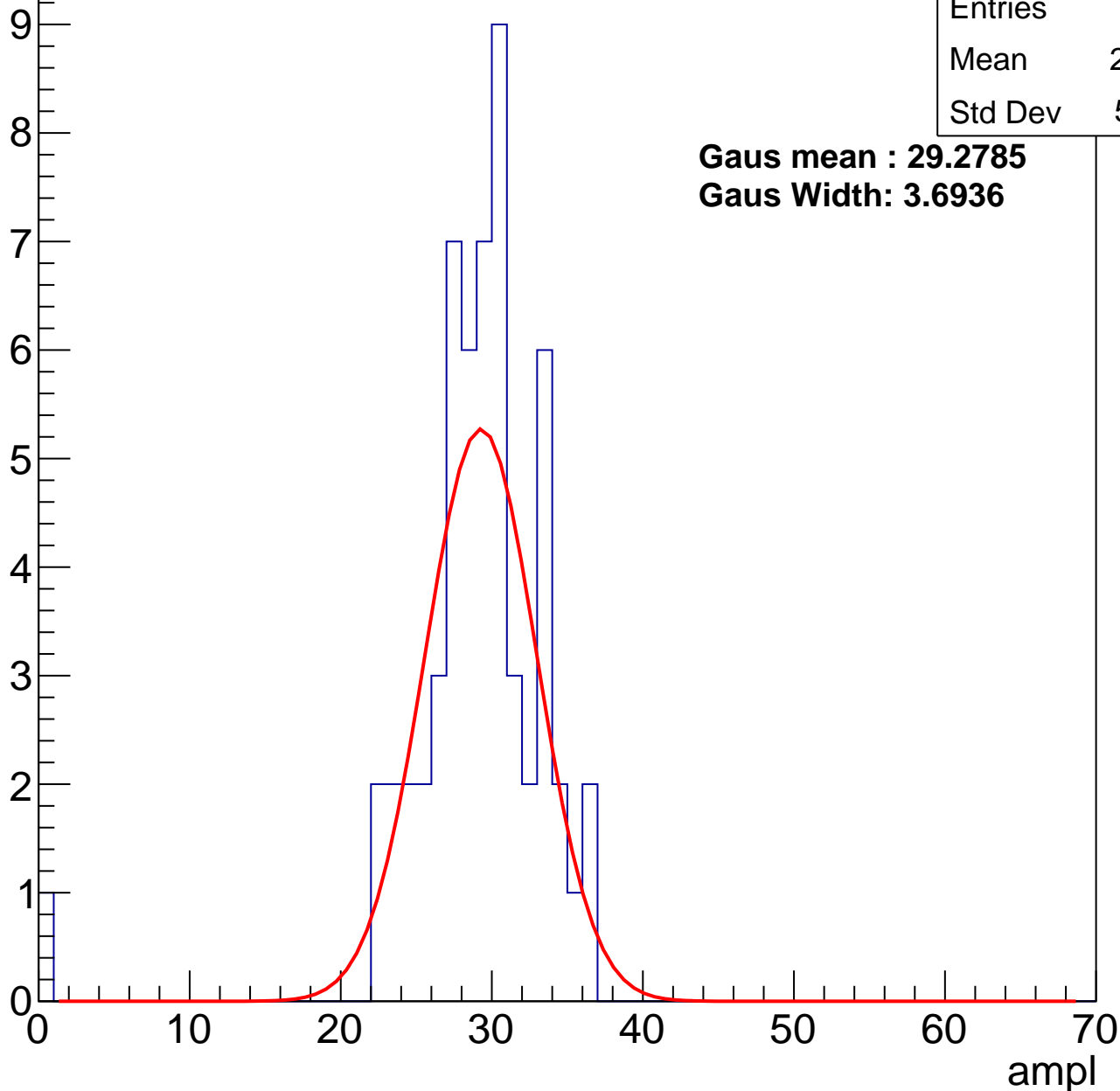
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28.53
Std Dev	5.061

**Gaus mean : 29.2785**

**Gaus Width: 3.6936**



# B1L002S, U26-ch4, adc1

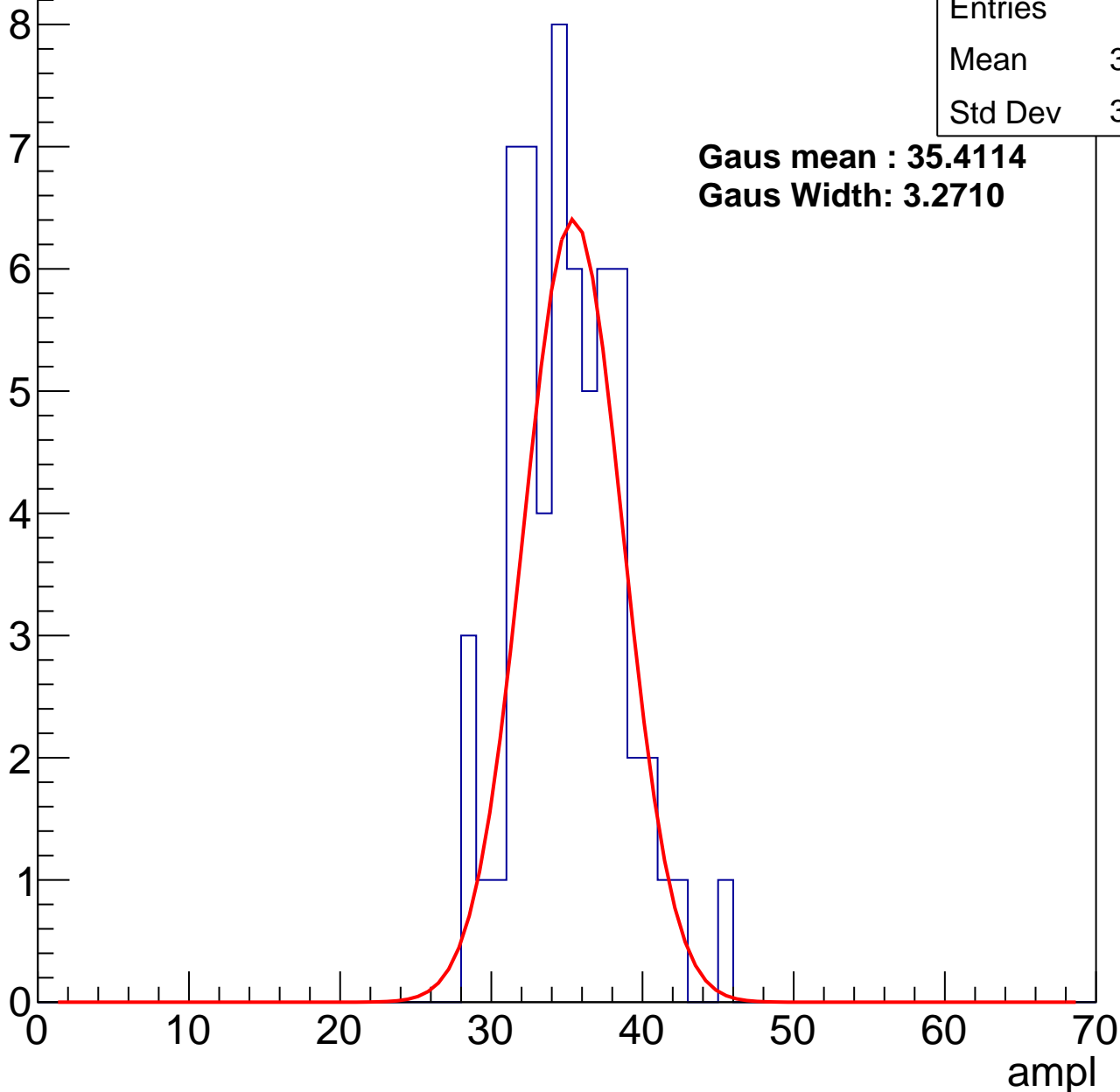
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.66
Std Dev	3.506

**Gaus mean : 35.4114**

**Gaus Width: 3.2710**



# B1L002S, U26-ch4, adc2

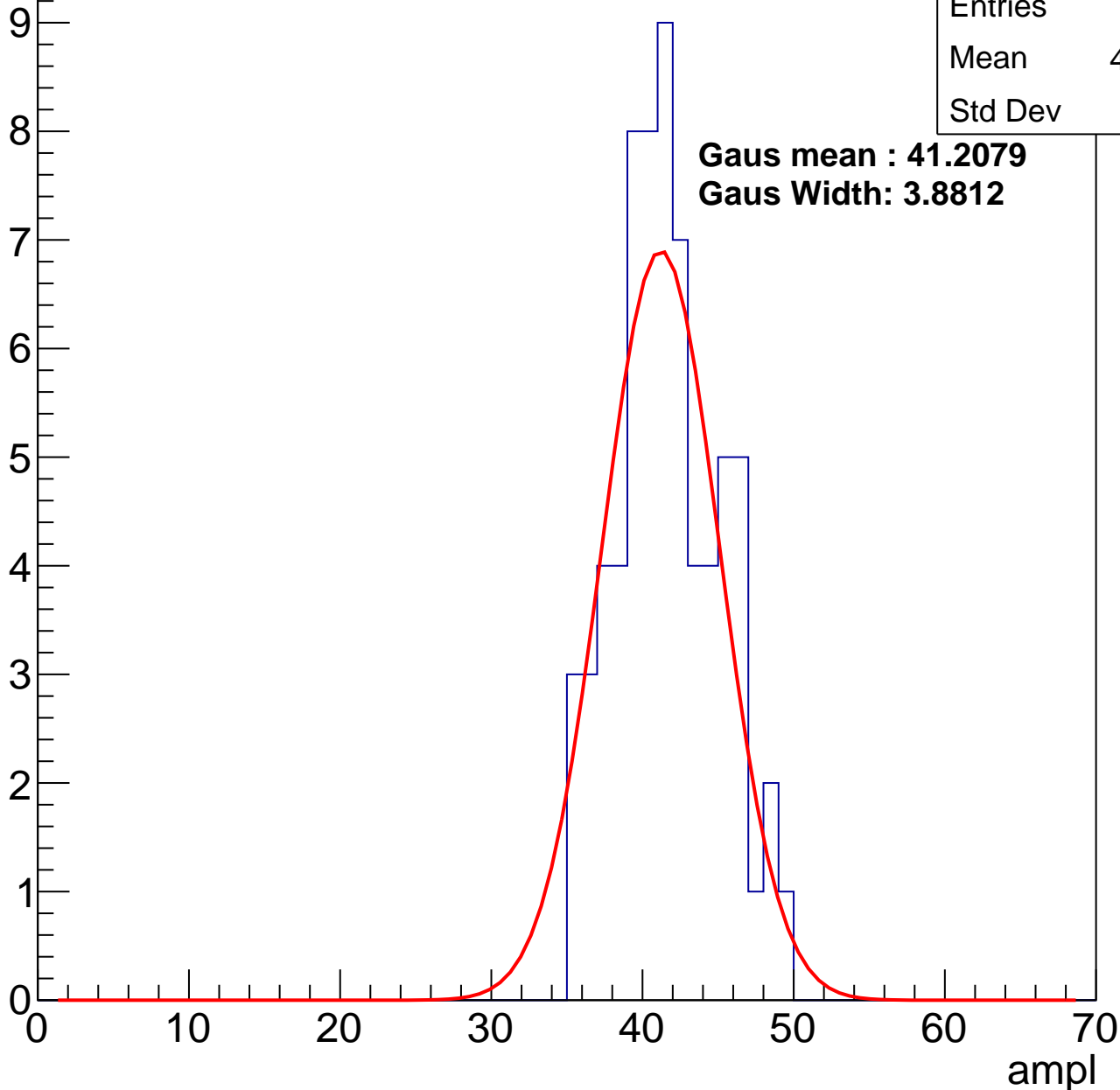
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.22
Std Dev	3.39

**Gaus mean : 41.2079**

**Gaus Width: 3.8812**

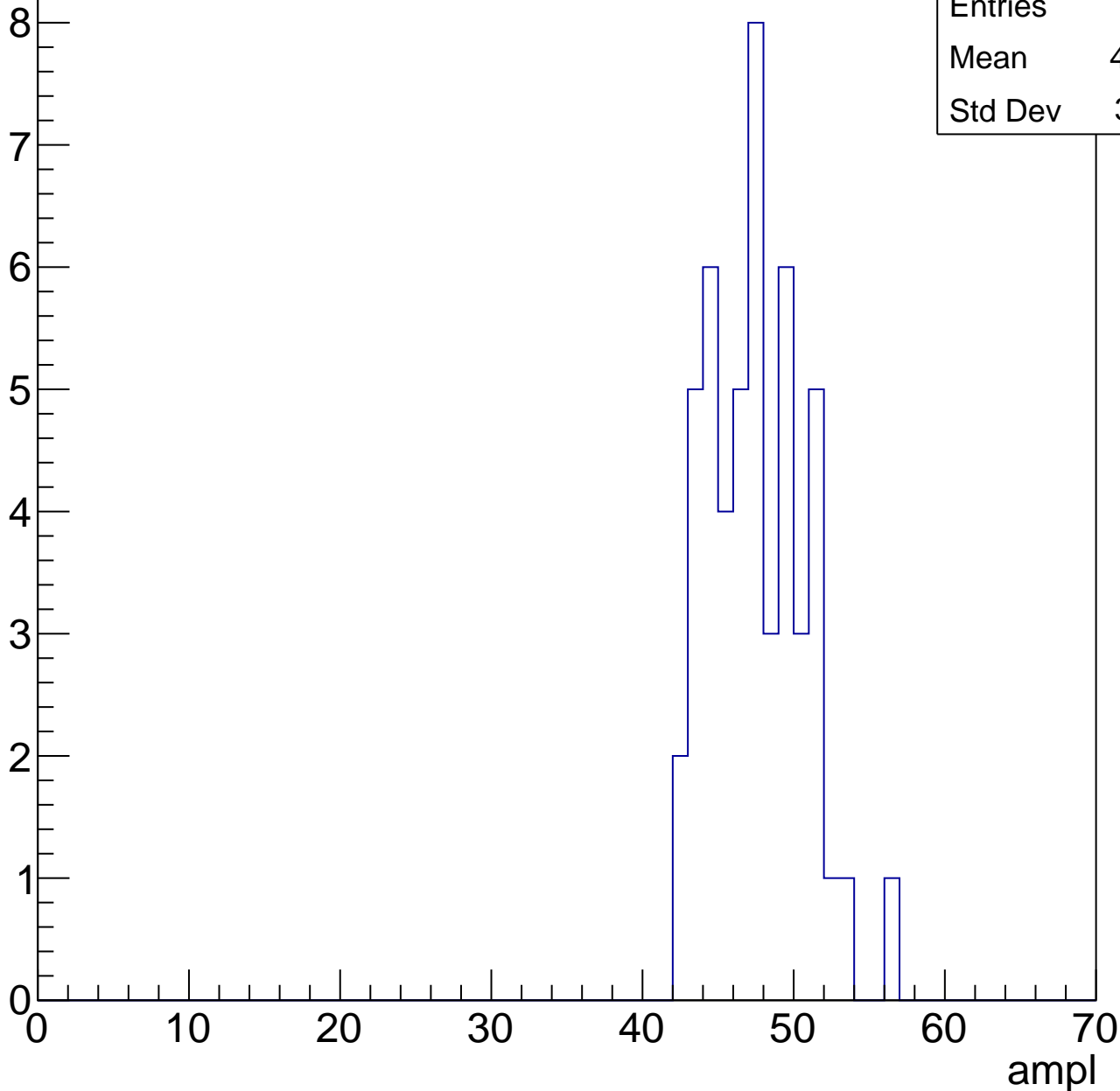


# B1L002S, U26-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	47.06
Std Dev	3.101

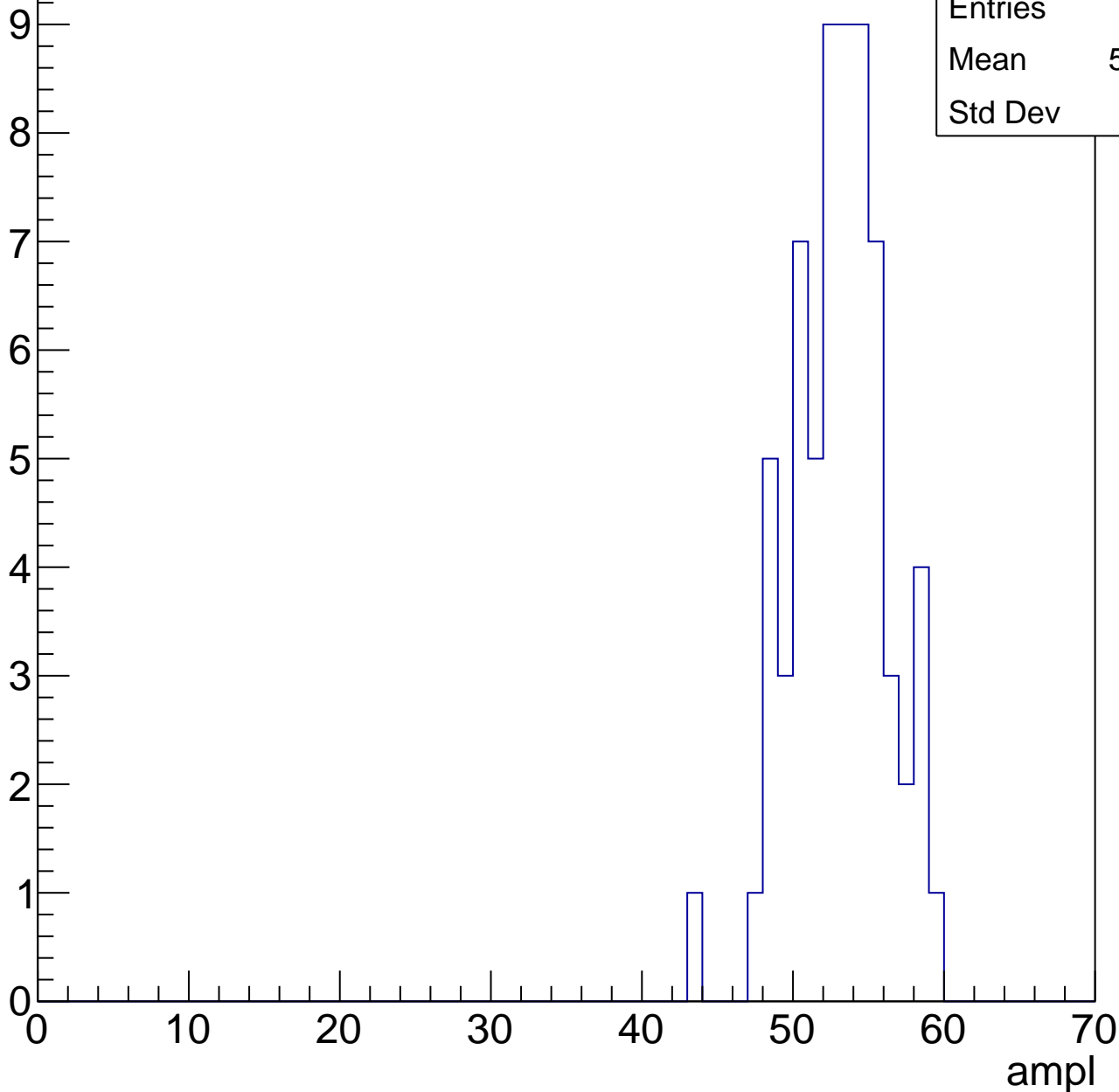


# B1L002S, U26-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	52.59
Std Dev	3.07

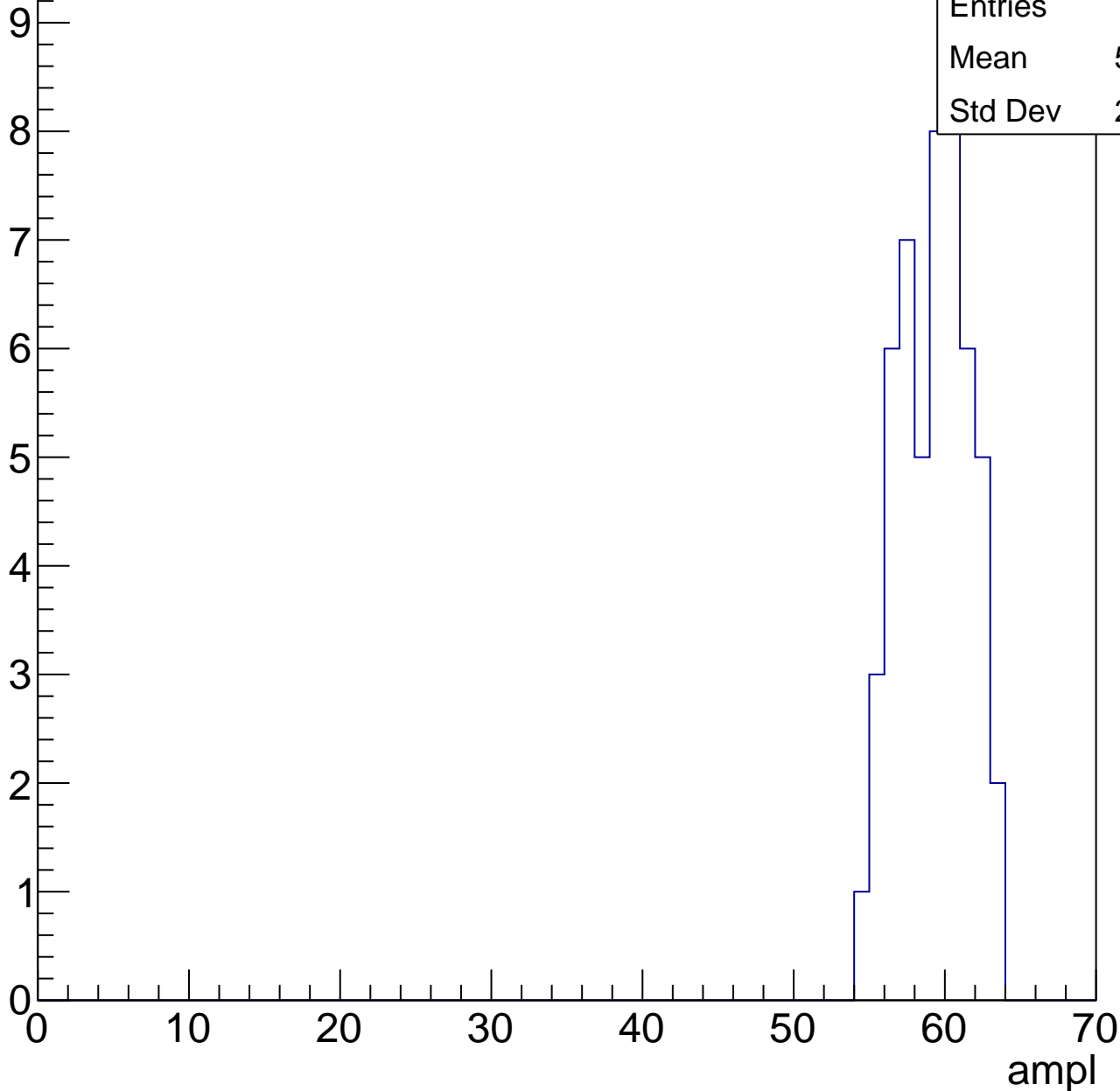


# B1L002S, U26-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.81
Std Dev	2.271

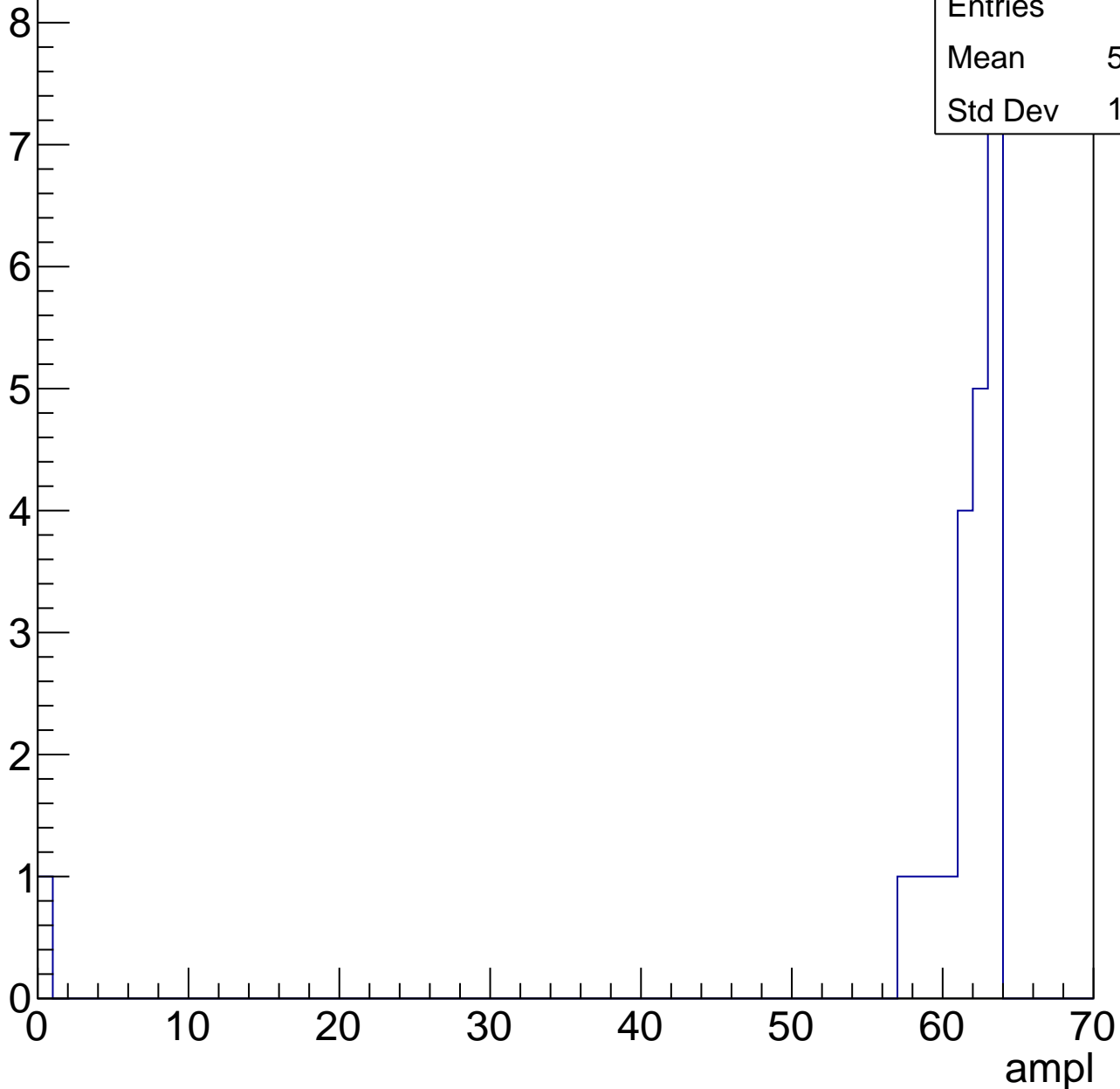


# B1L002S, U26-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.73
Std Dev	12.92





# B1L002S, U26-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch5, adc0

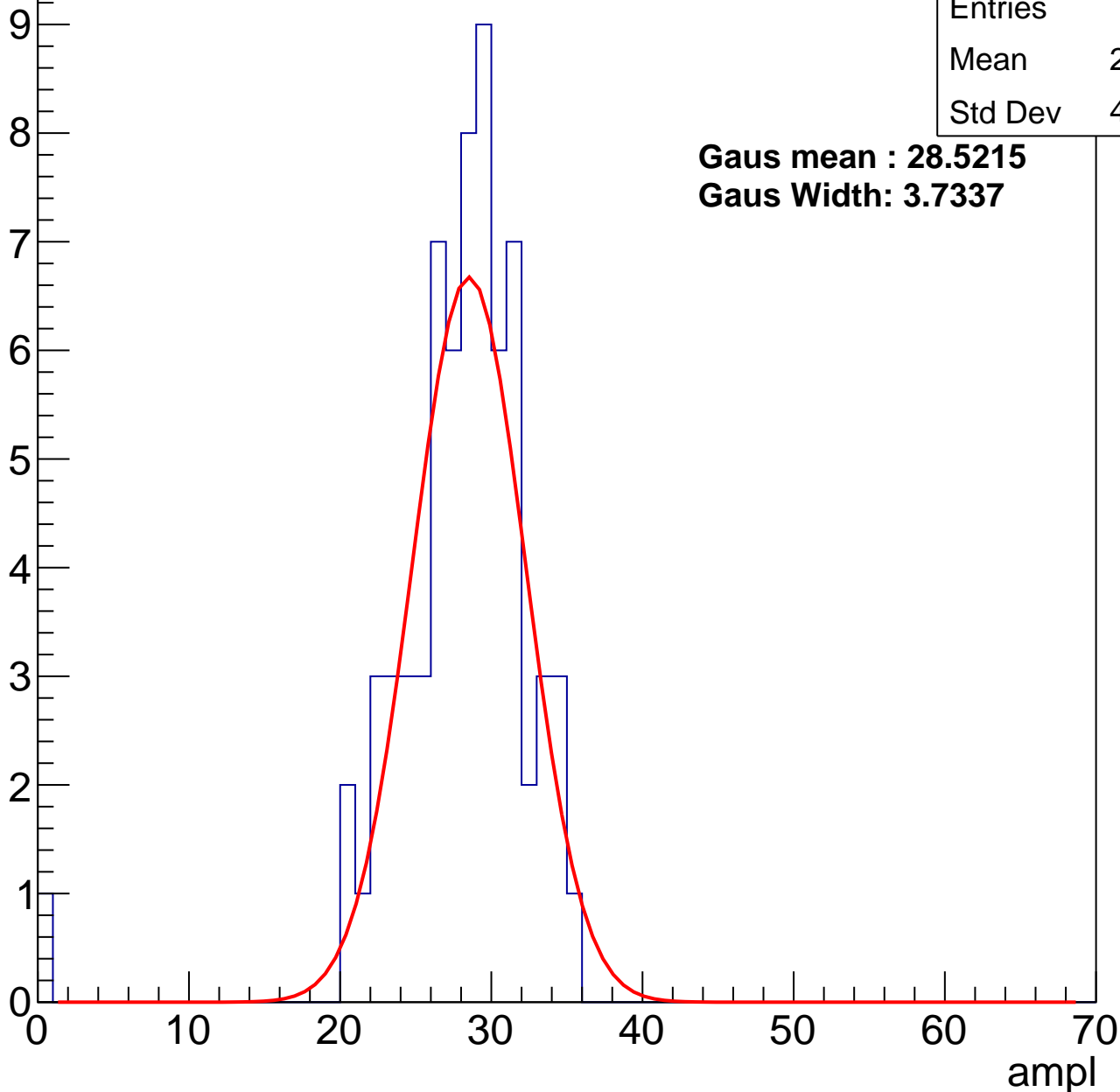
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.49
Std Dev	4.843

**Gaus mean : 28.5215**

**Gaus Width: 3.7337**



# B1L002S, U26-ch5, adc1

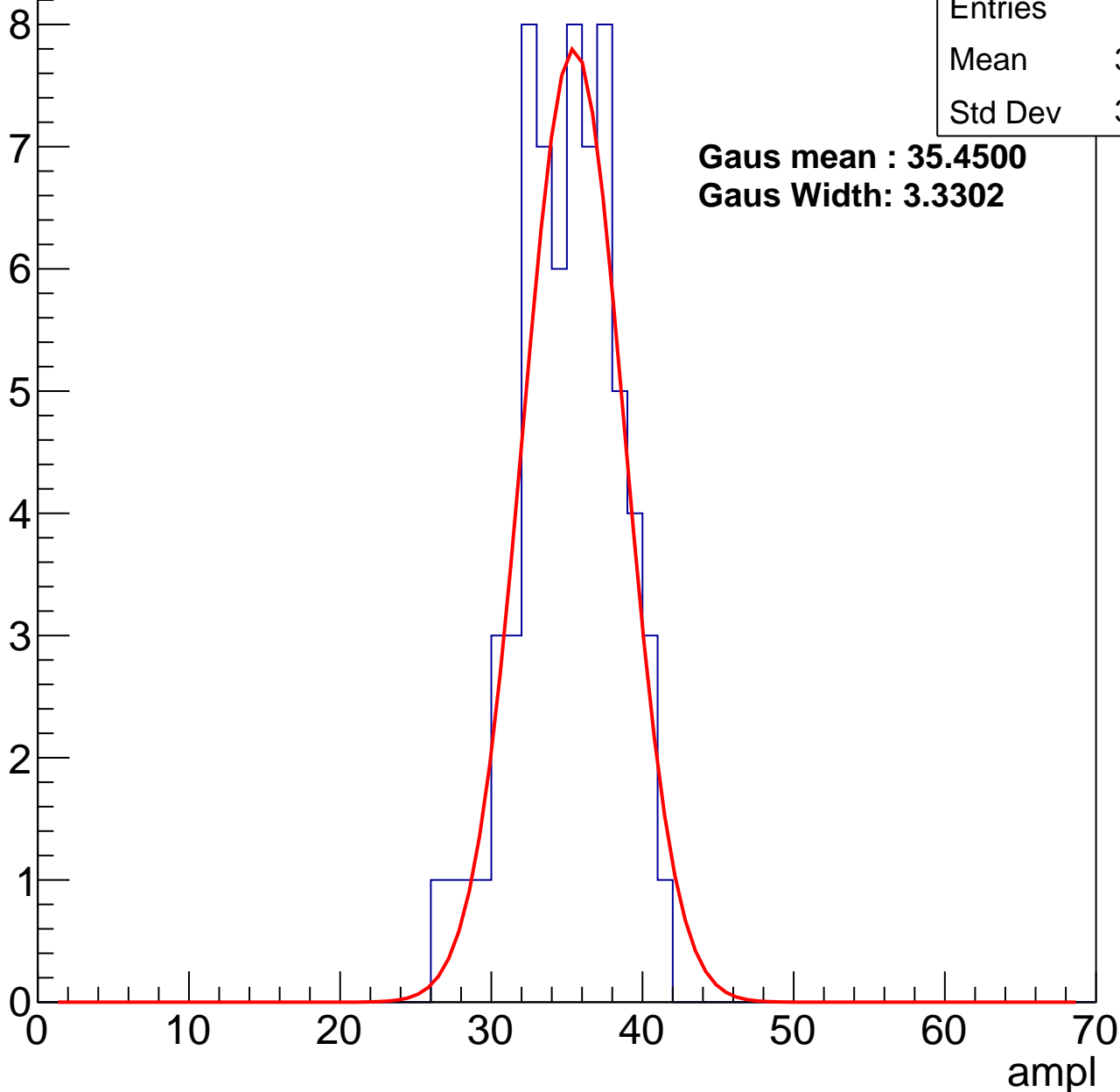
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	34.61
Std Dev	3.251

**Gaus mean : 35.4500**

**Gaus Width: 3.3302**



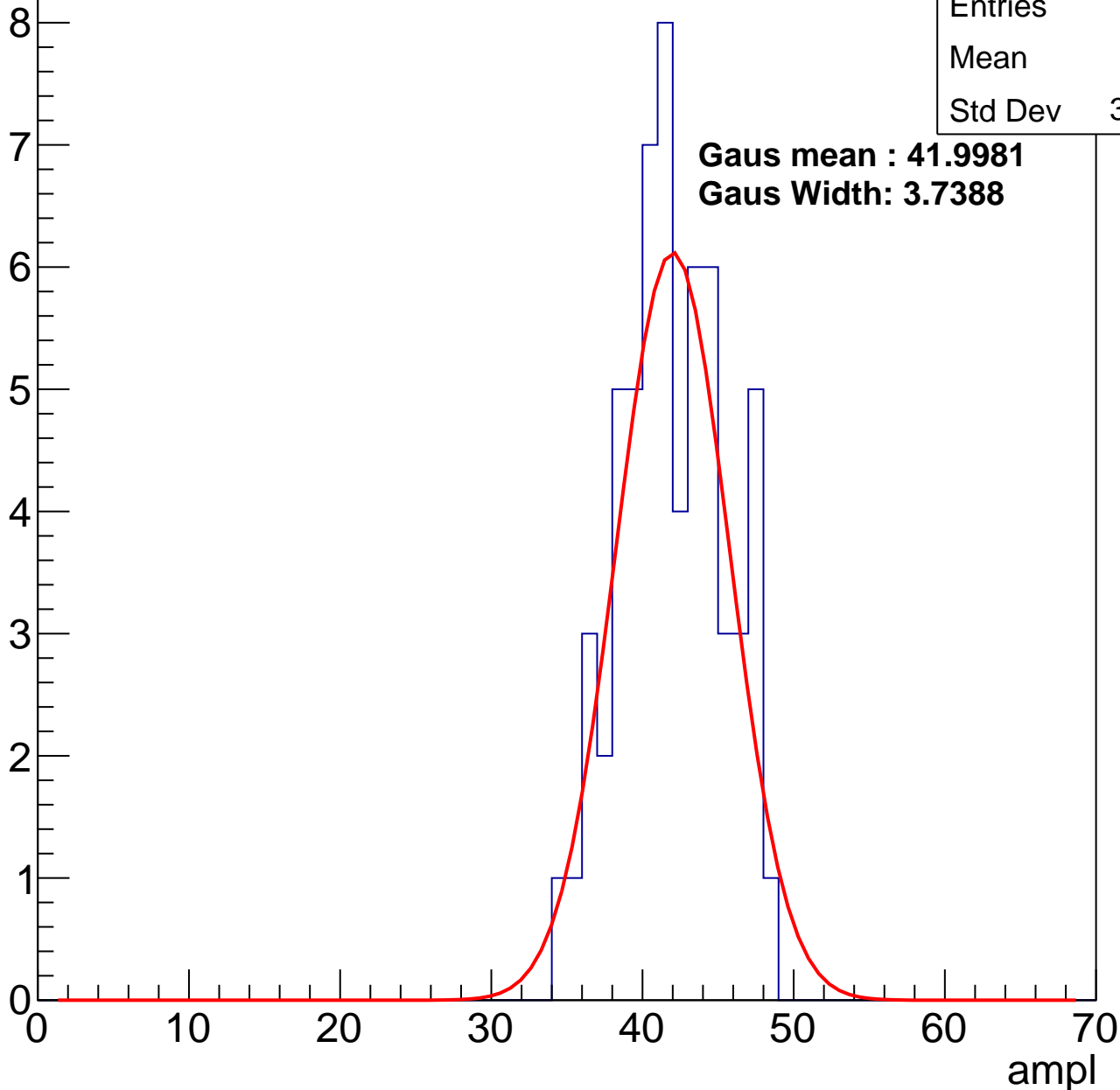
# B1L002S, U26-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.5
Std Dev	3.374

**Gaus mean : 41.9981**  
**Gaus Width: 3.7388**

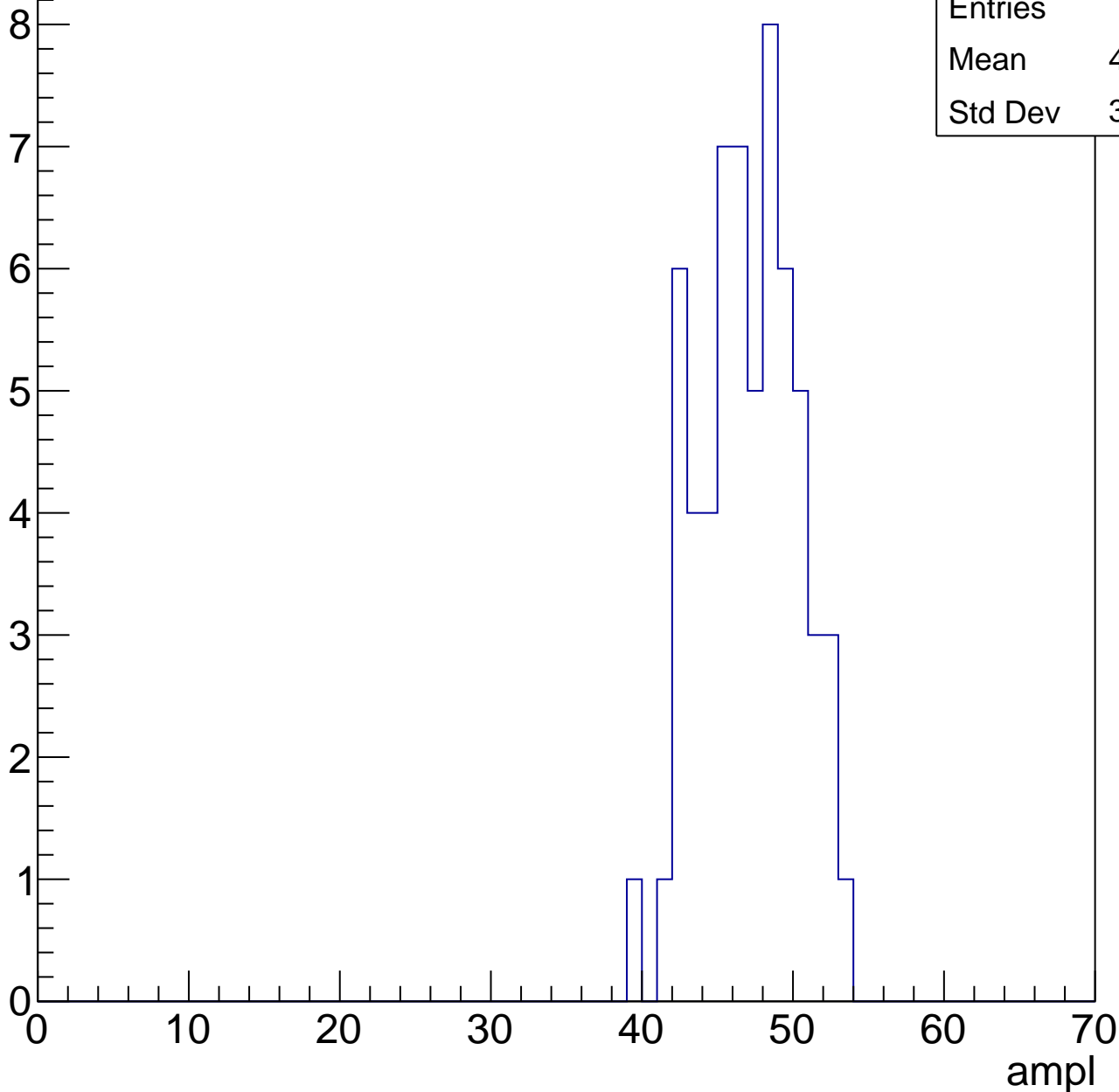


# B1L002S, U26-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

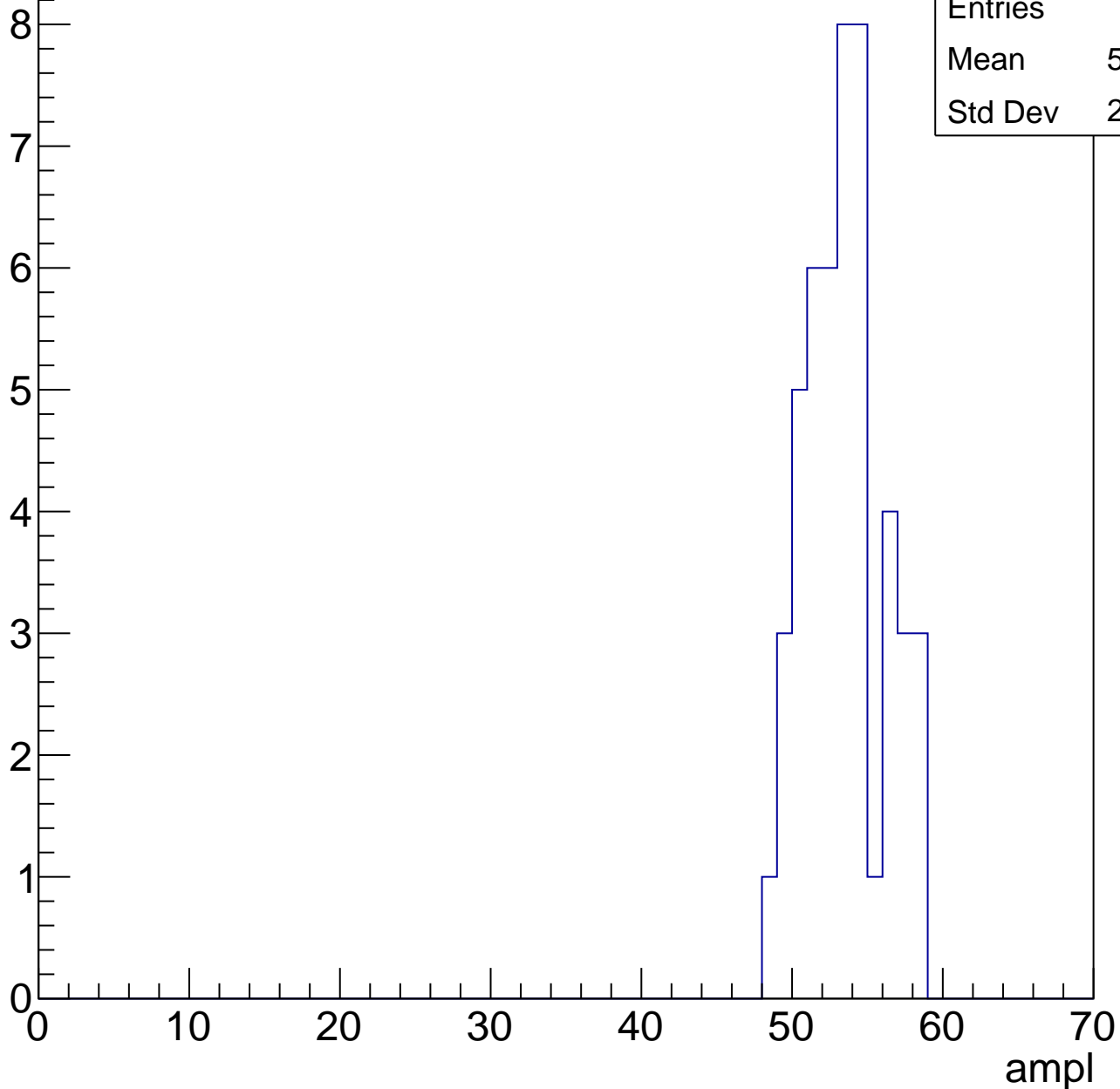
Entries	61
Mean	46.59
Std Dev	3.164



# B1L002S, U26-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

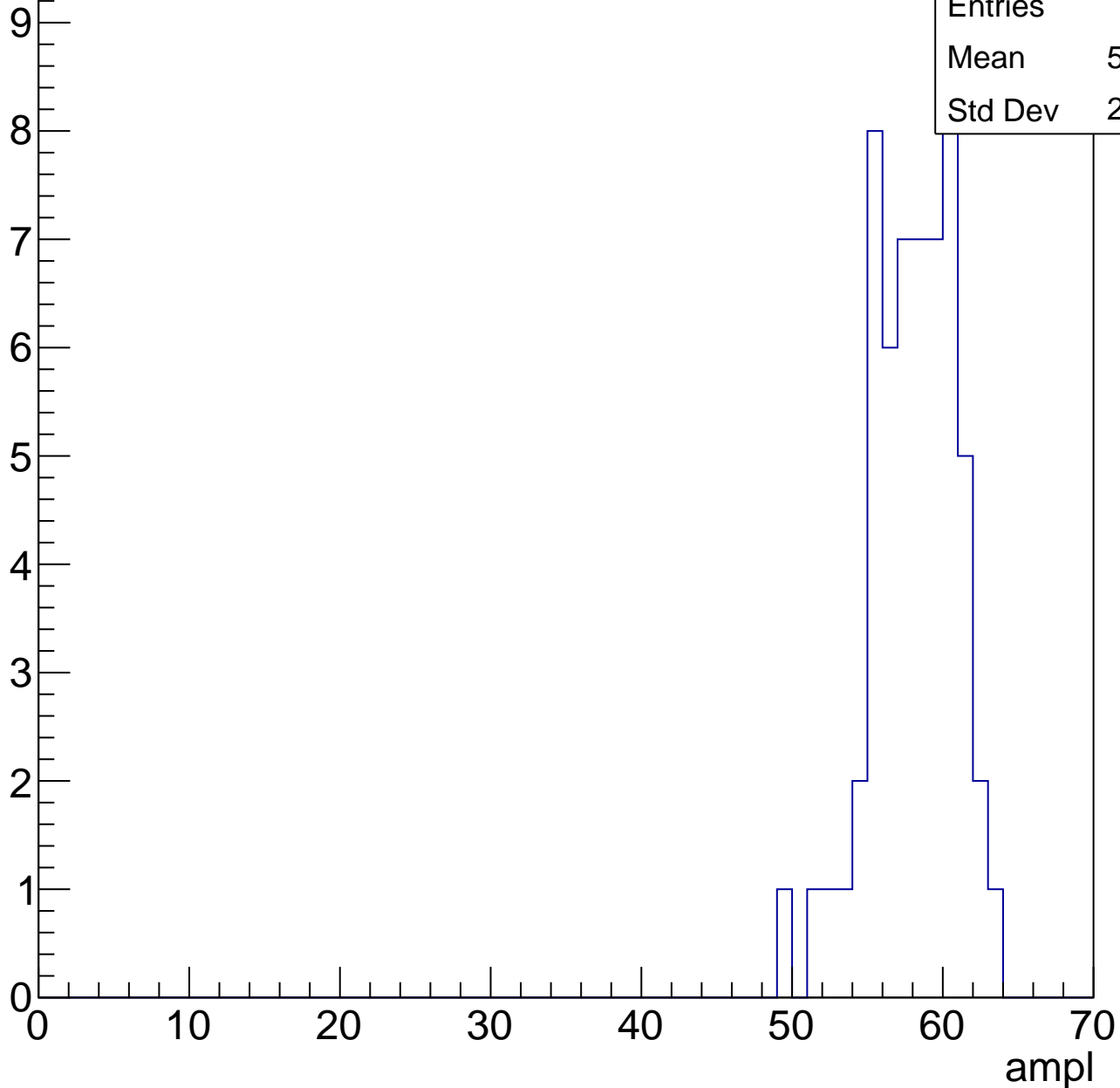


Entries	48
Mean	52.98
Std Dev	2.578

# B1L002S, U26-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



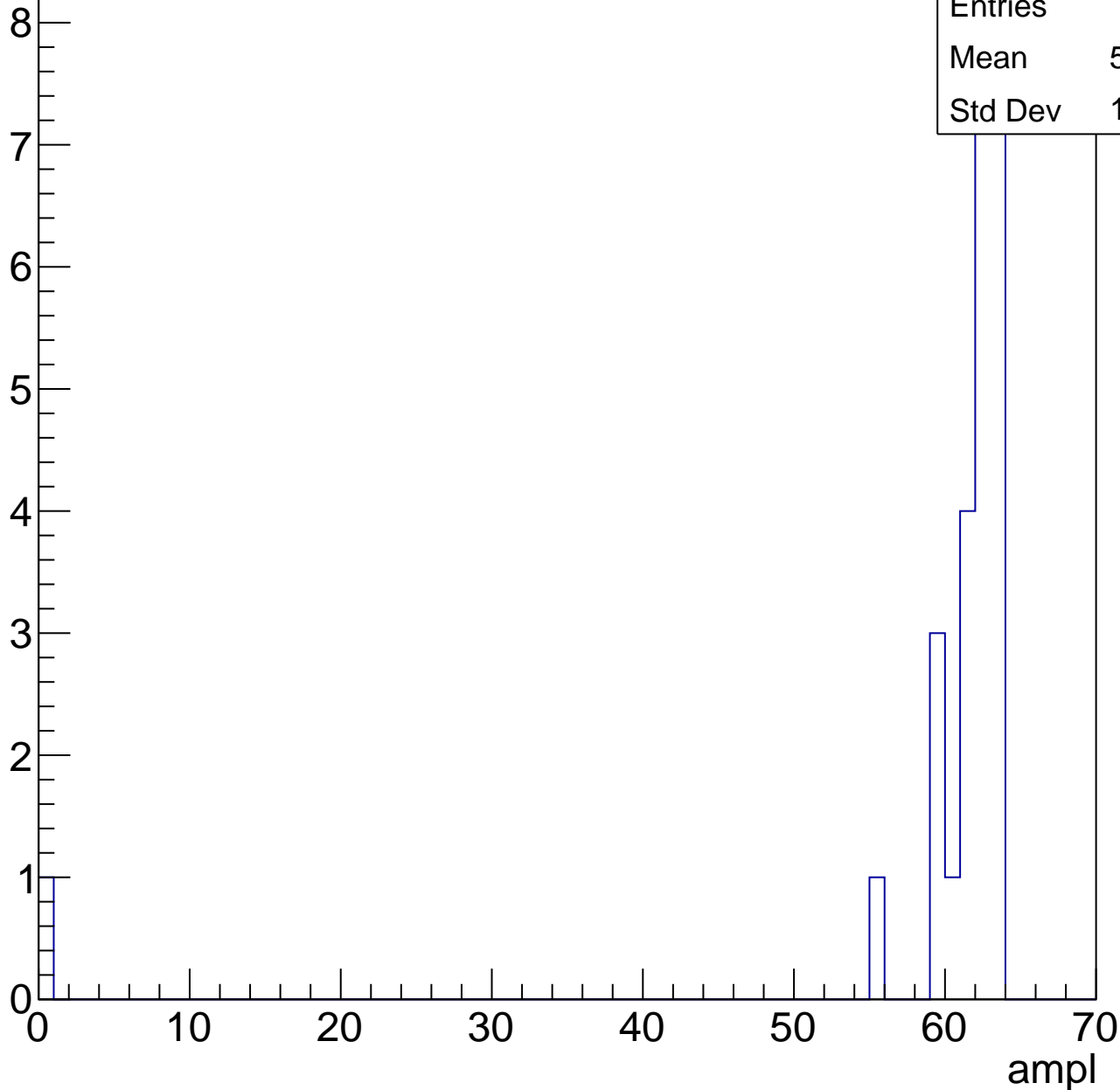
Entries	58
Mean	57.57
Std Dev	2.817

# B1L002S, U26-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	59.08
Std Dev	11.95

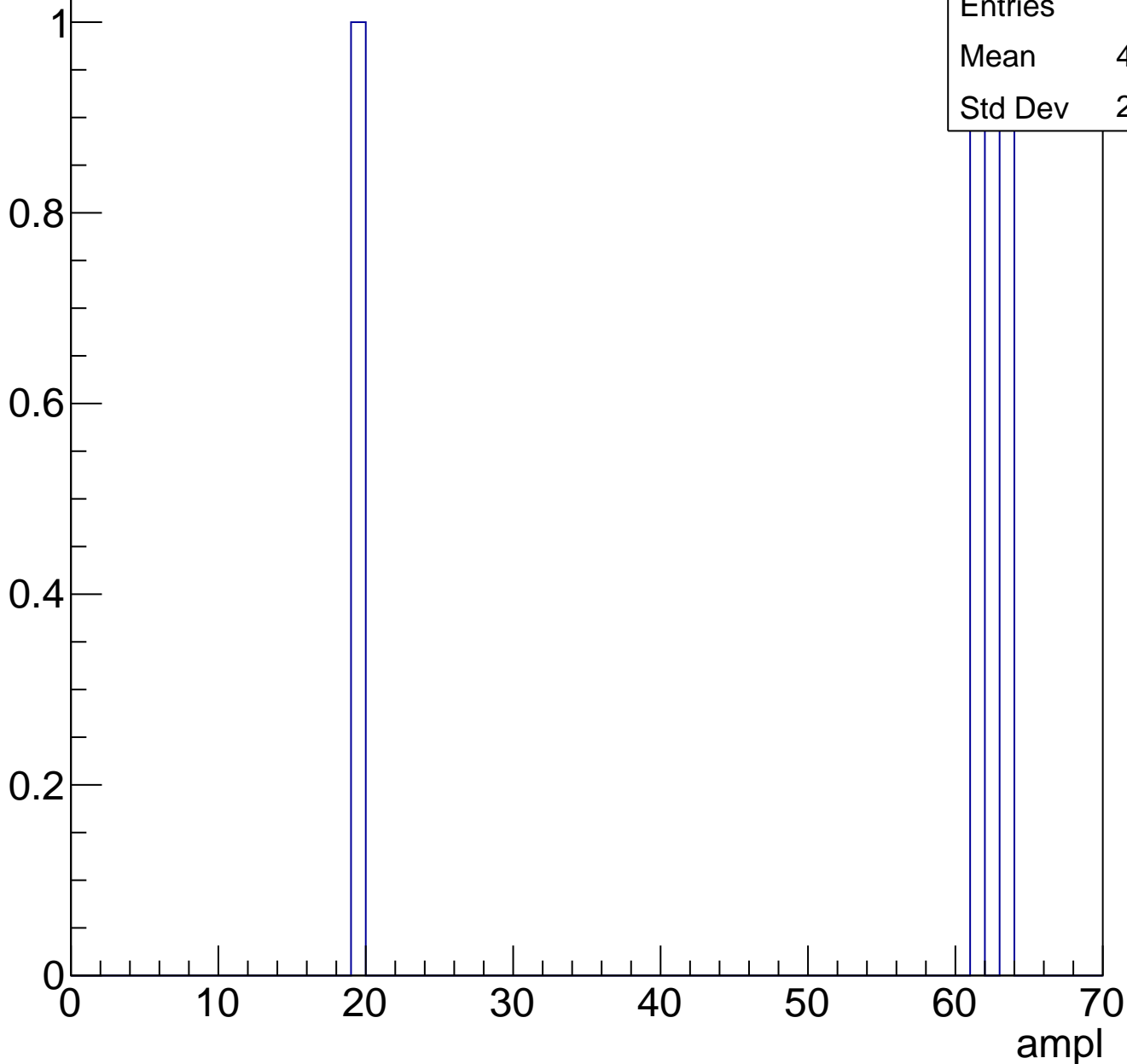




# B1L002S, U26-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch6, adc0

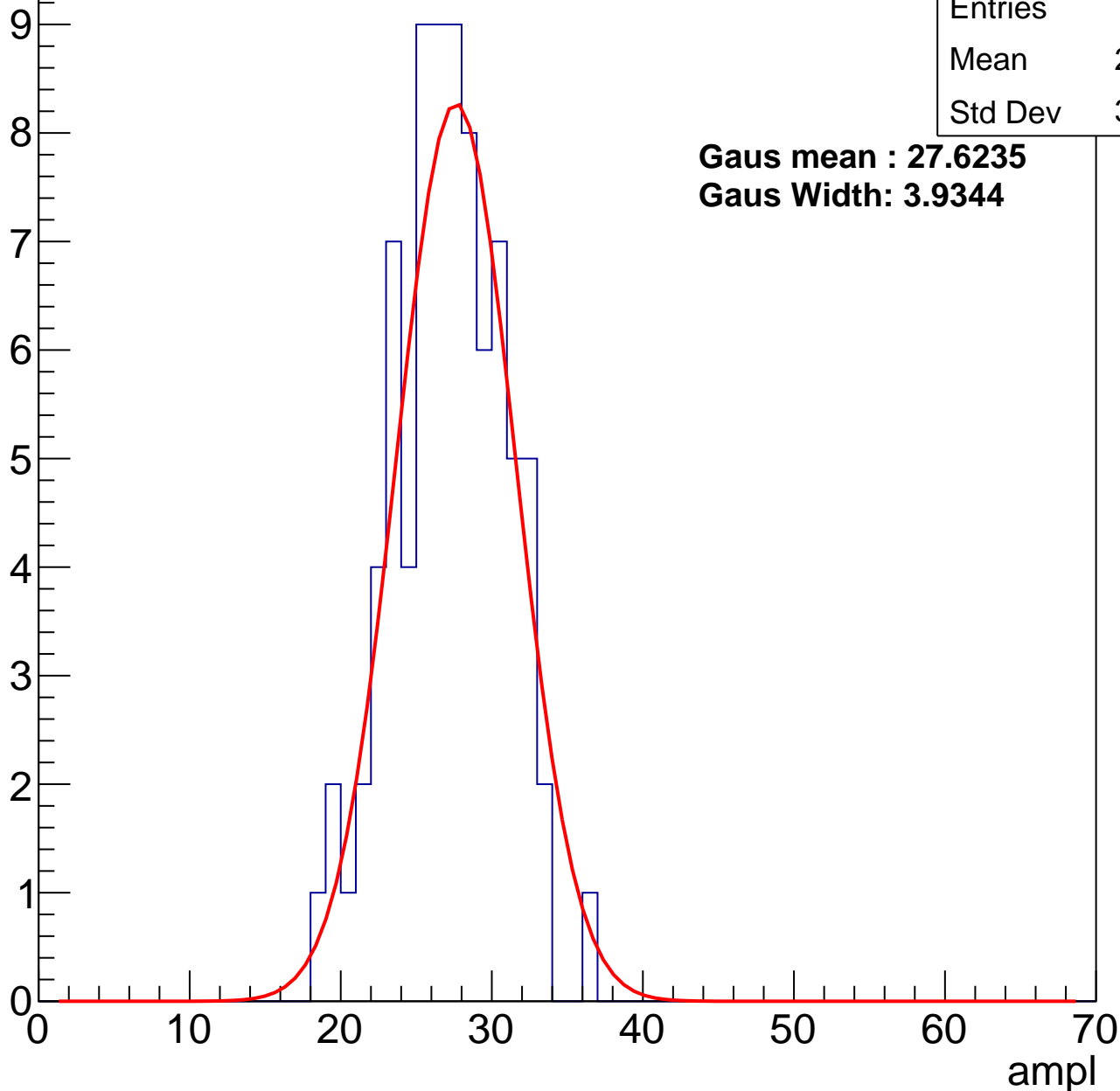
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	26.71
Std Dev	3.611

**Gaus mean : 27.6235**

**Gaus Width: 3.9344**



# B1L002S, U26-ch6, adc1

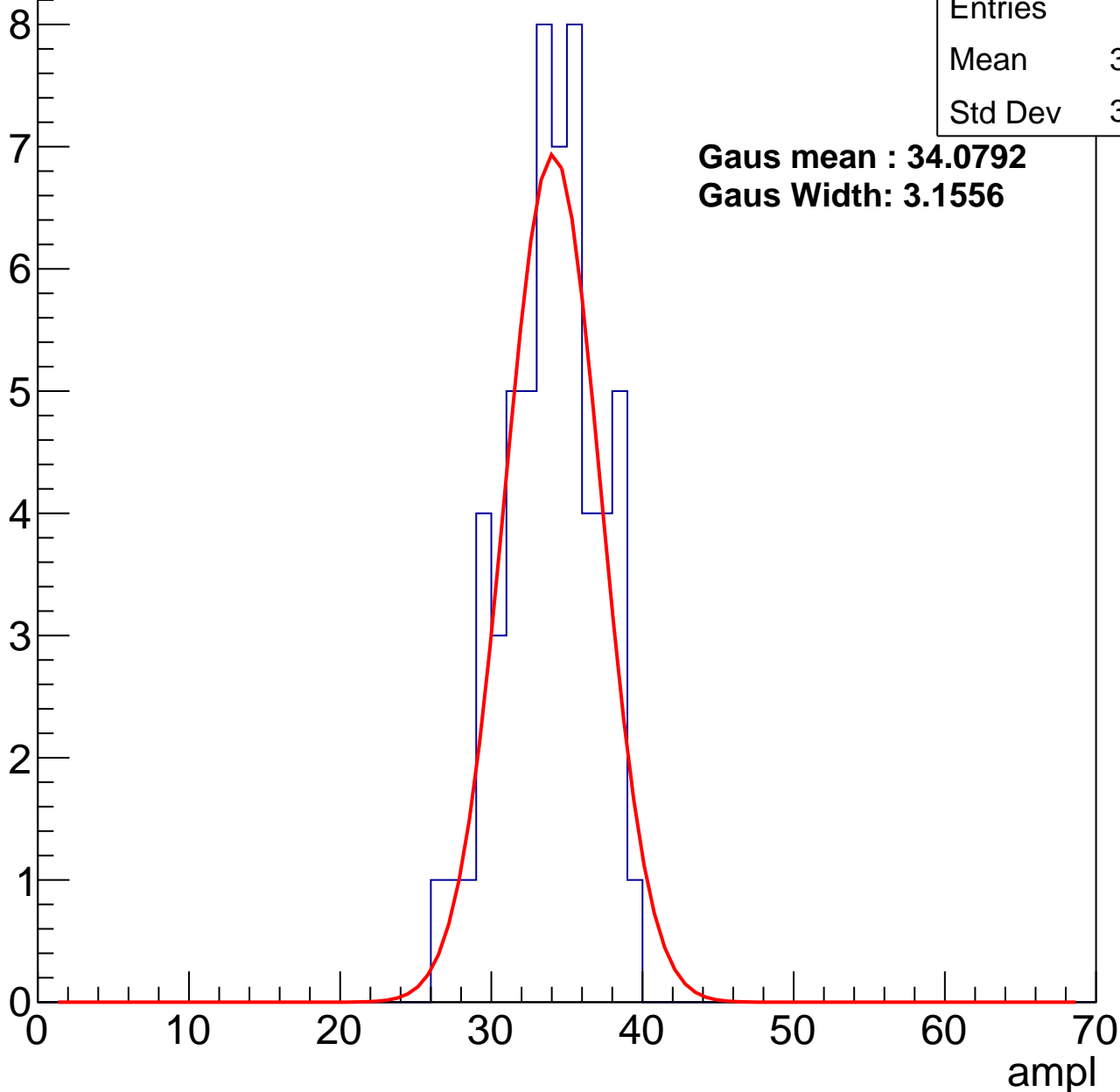
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.42
Std Dev	3.008

**Gaus mean : 34.0792**

**Gaus Width: 3.1556**



# B1L002S, U26-ch6, adc2

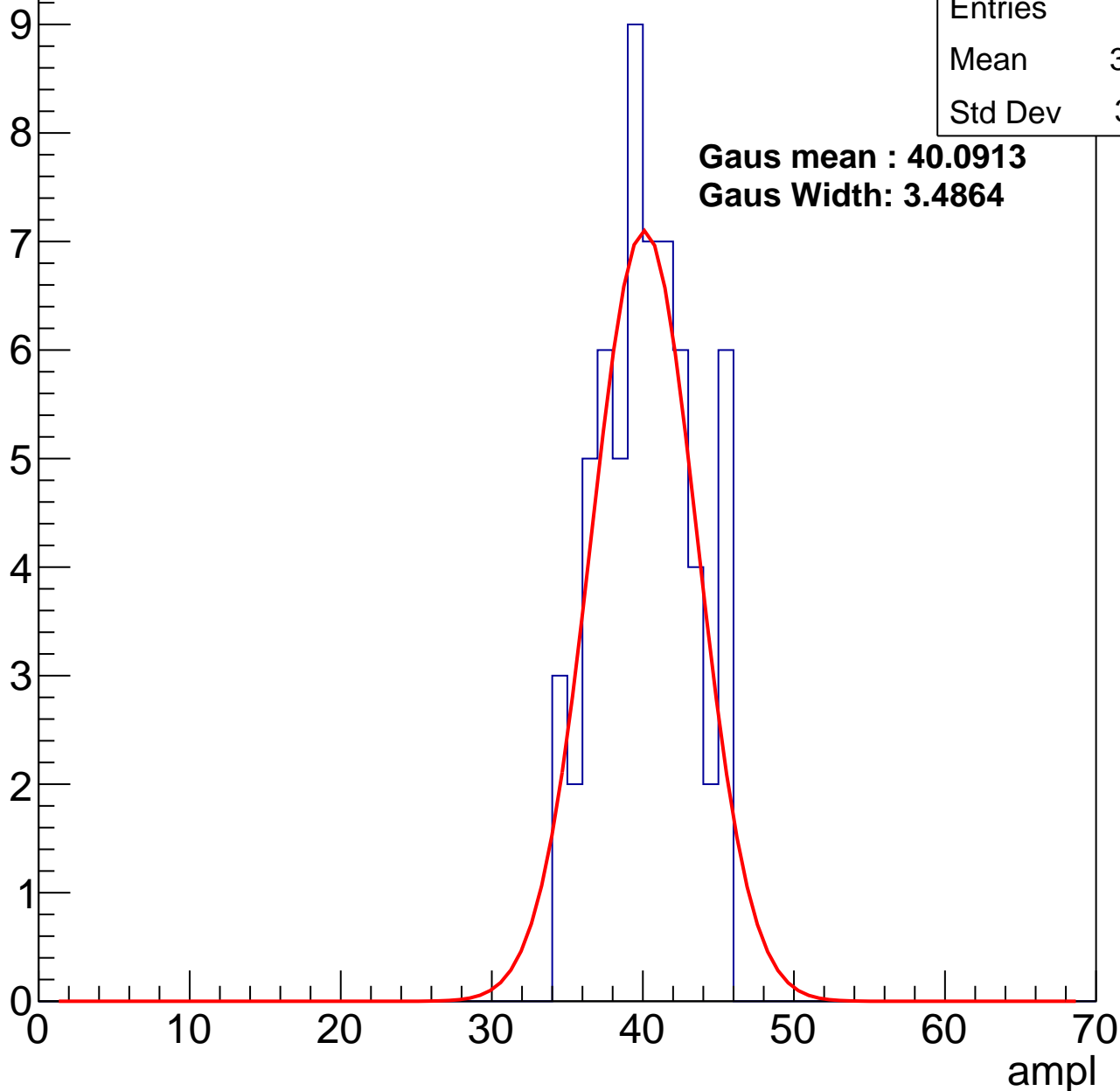
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.74
Std Dev	3.021

**Gaus mean : 40.0913**

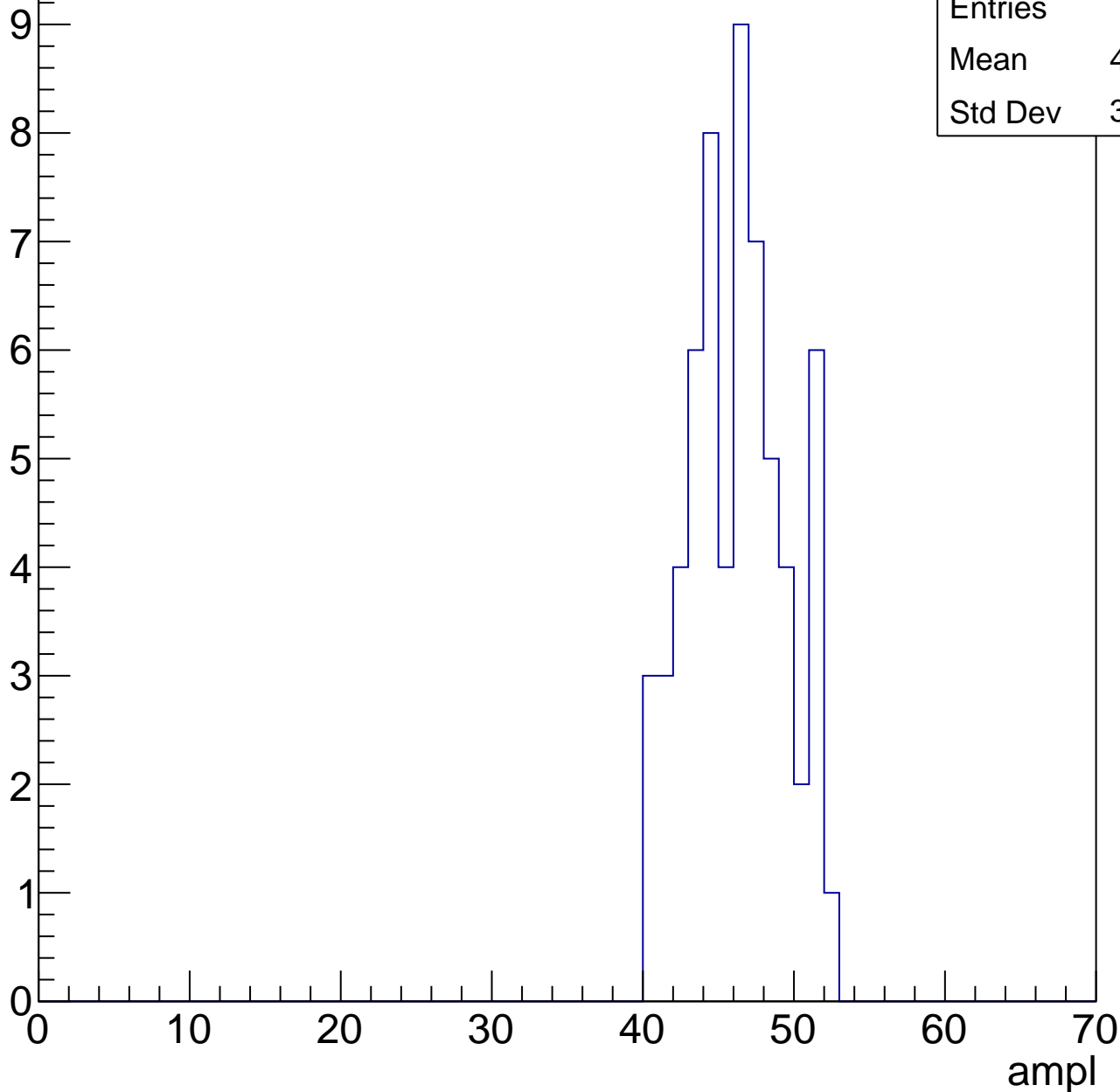
**Gaus Width: 3.4864**



# B1L002S, U26-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

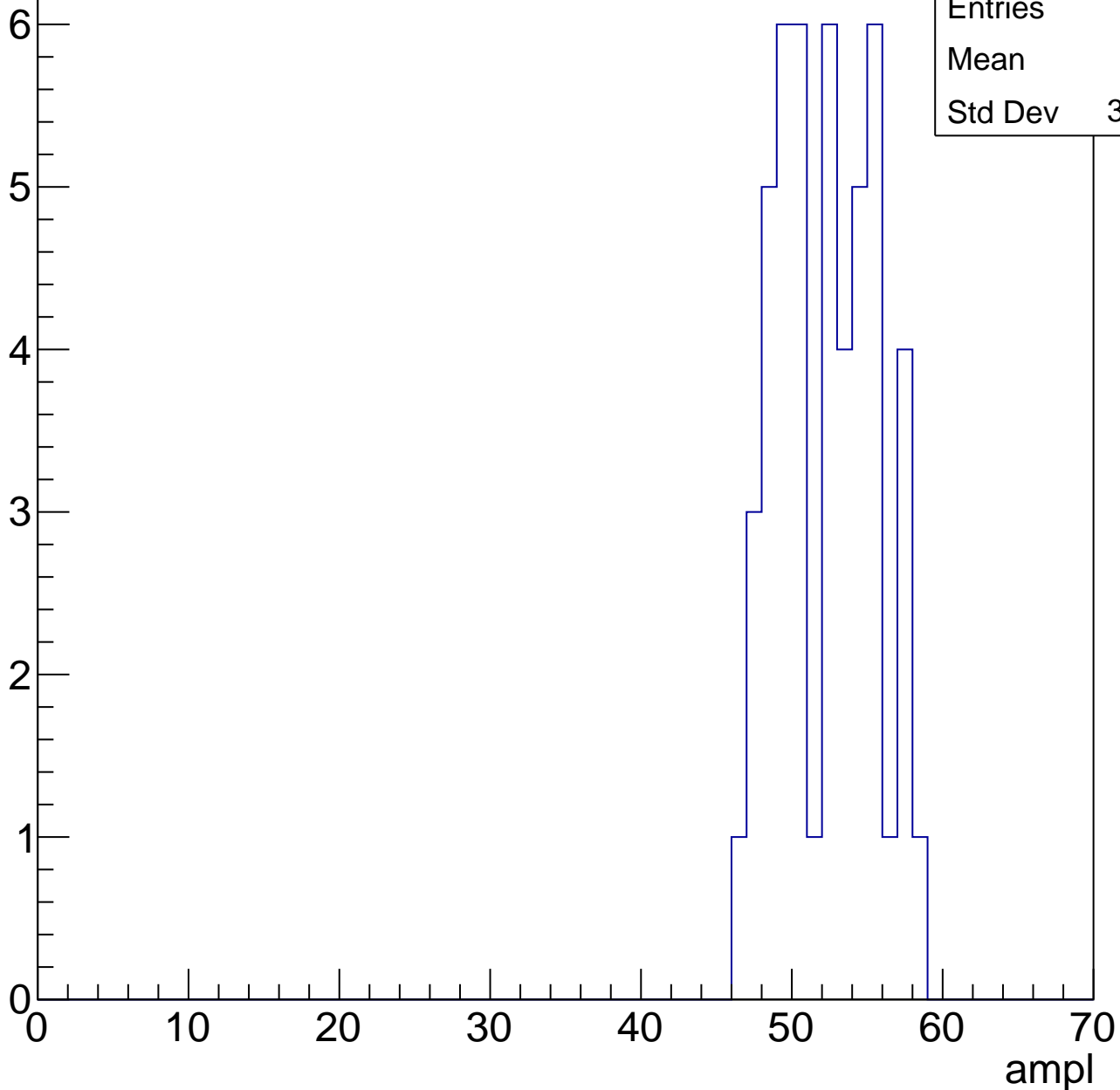


# B1L002S, U26-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

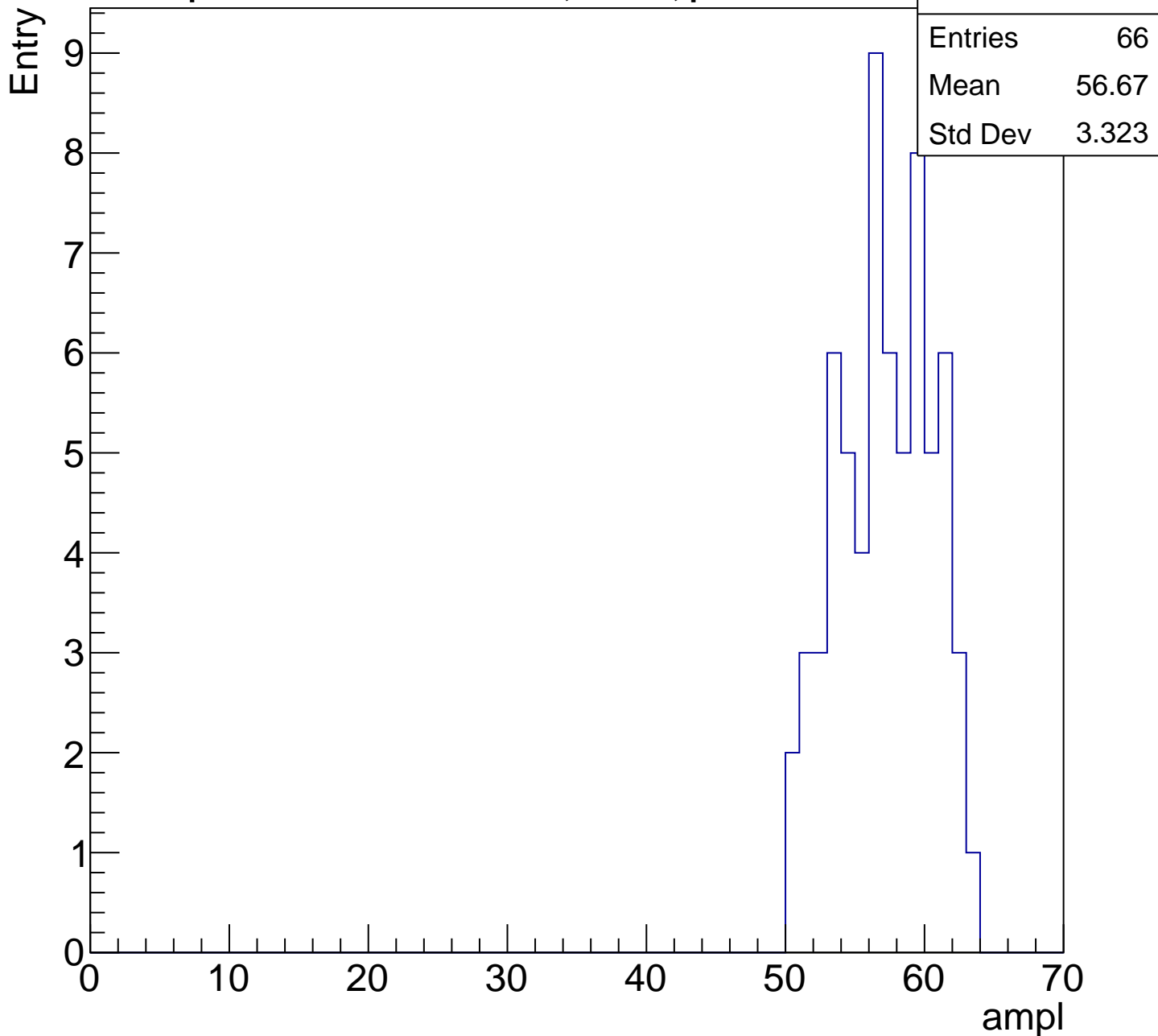
Entry

Entries	49
Mean	51.8
Std Dev	3.188



# B1L002S, U26-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

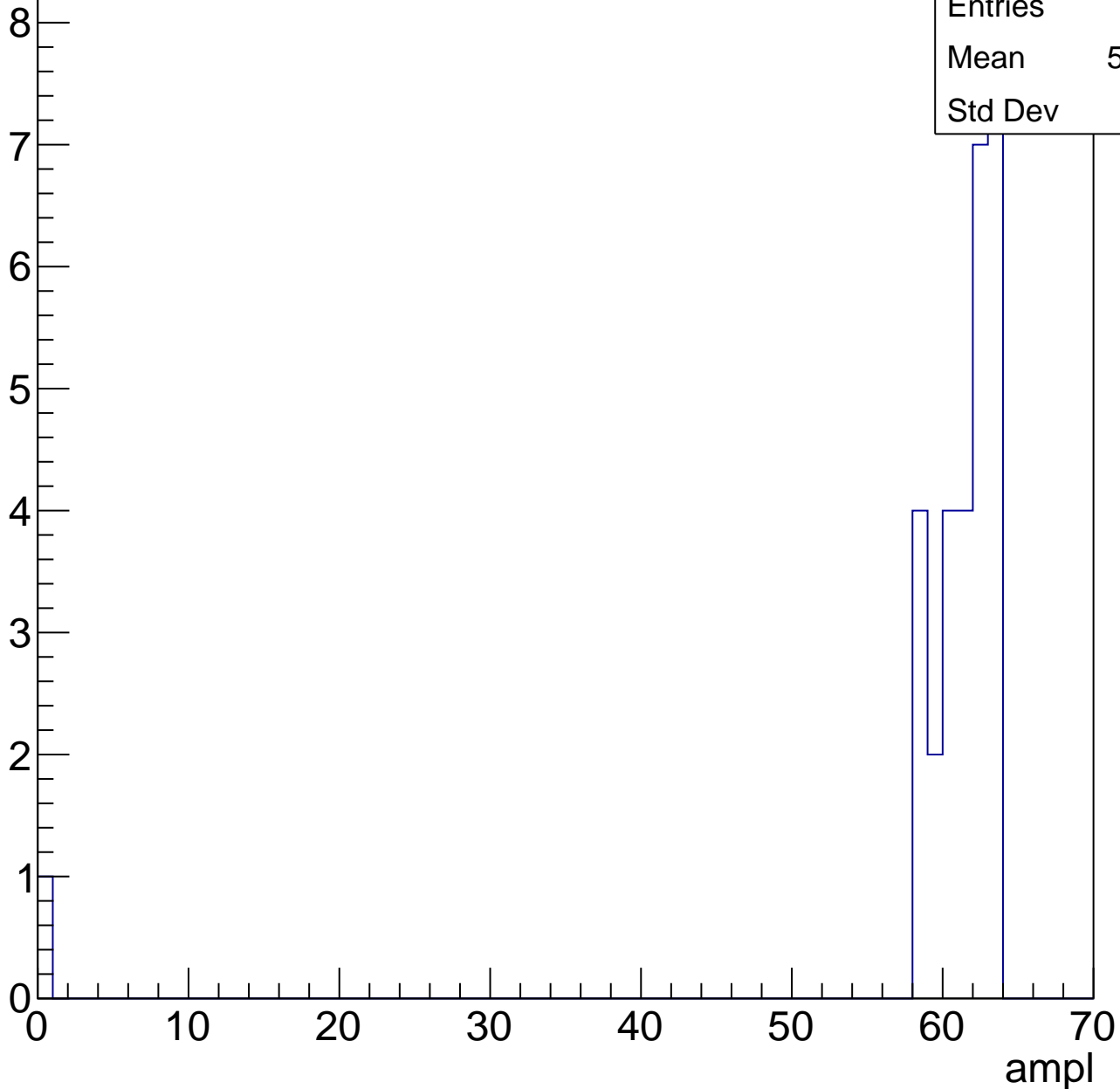


# B1L002S, U26-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59.07
Std Dev	11.1

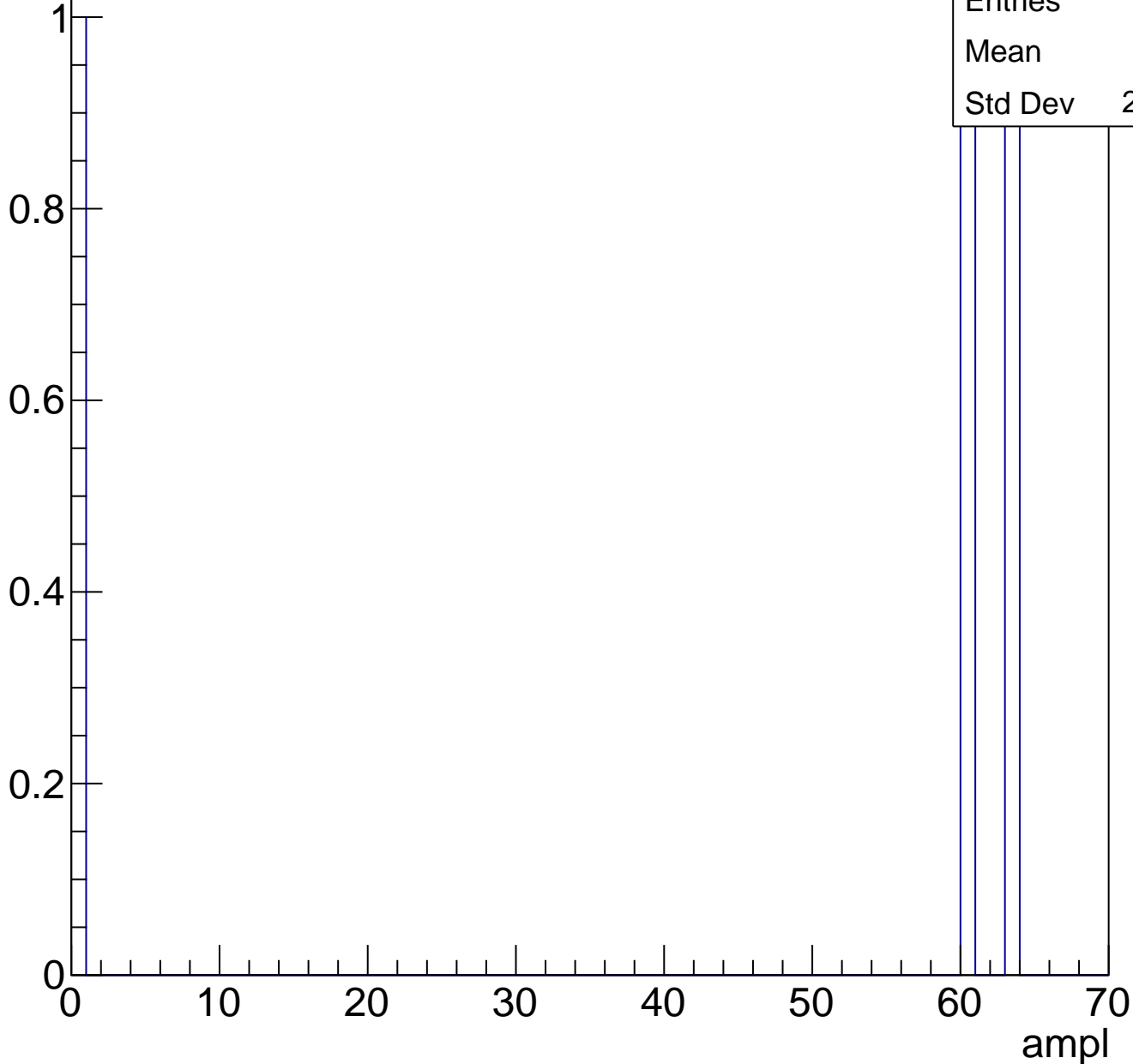




# B1L002S, U26-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch7, adc0

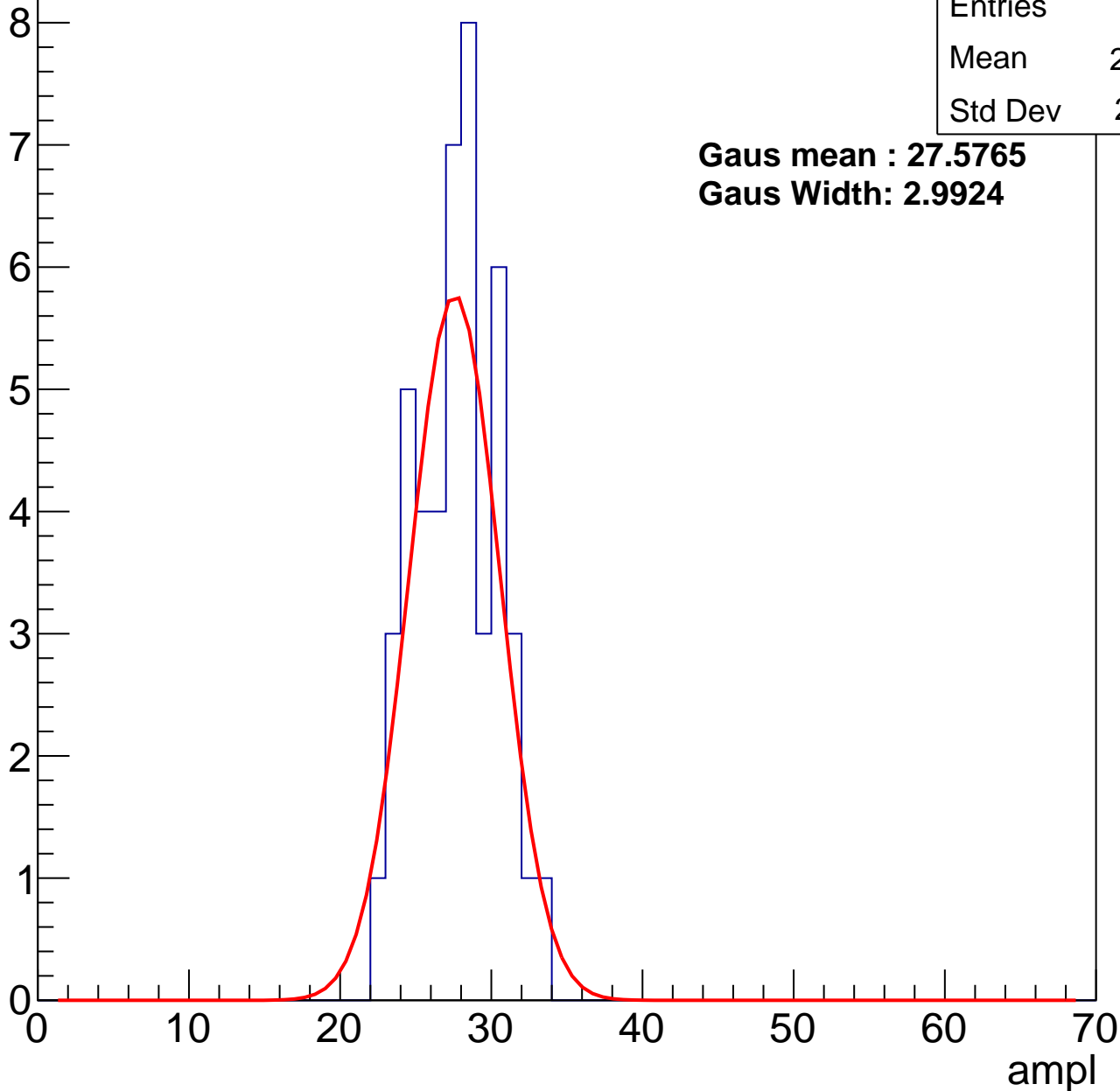
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	27.24
Std Dev	2.631

**Gaus mean : 27.5765**

**Gaus Width: 2.9924**



# B1L002S, U26-ch7, adc1

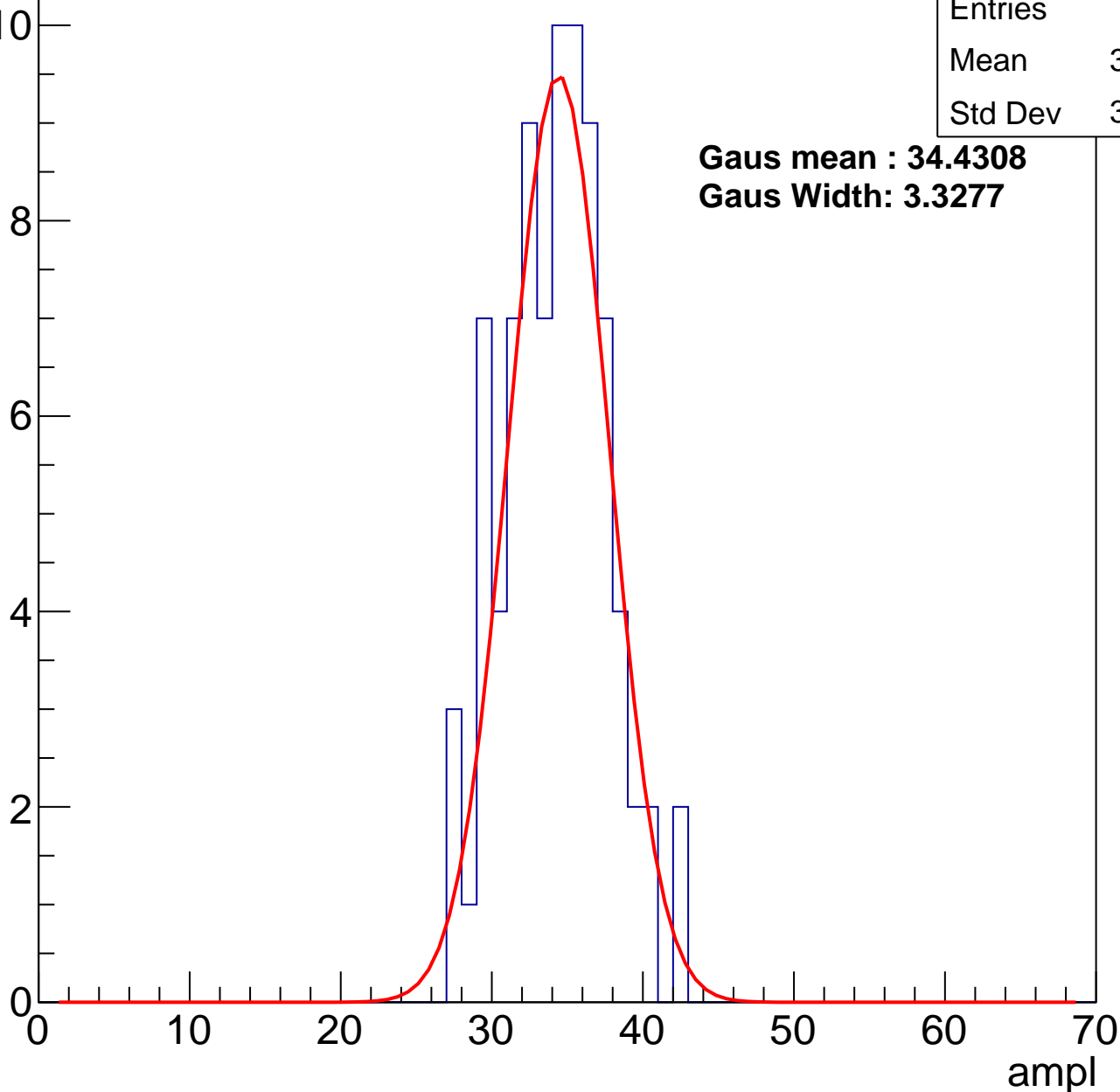
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	84
Mean	33.75
Std Dev	3.355

**Gaus mean : 34.4308**

**Gaus Width: 3.3277**



# B1L002S, U26-ch7, adc2

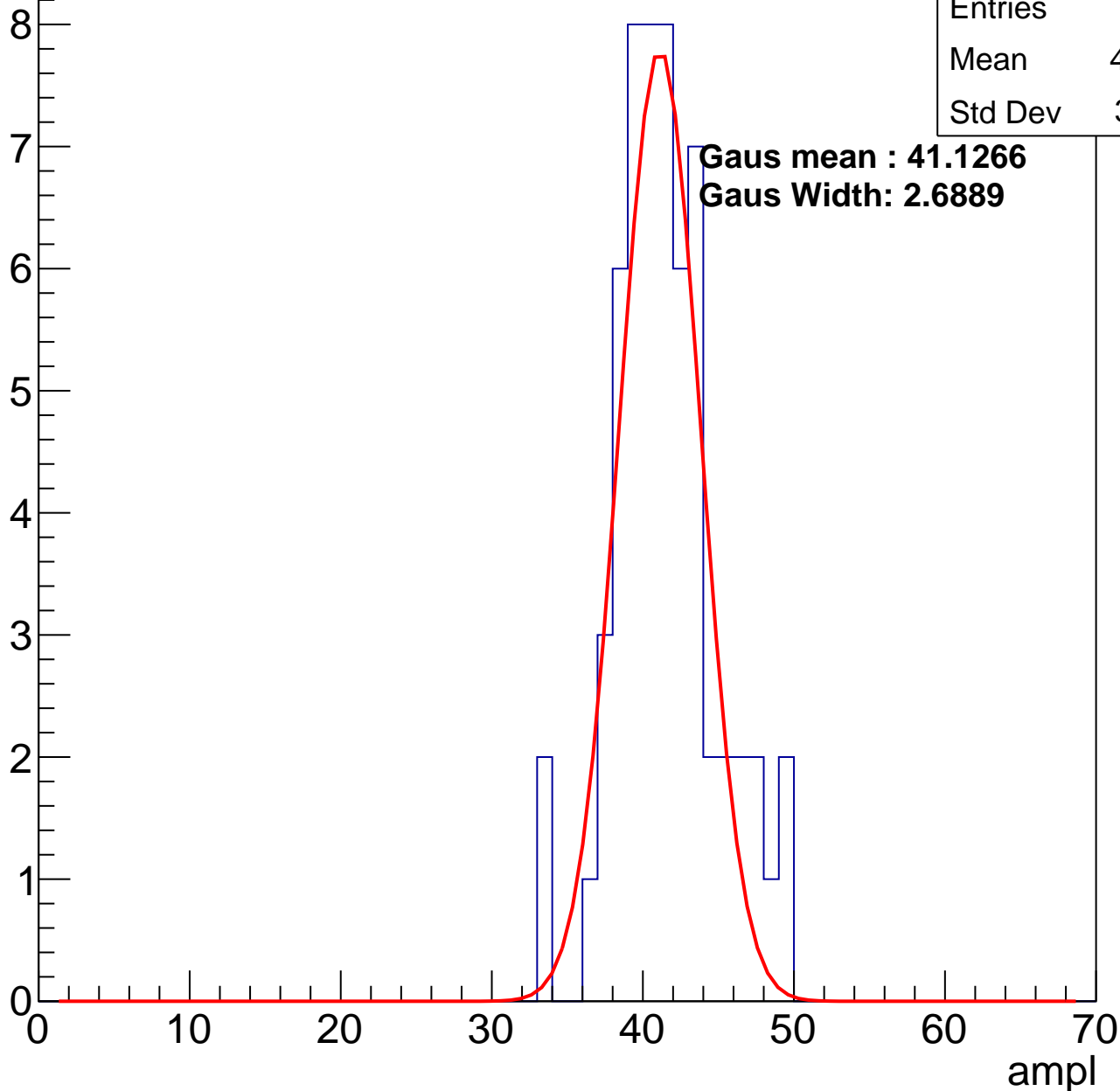
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.07
Std Dev	3.361

**Gaus mean : 41.1266**

**Gaus Width: 2.6889**

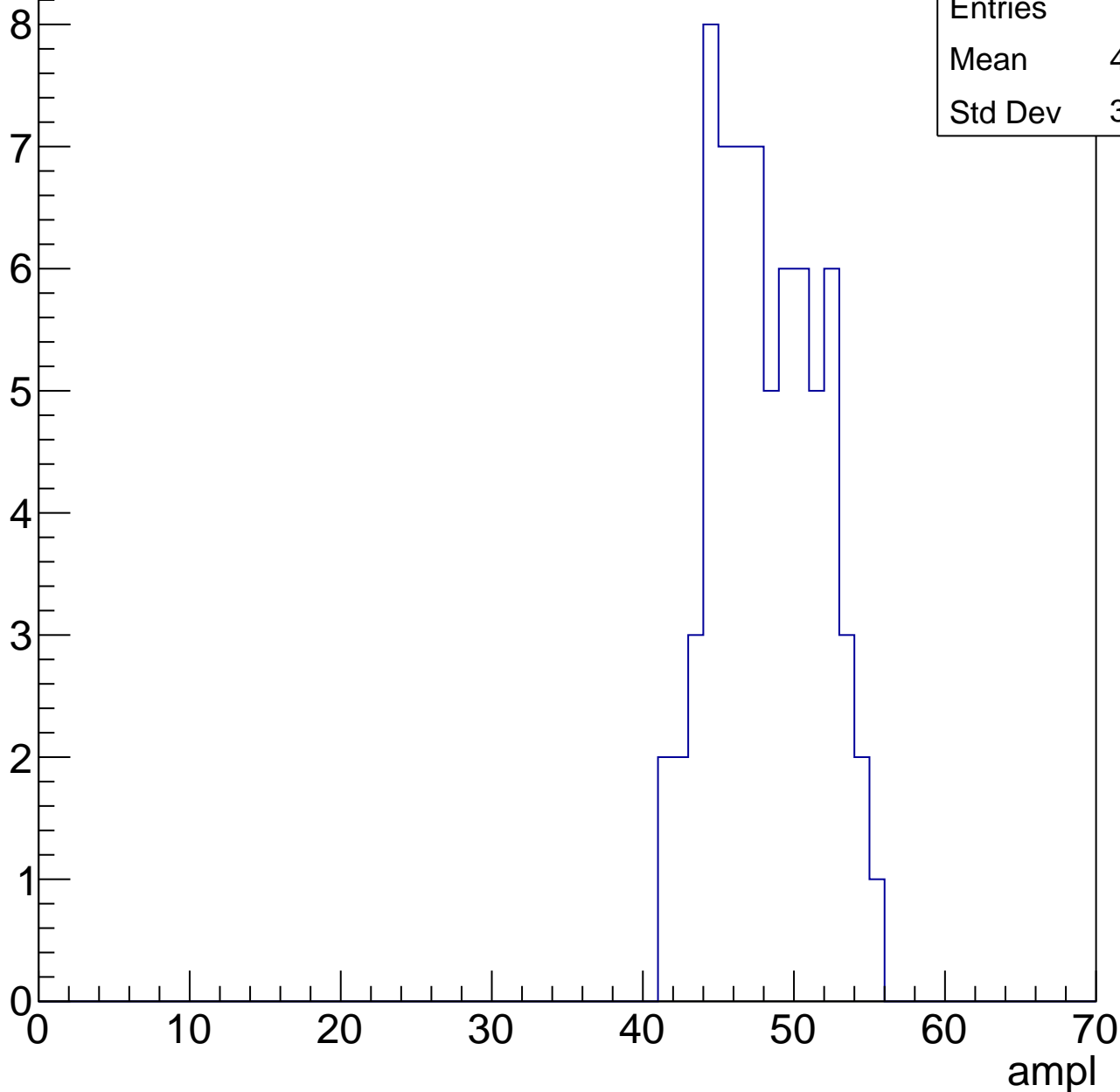


# B1L002S, U26-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

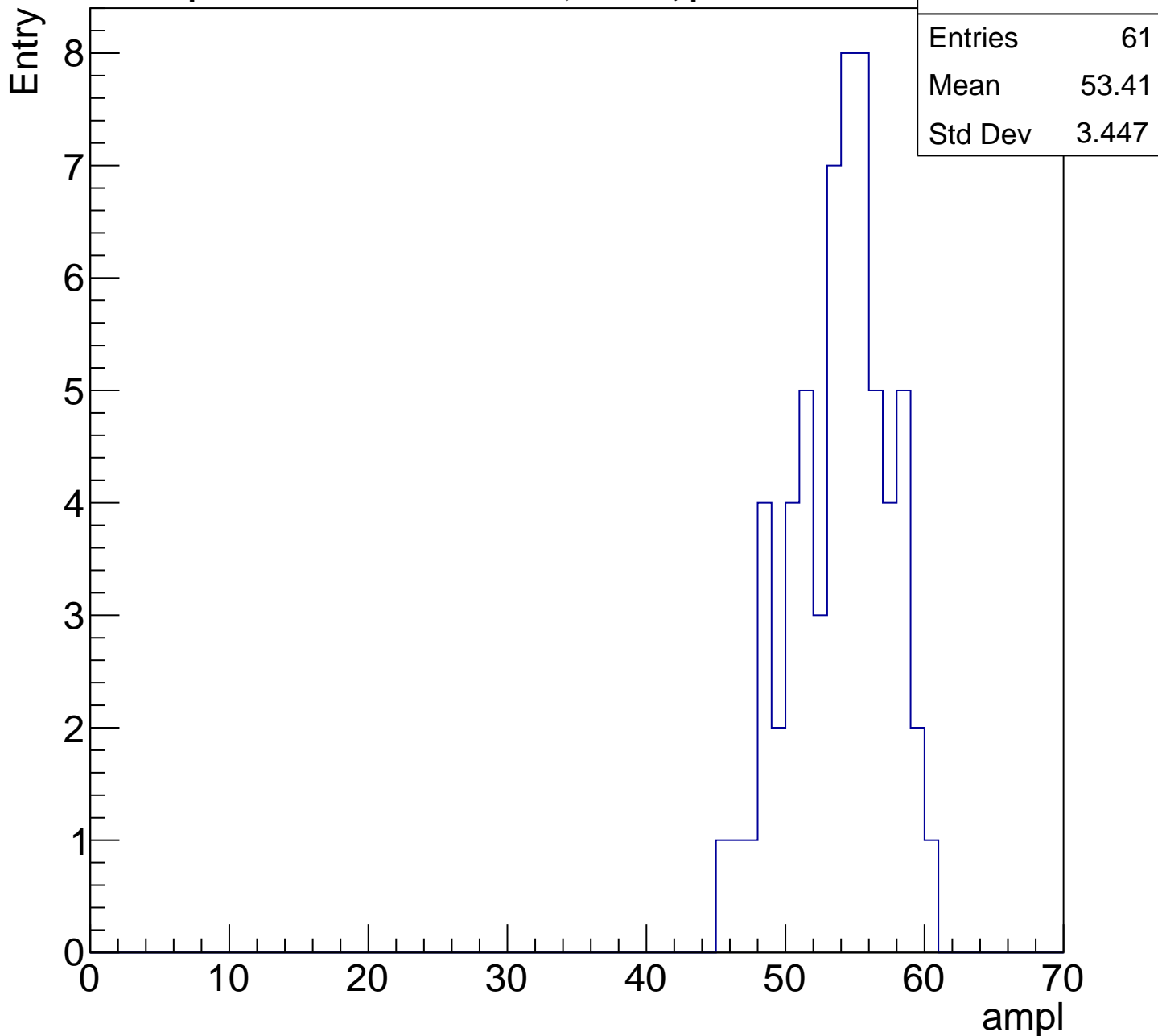
Entry

Entries	70
Mean	47.66
Std Dev	3.443



# B1L002S, U26-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

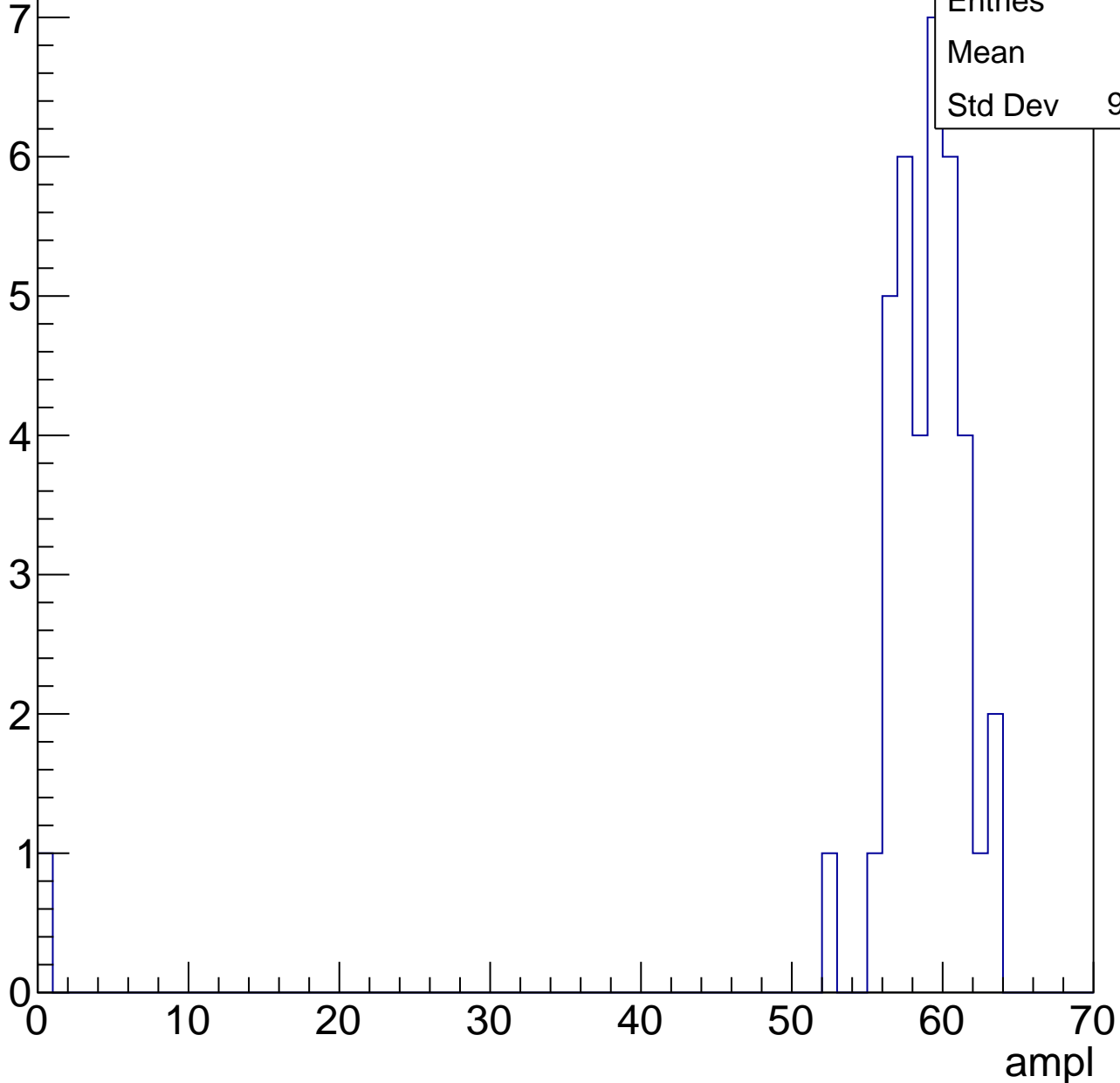


# B1L002S, U26-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

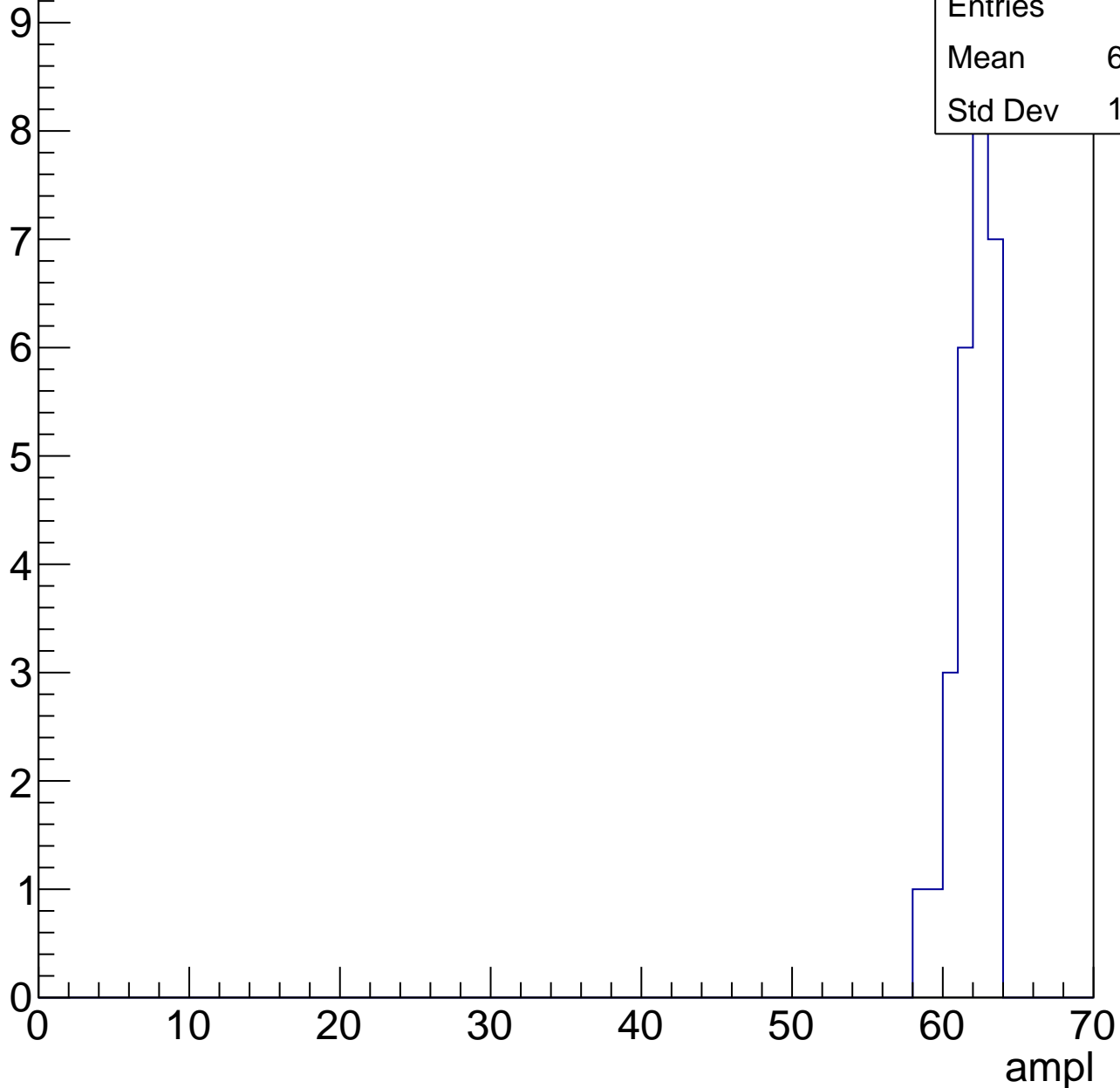
Entries	38
Mean	57
Std Dev	9.638



# B1L002S, U26-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



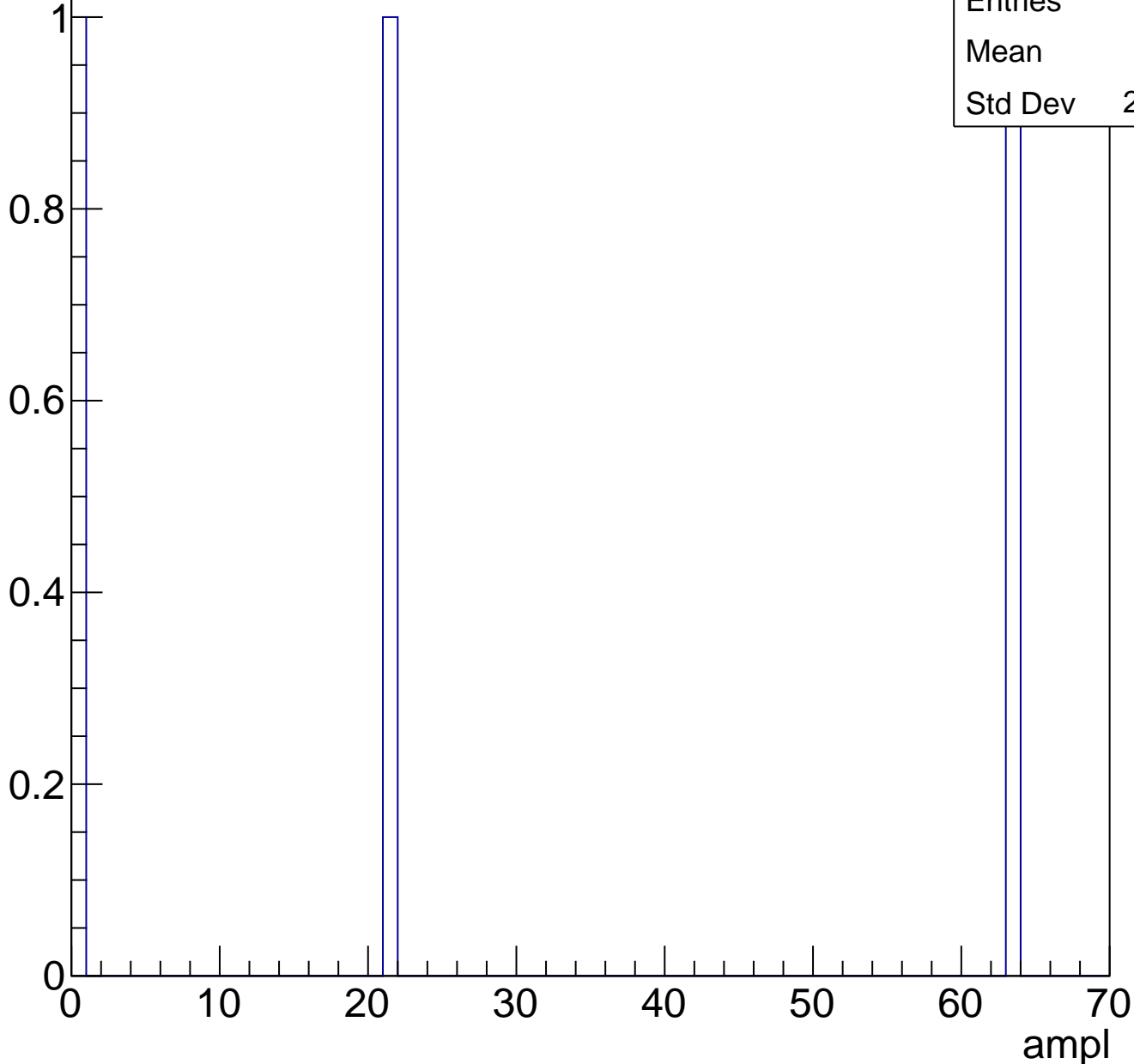
Entries	27
Mean	61.56
Std Dev	1.286



# B1L002S, U26-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L002S, U26-ch8, adc0

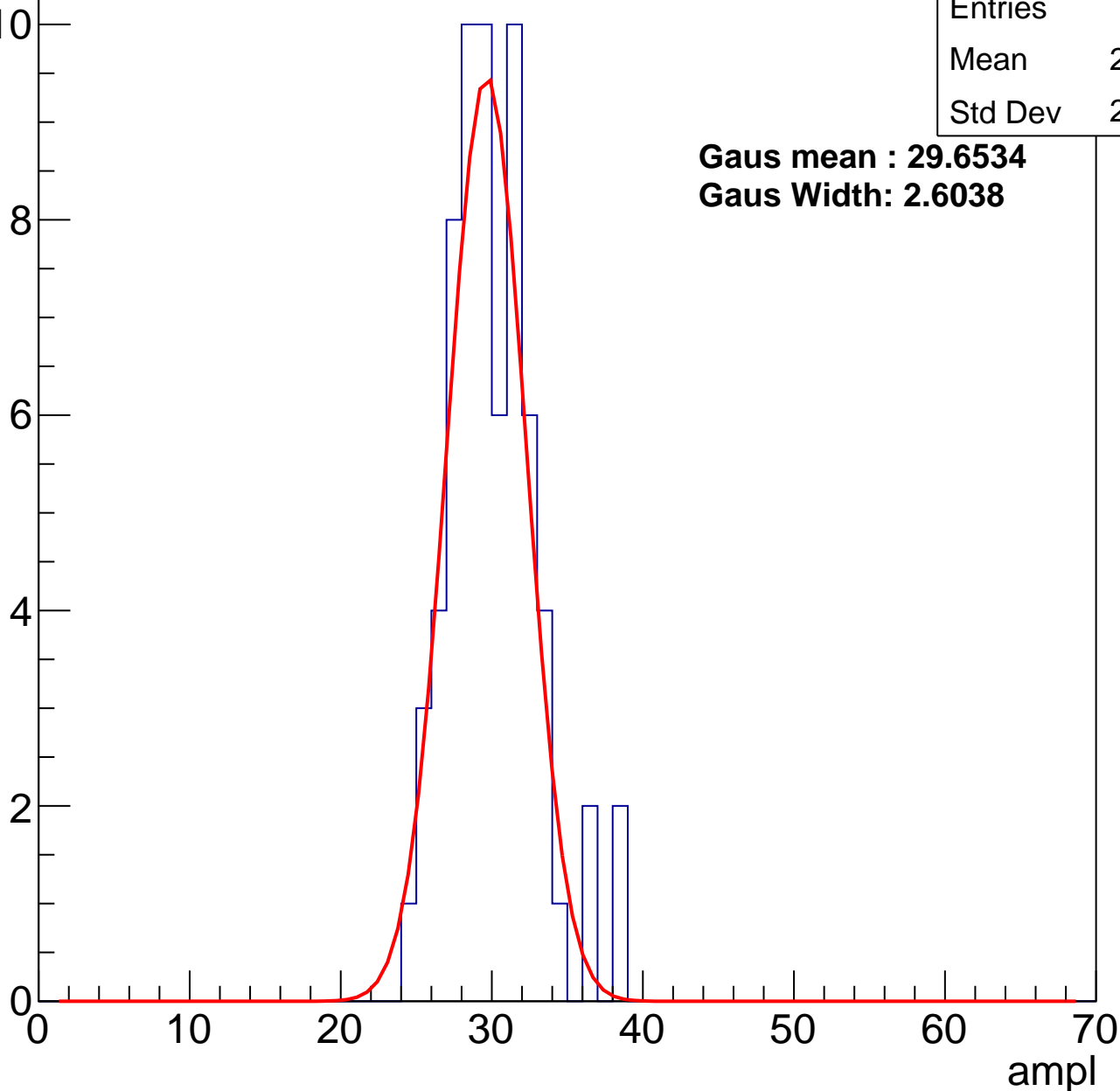
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.63
Std Dev	2.926

**Gaus mean : 29.6534**

**Gaus Width: 2.6038**



# B1L002S, U26-ch8, adc1

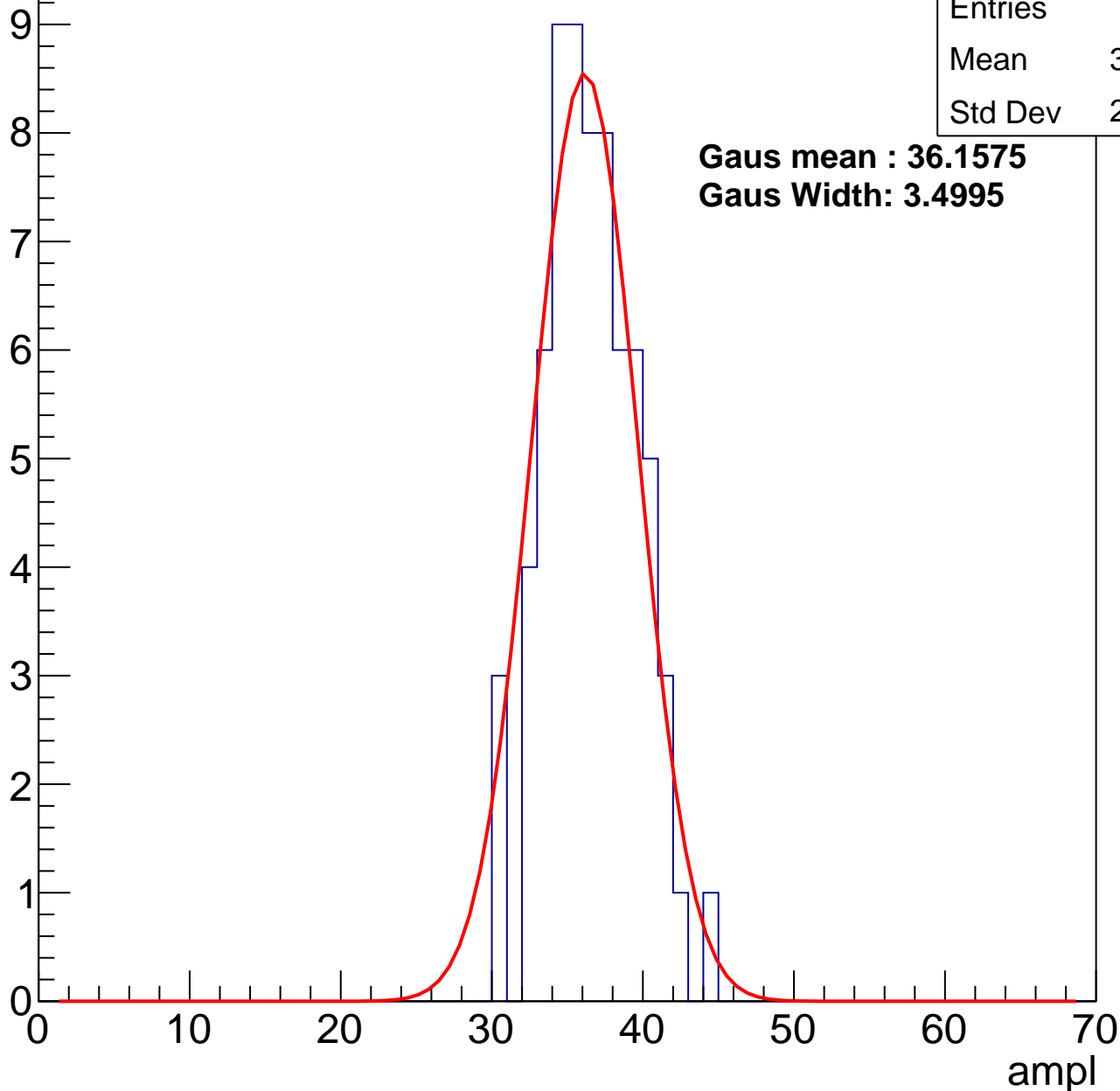
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	36.12
Std Dev	2.976

**Gaus mean : 36.1575**

**Gaus Width: 3.4995**



# B1L002S, U26-ch8, adc2

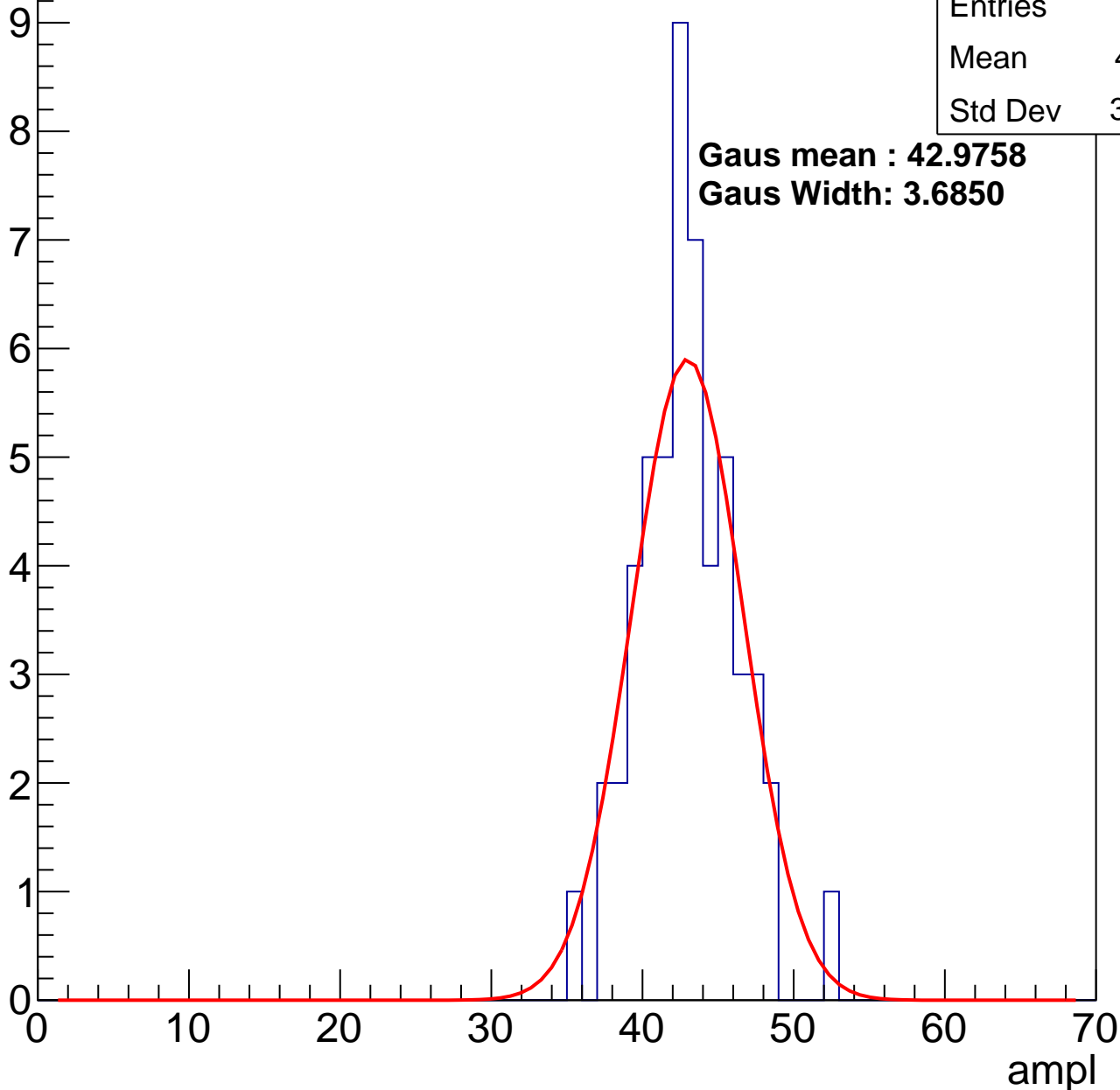
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	42.51
Std Dev	3.196

**Gaus mean : 42.9758**

**Gaus Width: 3.6850**



# B1L002S, U26-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

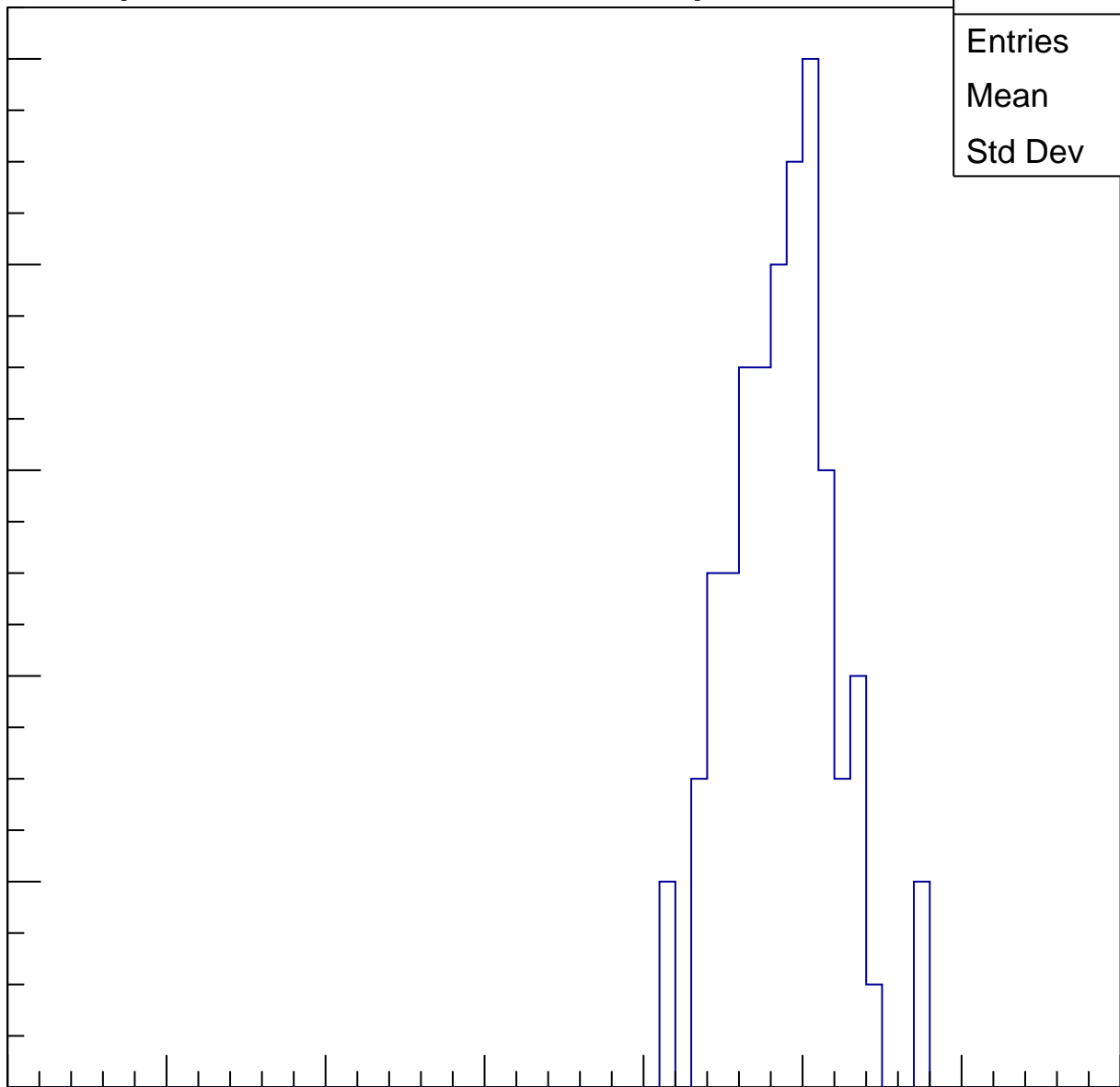
Entries	72
Mean	48.25
Std Dev	3.286

Entry

10  
8  
6  
4  
2  
0

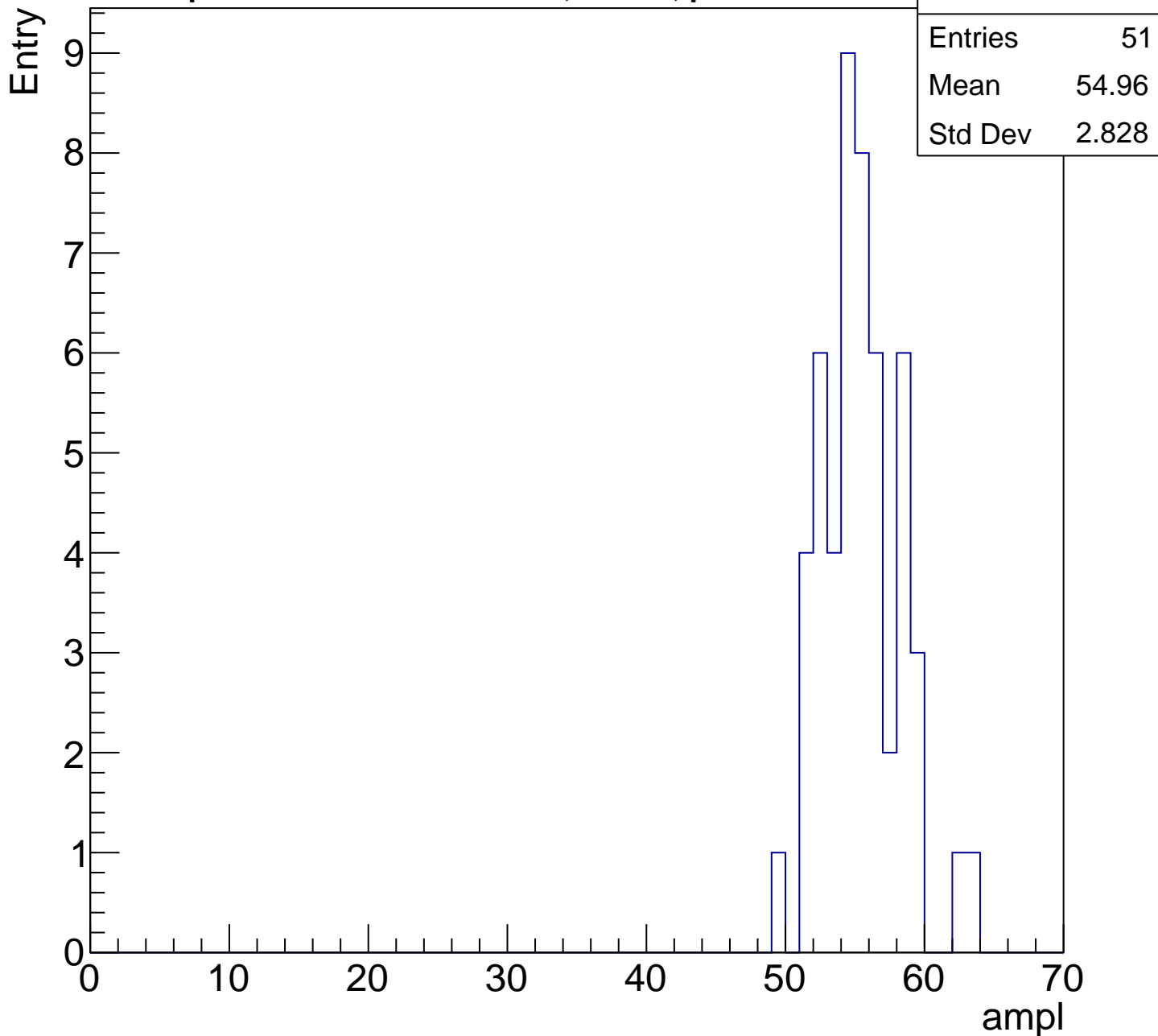
0 10 20 30 40 50 60 70

ampl



# B1L002S, U26-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

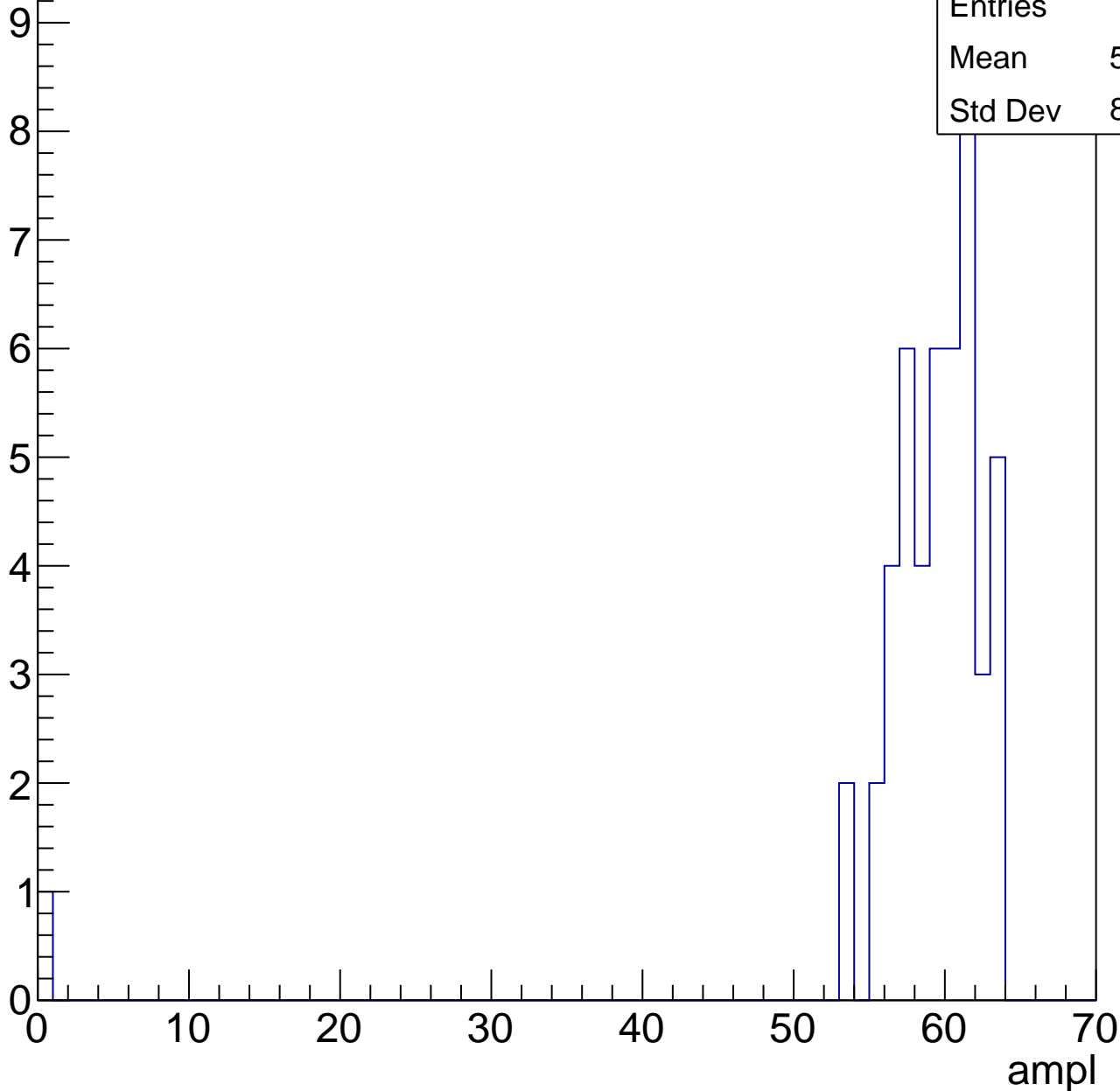


# B1L002S, U26-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	57.88
Std Dev	8.824

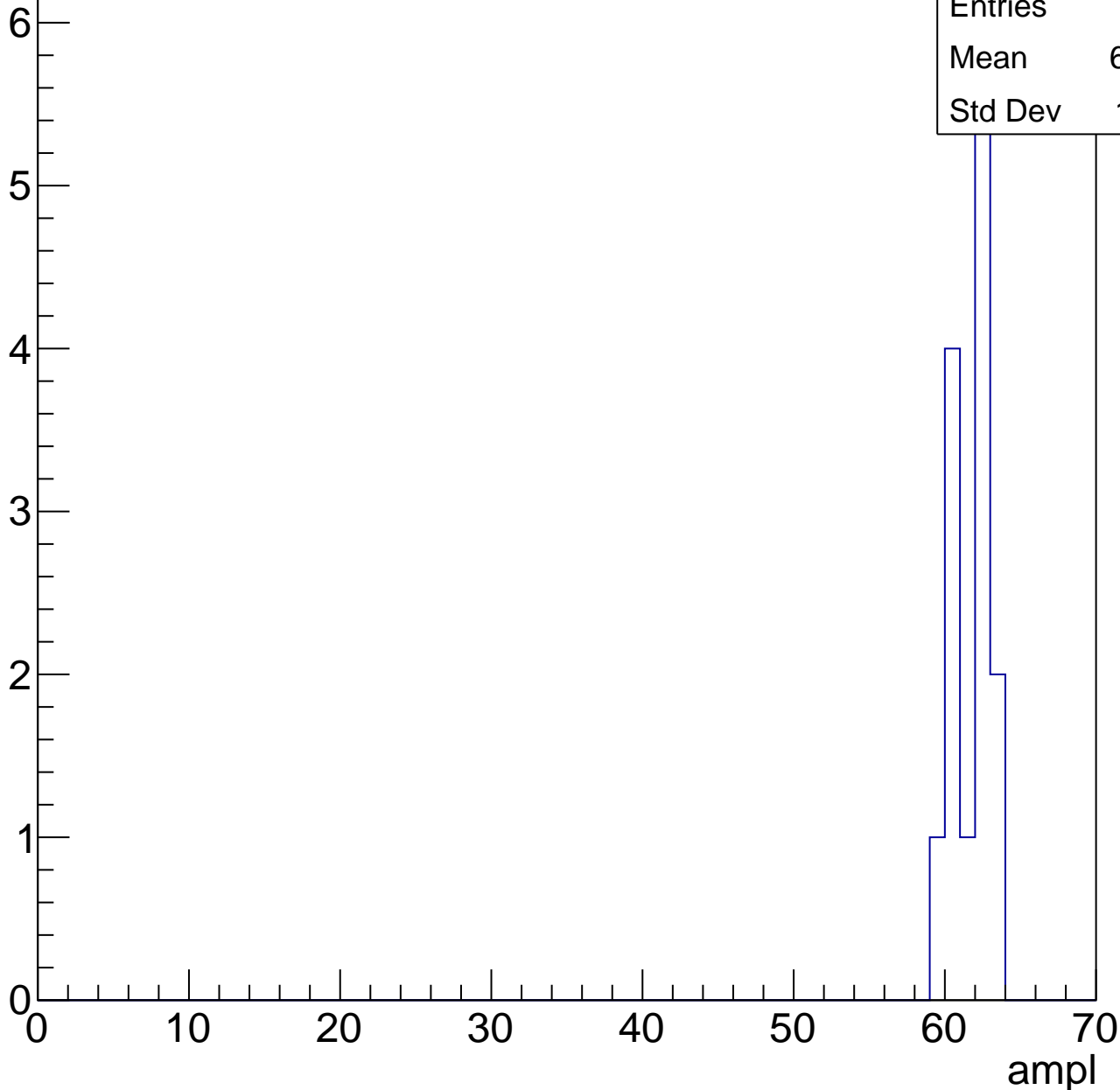


# B1L002S, U26-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.29
Std Dev	1.221

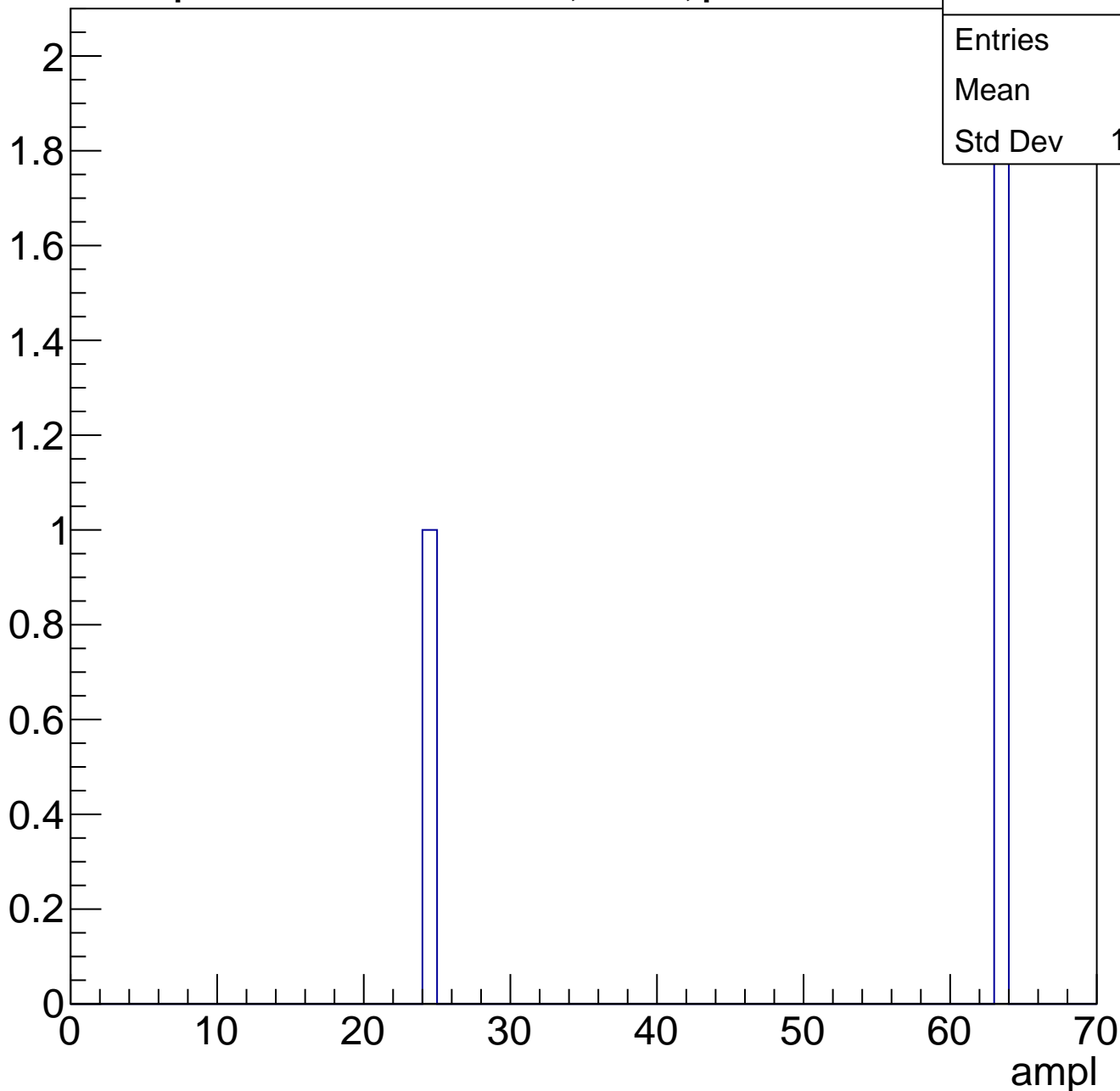




# B1L002S, U26-ch8, adc7

**calib\_packv5\_042523\_0143.root, FC#10, port B3**

Entries	3
Mean	50
Std Dev	18.38



# B1L002S, U26-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	88
Mean	28.7
Std Dev	3.574

**Gaus mean : 29.2474**

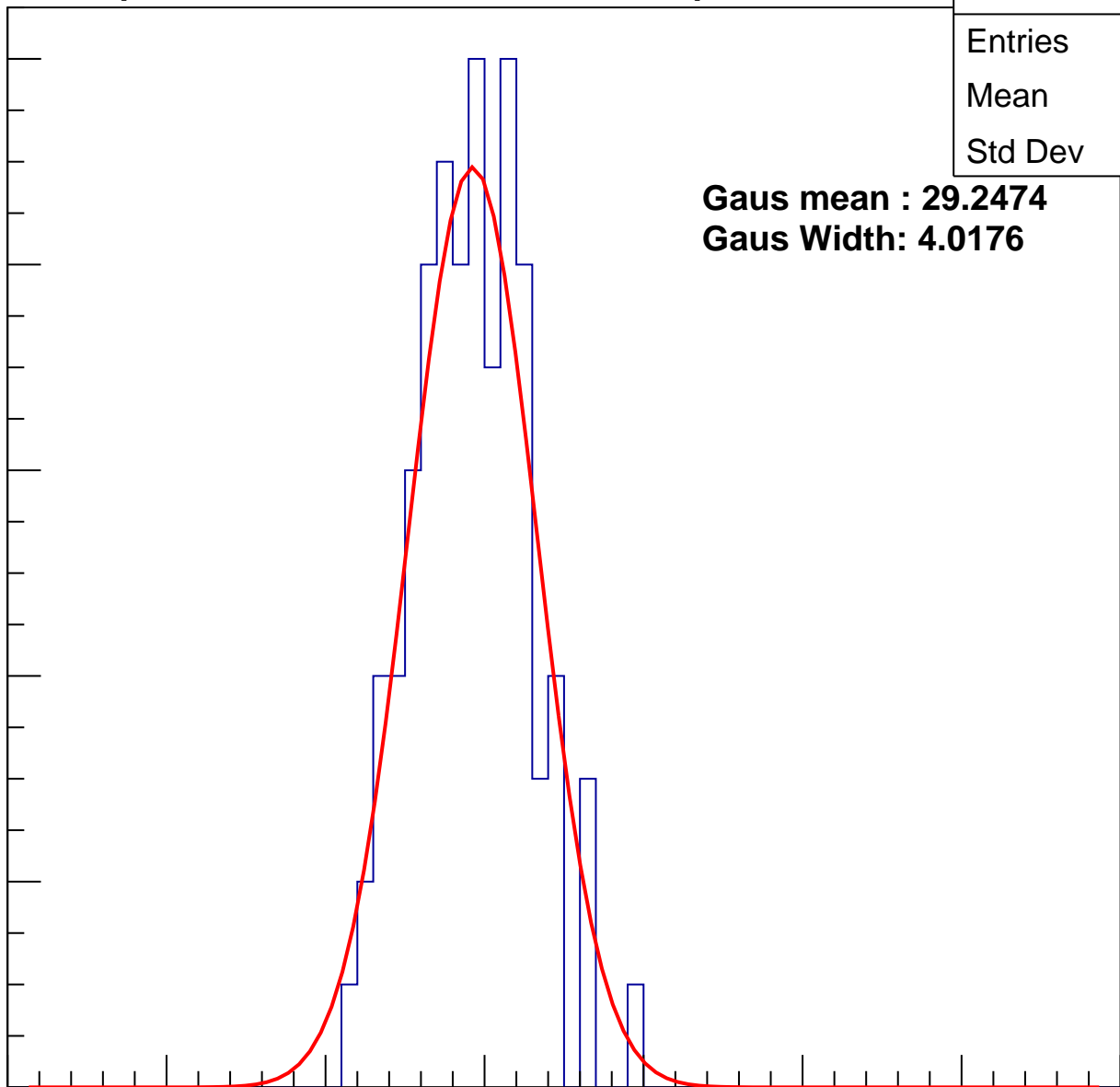
**Gaus Width: 4.0176**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch9, adc1

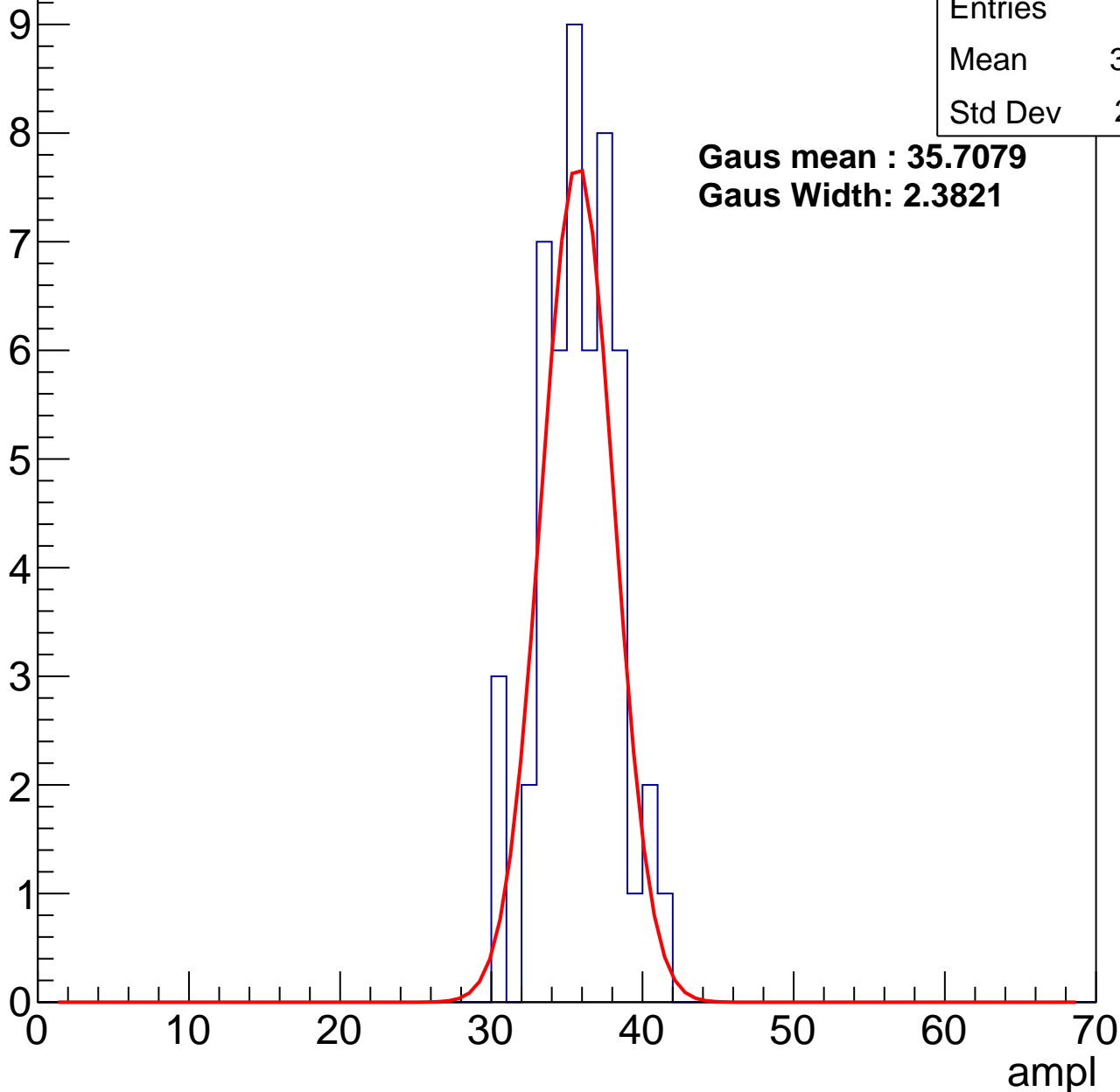
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	35.37
Std Dev	2.481

**Gaus mean : 35.7079**

**Gaus Width: 2.3821**



# B1L002S, U26-ch9, adc2

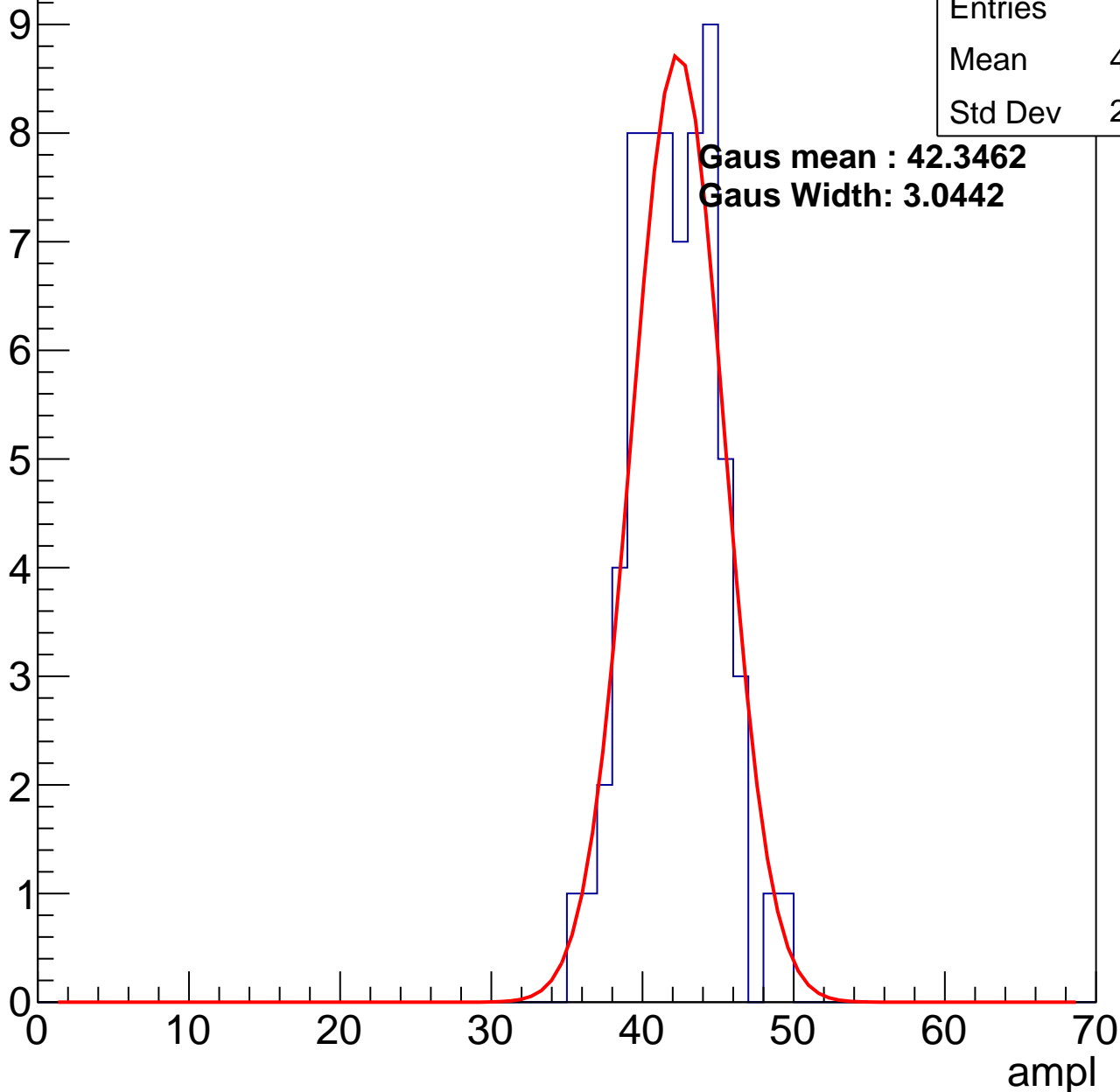
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.68
Std Dev	2.824

**Gaus mean : 42.3462**

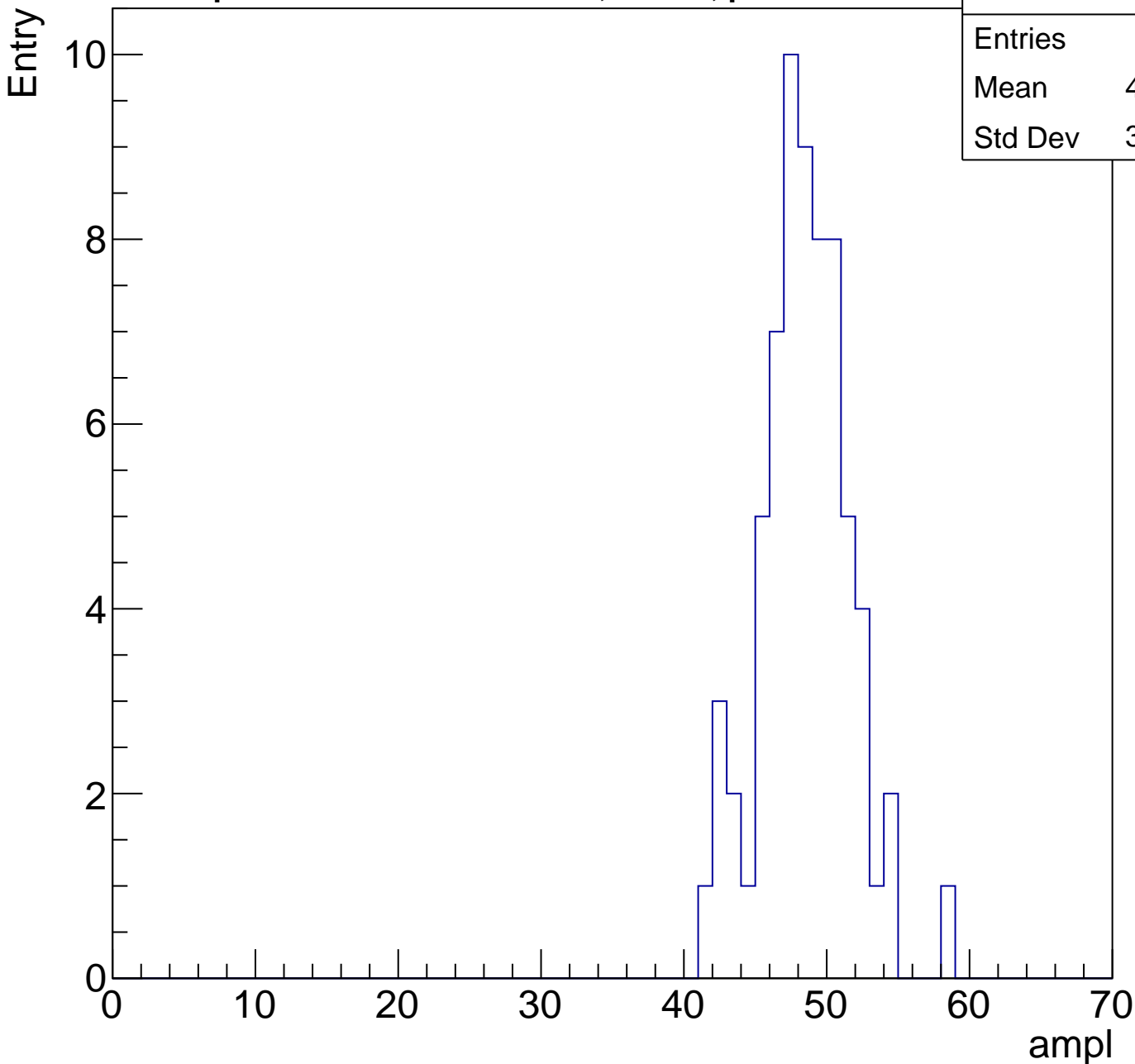
**Gaus Width: 3.0442**



# B1L002S, U26-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	48.06
Std Dev	3.119

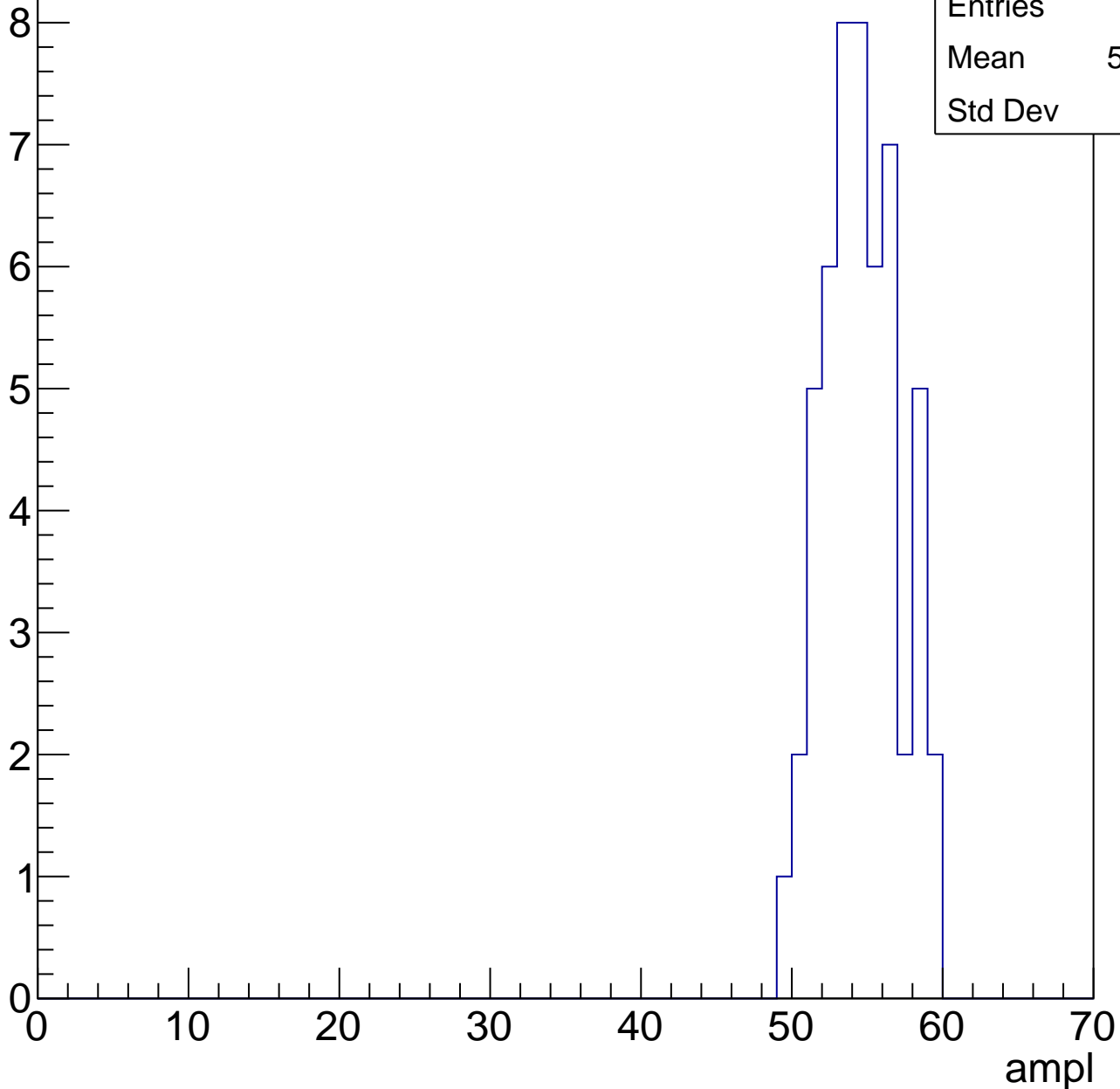


# B1L002S, U26-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	54.15
Std Dev	2.46

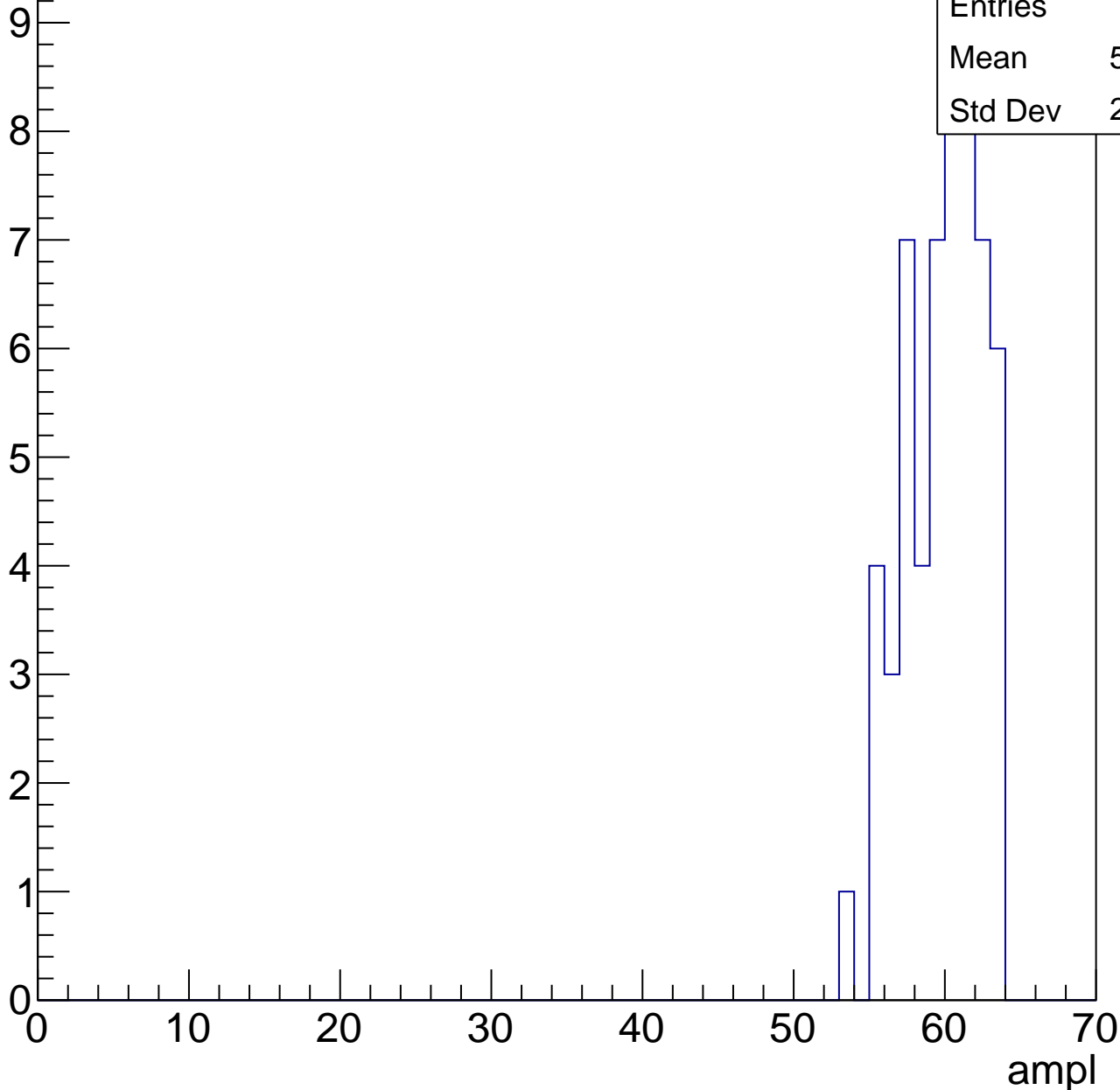


# B1L002S, U26-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

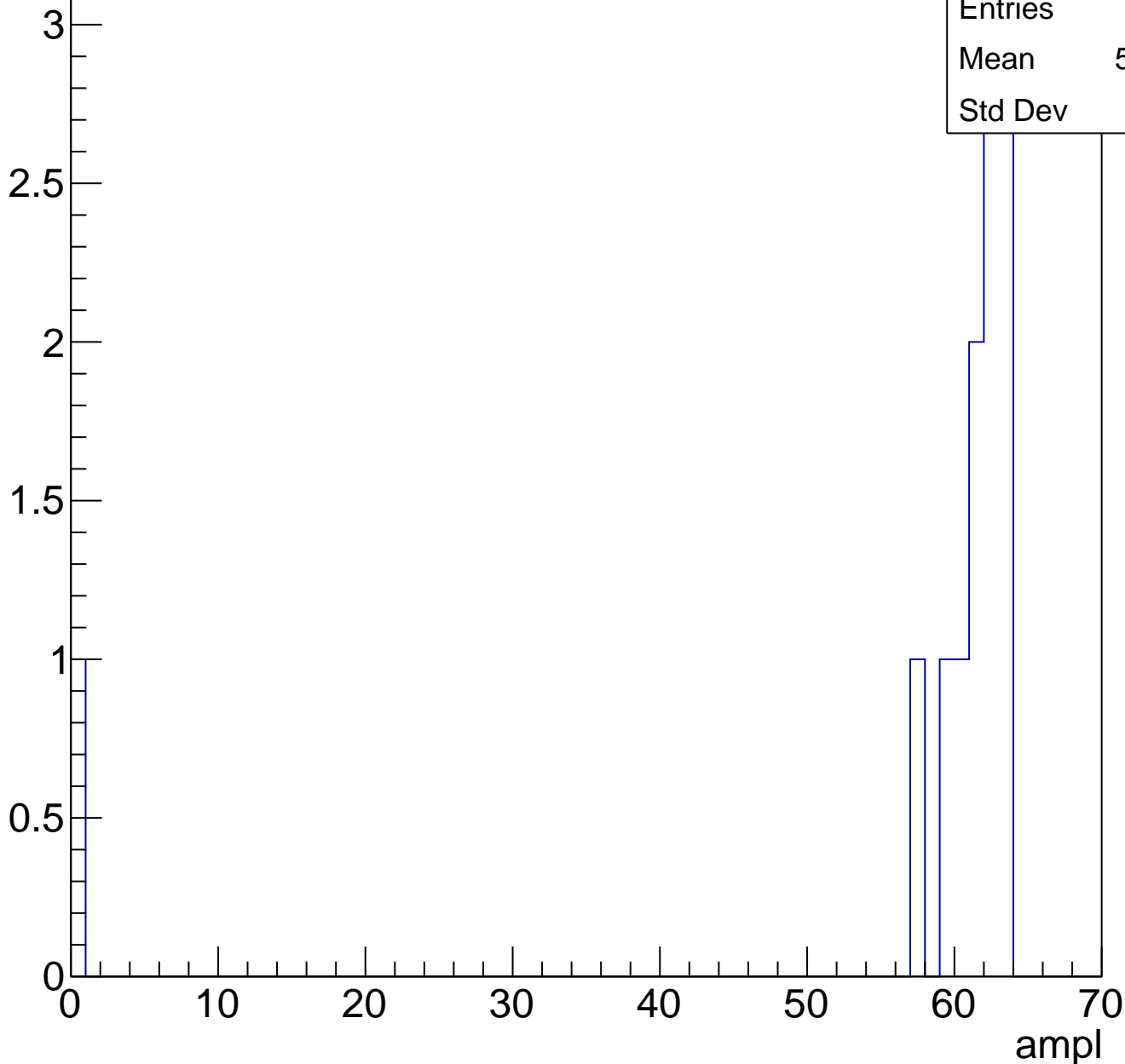
Entries	56
Mean	59.38
Std Dev	2.504



# B1L002S, U26-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch10, adc0

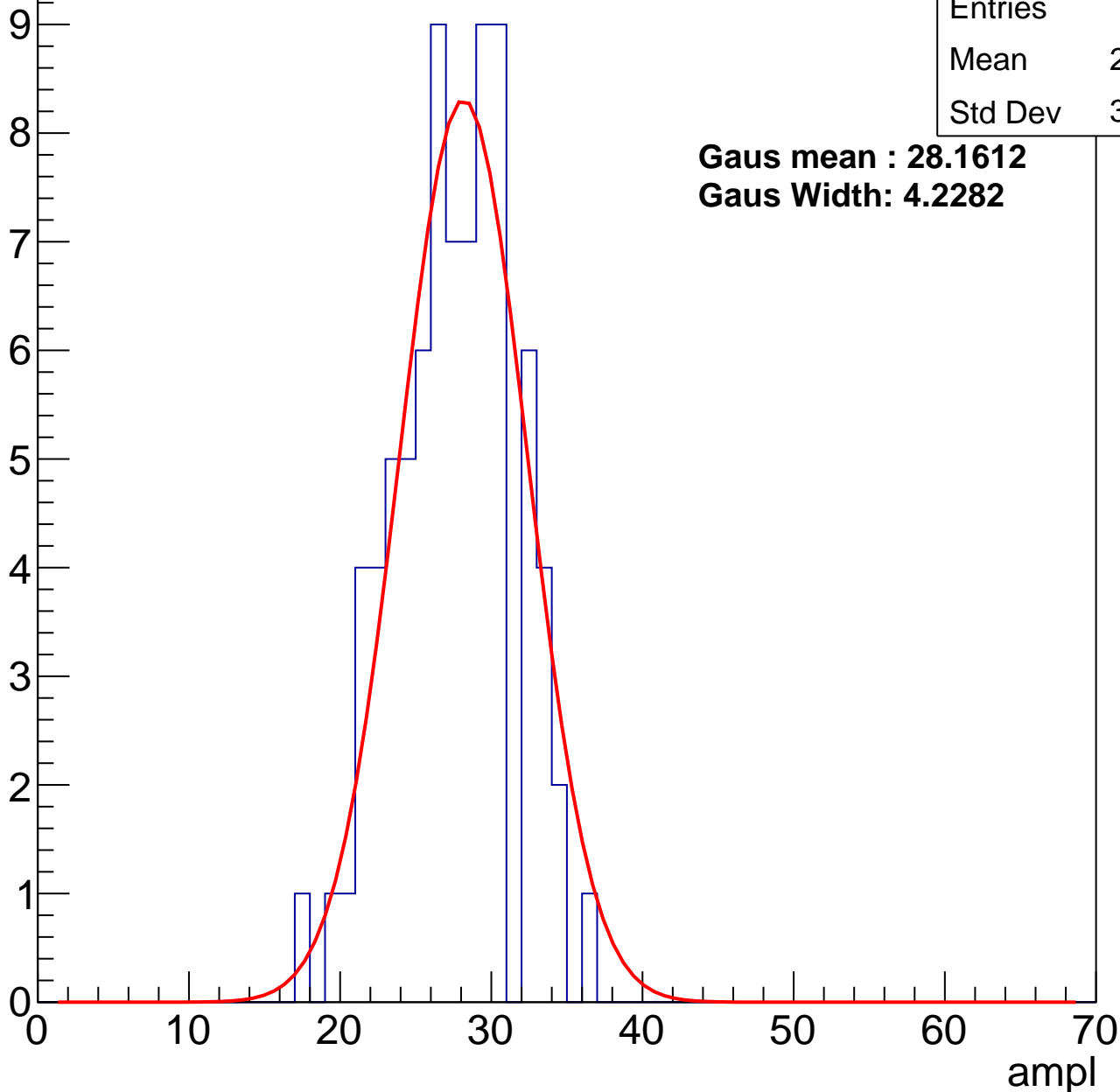
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	27.05
Std Dev	3.868

**Gaus mean : 28.1612**

**Gaus Width: 4.2282**



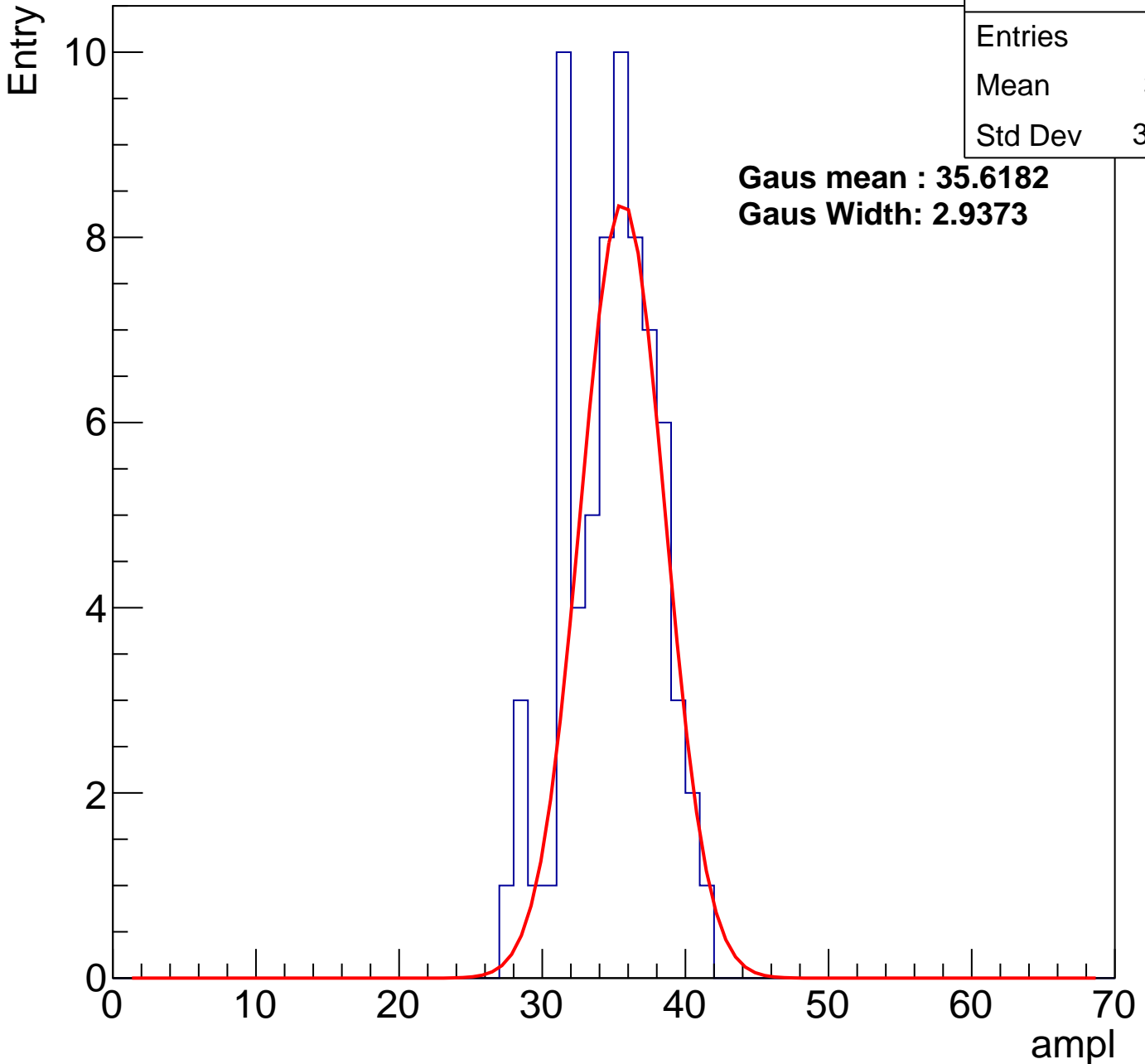
# B1L002S, U26-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	34.4
Std Dev	3.151

**Gaus mean : 35.6182**

**Gaus Width: 2.9373**



# B1L002S, U26-ch10, adc2

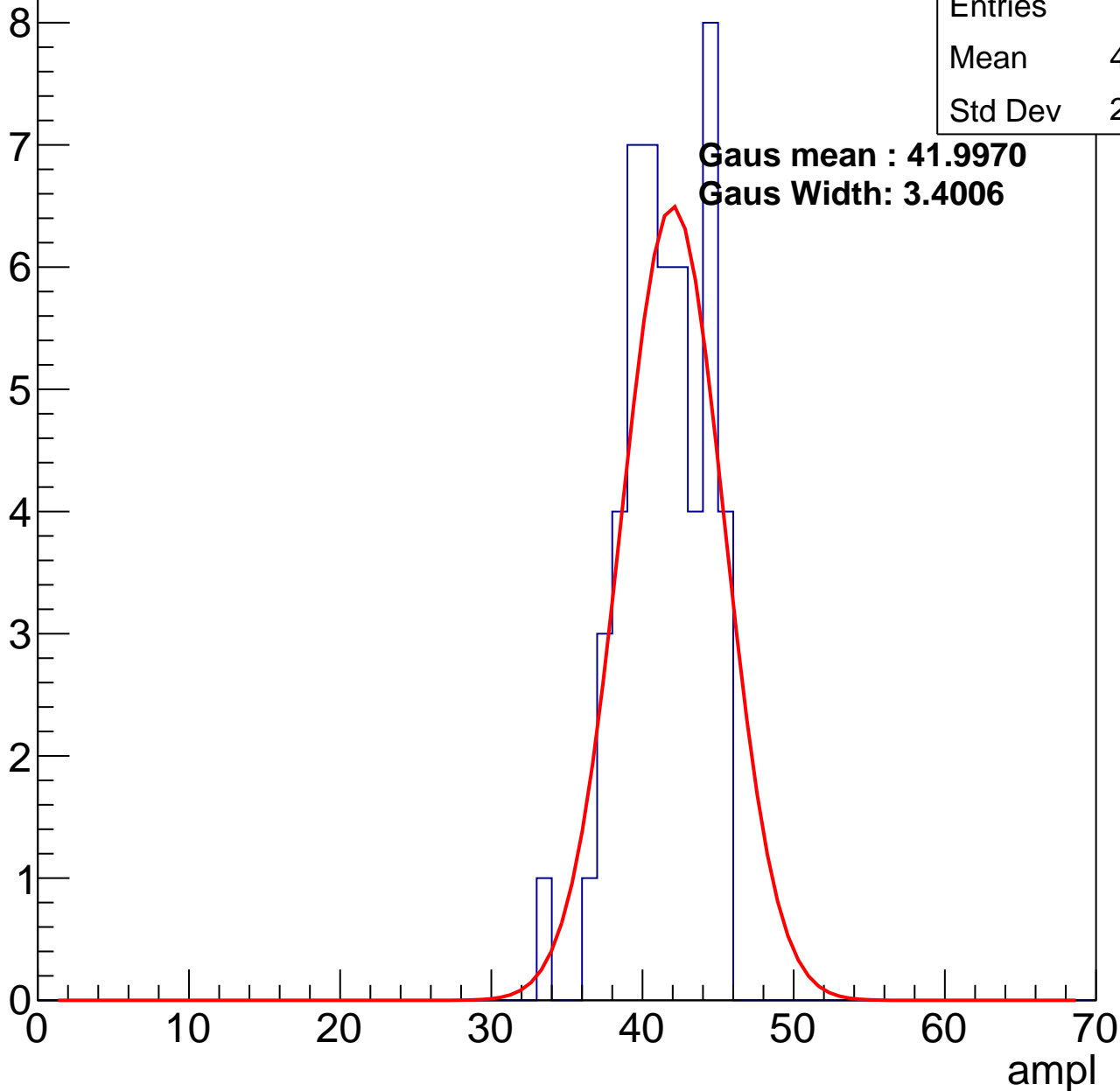
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.92
Std Dev	2.678

**Gaus mean : 41.9970**

**Gaus Width: 3.4006**

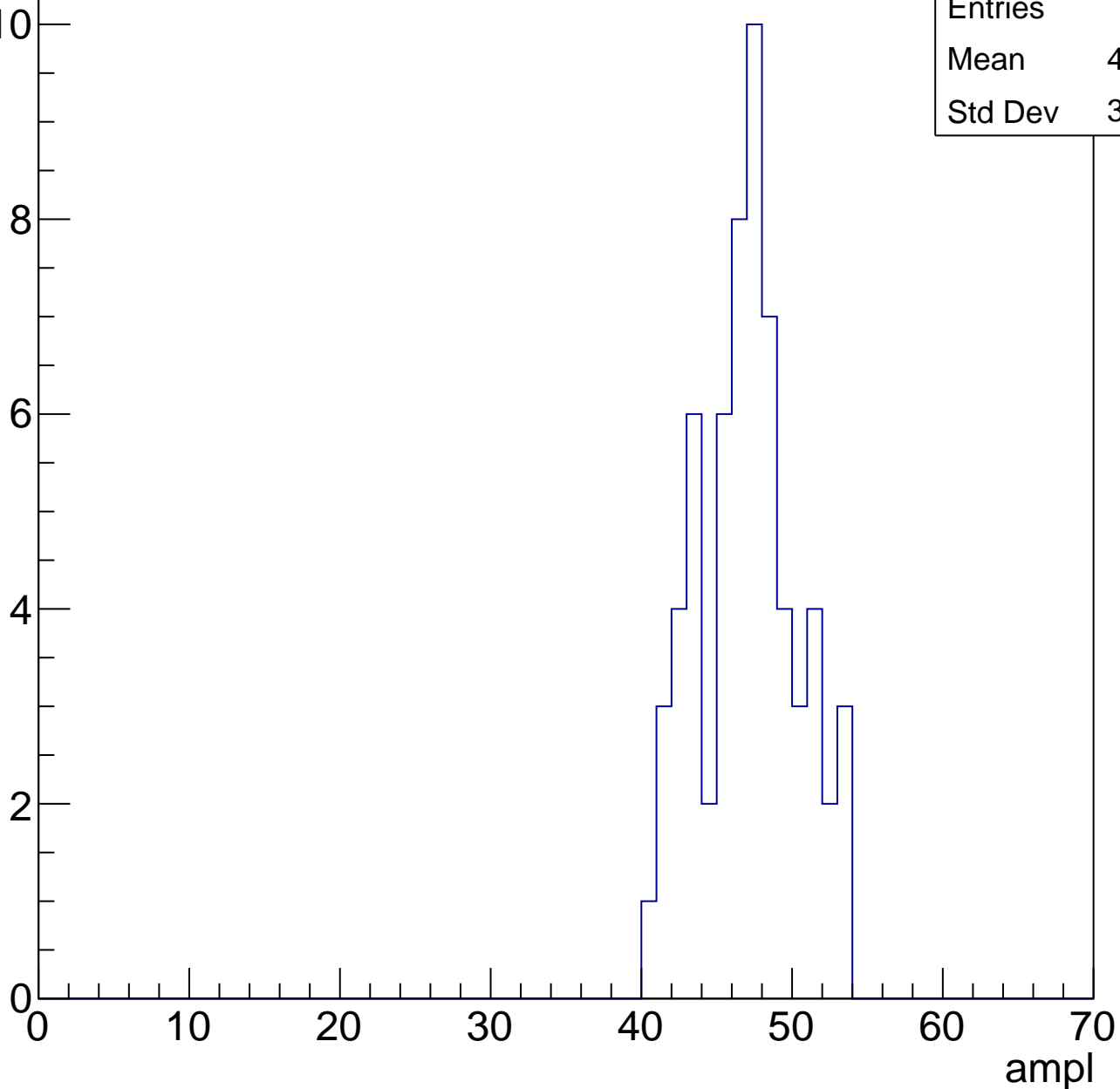


# B1L002S, U26-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

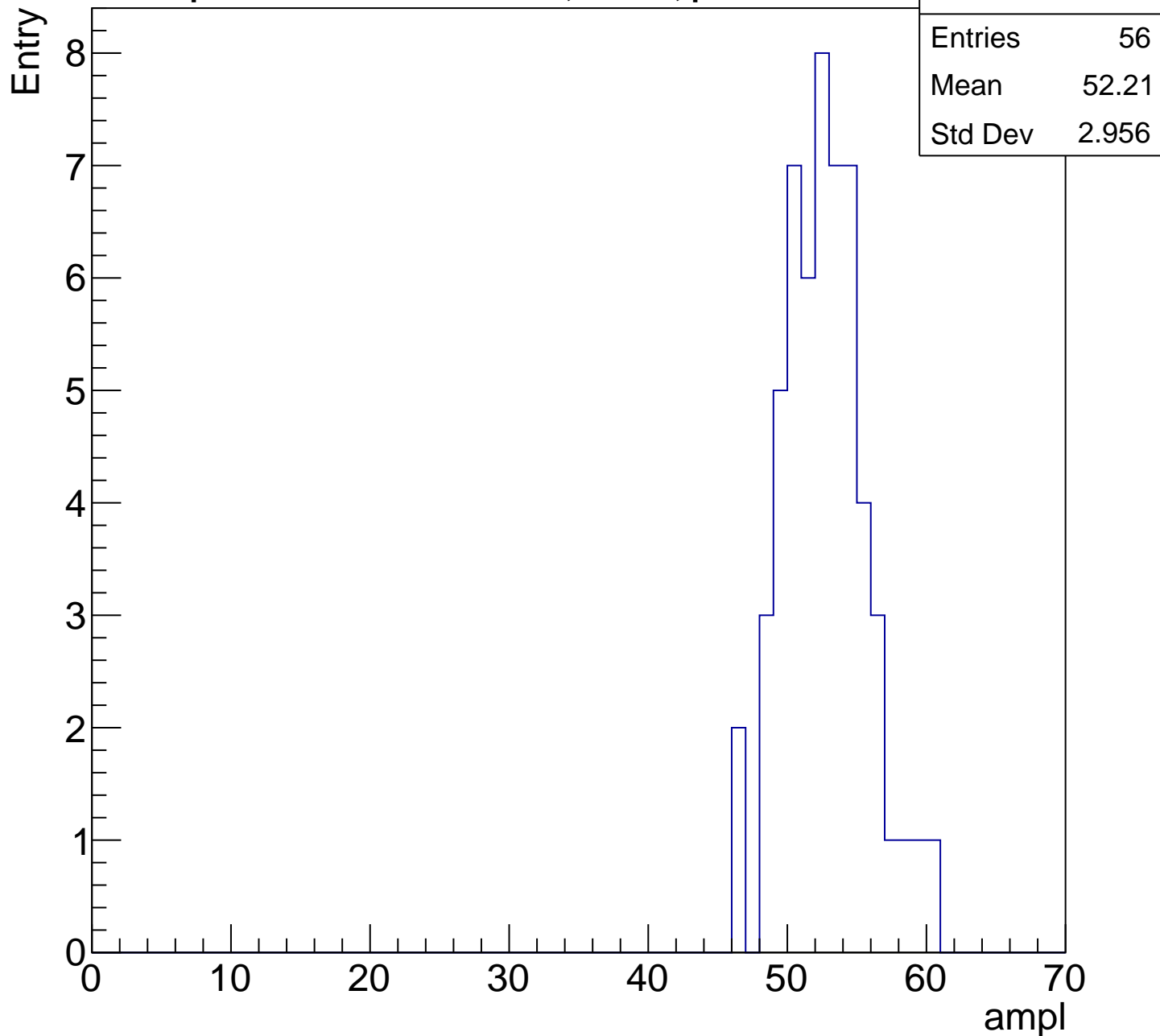
Entry

Entries	63
Mean	46.57
Std Dev	3.245



# B1L002S, U26-ch10, adc4

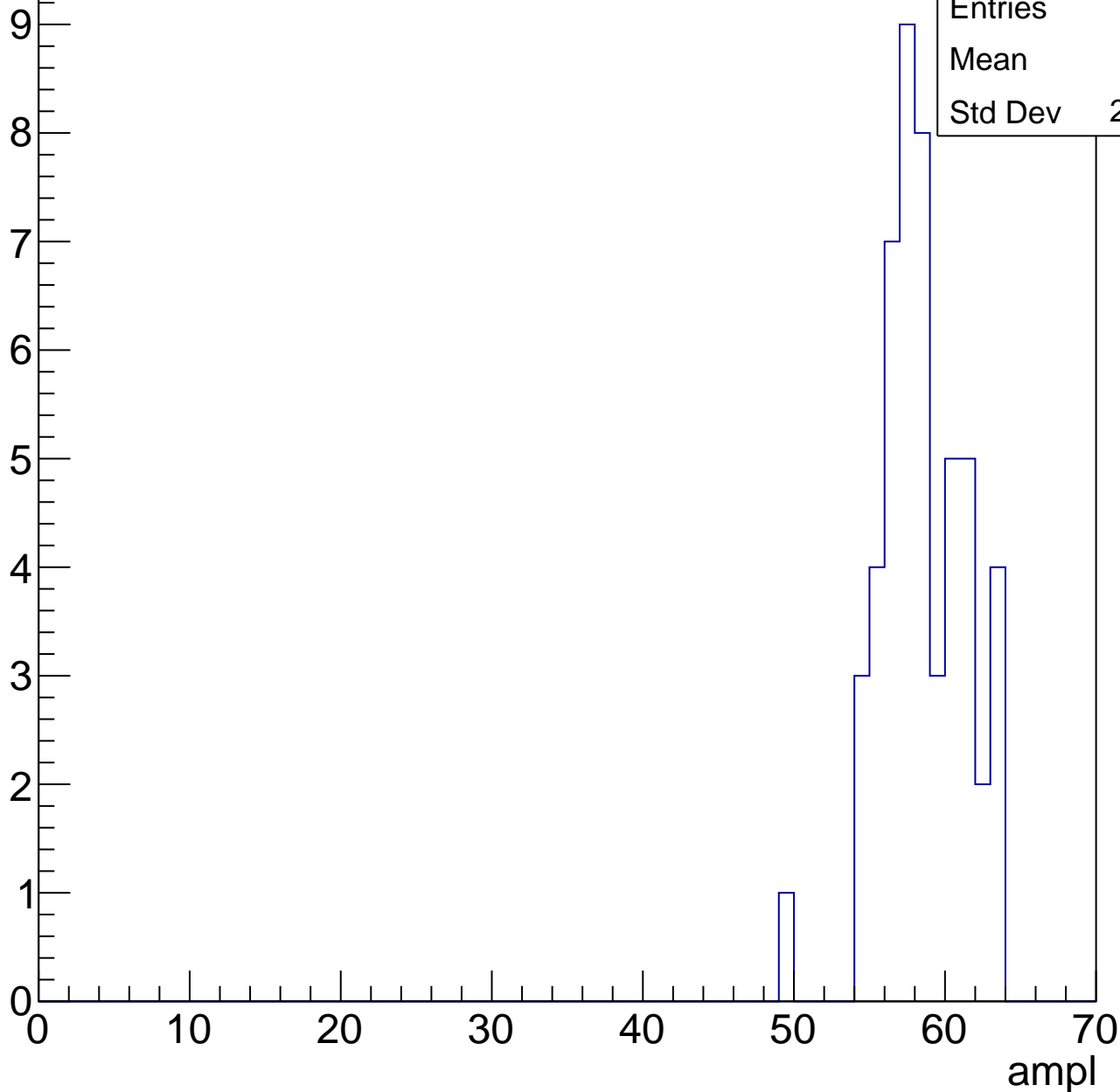
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

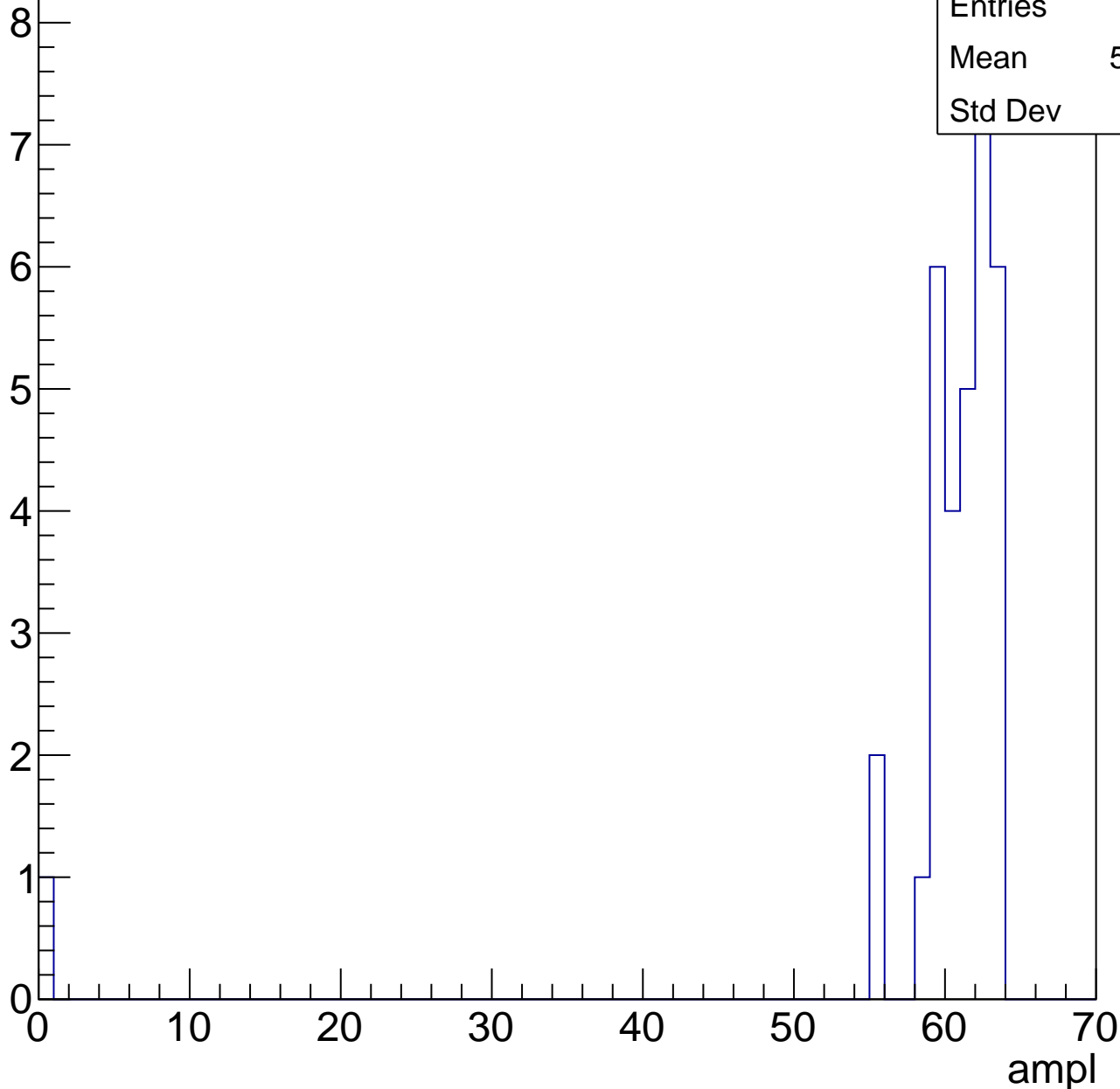


# B1L002S, U26-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	58.82
Std Dev	10.6





# B1L002S, U26-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch11, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	85
Mean	28.15
Std Dev	3.743

**Gaus mean : 28.6253**

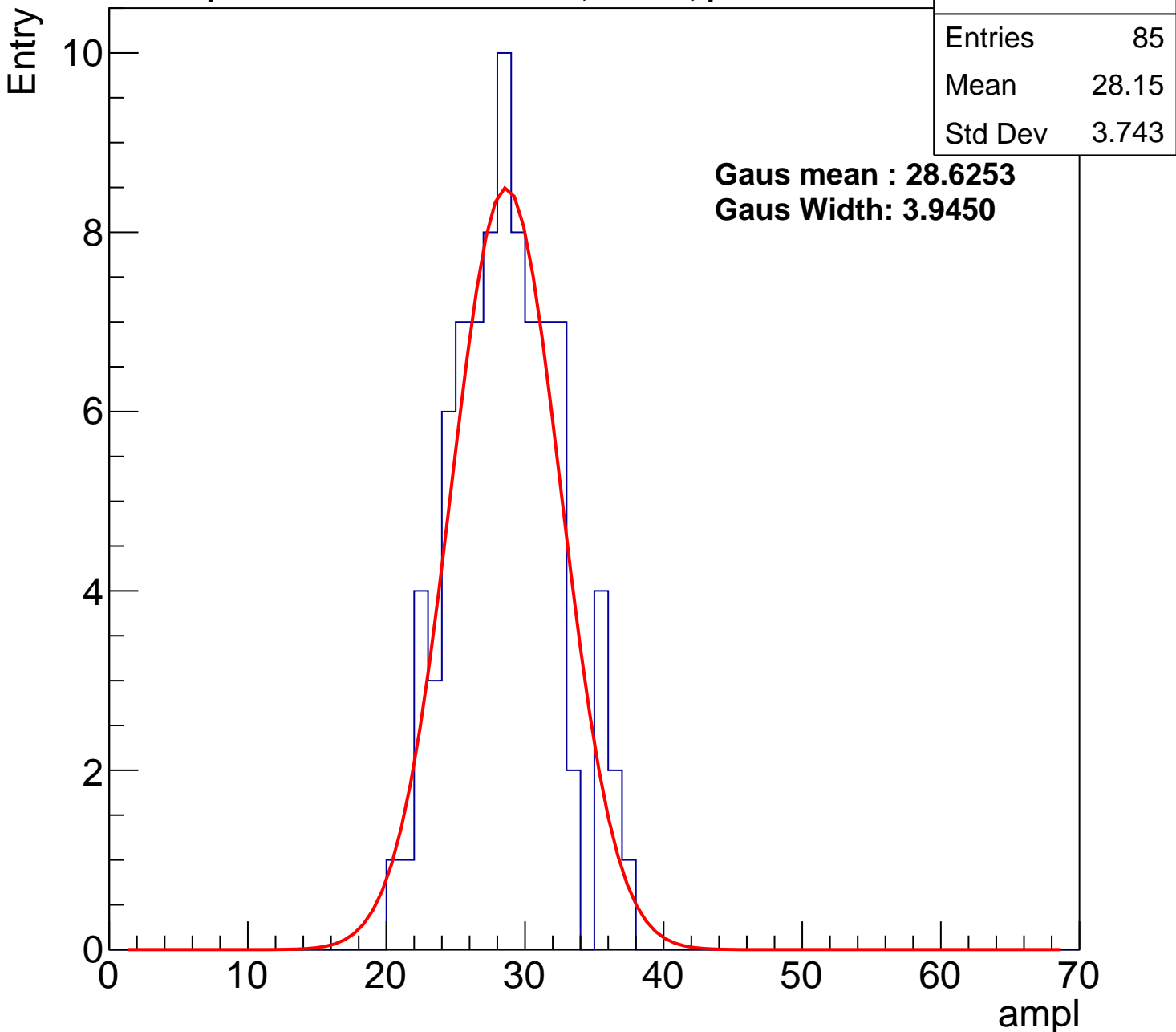
**Gaus Width: 3.9450**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch11, adc1

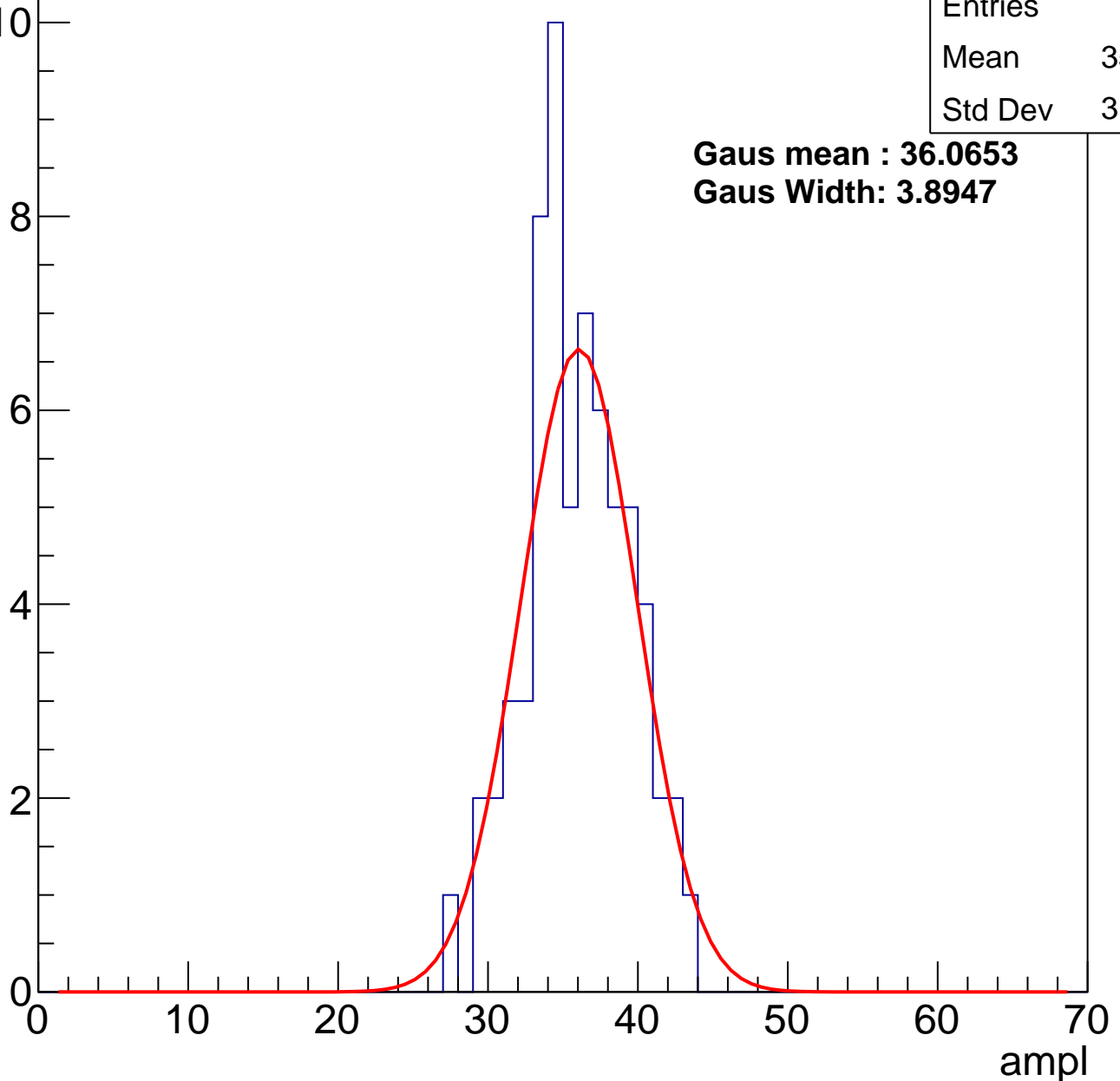
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	35.47
Std Dev	3.434

**Gaus mean : 36.0653**

**Gaus Width: 3.8947**



# B1L002S, U26-ch11, adc2

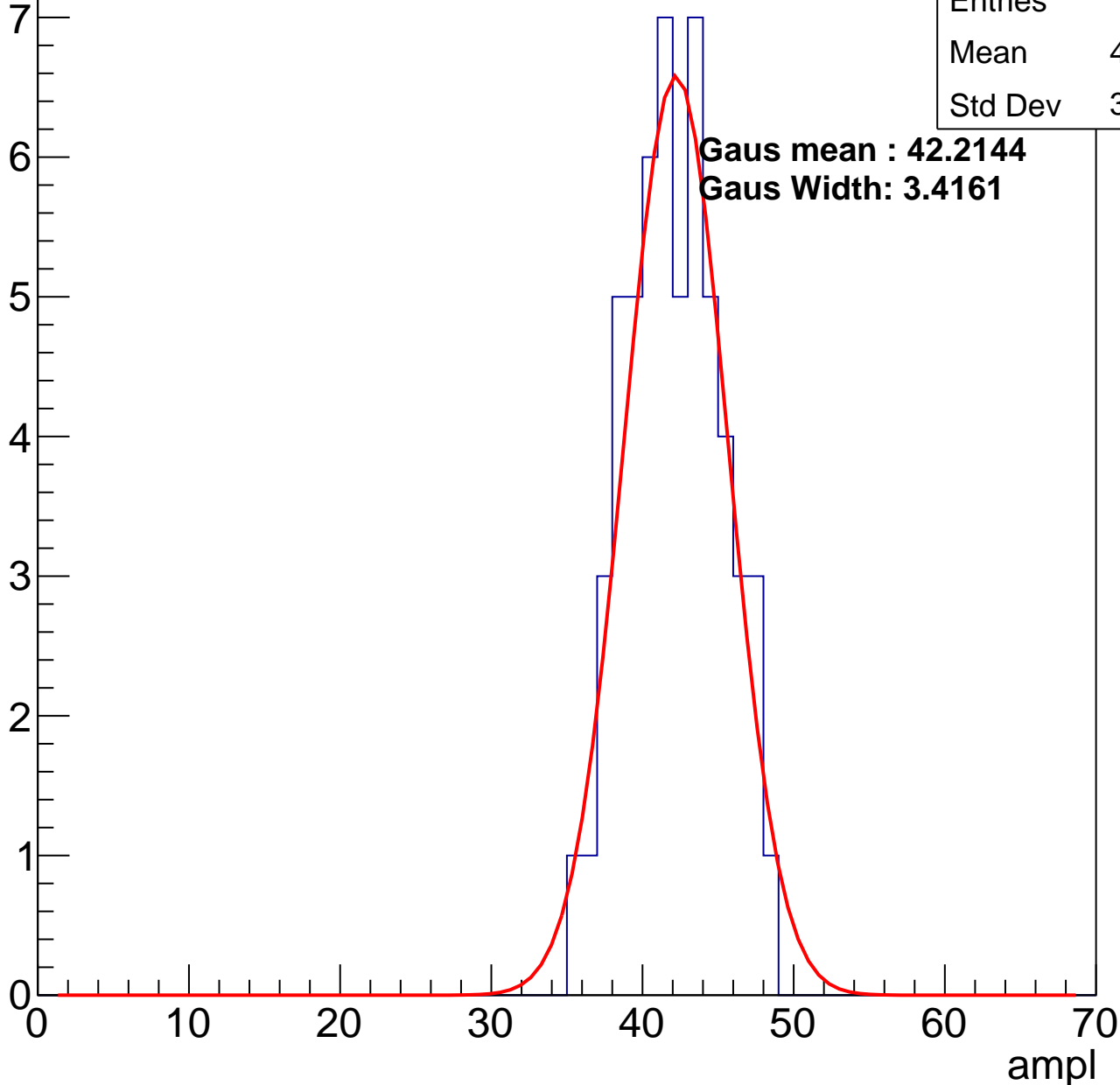
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.64
Std Dev	3.079

**Gaus mean : 42.2144**

**Gaus Width: 3.4161**

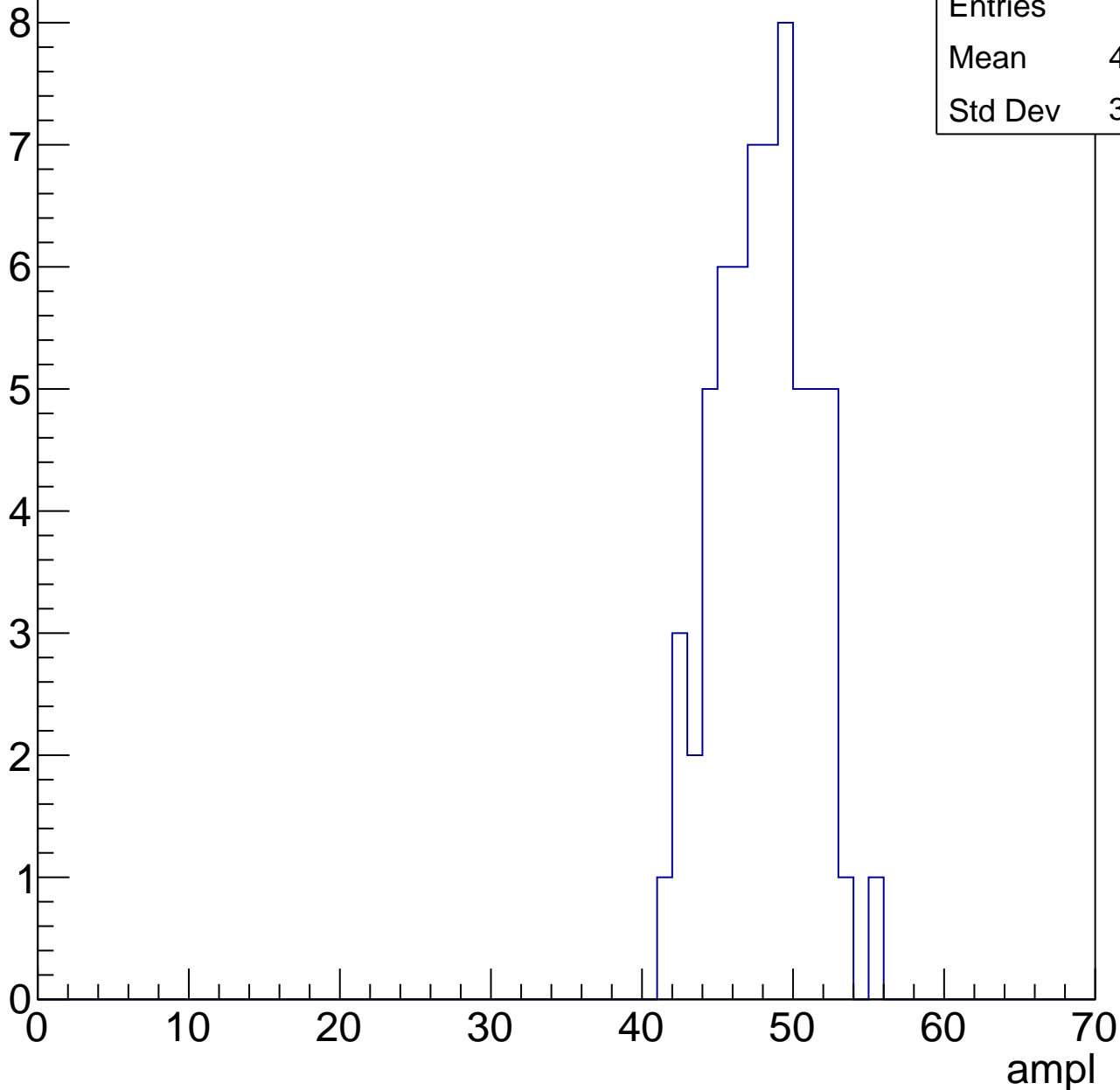


# B1L002S, U26-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

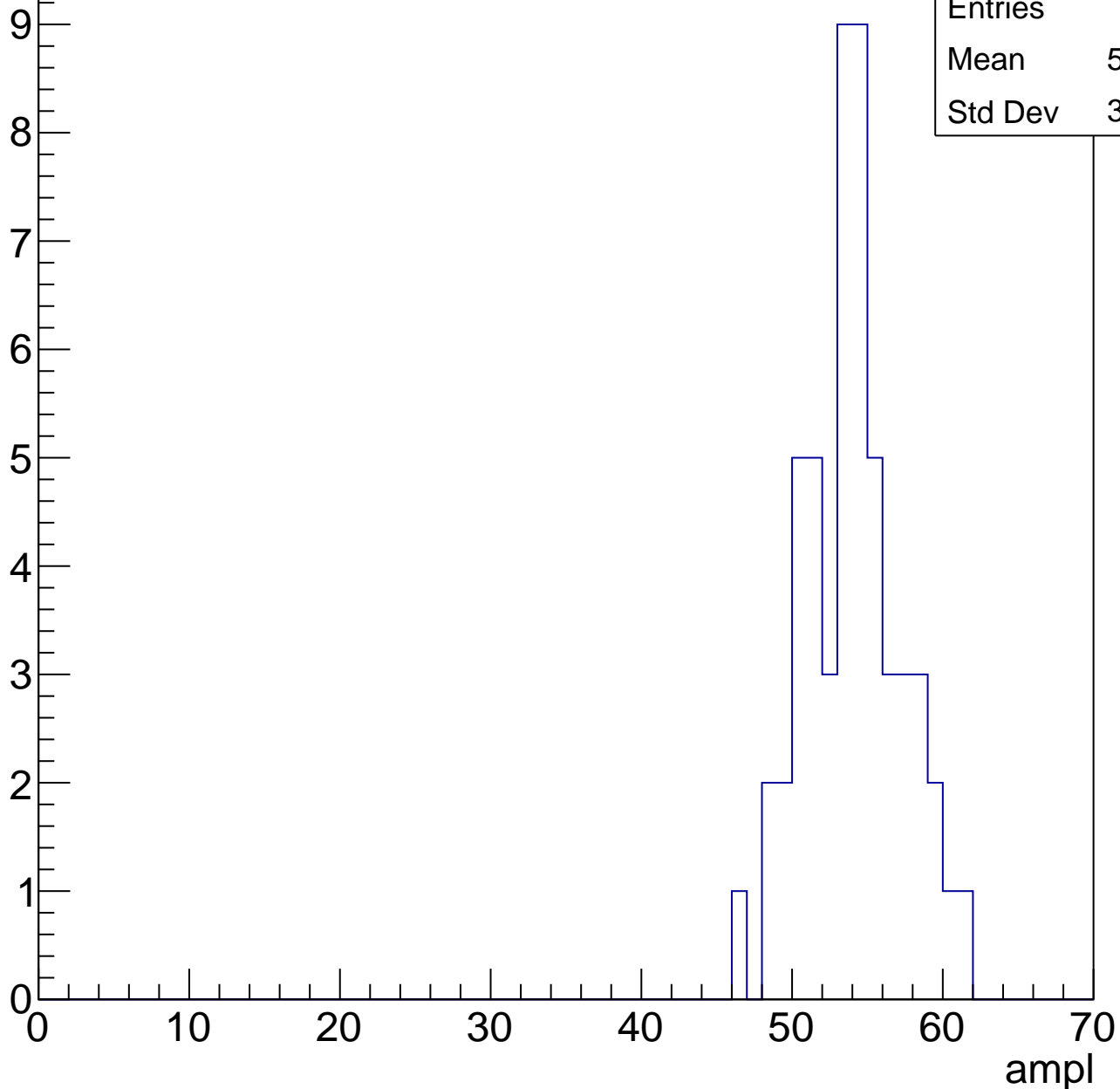
Entries	62
Mean	47.56
Std Dev	3.078



# B1L002S, U26-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



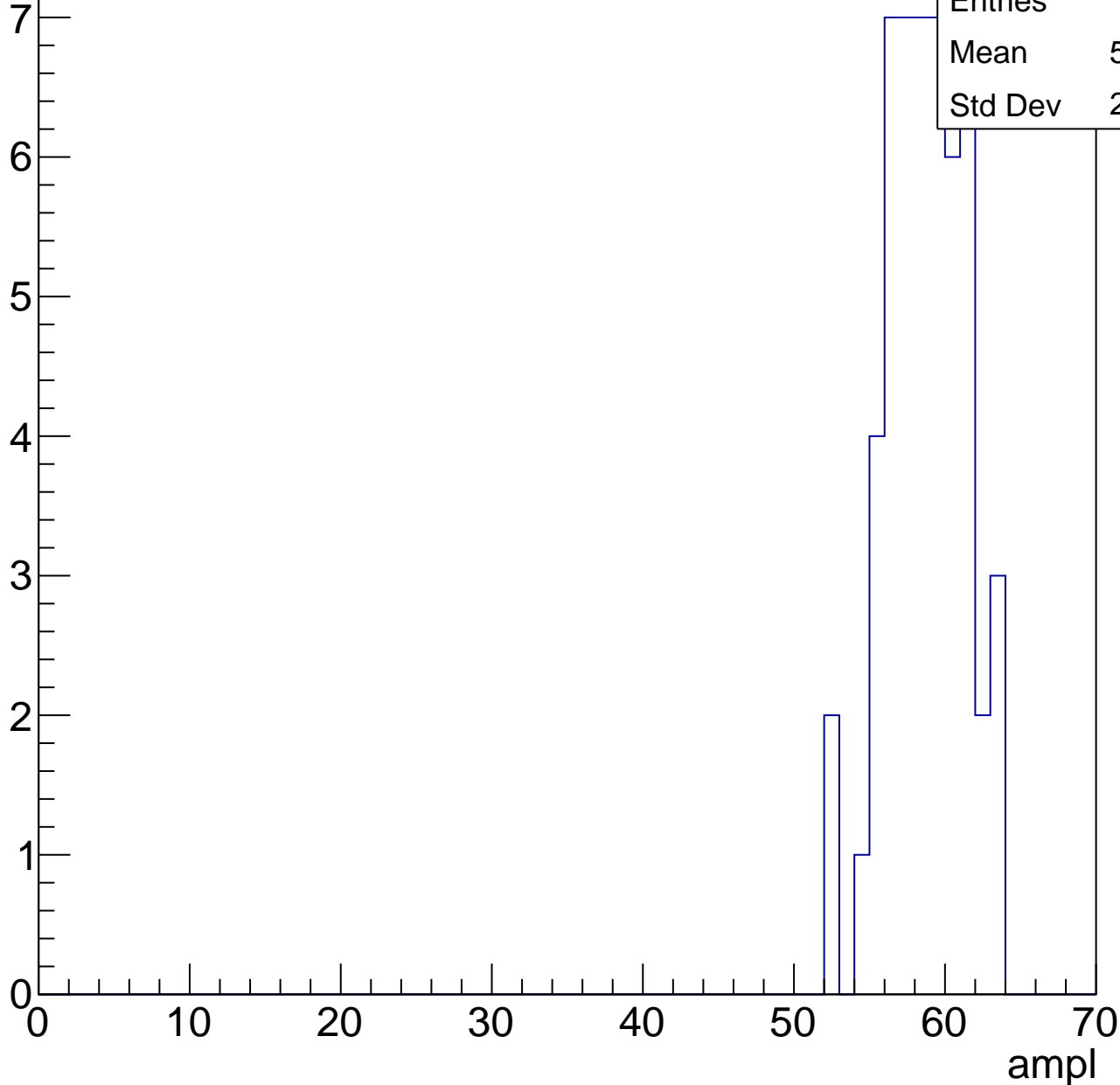
Entries	54
Mean	53.54
Std Dev	3.184

# B1L002S, U26-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	58.26
Std Dev	2.593

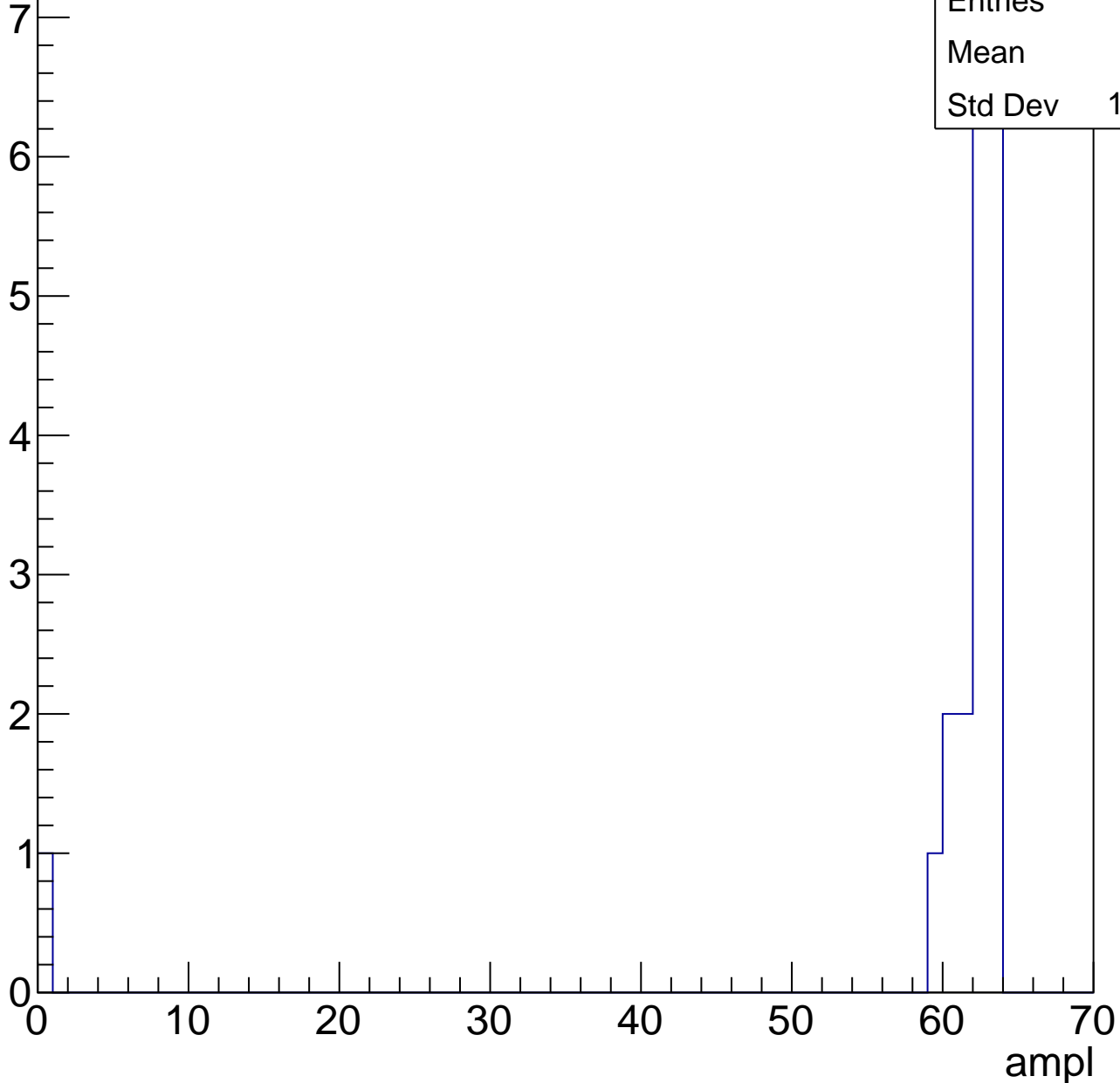


# B1L002S, U26-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.8
Std Dev	13.54

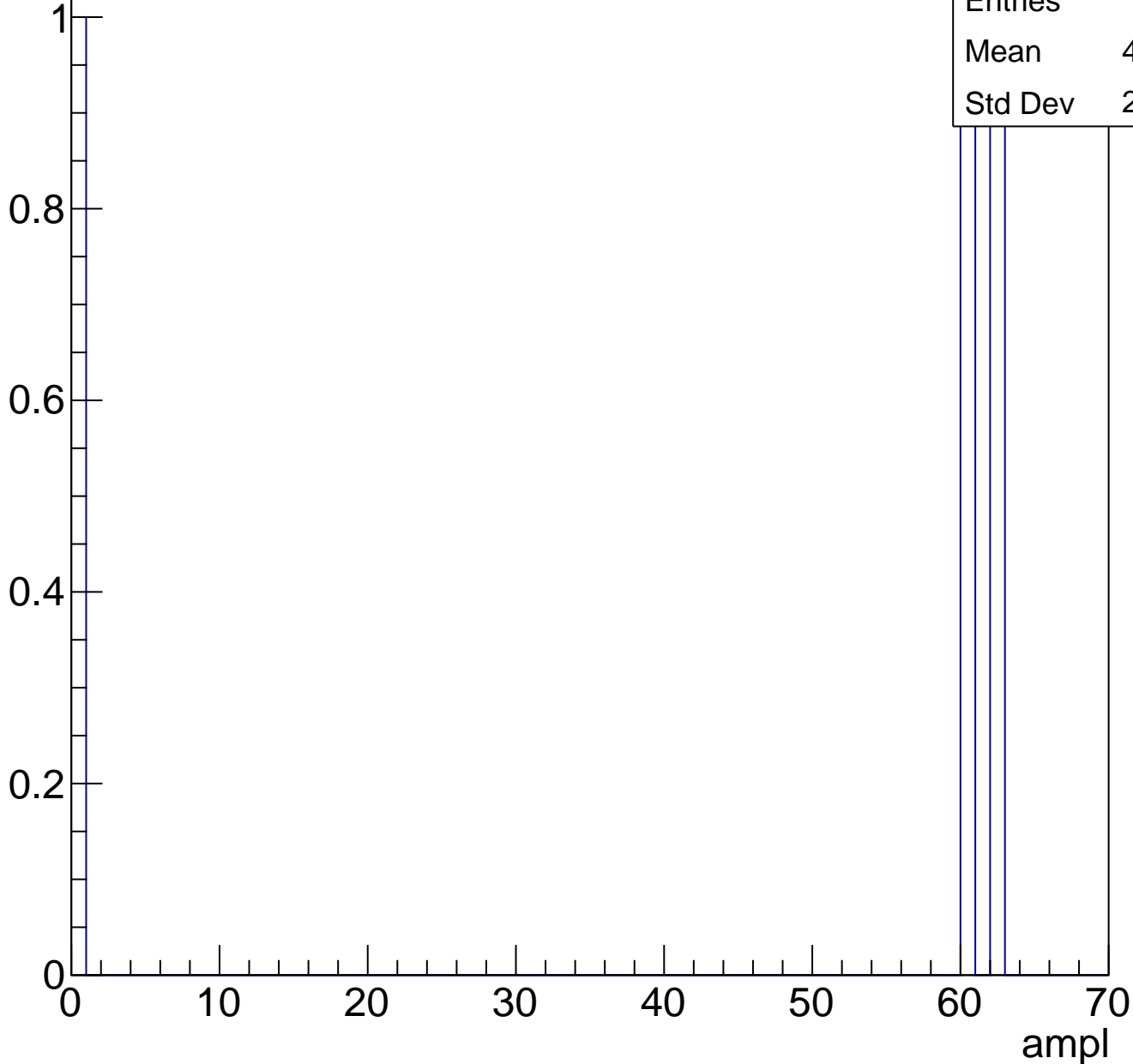




# B1L002S, U26-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch12, adc0

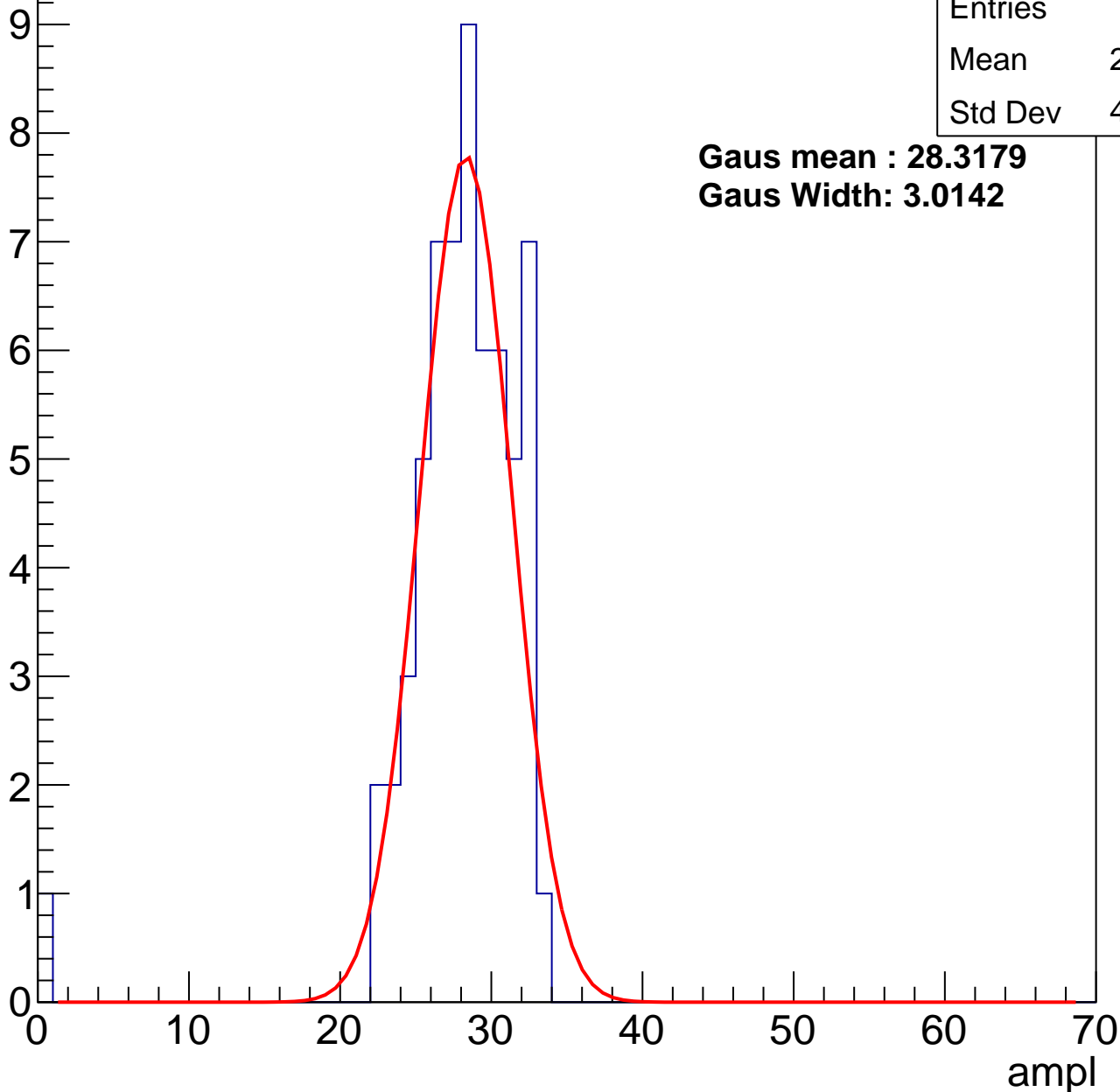
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.48
Std Dev	4.489

**Gaus mean : 28.3179**

**Gaus Width: 3.0142**



# B1L002S, U26-ch12, adc1

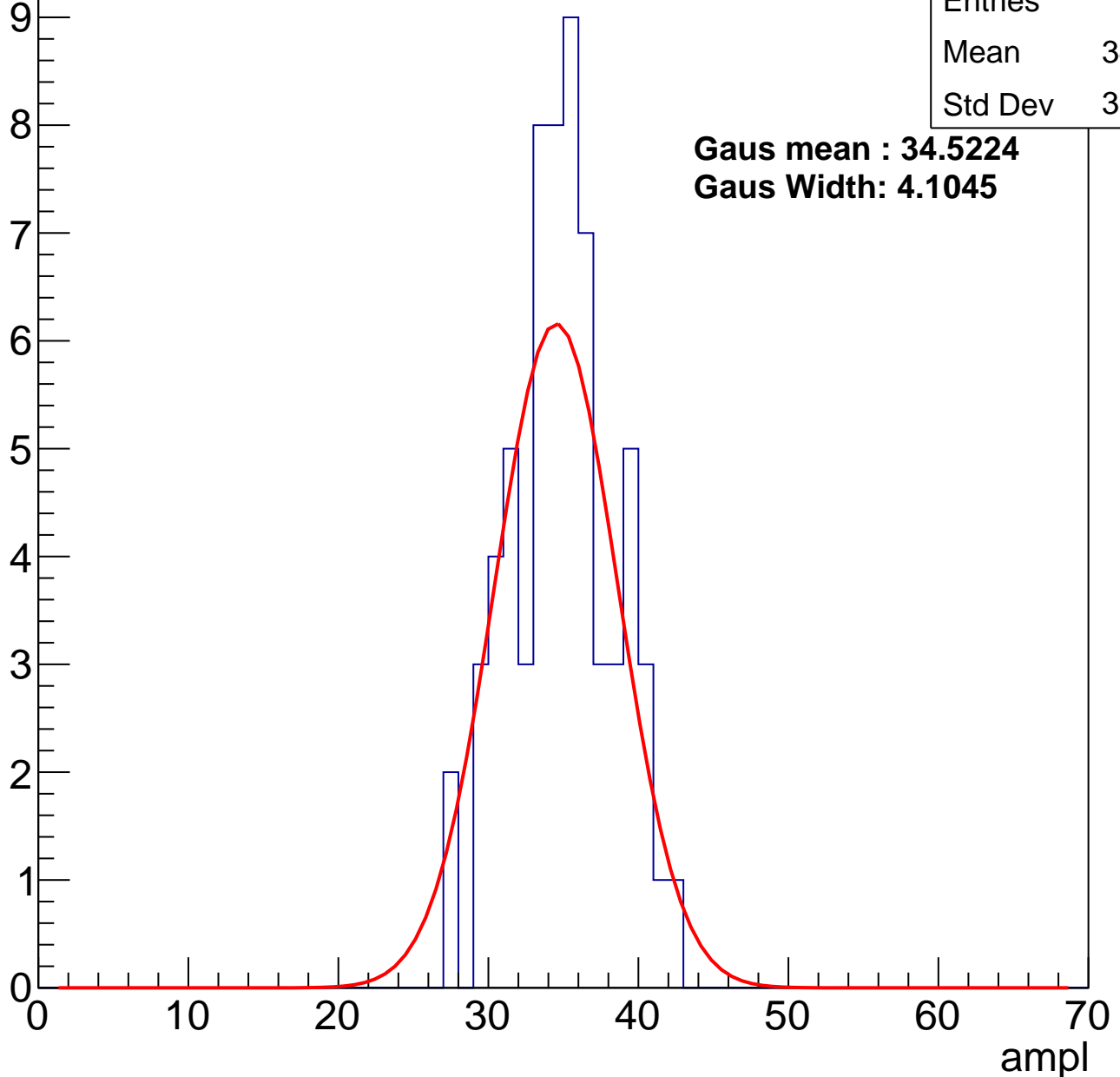
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.43
Std Dev	3.392

**Gaus mean : 34.5224**

**Gaus Width: 4.1045**



# B1L002S, U26-ch12, adc2

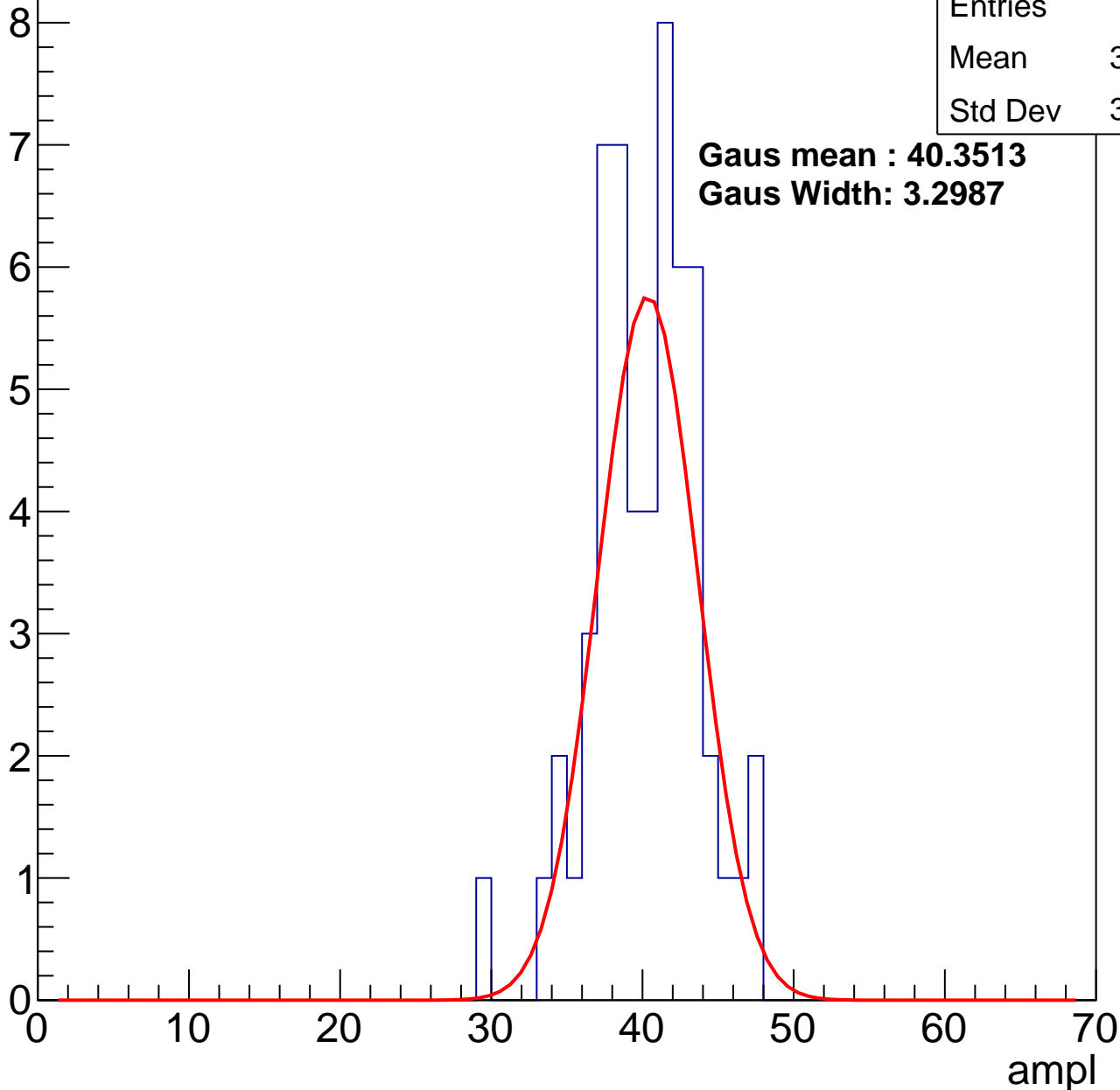
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	39.73
Std Dev	3.492

**Gaus mean : 40.3513**

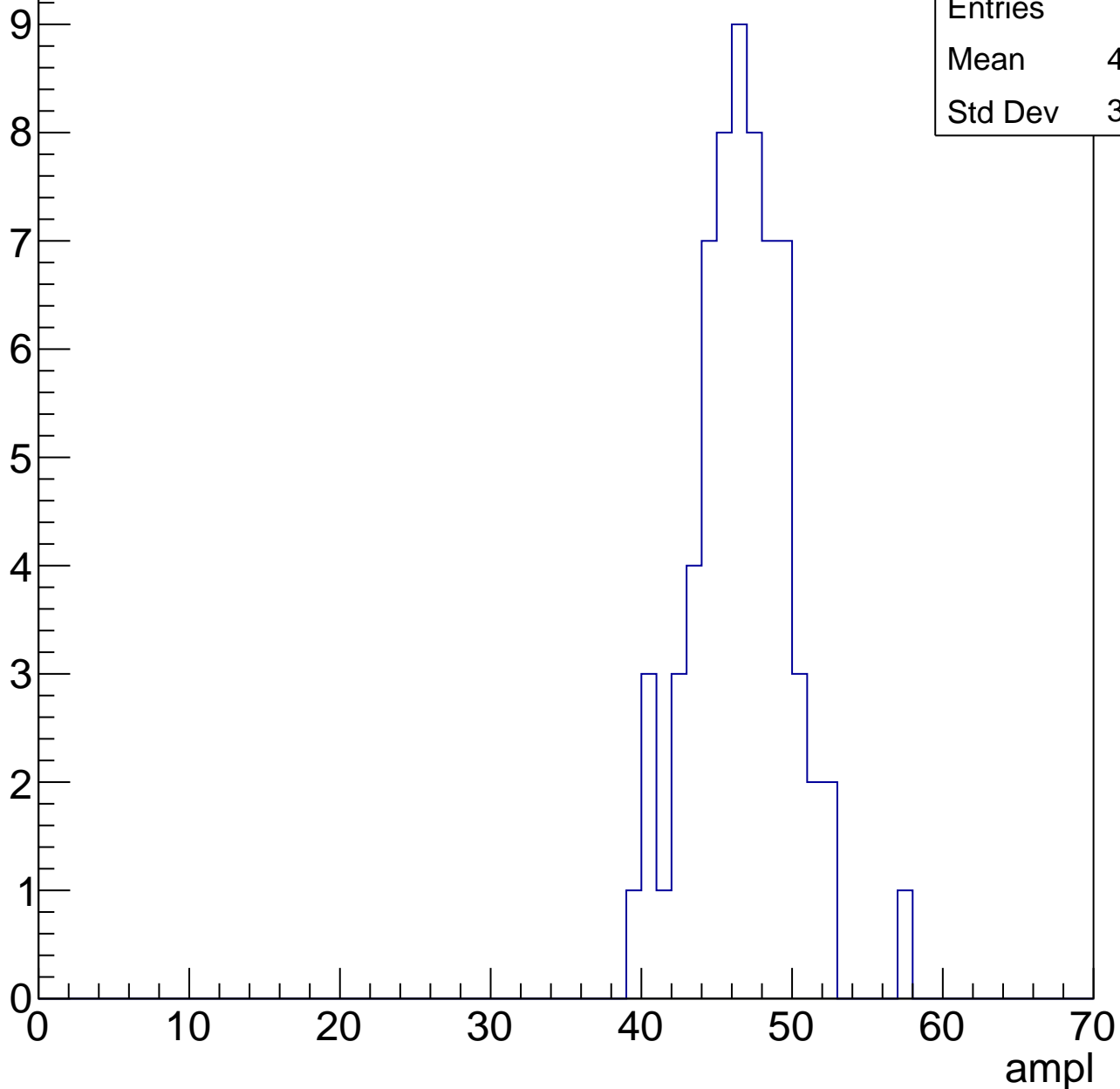
**Gaus Width: 3.2987**



# B1L002S, U26-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

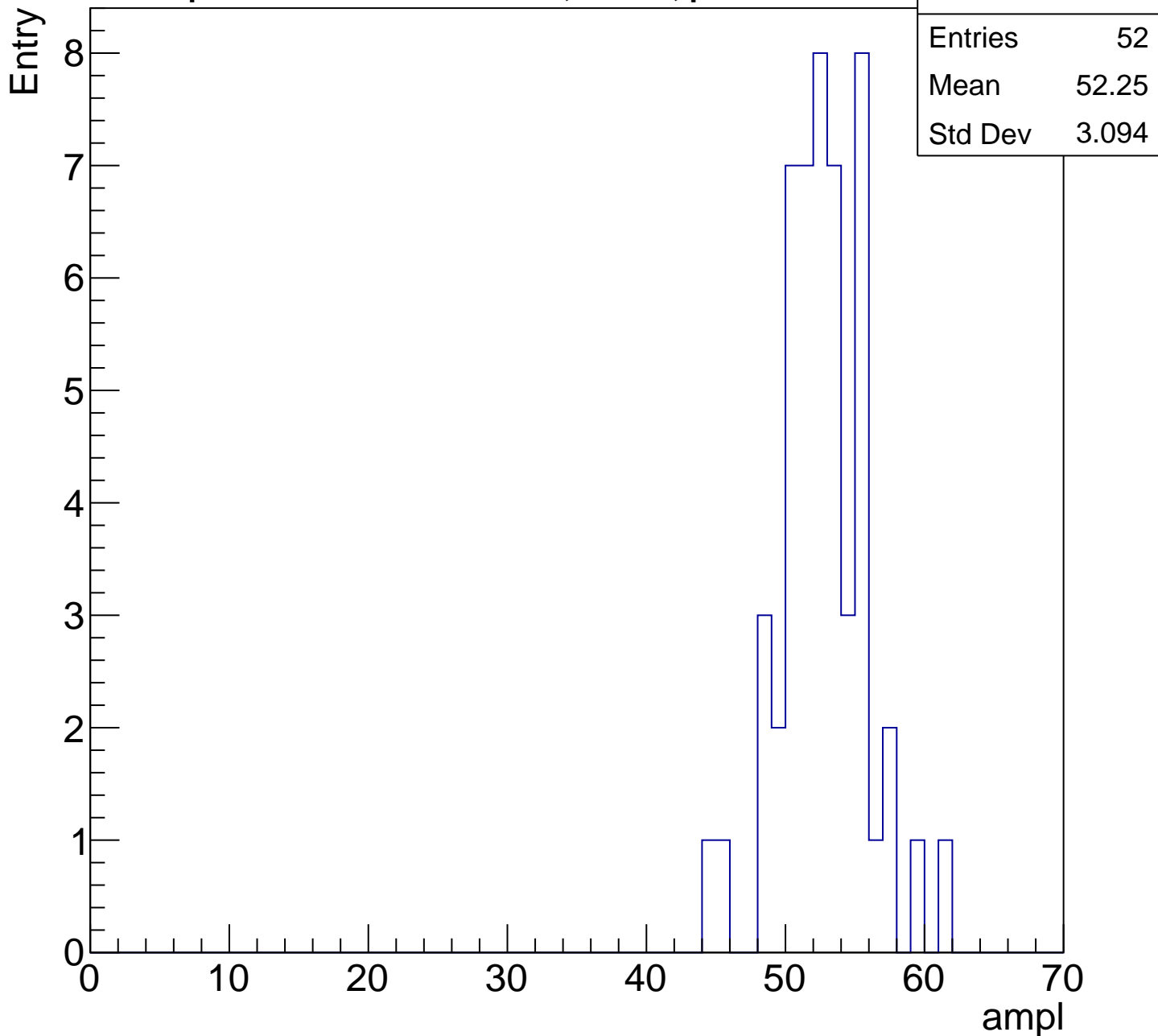
Entry



Entries	66
Mean	46.18
Std Dev	3.233

# B1L002S, U26-ch12, adc4

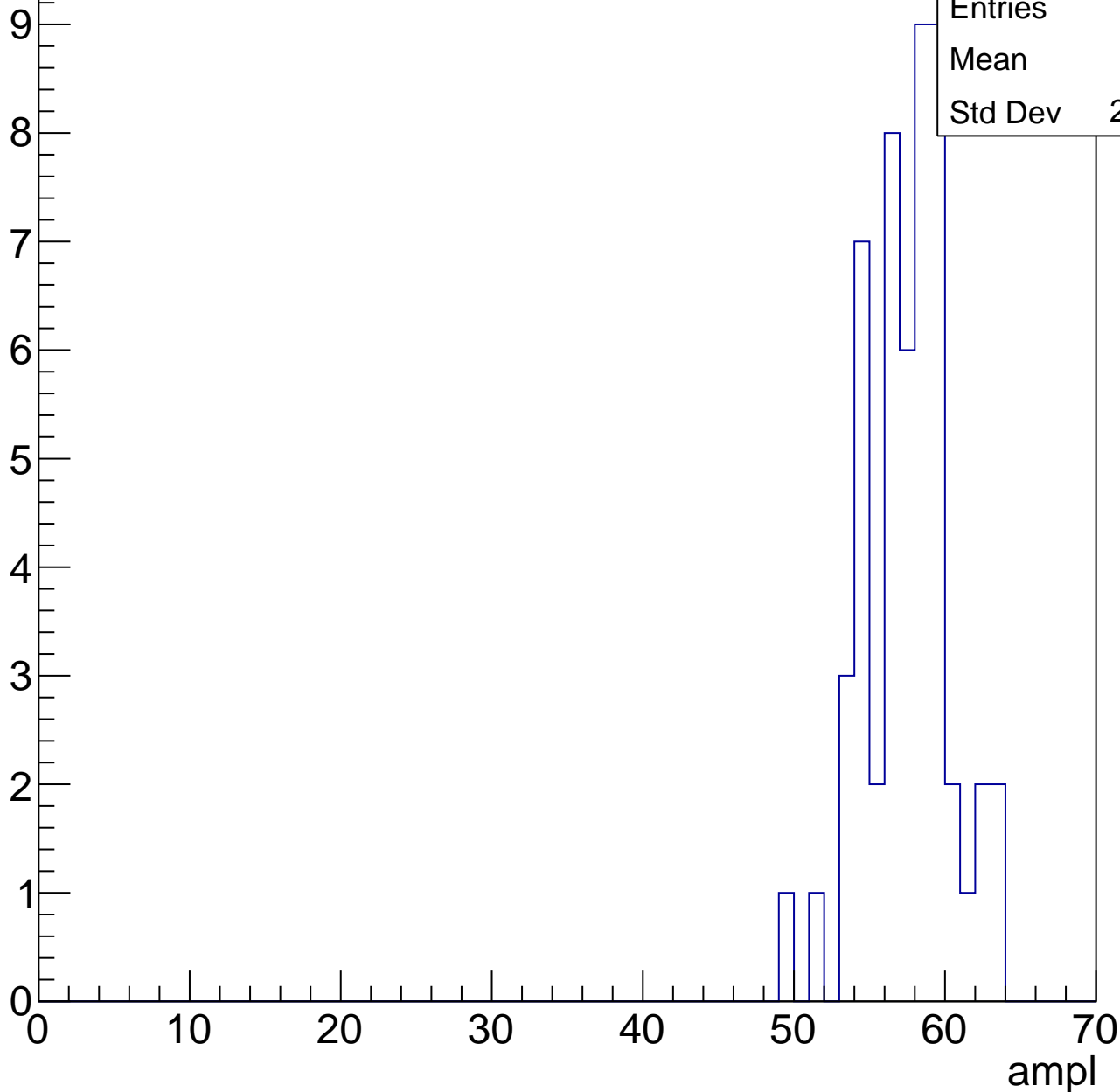
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

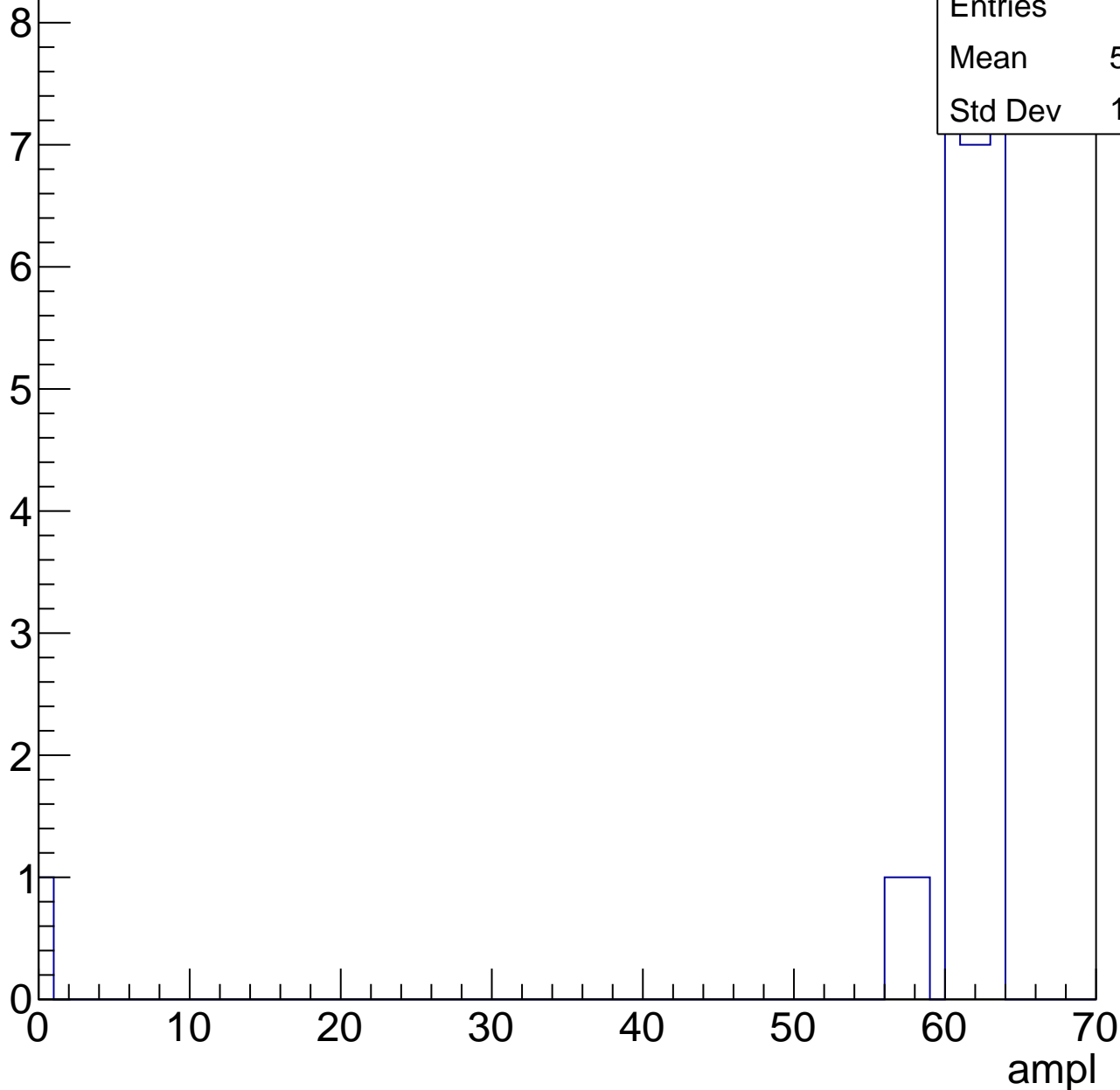


# B1L002S, U26-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	59.29
Std Dev	10.46

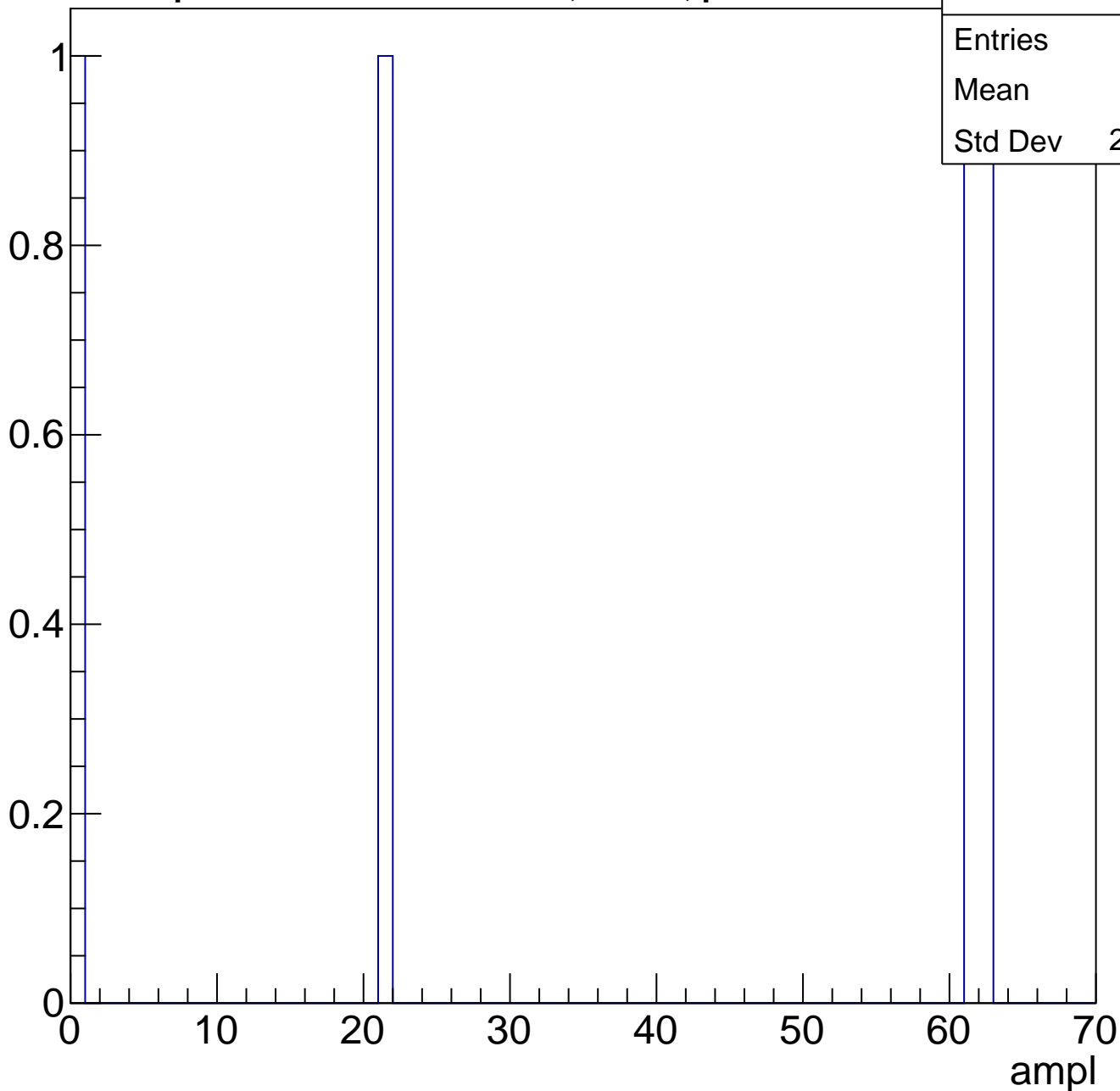




# B1L002S, U26-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch13, adc0

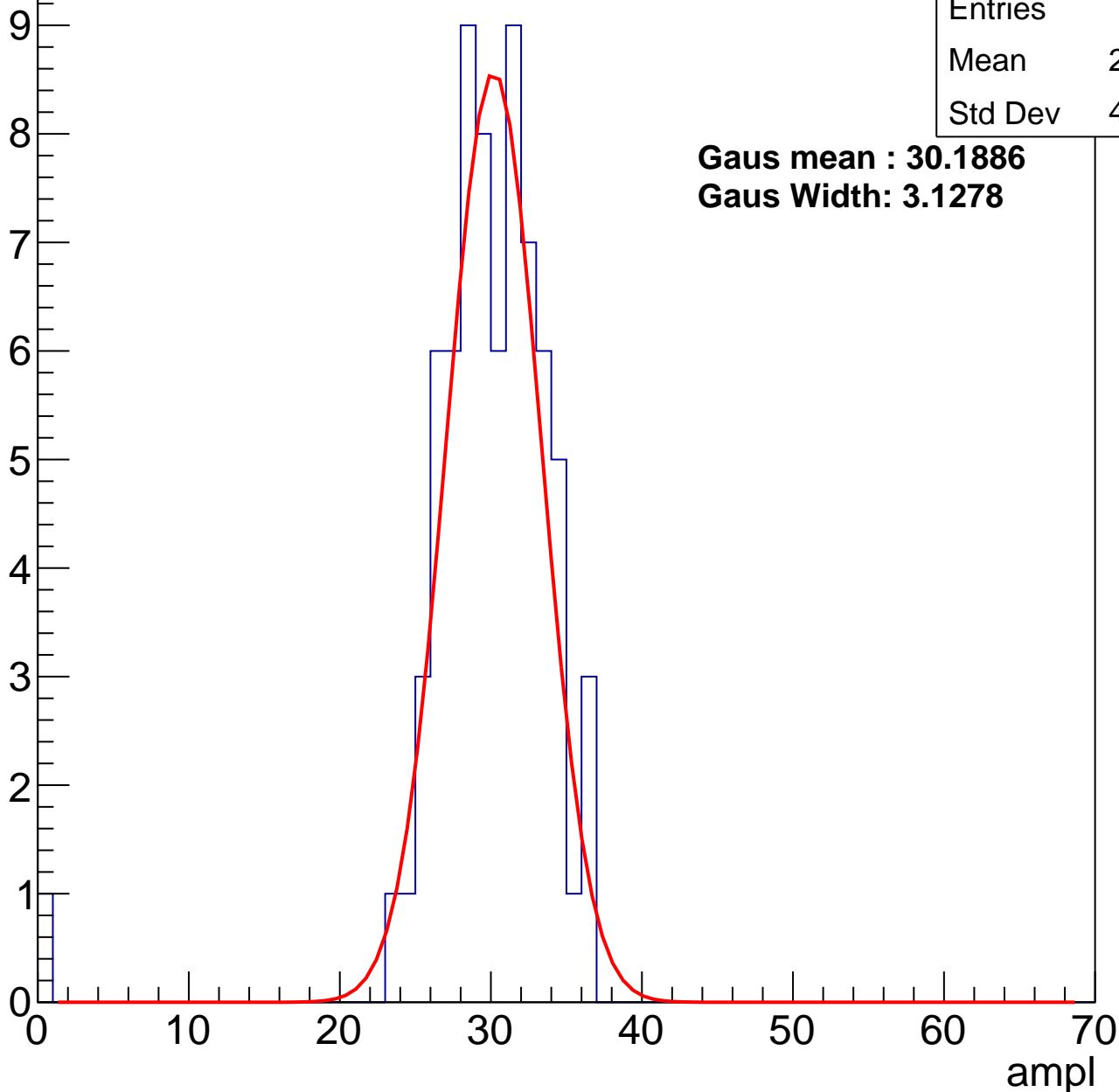
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	29.42
Std Dev	4.615

**Gaus mean : 30.1886**

**Gaus Width: 3.1278**



# B1L002S, U26-ch13, adc1

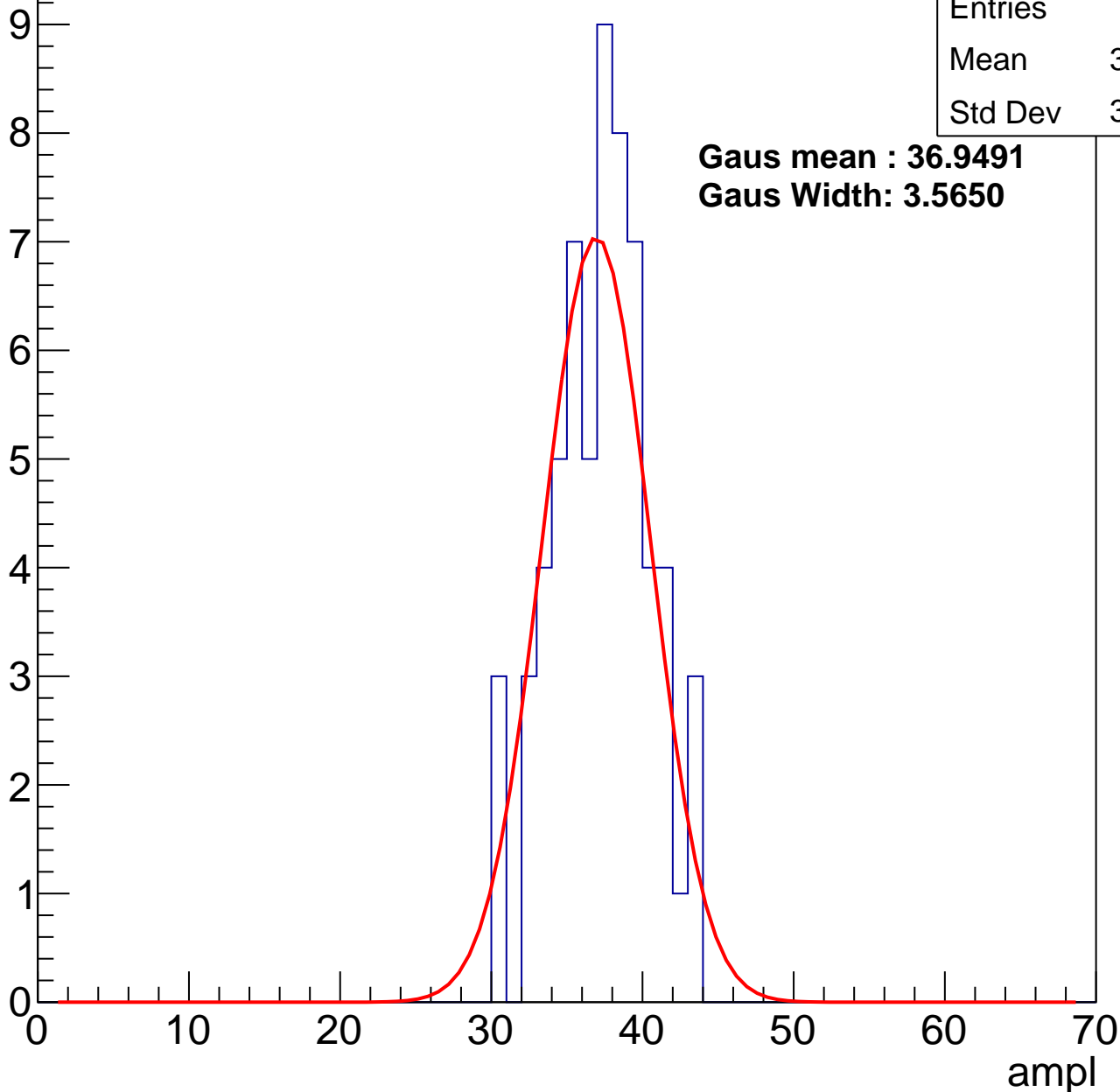
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	36.79
Std Dev	3.163

**Gaus mean : 36.9491**

**Gaus Width: 3.5650**



# B1L002S, U26-ch13, adc2

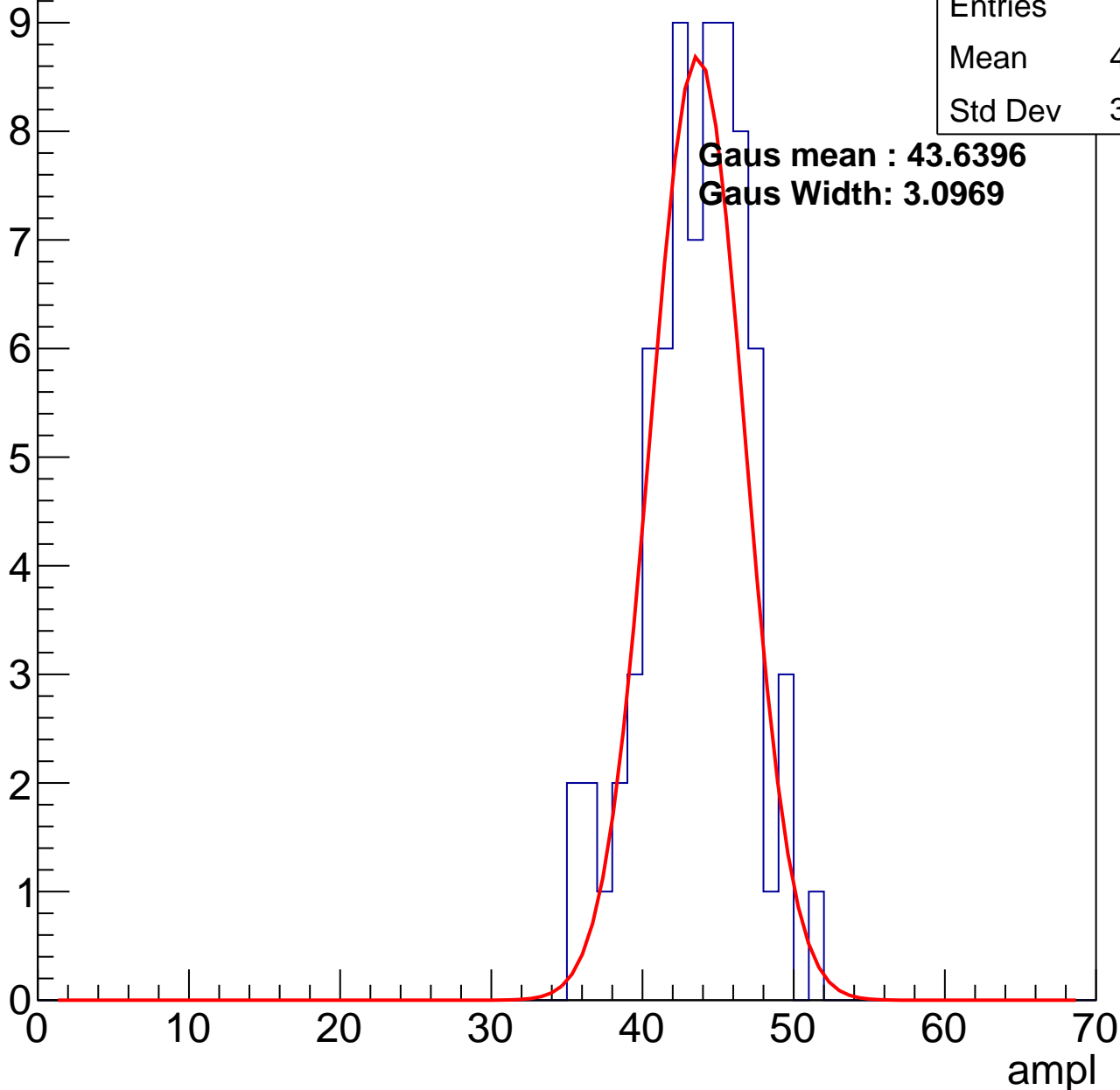
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	43.12
Std Dev	3.378

**Gaus mean : 43.6396**

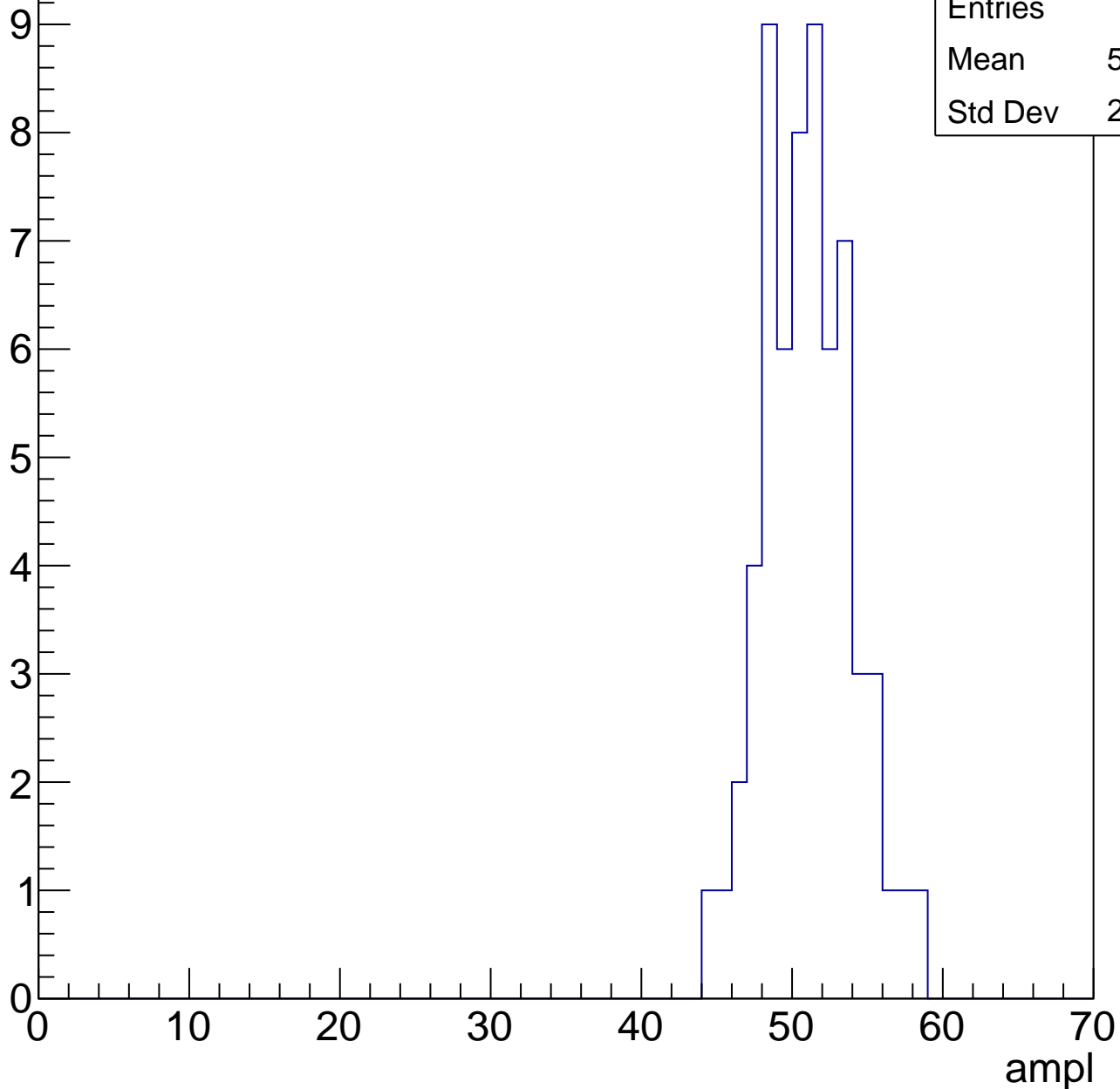
**Gaus Width: 3.0969**



# B1L002S, U26-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	62
Mean	50.56
Std Dev	2.894

# B1L002S, U26-ch13, adc4

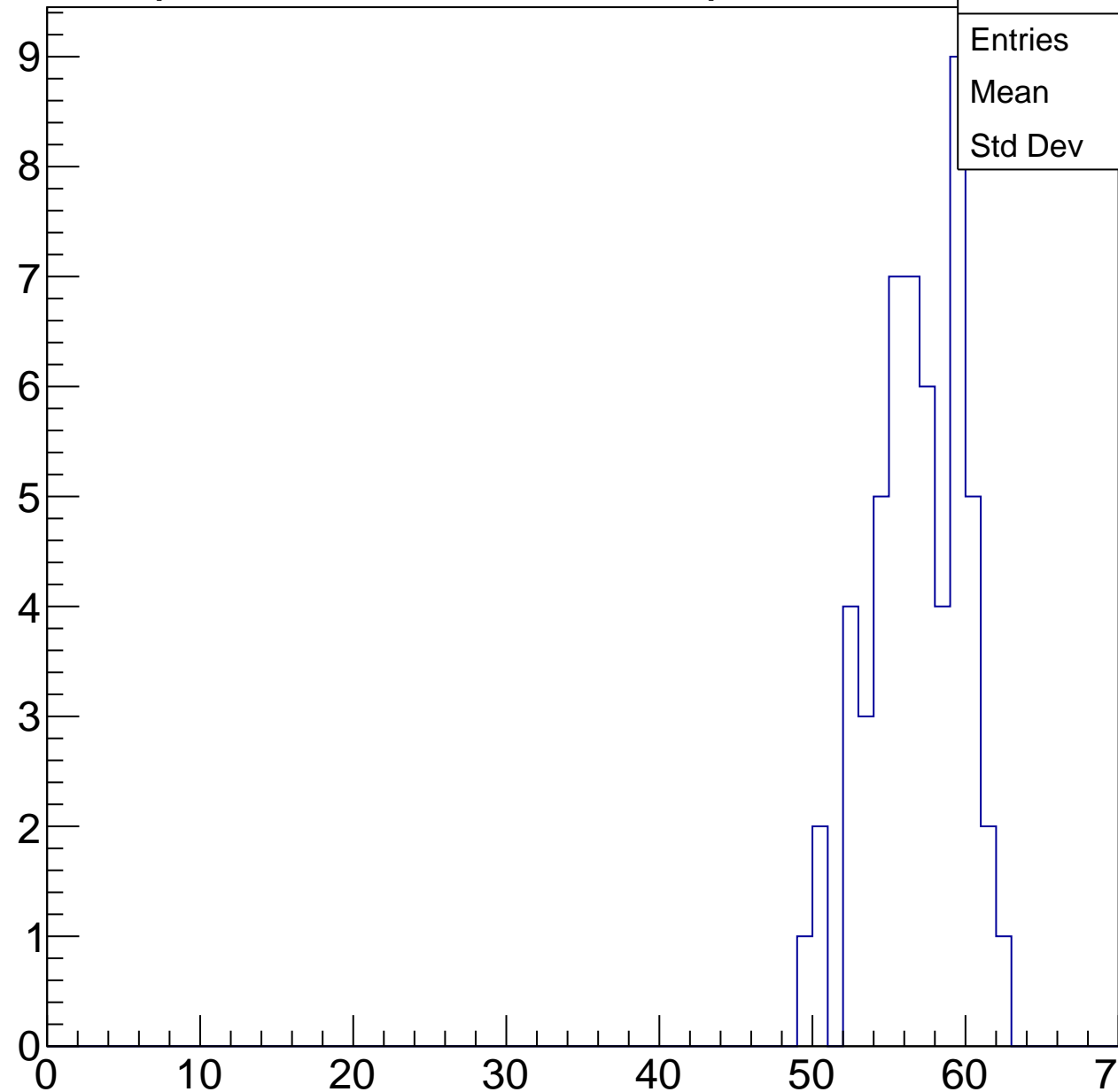
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	56.29
Std Dev	2.998

ampl

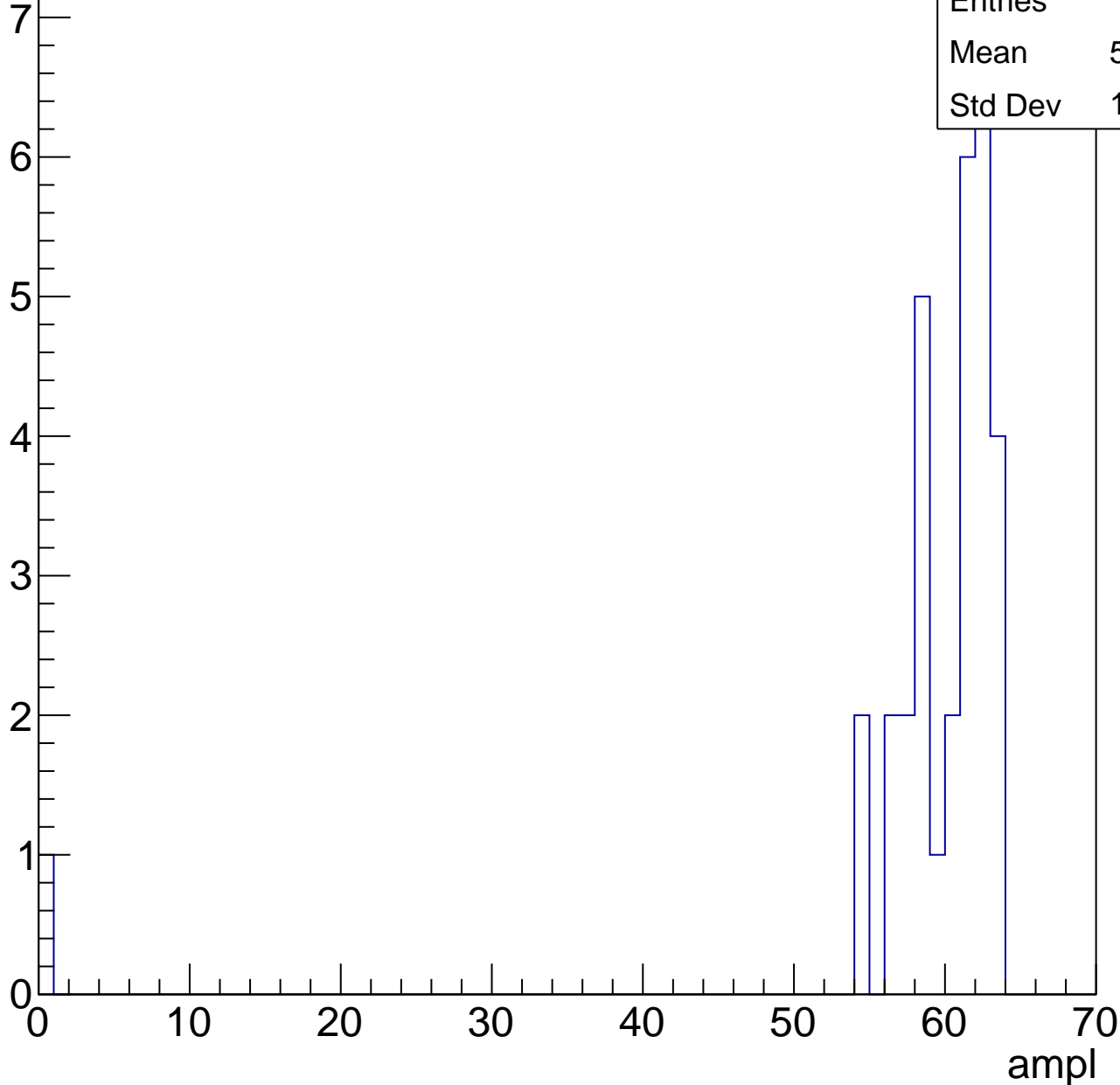


# B1L002S, U26-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	57.97
Std Dev	10.73

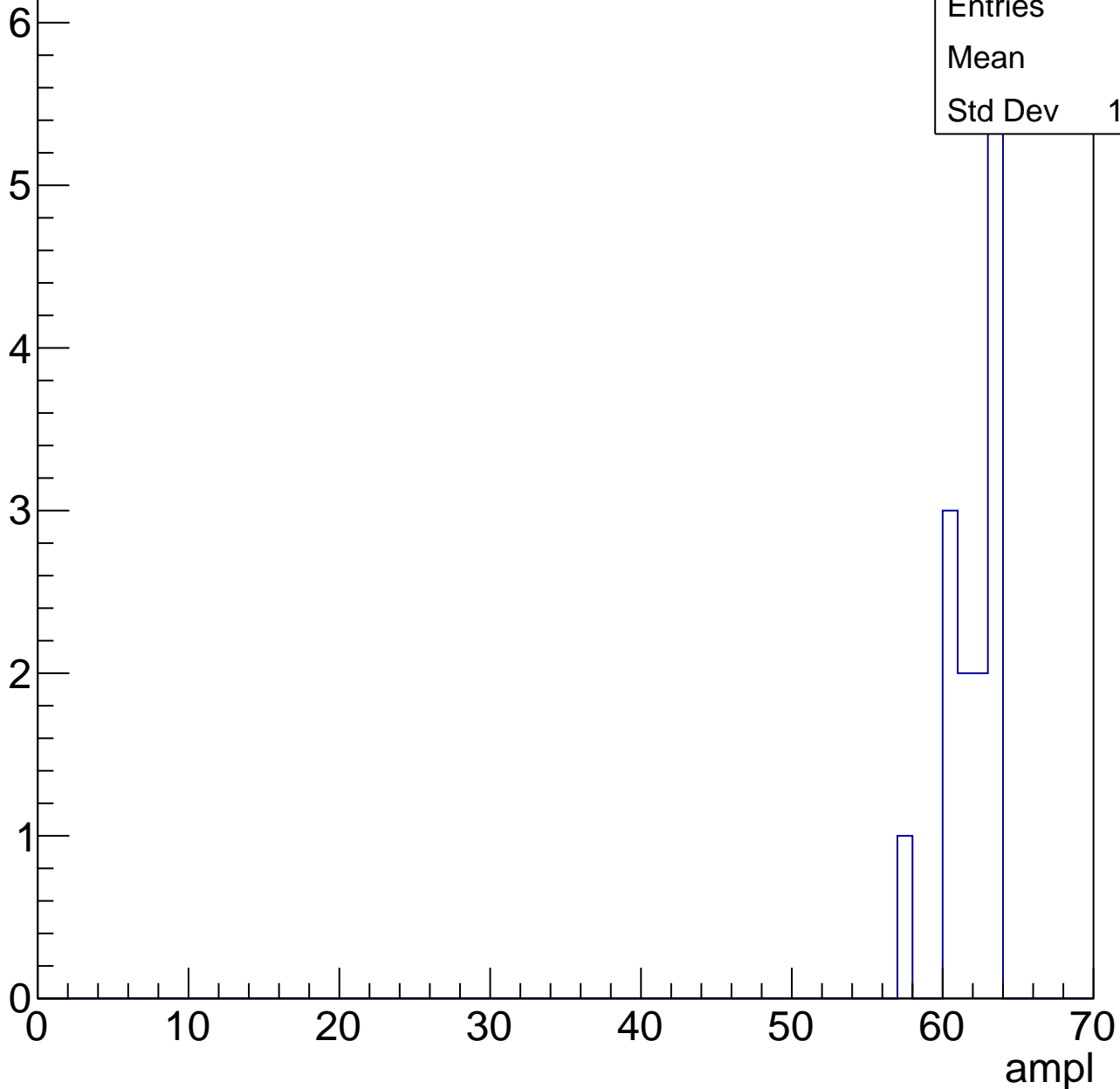


# B1L002S, U26-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.5
Std Dev	1.722

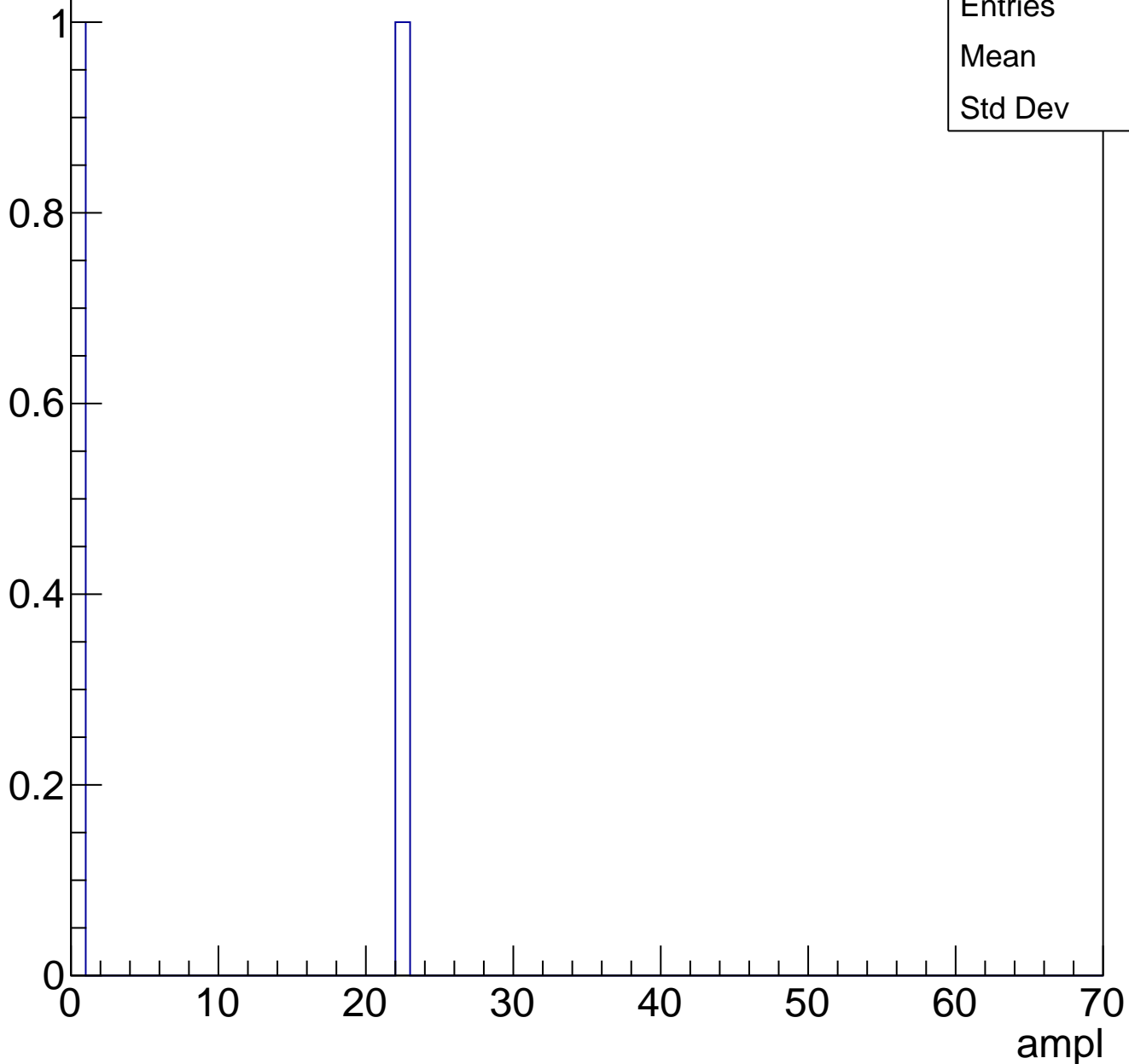




# B1L002S, U26-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U26-ch14, adc0

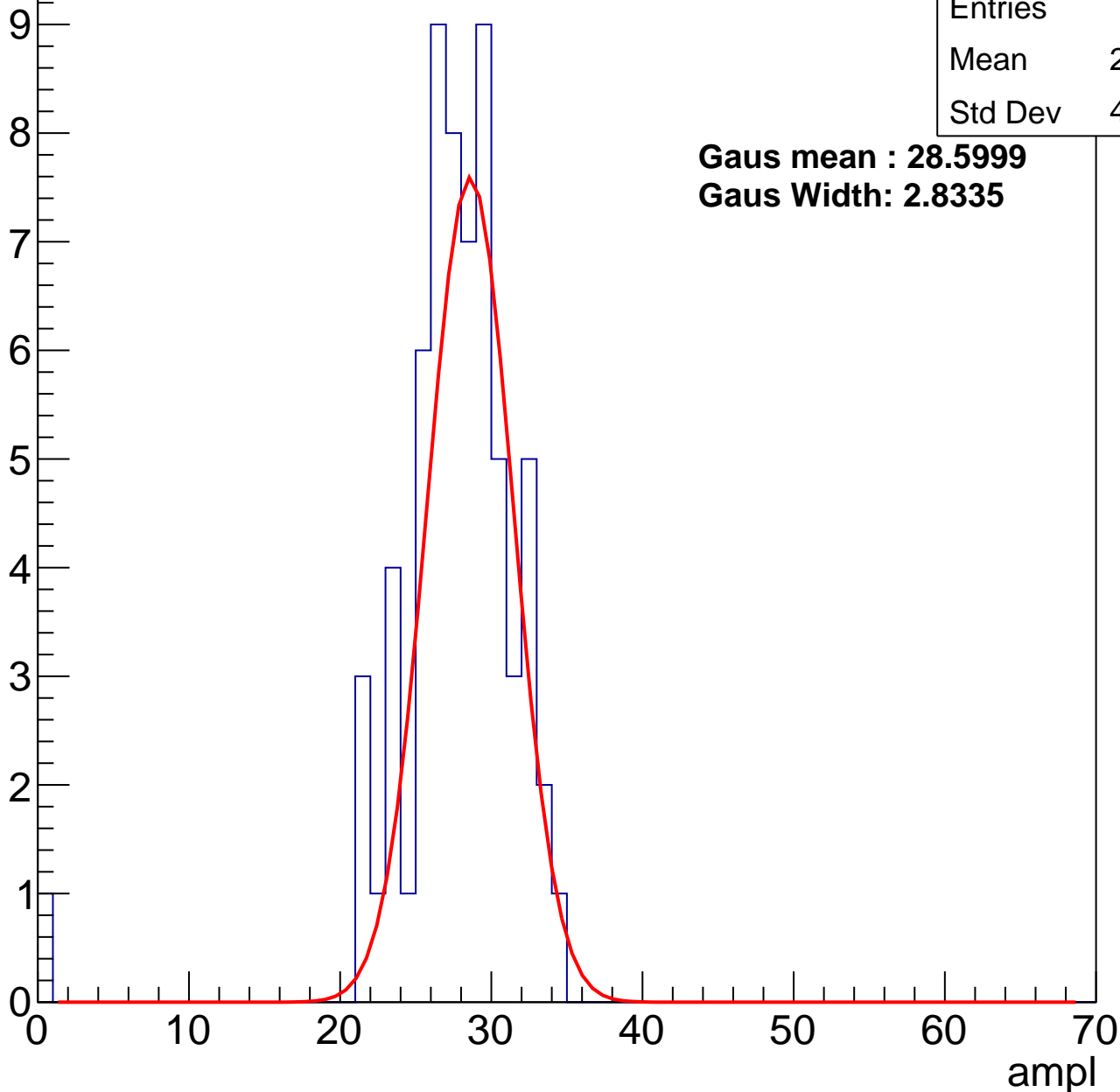
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.09
Std Dev	4.556

**Gaus mean : 28.5999**

**Gaus Width: 2.8335**



# B1L002S, U26-ch14, adc1

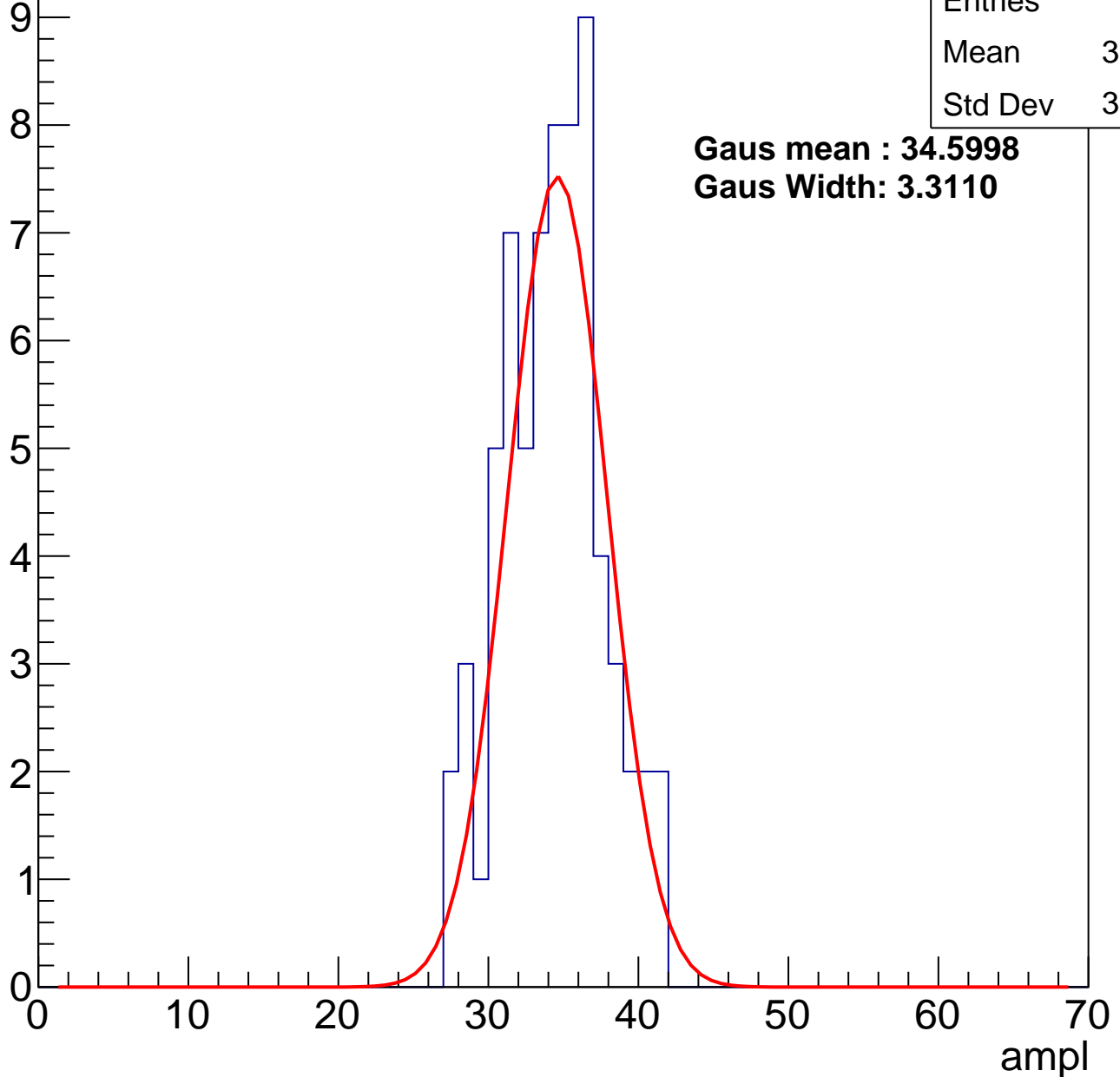
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.87
Std Dev	3.316

**Gaus mean : 34.5998**

**Gaus Width: 3.3110**



# B1L002S, U26-ch14, adc2

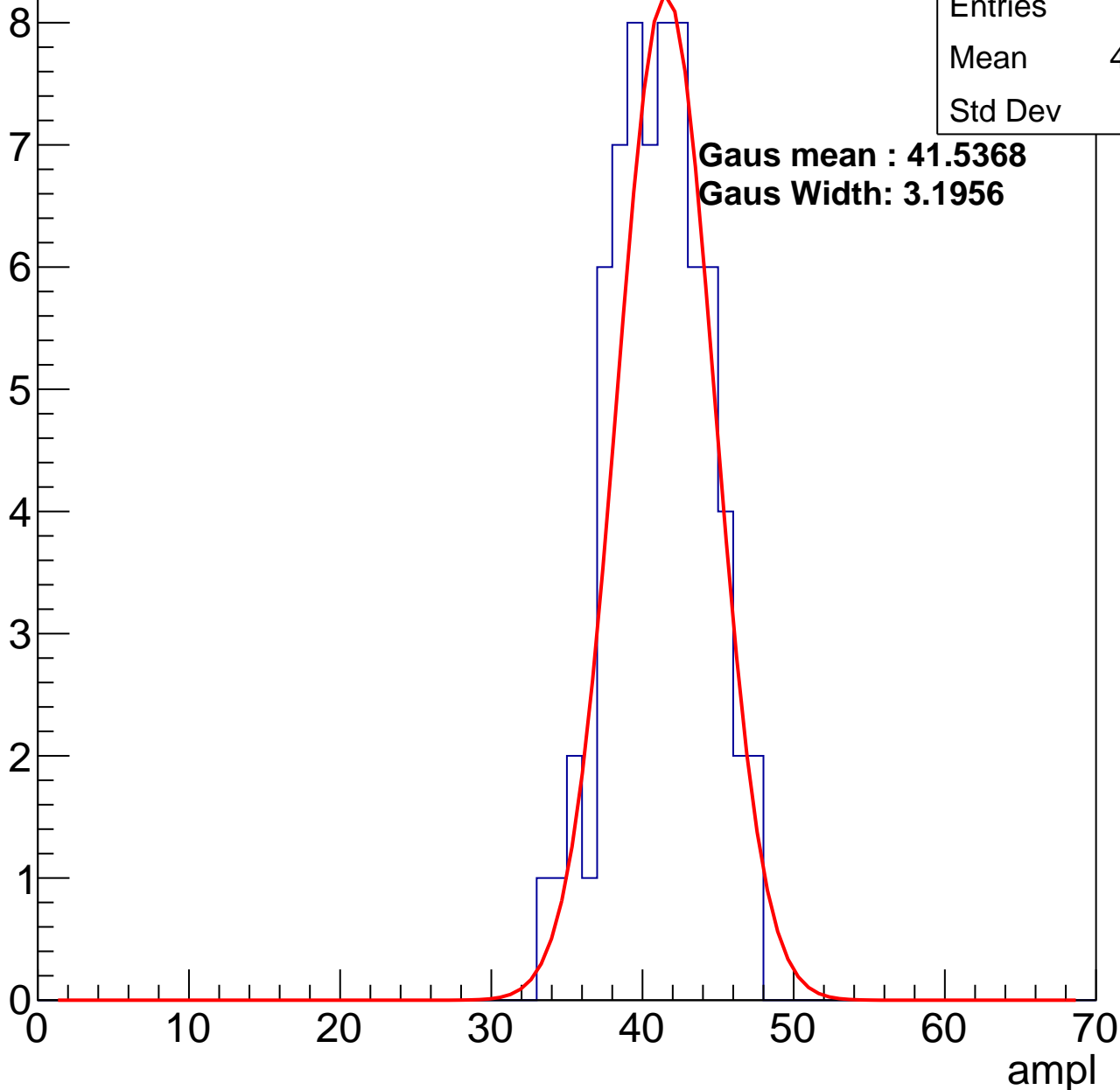
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	40.65
Std Dev	3.12

**Gaus mean : 41.5368**

**Gaus Width: 3.1956**

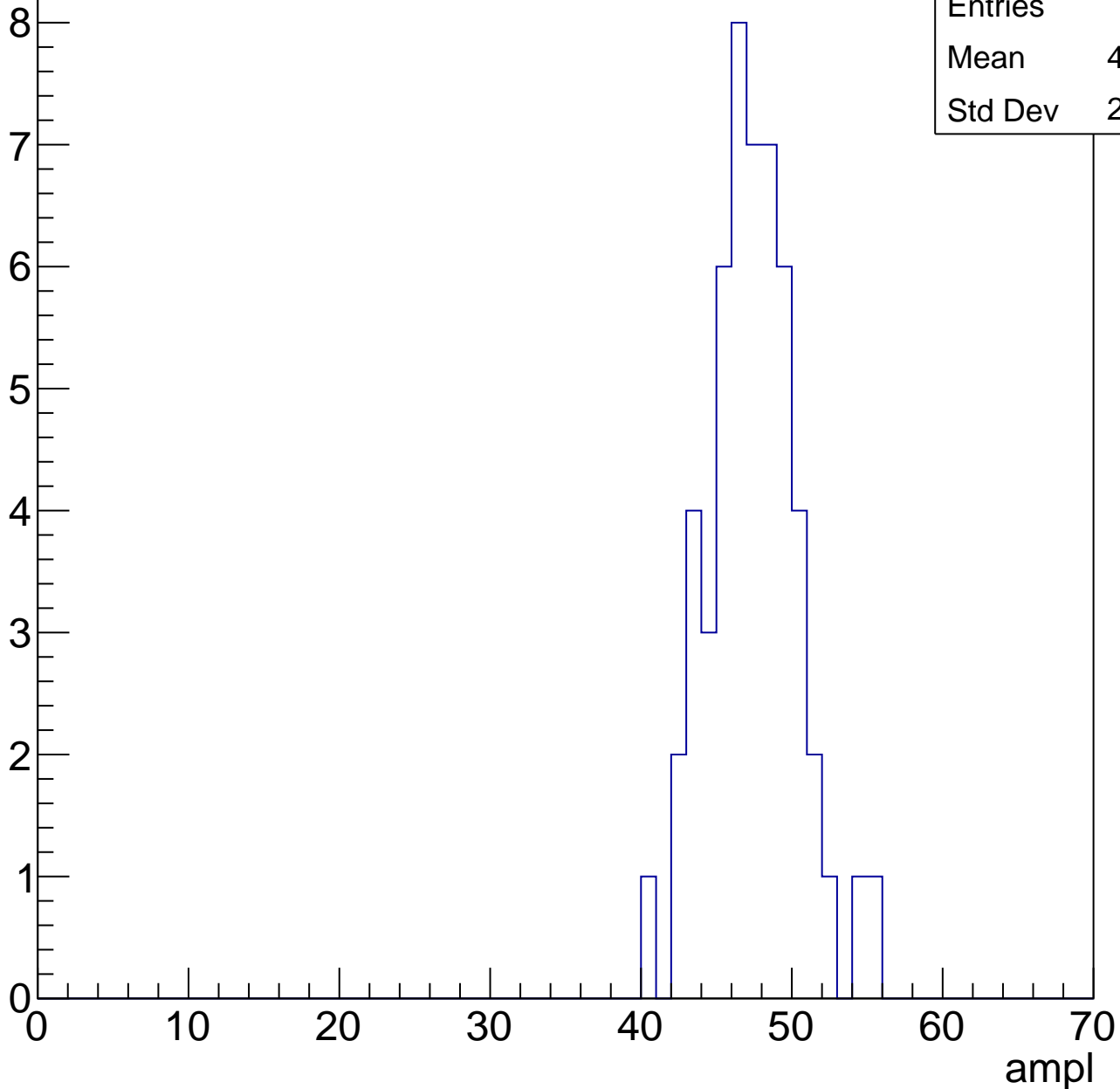


# B1L002S, U26-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.94
Std Dev	2.942

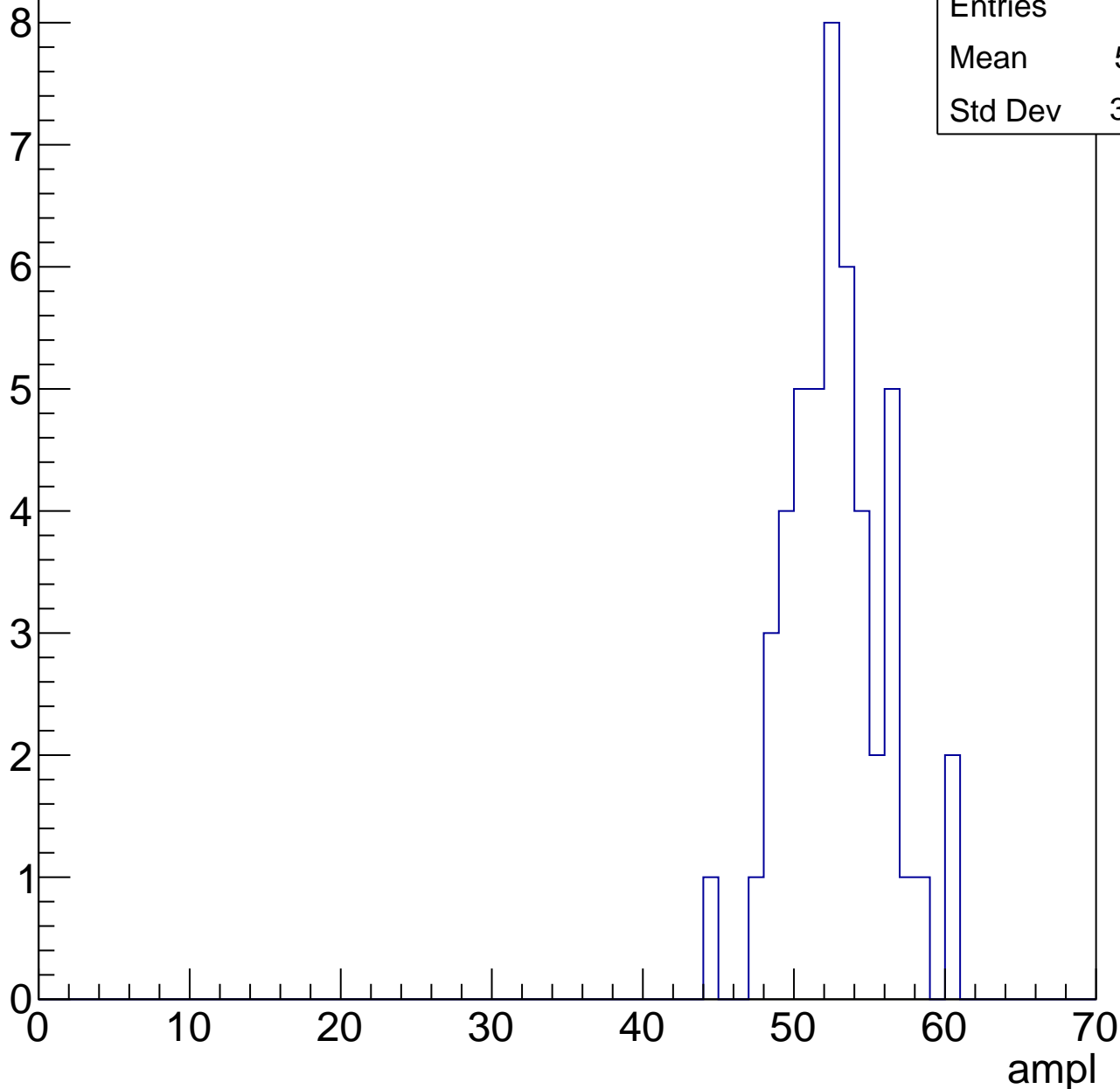


# B1L002S, U26-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	52.31
Std Dev	3.235

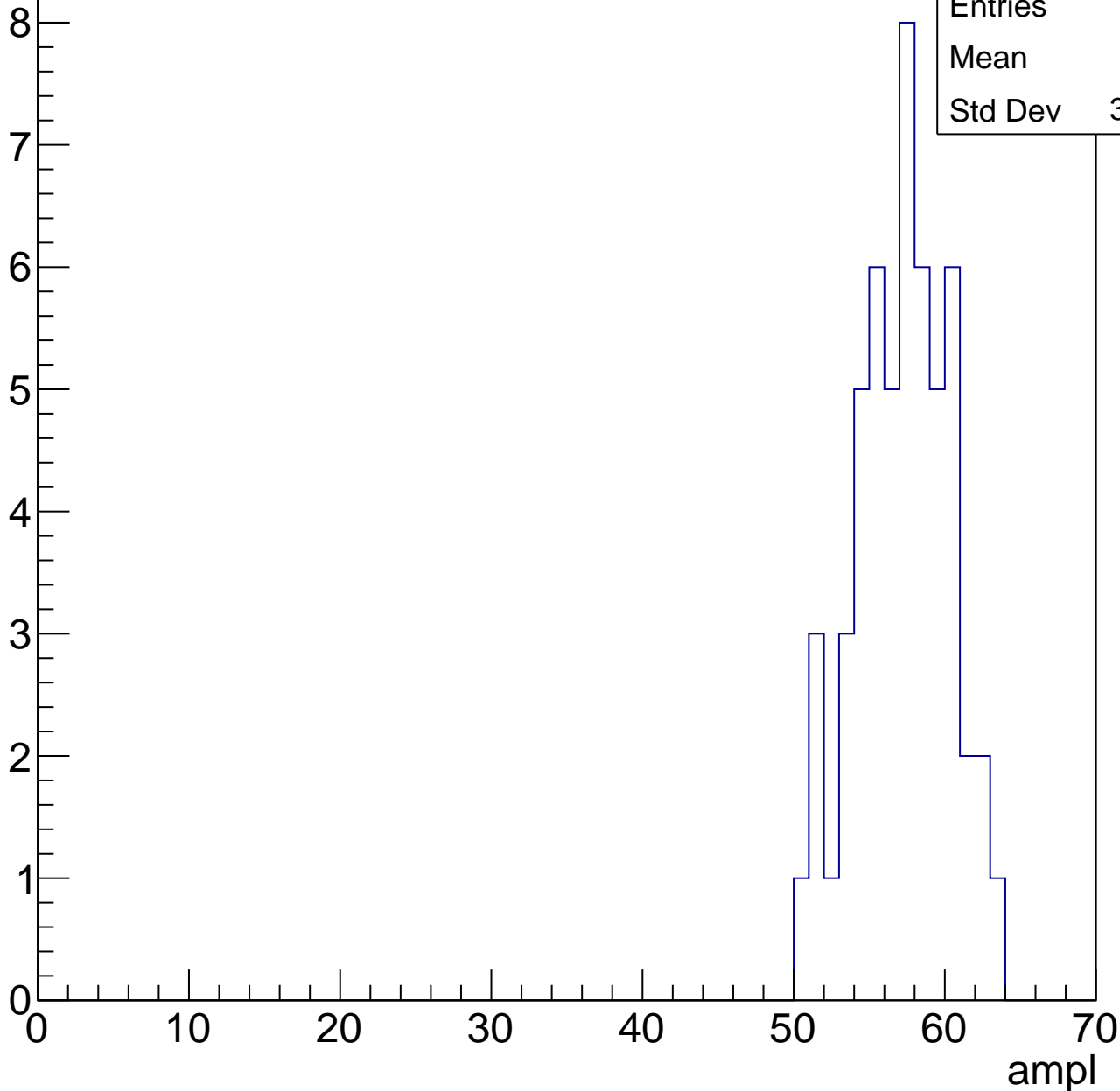


# B1L002S, U26-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	56.7
Std Dev	3.035

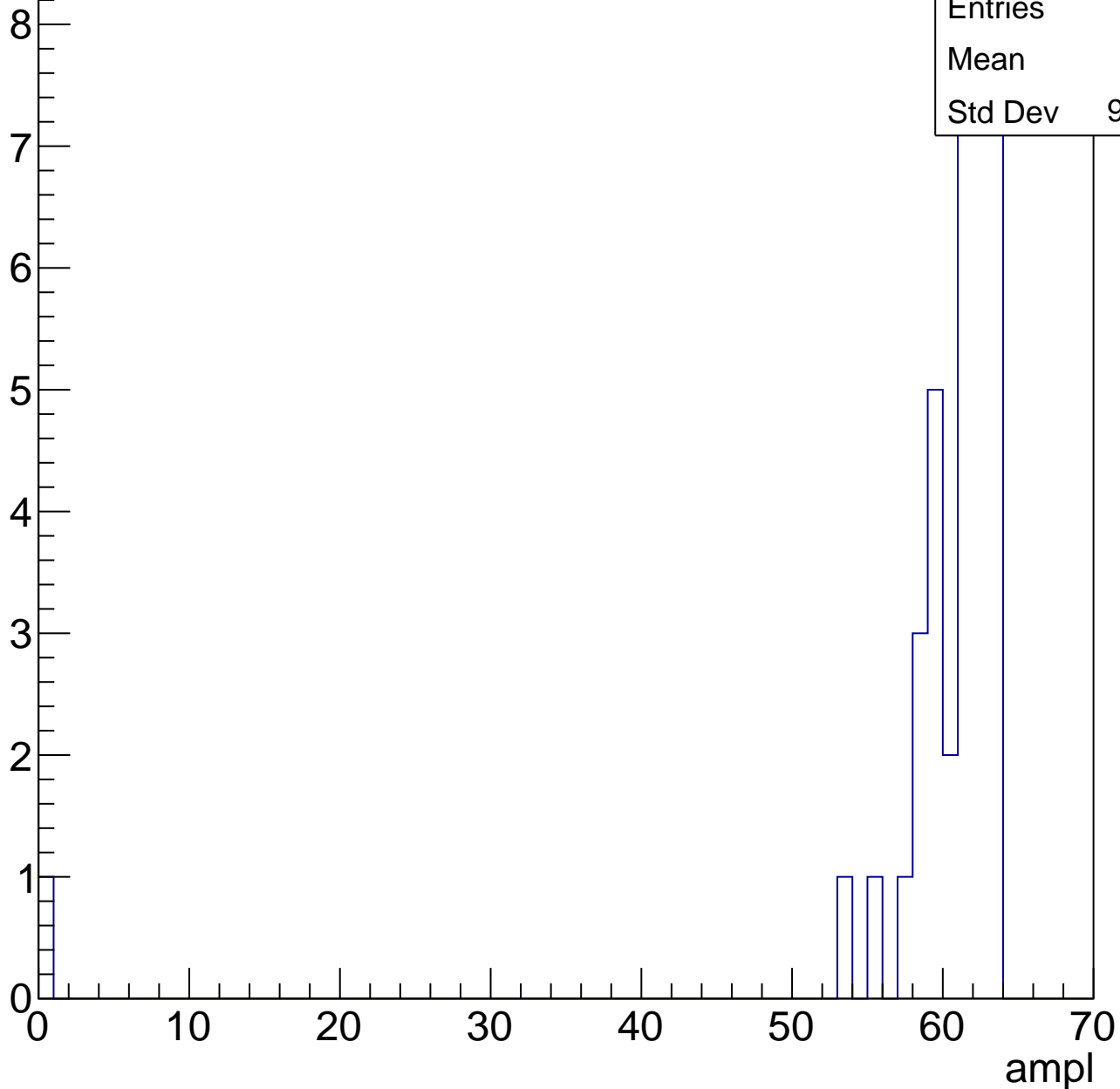


# B1L002S, U26-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	59
Std Dev	9.966

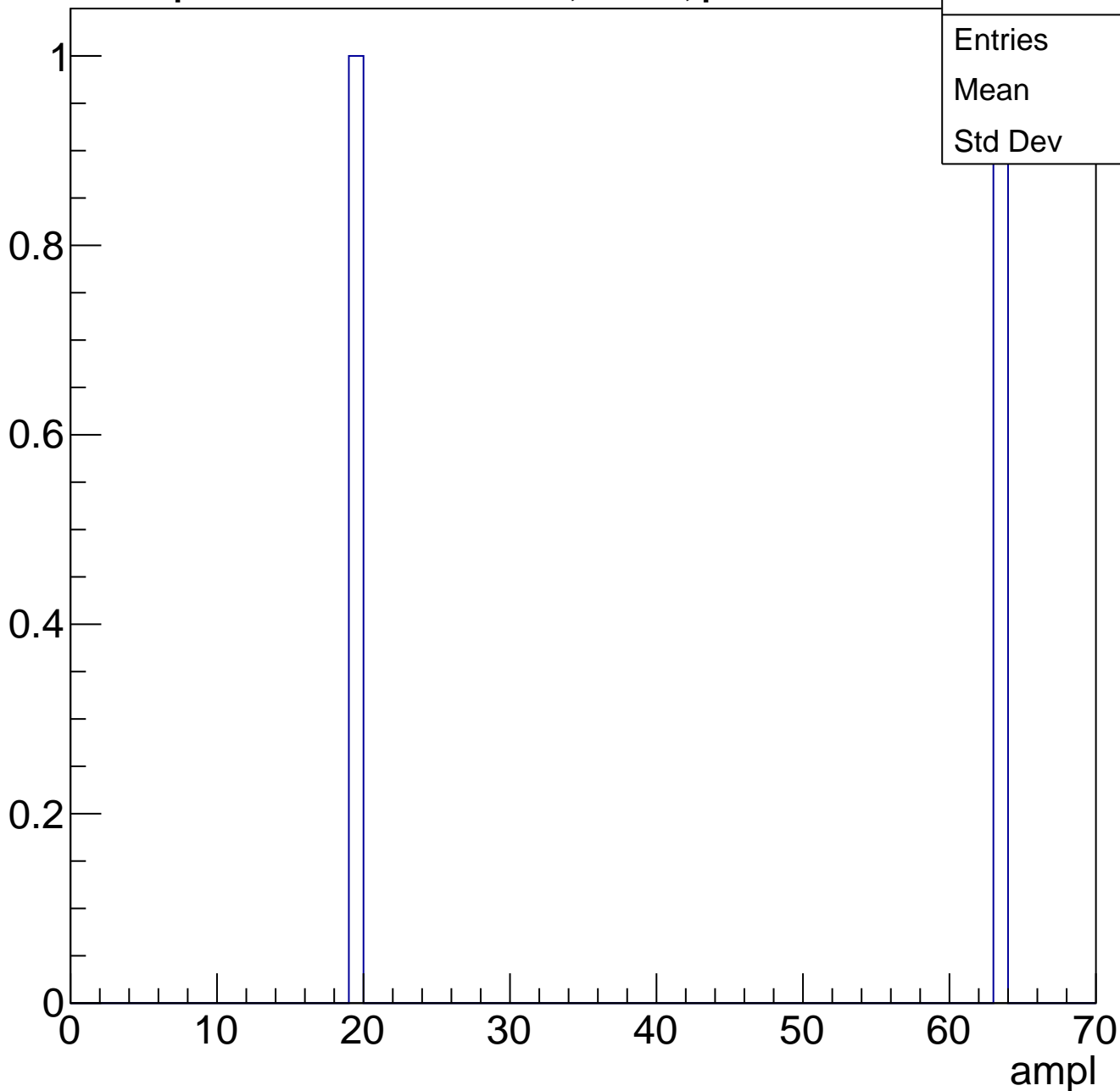




# B1L002S, U26-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch15, adc0

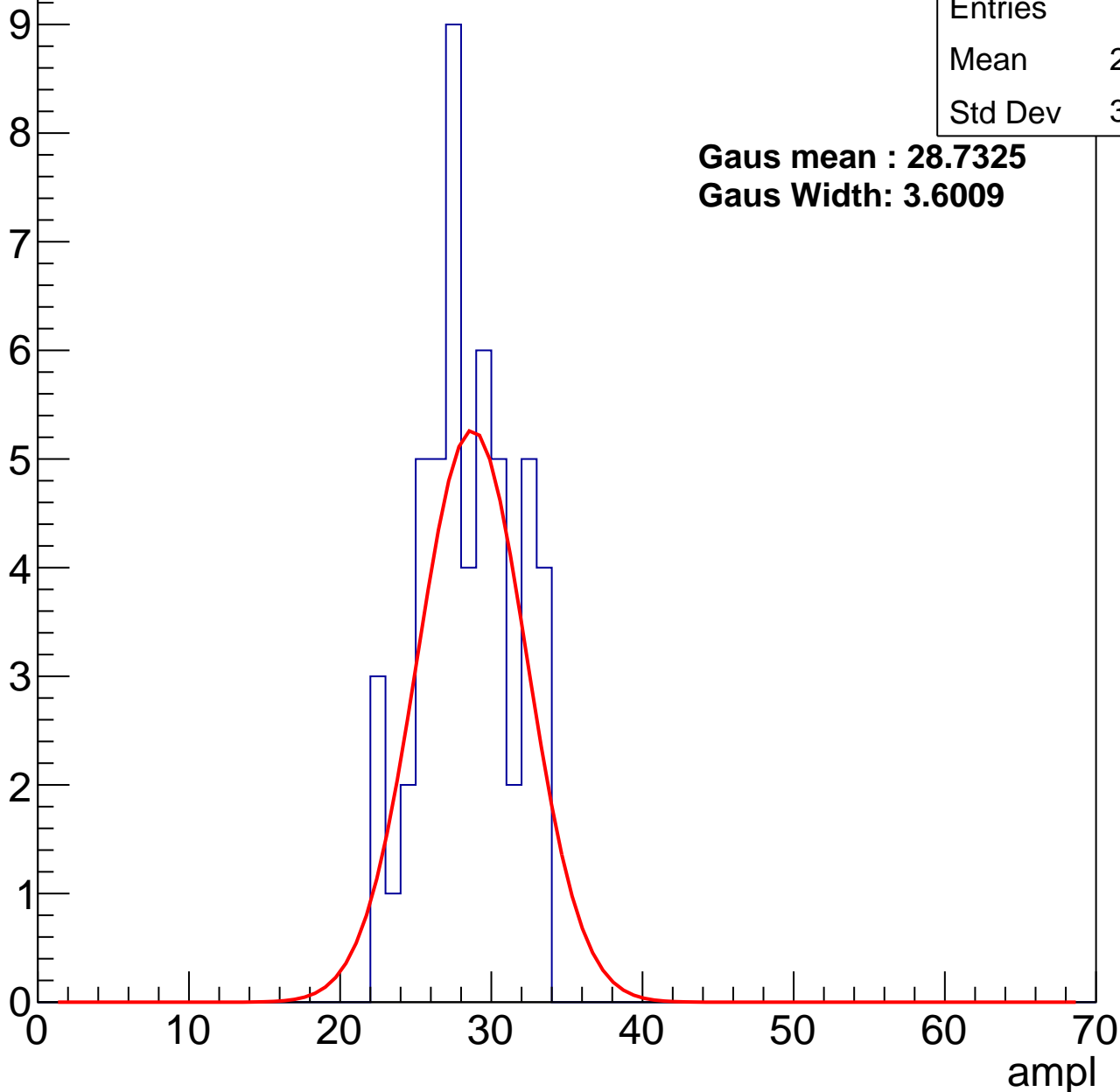
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	27.94
Std Dev	3.012

**Gaus mean : 28.7325**

**Gaus Width: 3.6009**



# B1L002S, U26-ch15, adc1

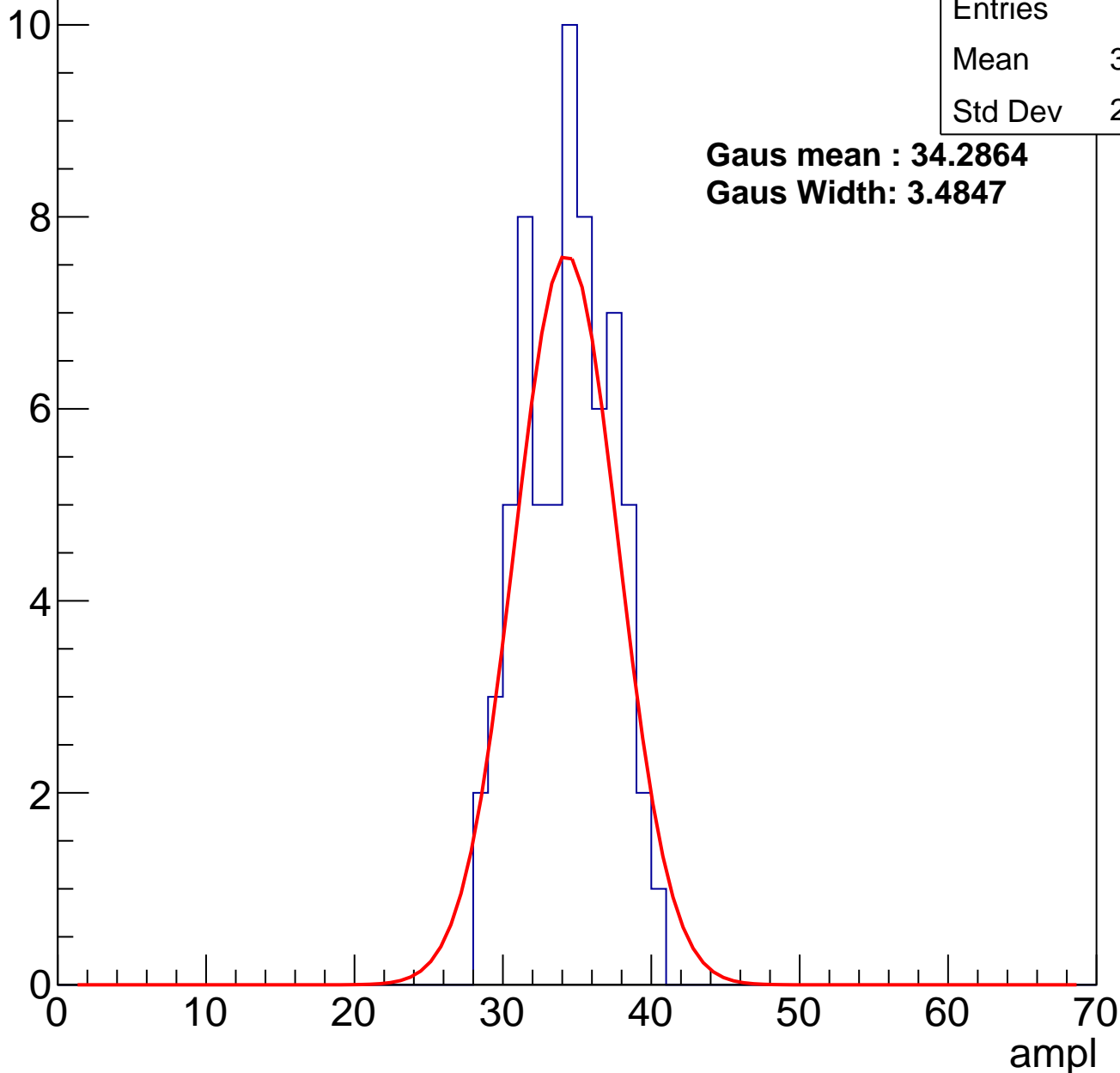
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	33.87
Std Dev	2.952

**Gaus mean : 34.2864**

**Gaus Width: 3.4847**

Entry



# B1L002S, U26-ch15, adc2

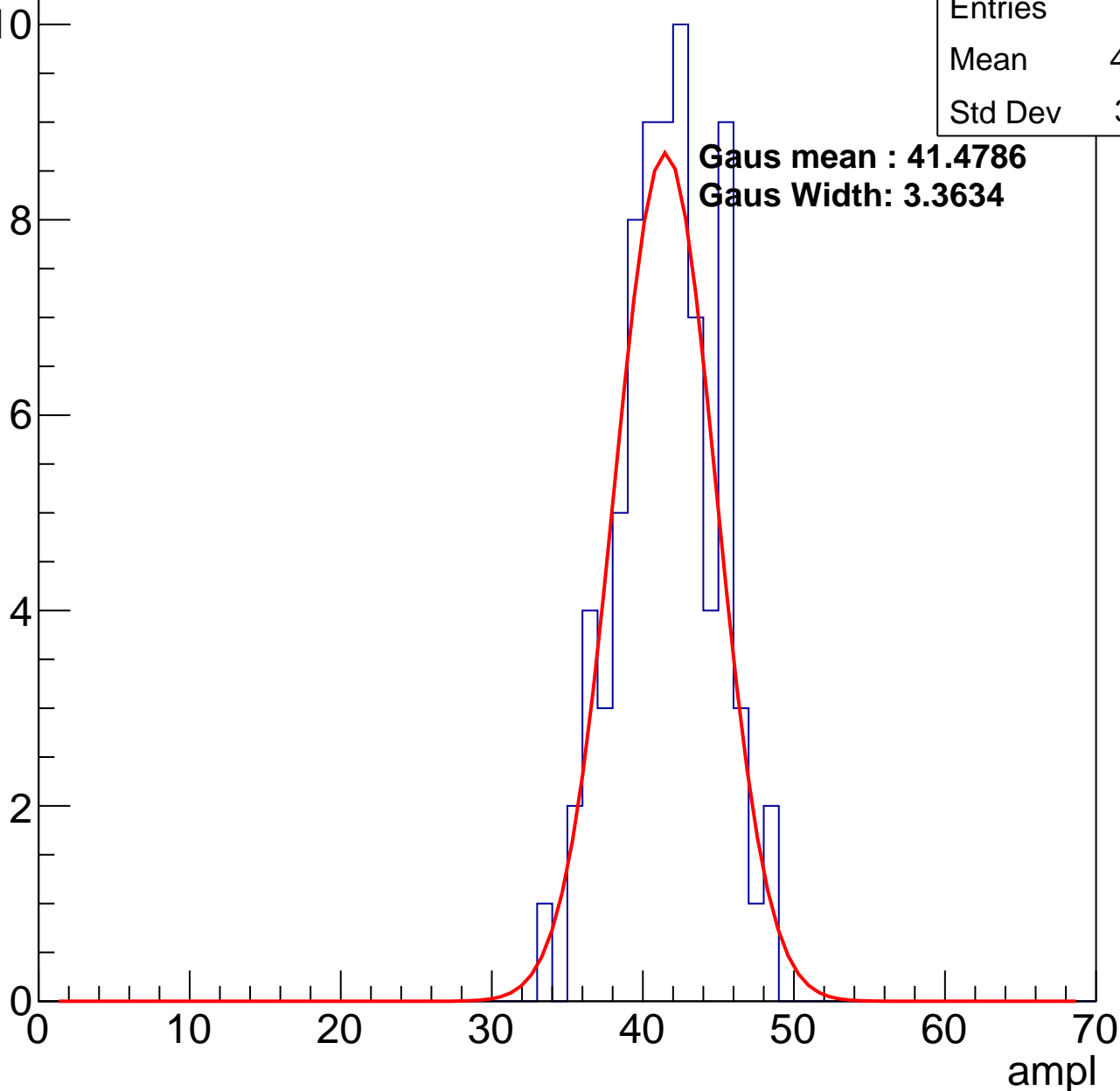
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	41.19
Std Dev	3.211

**Gaus mean : 41.4786**

**Gaus Width: 3.3634**

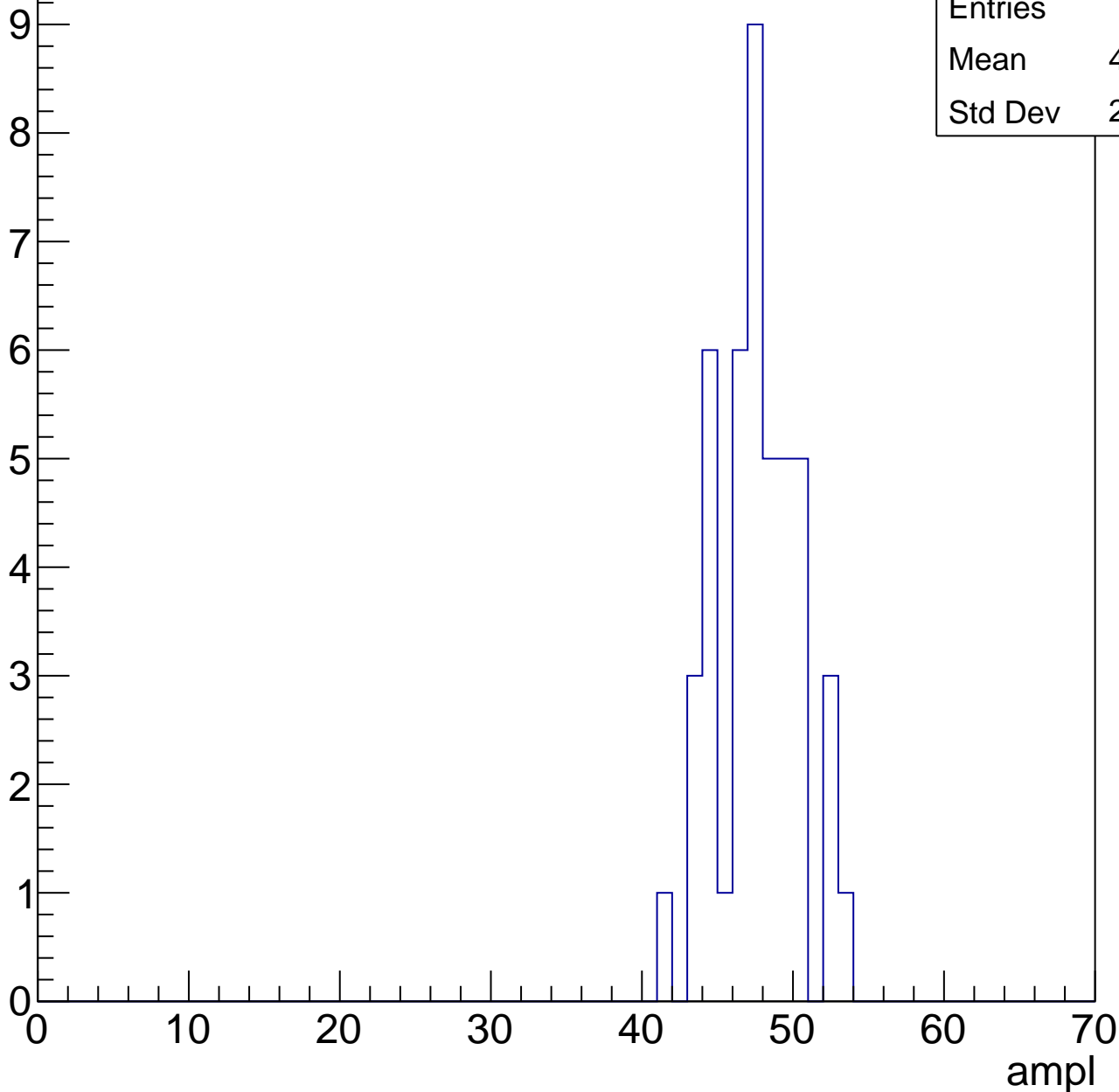


# B1L002S, U26-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	47.16
Std Dev	2.699

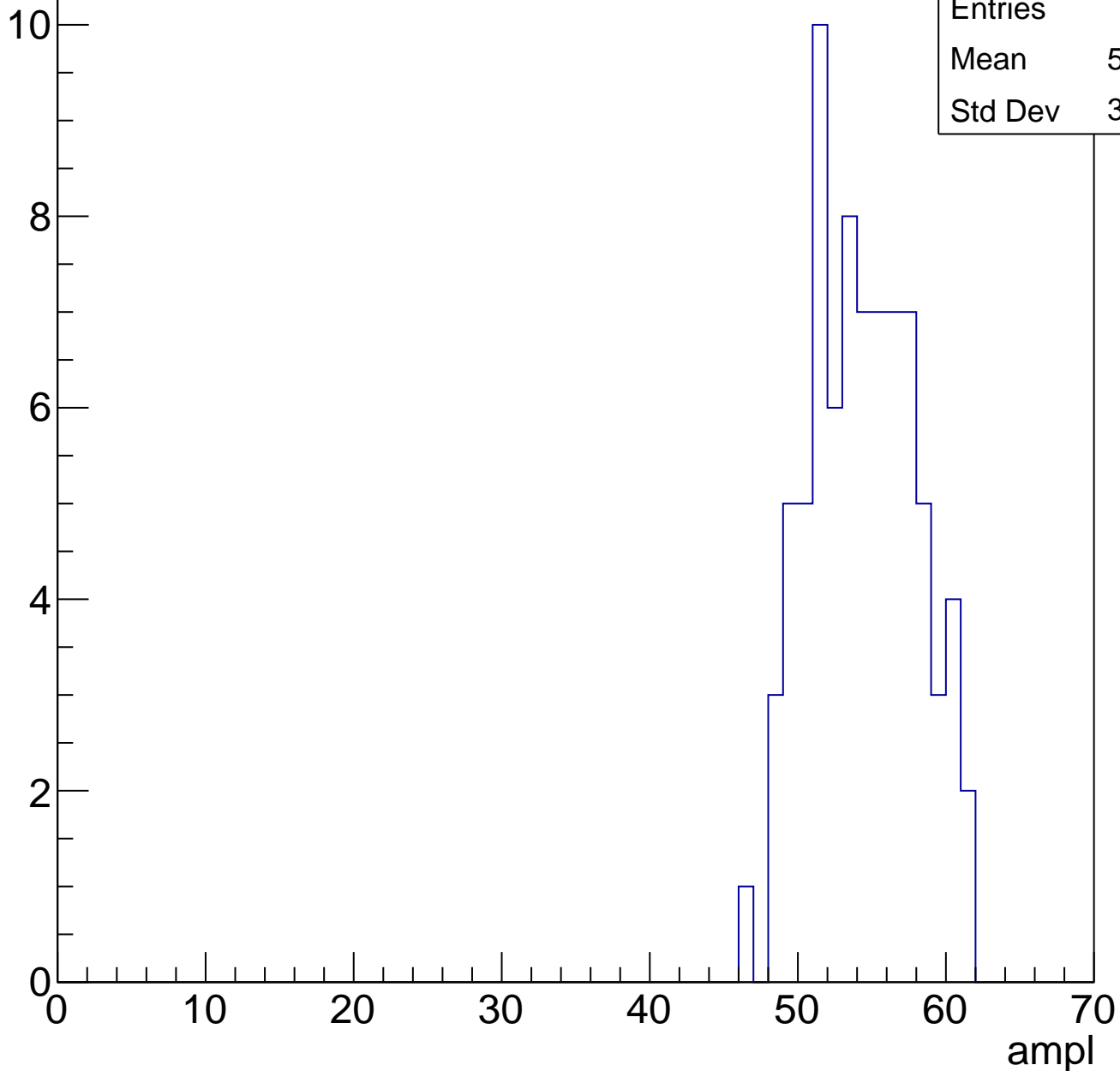


# B1L002S, U26-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	80
Mean	53.92
Std Dev	3.524

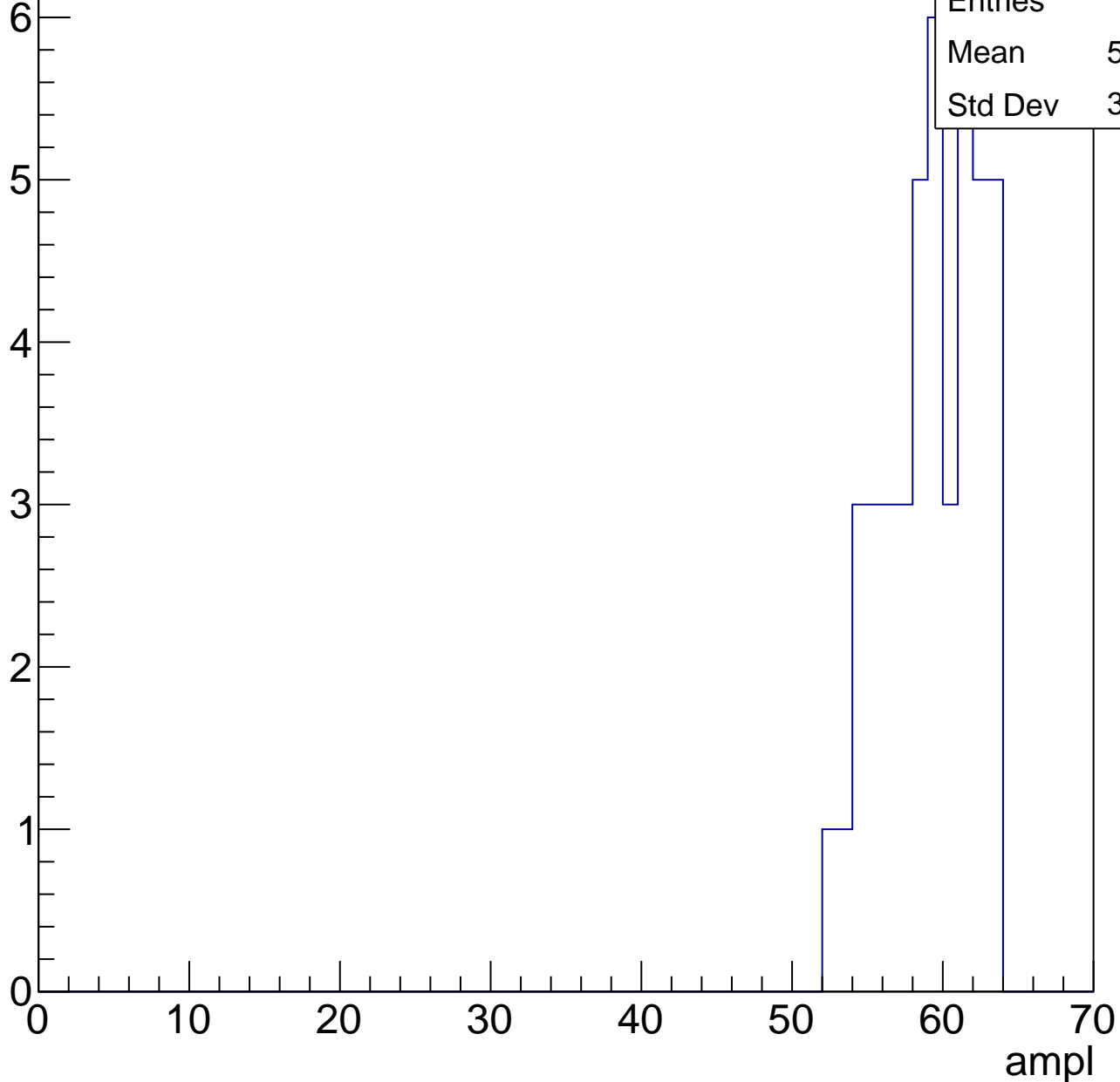
Entry



# B1L002S, U26-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

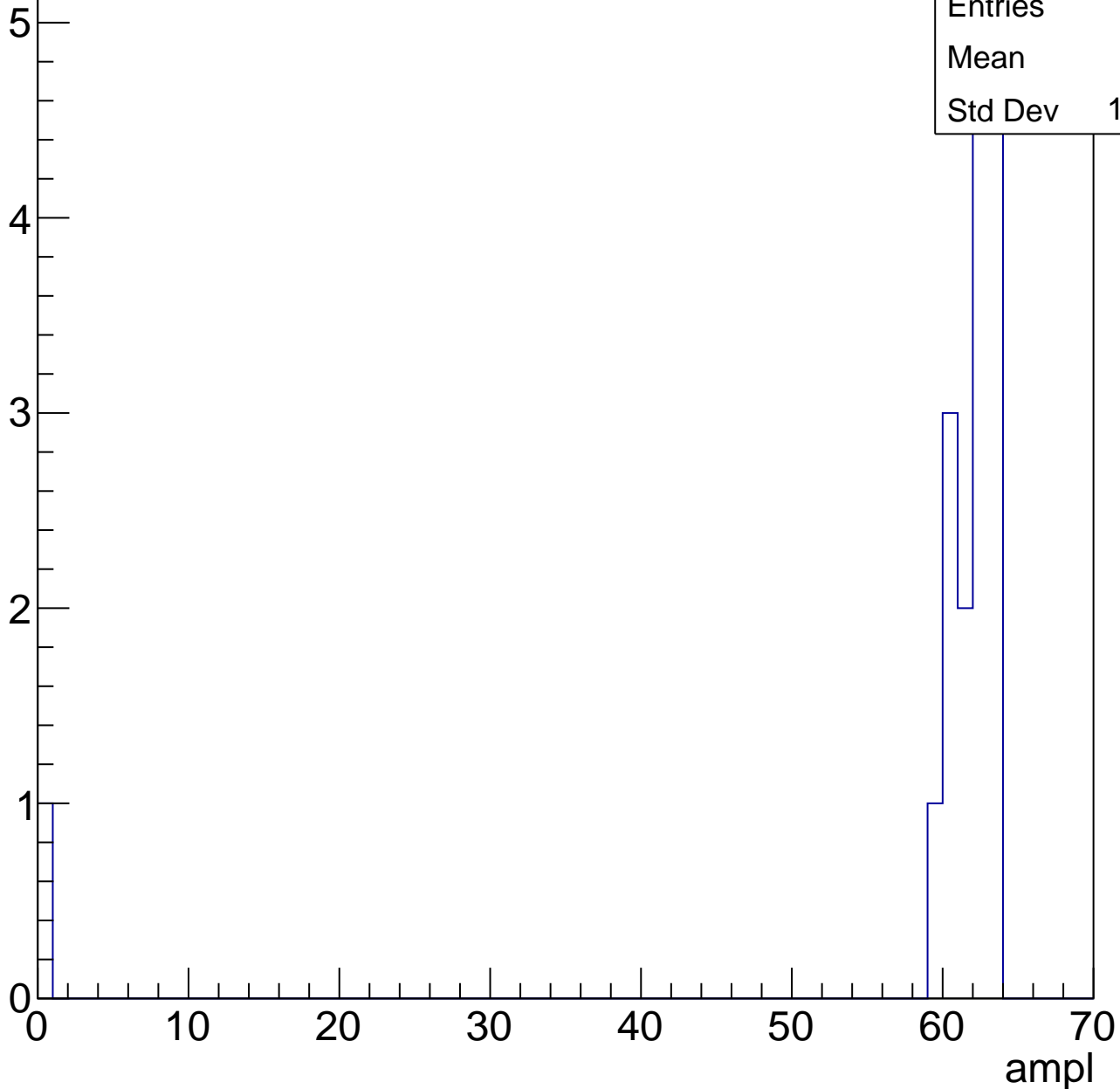


# B1L002S, U26-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58
Std Dev	14.55





# B1L002S, U26-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	20
Std Dev	0

# B1L002S, U26-ch16, adc0

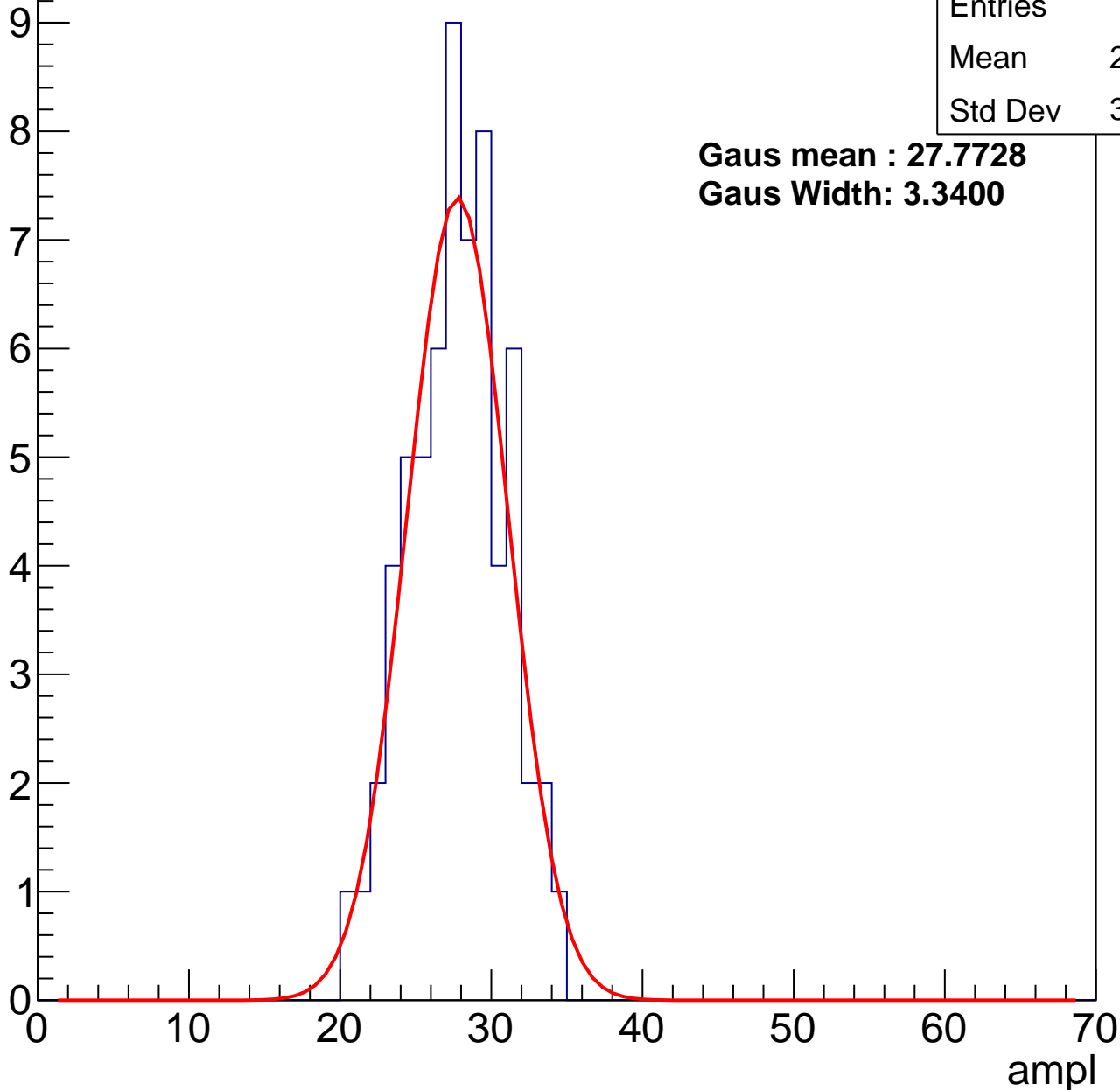
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.29
Std Dev	3.104

**Gaus mean : 27.7728**

**Gaus Width: 3.3400**



# B1L002S, U26-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	34
Std Dev	3.023

**Gaus mean : 34.2547**

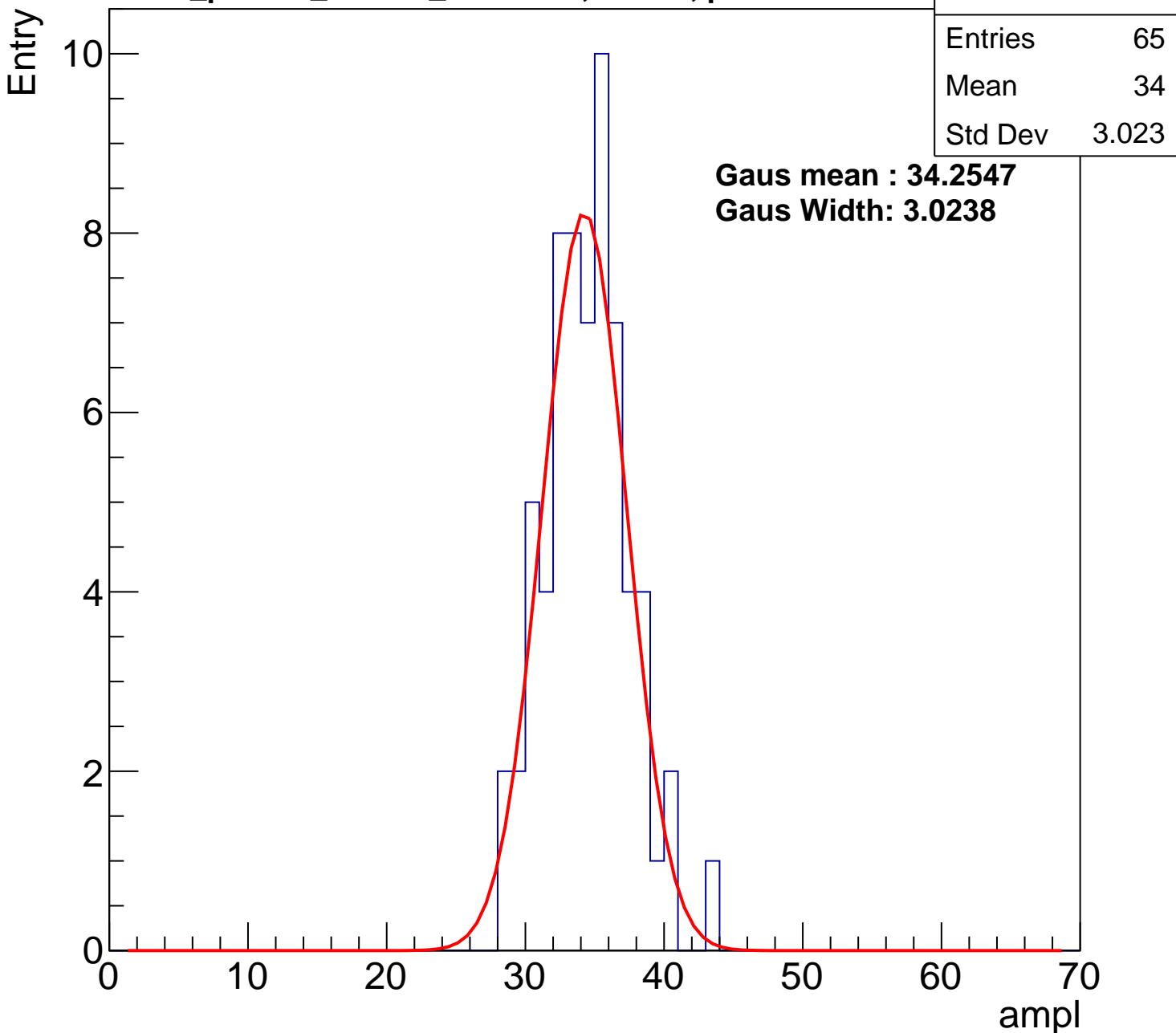
**Gaus Width: 3.0238**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch16, adc2

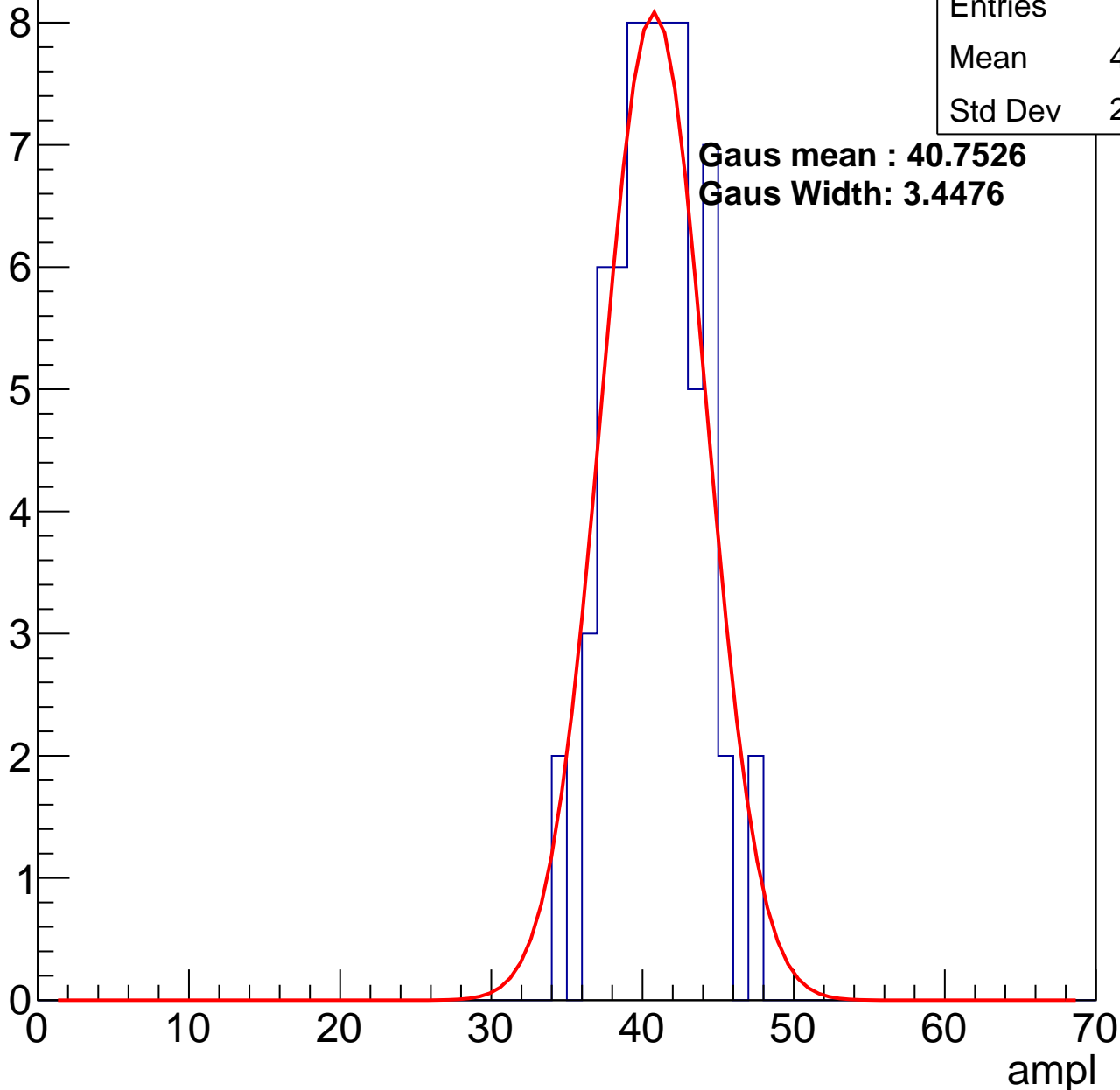
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.45
Std Dev	2.877

**Gaus mean : 40.7526**

**Gaus Width: 3.4476**

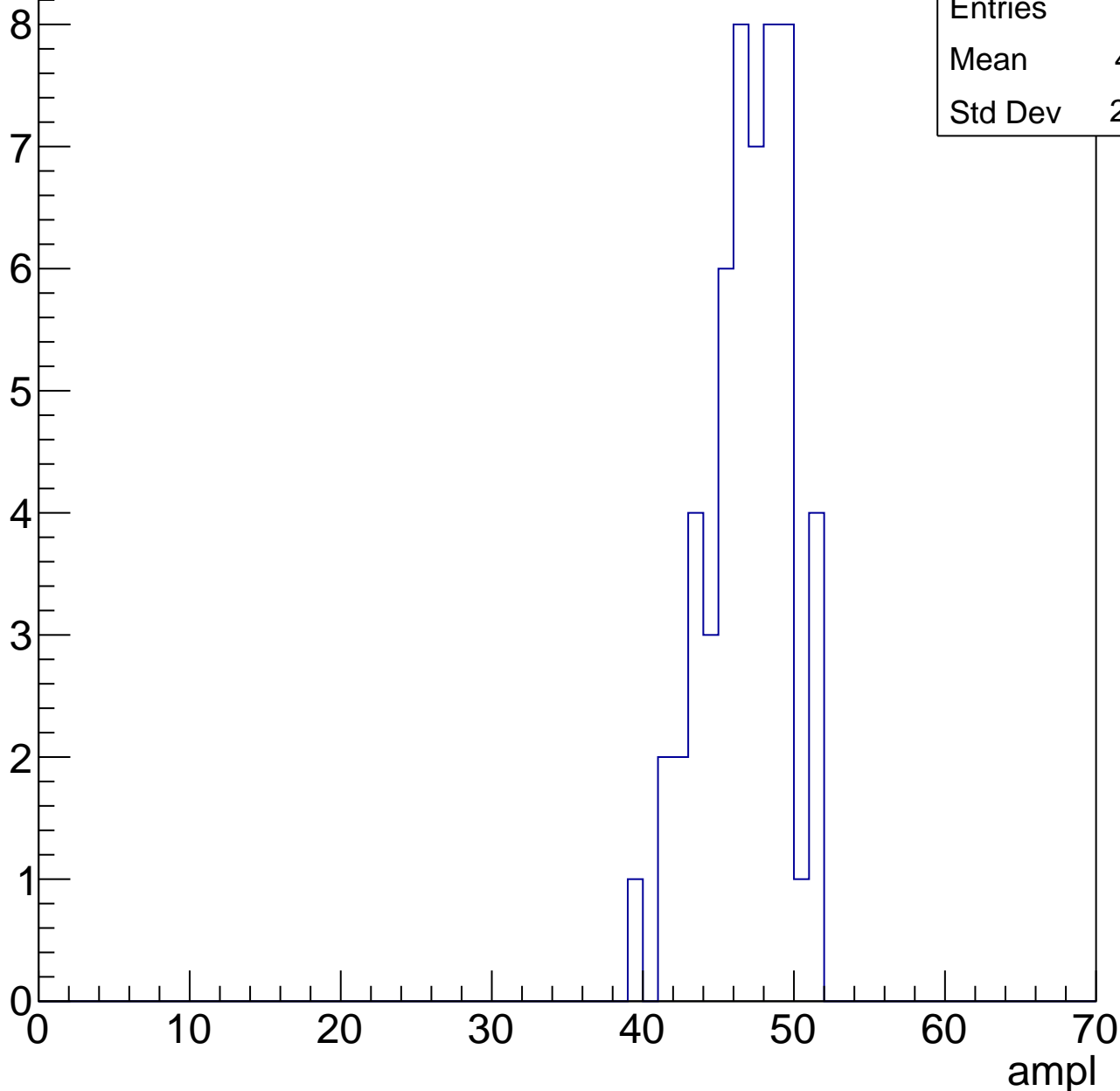


# B1L002S, U26-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	46.41
Std Dev	2.732

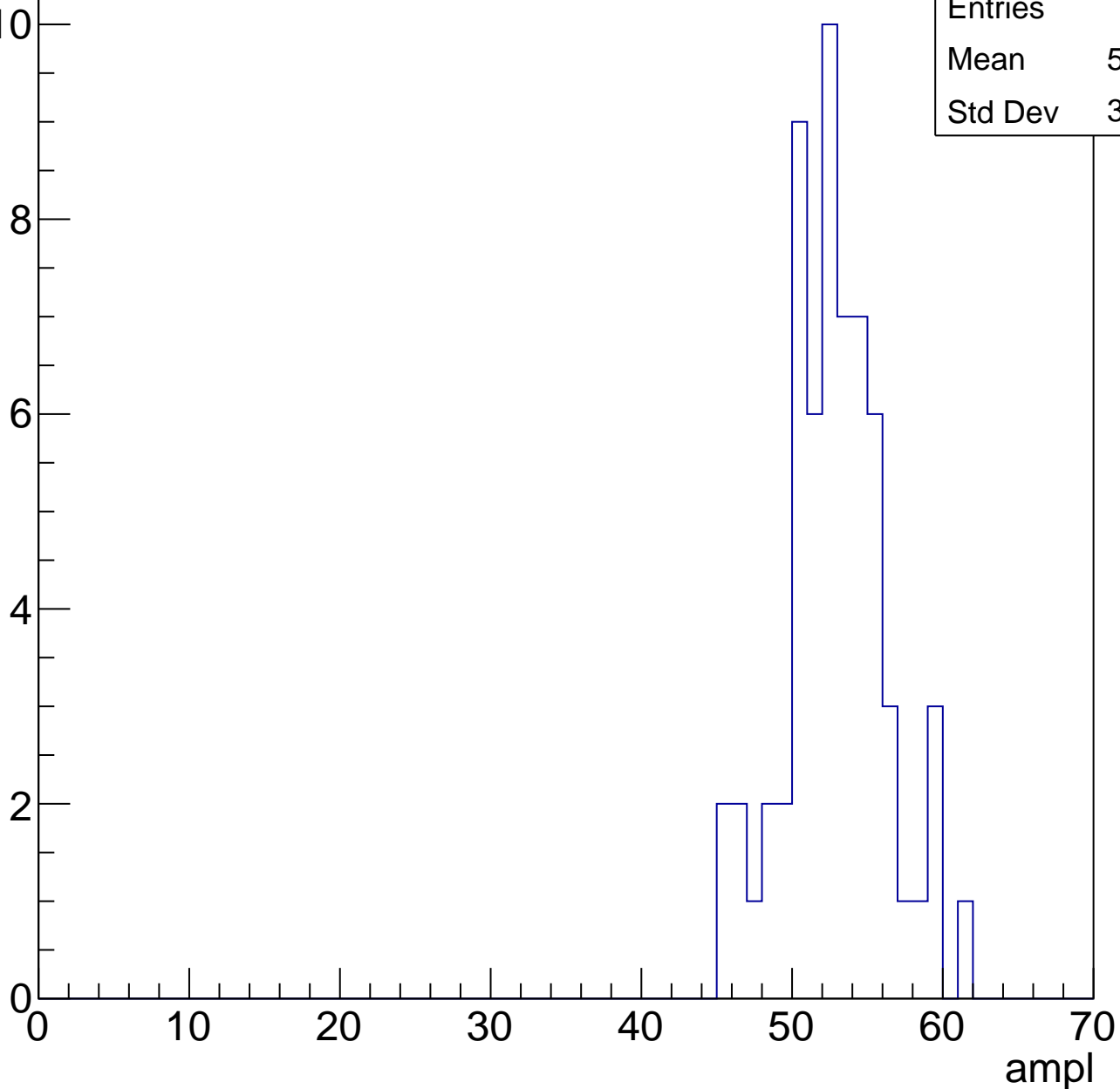


# B1L002S, U26-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

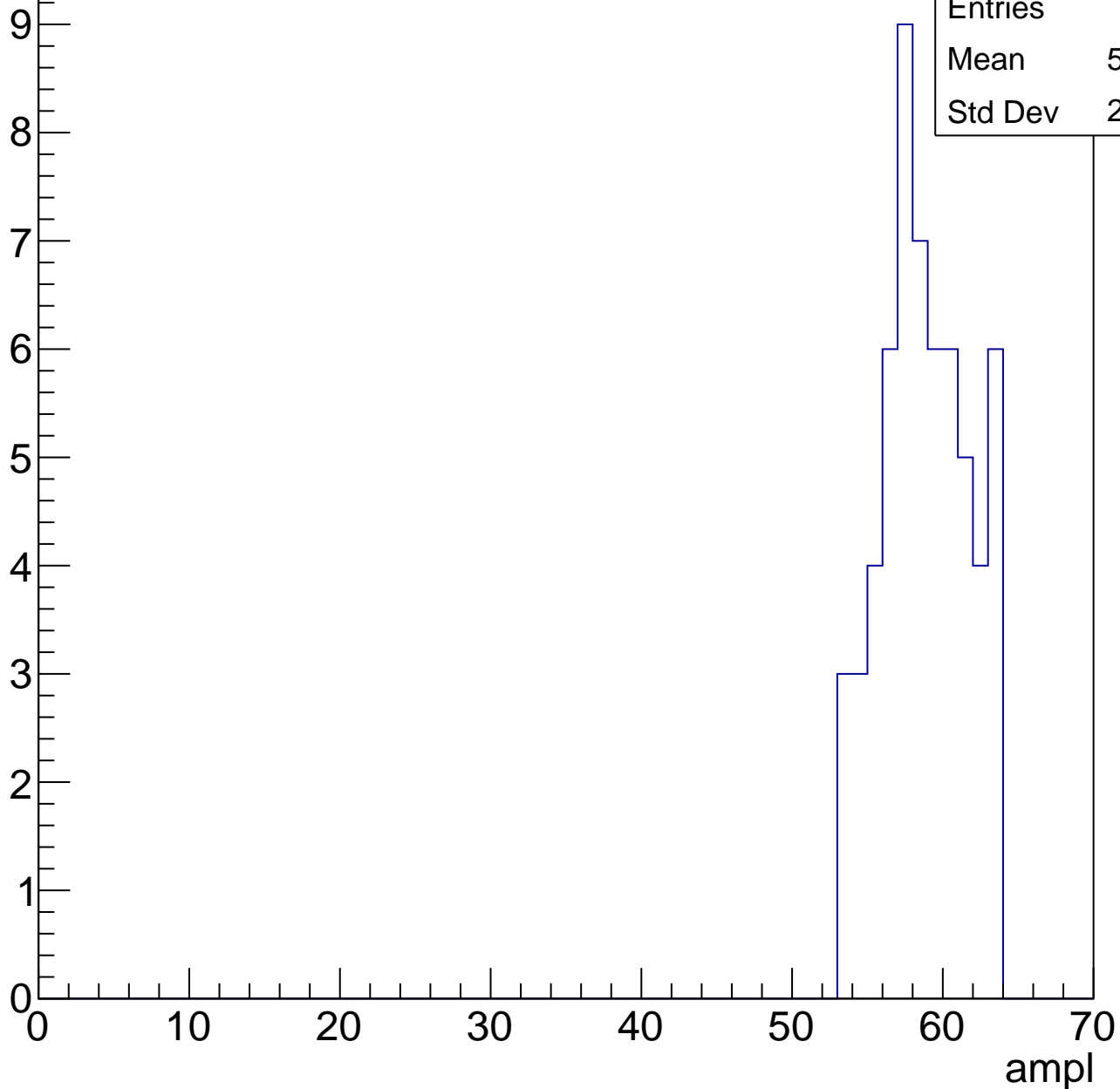
Entries	63
Mean	52.37
Std Dev	3.344



# B1L002S, U26-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

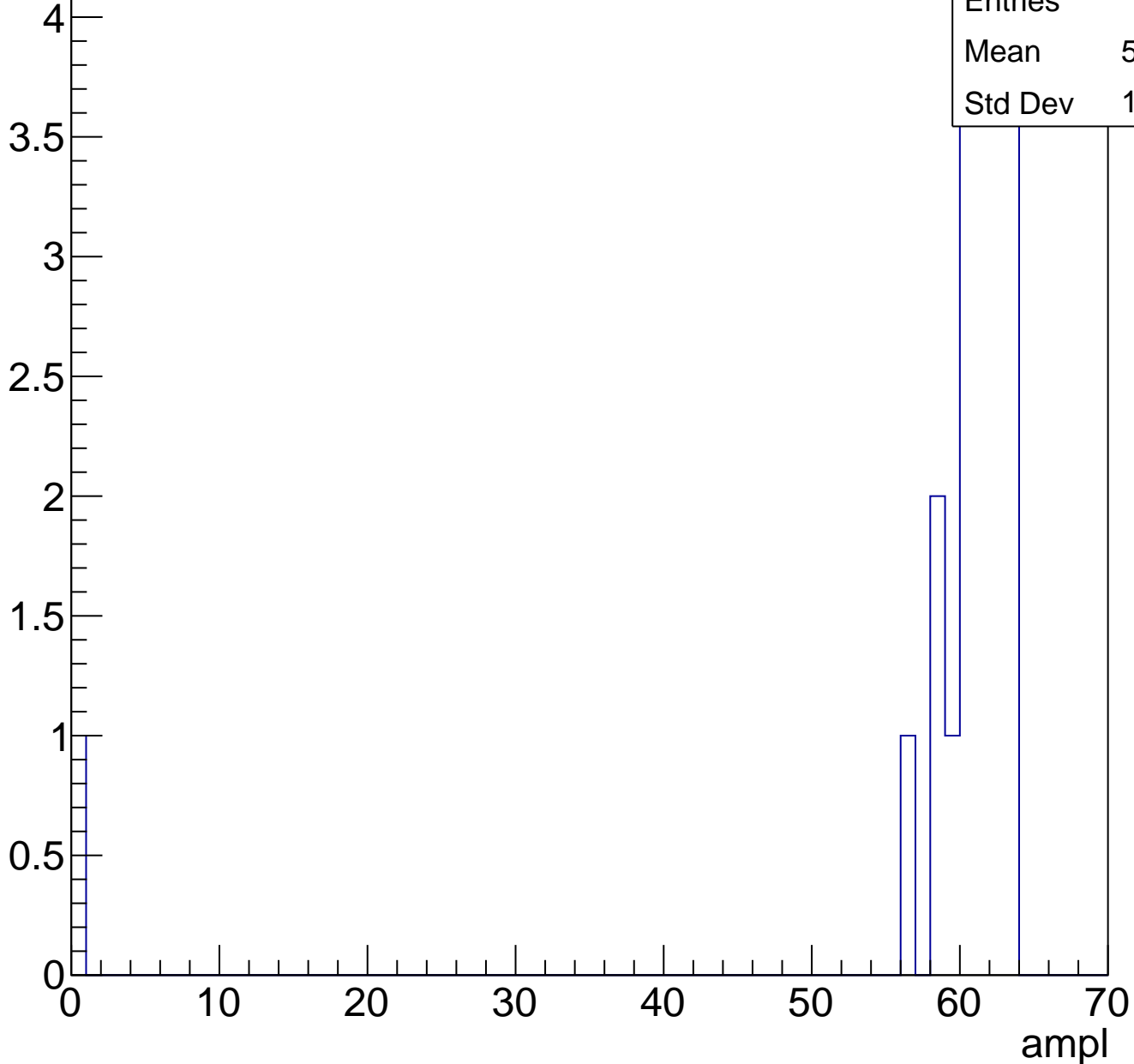


Entries	59
Mean	58.32
Std Dev	2.837

# B1L002S, U26-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch17, adc0

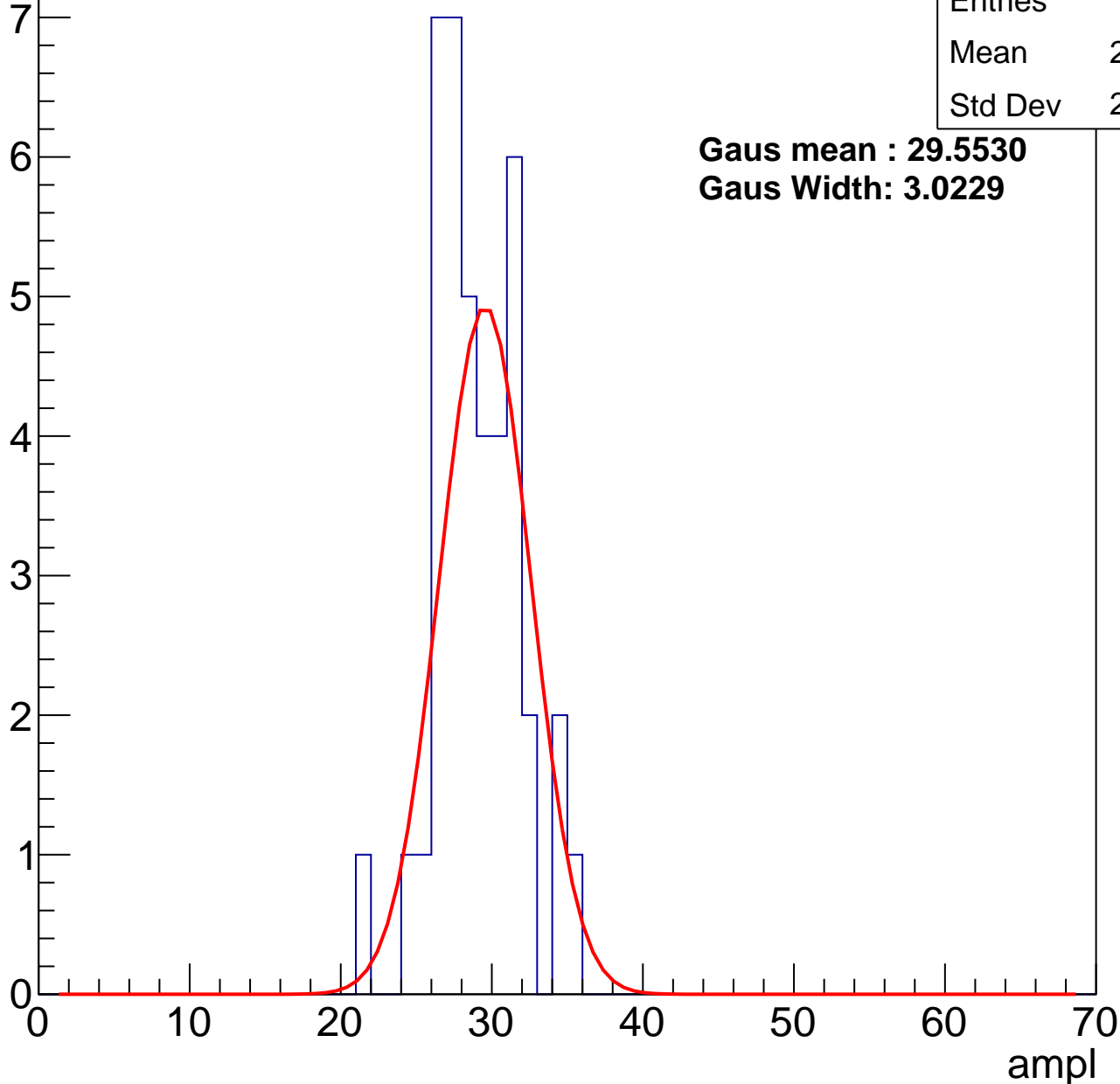
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	28.54
Std Dev	2.812

**Gaus mean : 29.5530**

**Gaus Width: 3.0229**



# B1L002S, U26-ch17, adc1

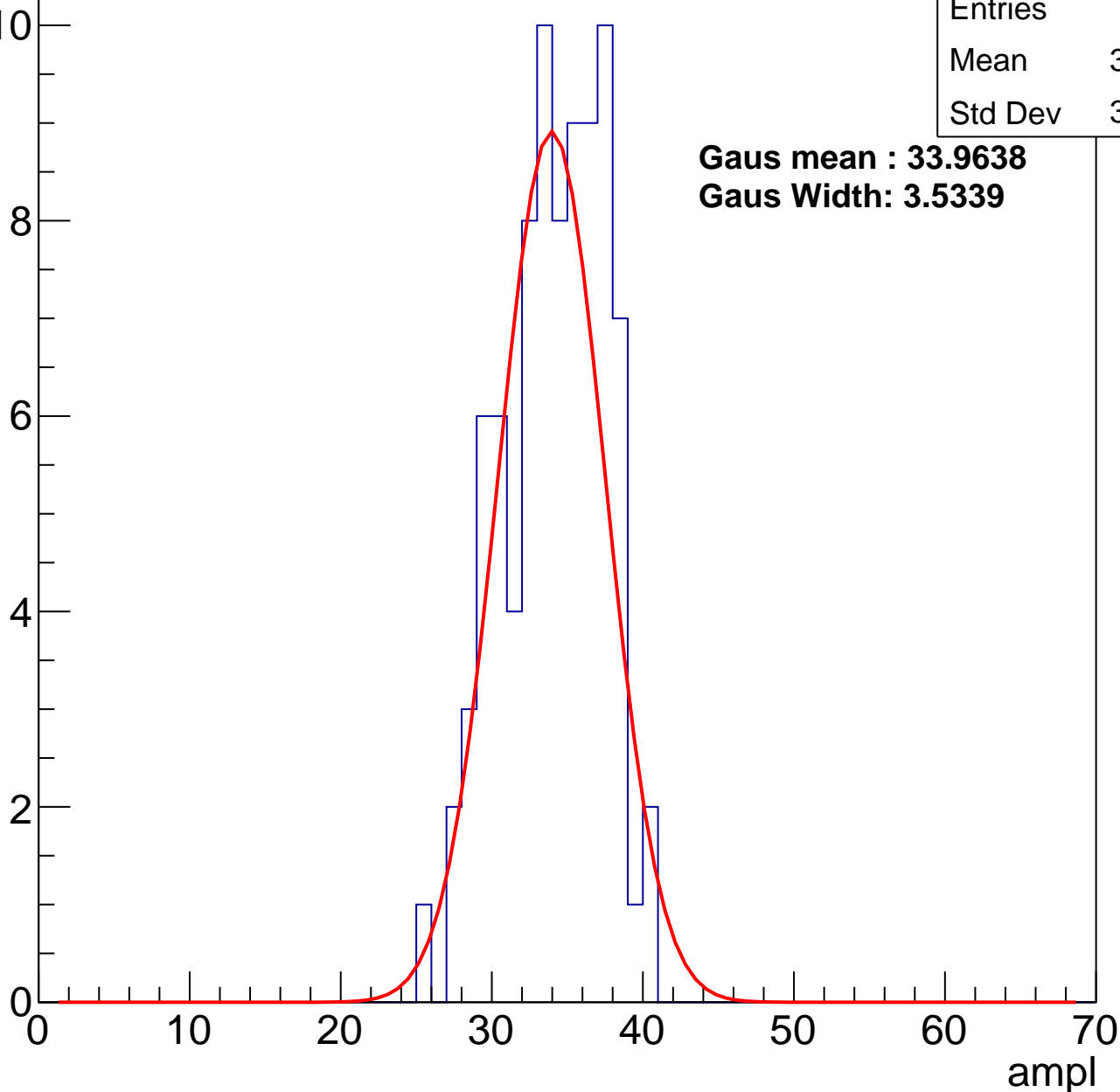
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	86
Mean	33.64
Std Dev	3.313

**Gaus mean : 33.9638**

**Gaus Width: 3.5339**



# B1L002S, U26-ch17, adc2

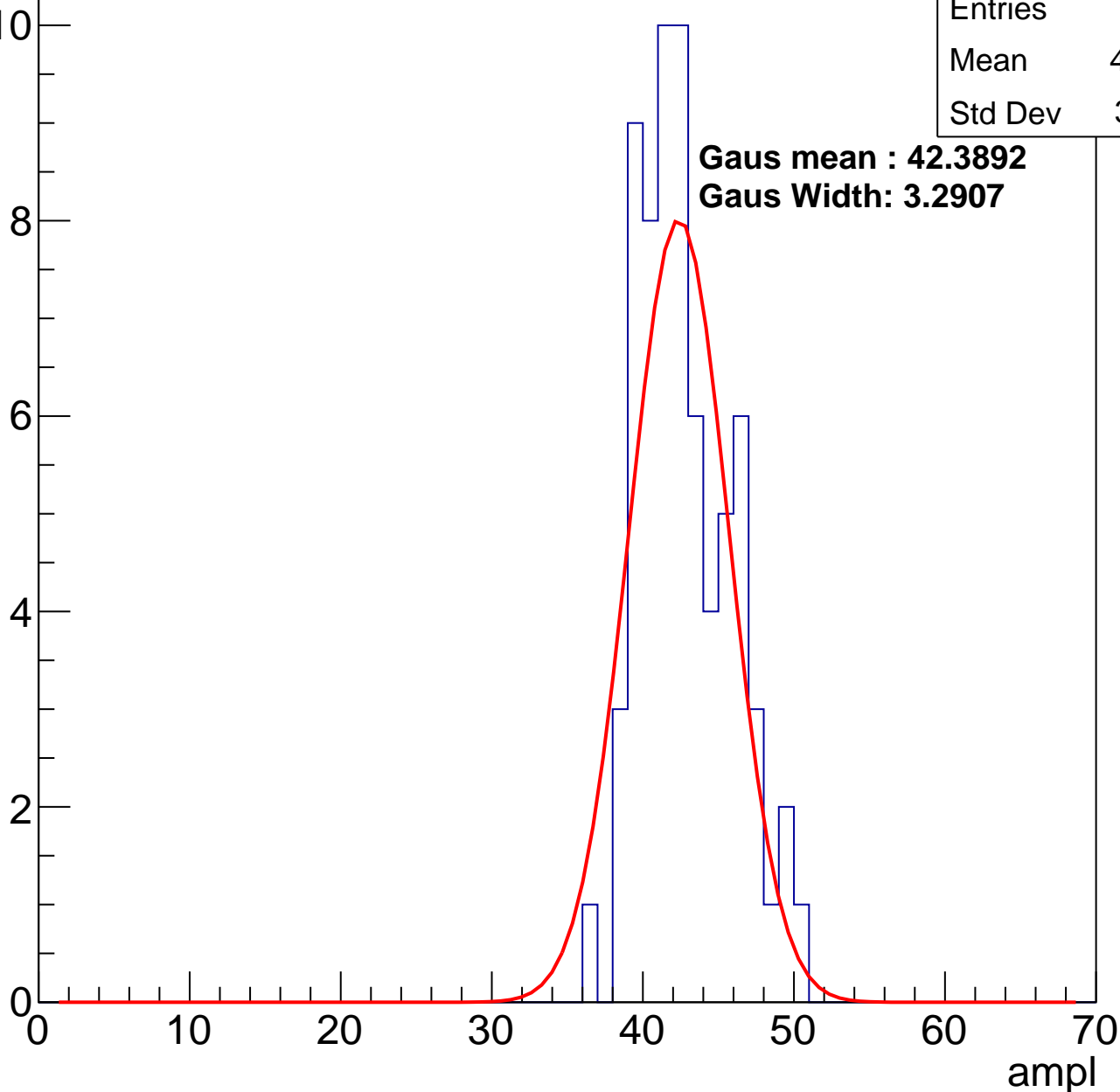
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	42.36
Std Dev	3.031

**Gaus mean : 42.3892**

**Gaus Width: 3.2907**

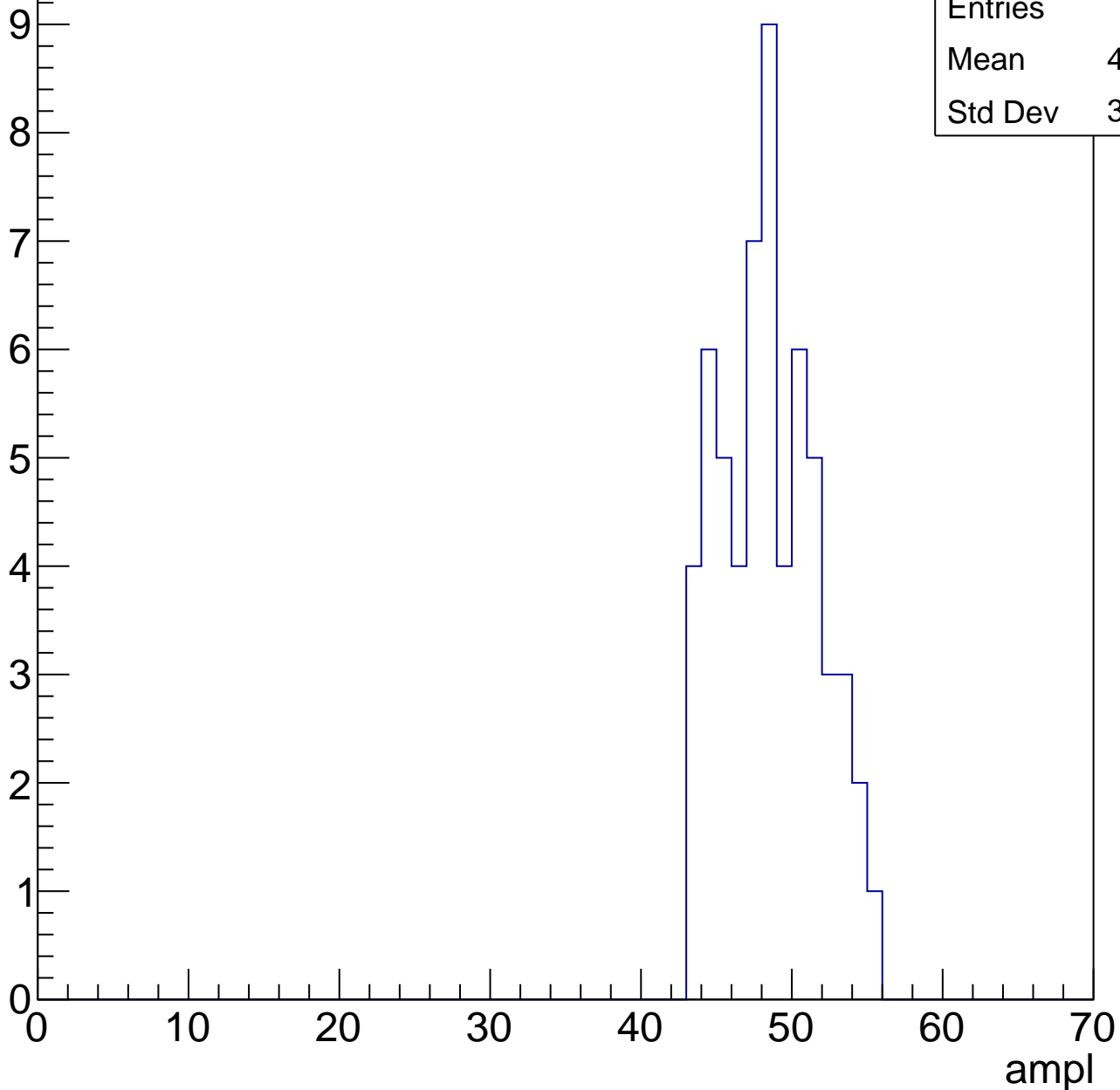


# B1L002S, U26-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	48.05
Std Dev	3.138

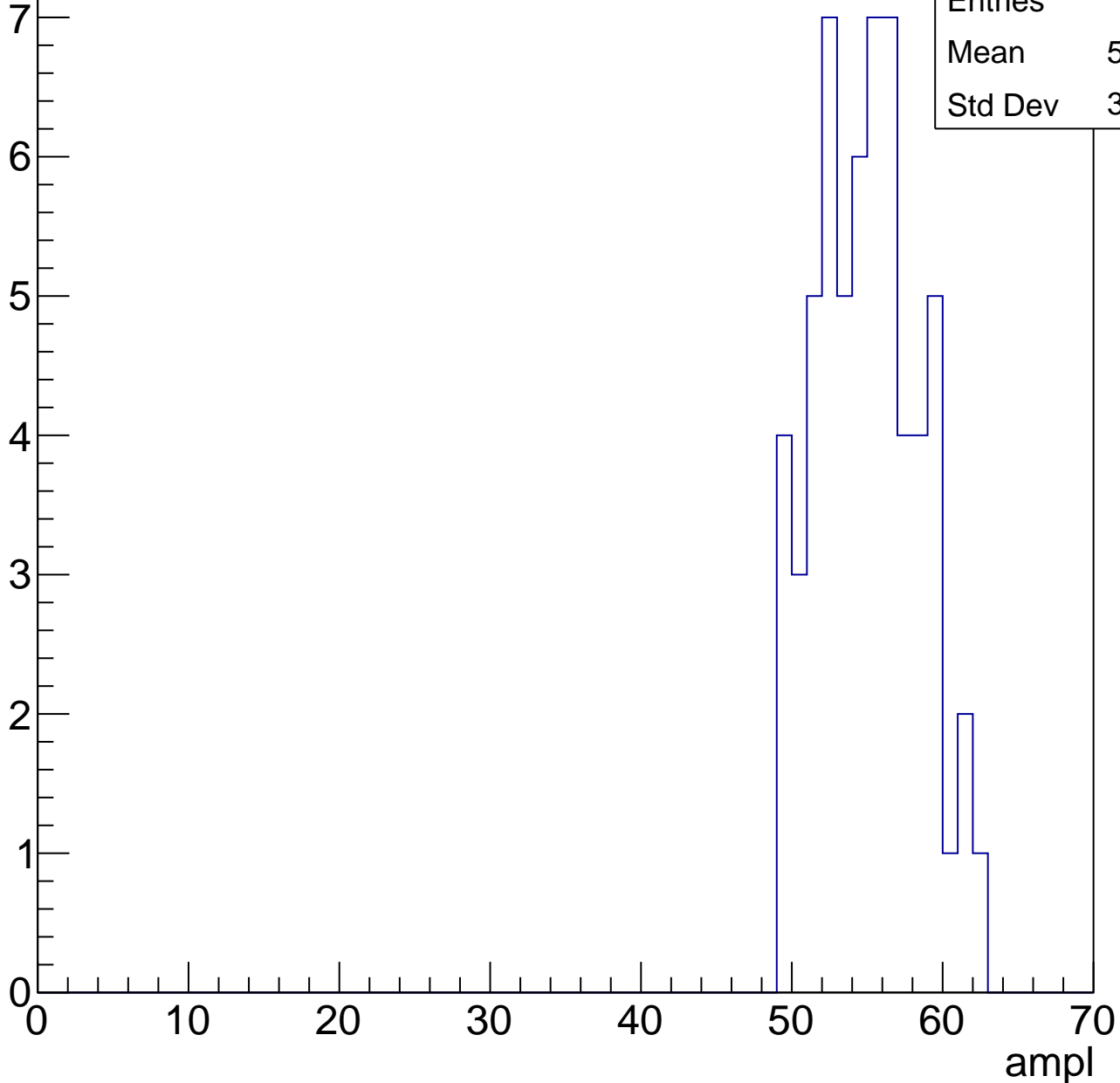


# B1L002S, U26-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	54.59
Std Dev	3.296



# B1L002S, U26-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

6

5

4

3

2

1

0

0

10

20

30

40

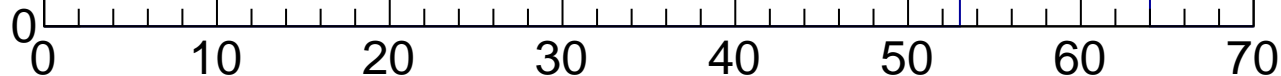
50

60

70

ampl

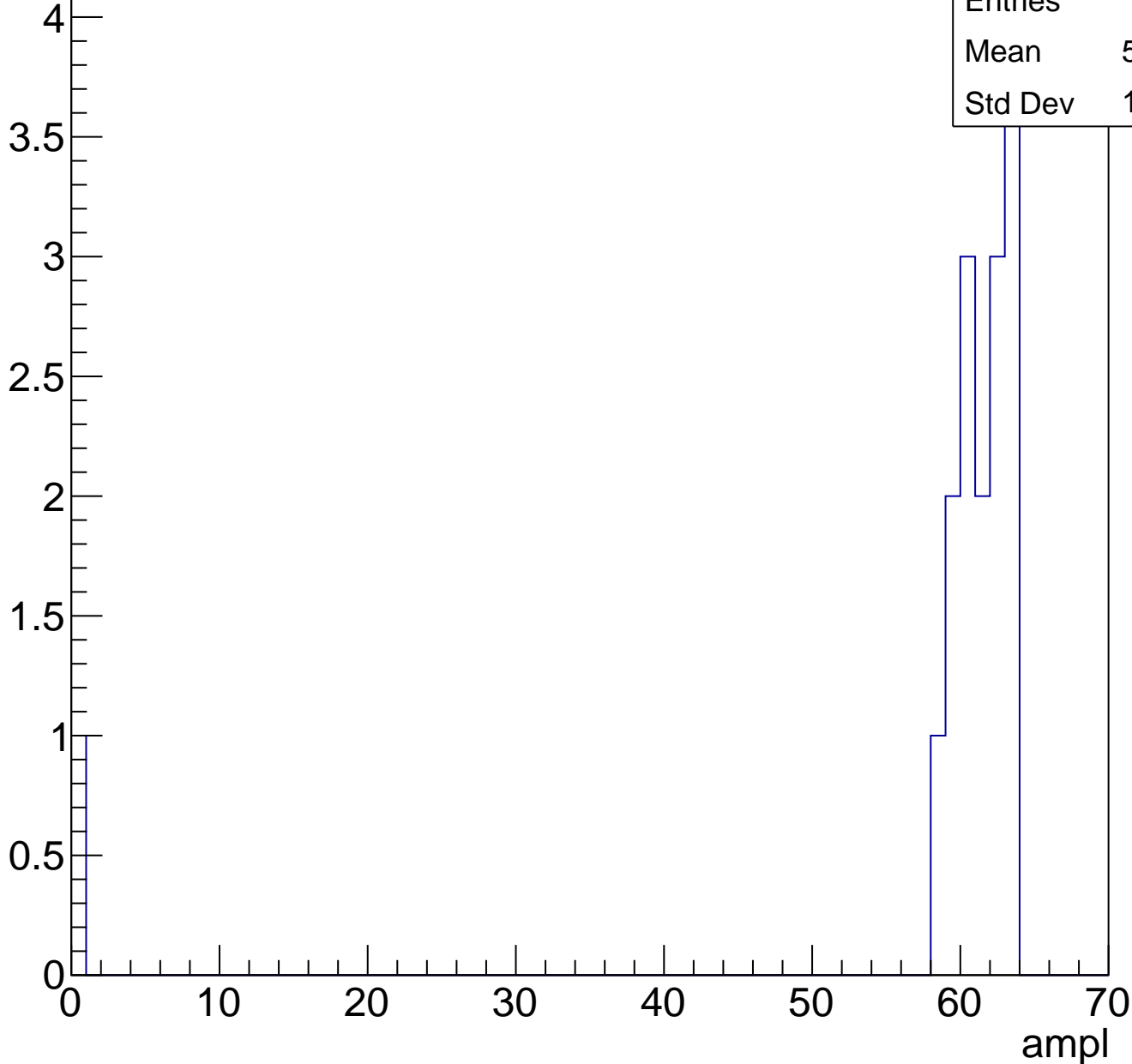
Entries	46
Mean	59
Std Dev	2.851



# B1L002S, U26-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L002S, U26-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	85
Mean	26.8
Std Dev	4.448

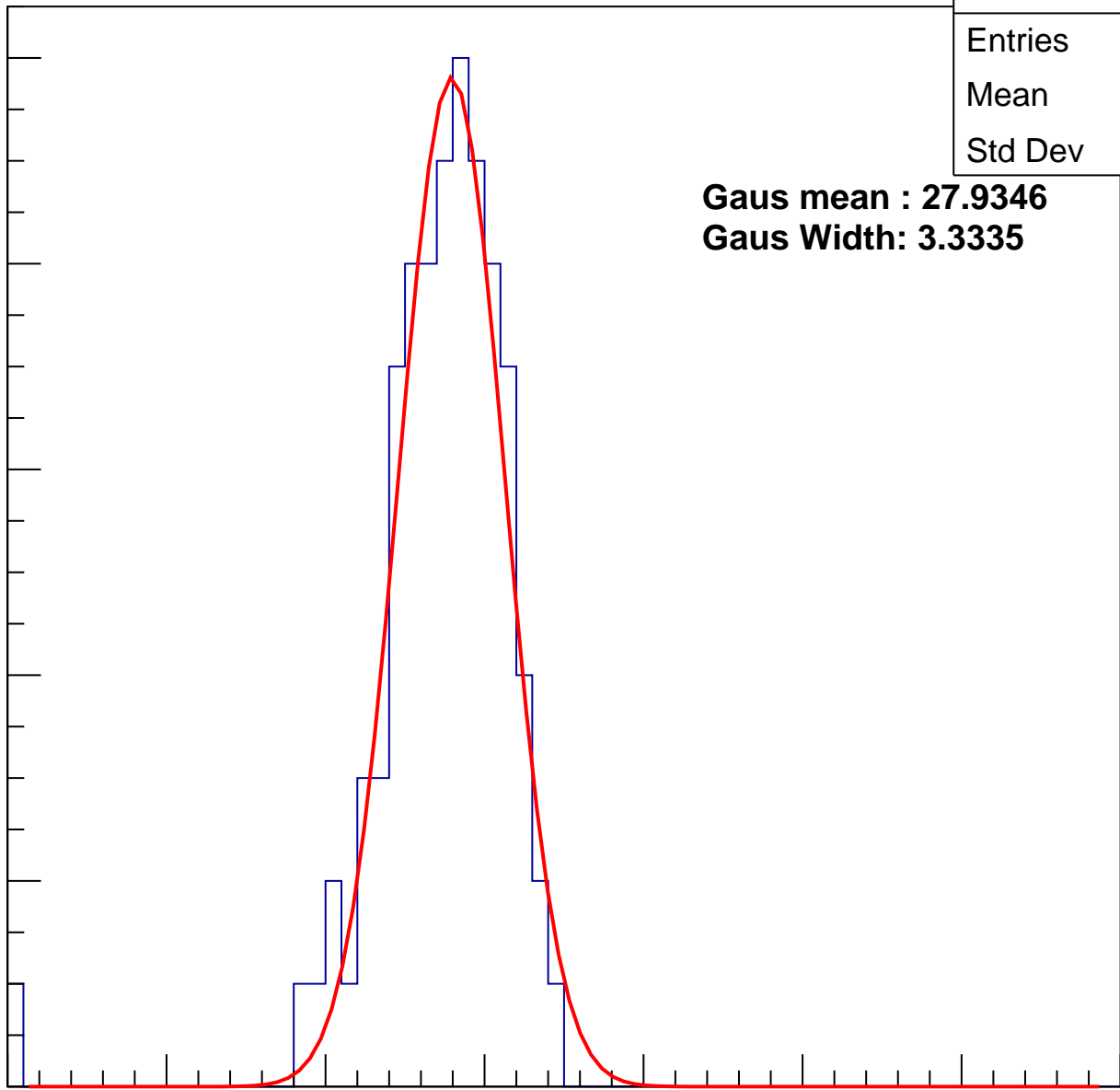
**Gaus mean : 27.9346**

**Gaus Width: 3.3335**

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch18, adc1

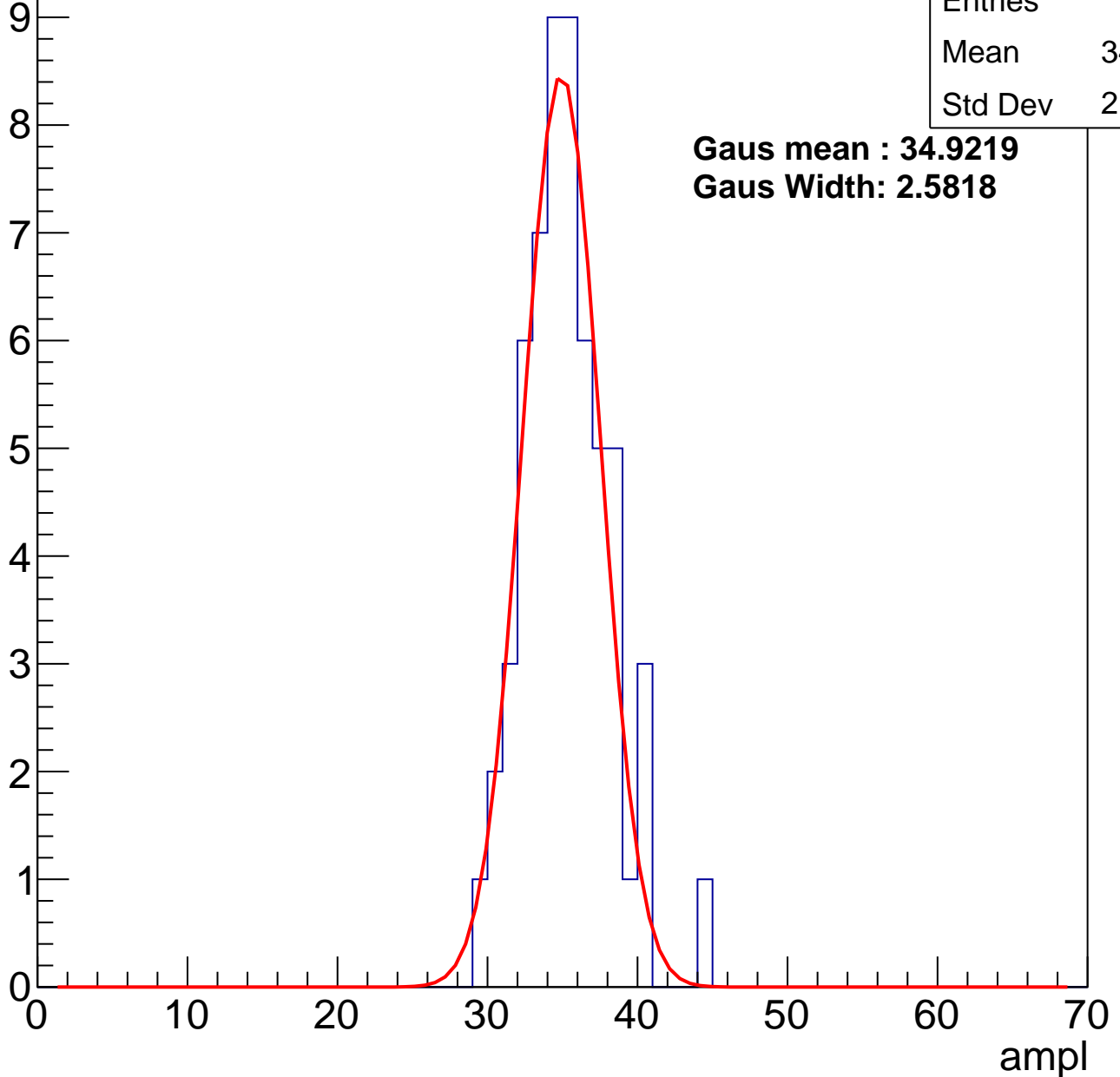
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	34.83
Std Dev	2.835

**Gaus mean : 34.9219**

**Gaus Width: 2.5818**



# B1L002S, U26-ch18, adc2

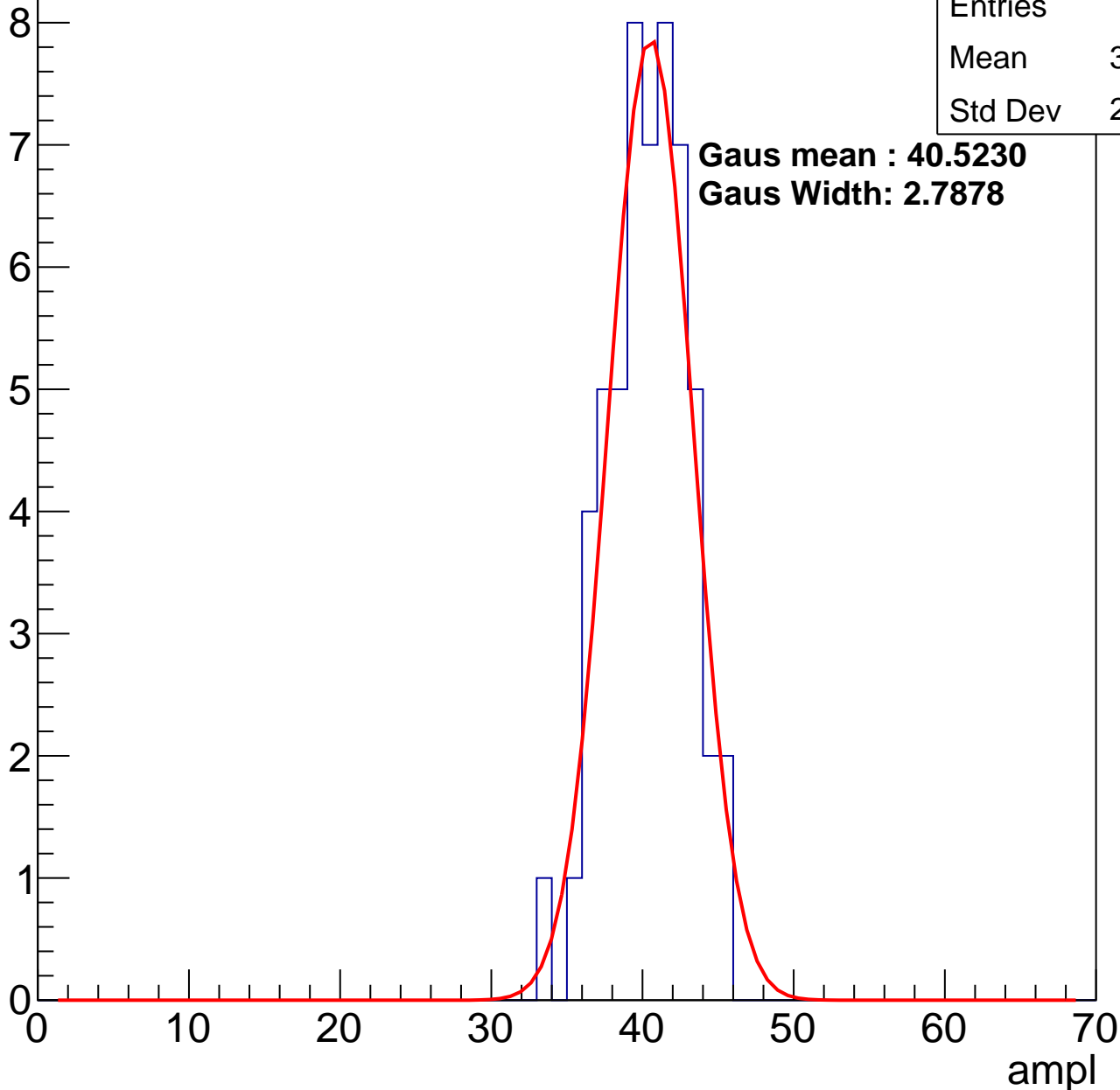
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	39.89
Std Dev	2.605

**Gaus mean : 40.5230**

**Gaus Width: 2.7878**

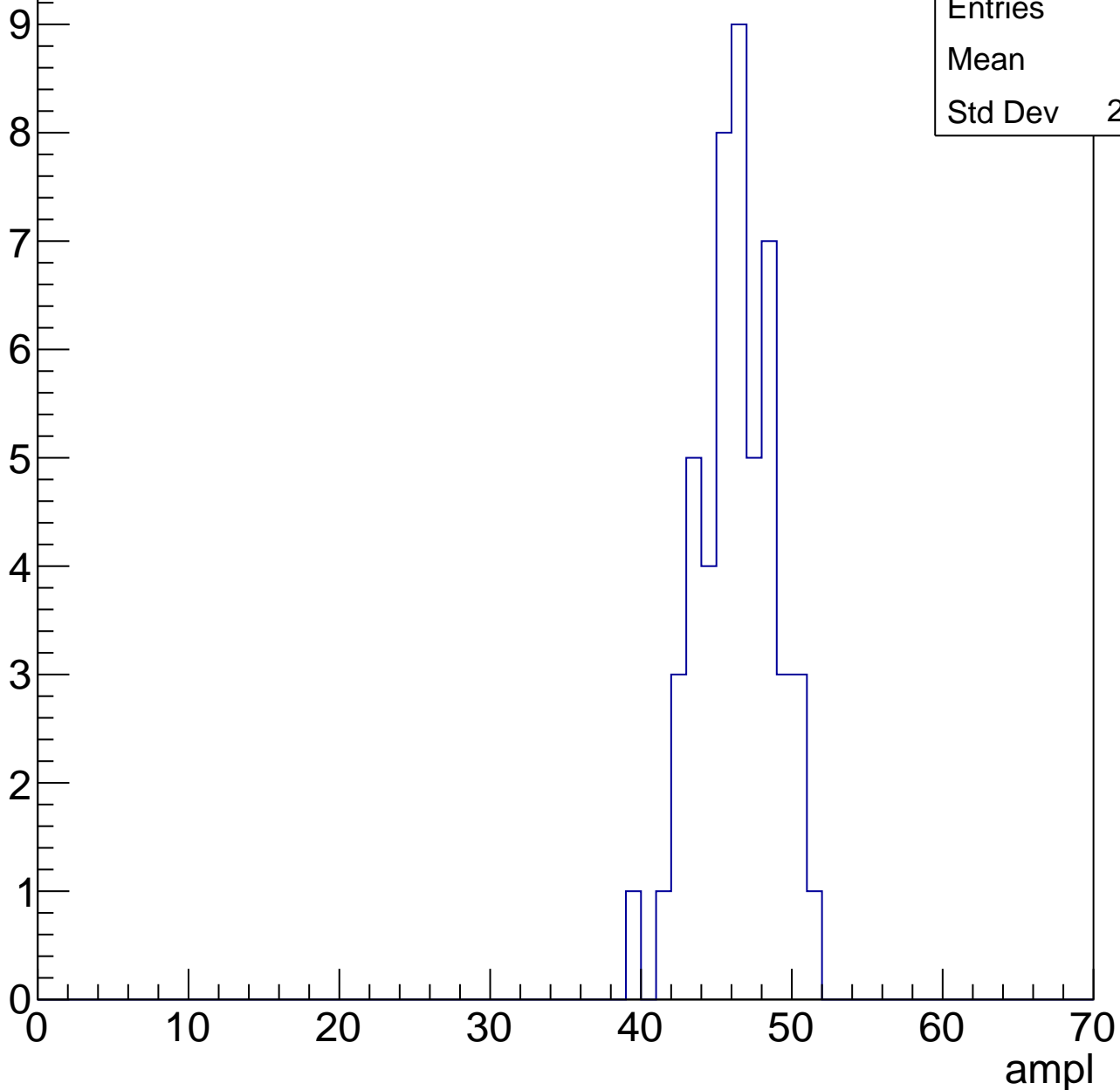


# B1L002S, U26-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	45.8
Std Dev	2.538

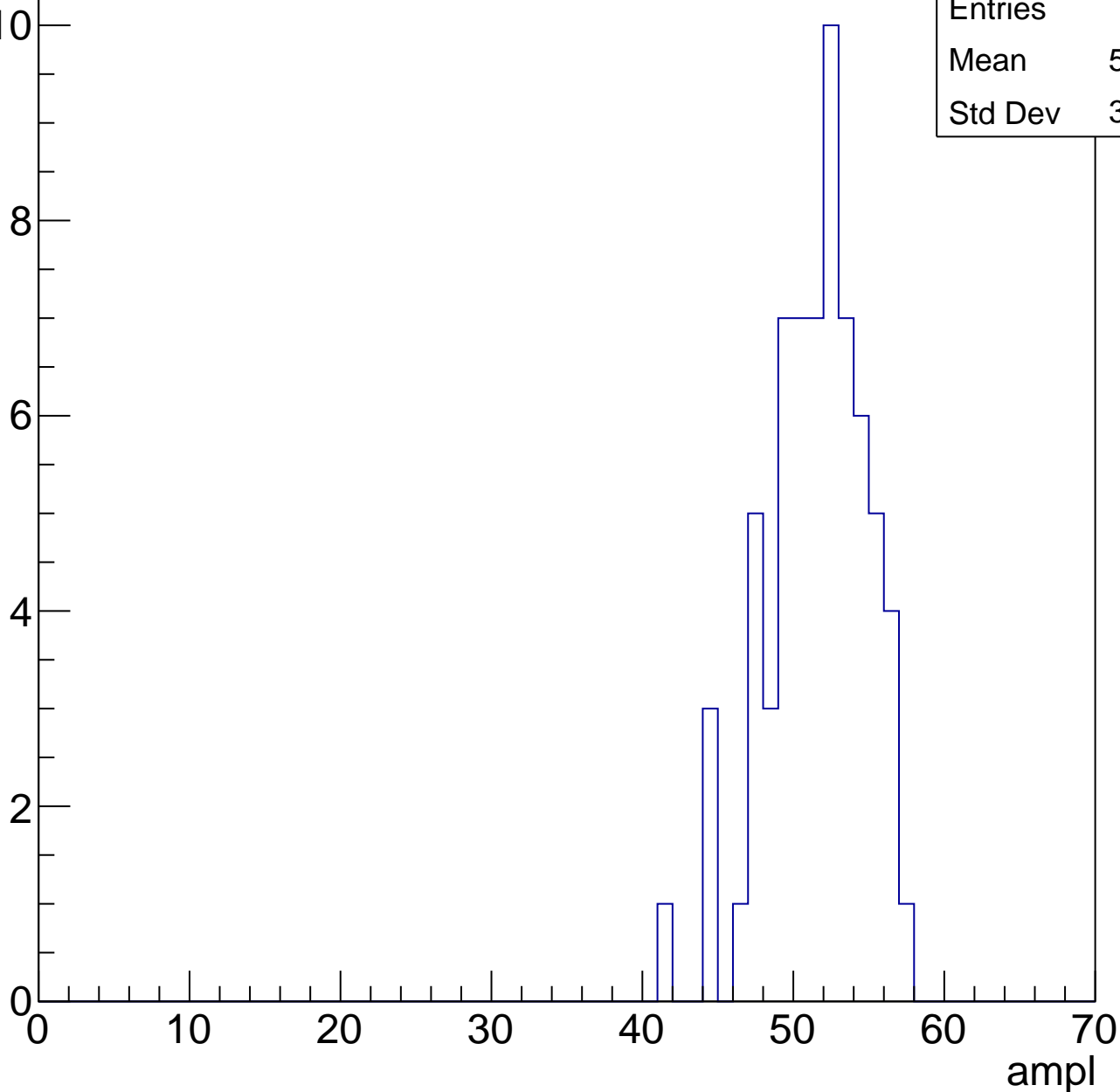


# B1L002S, U26-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	51.03
Std Dev	3.283

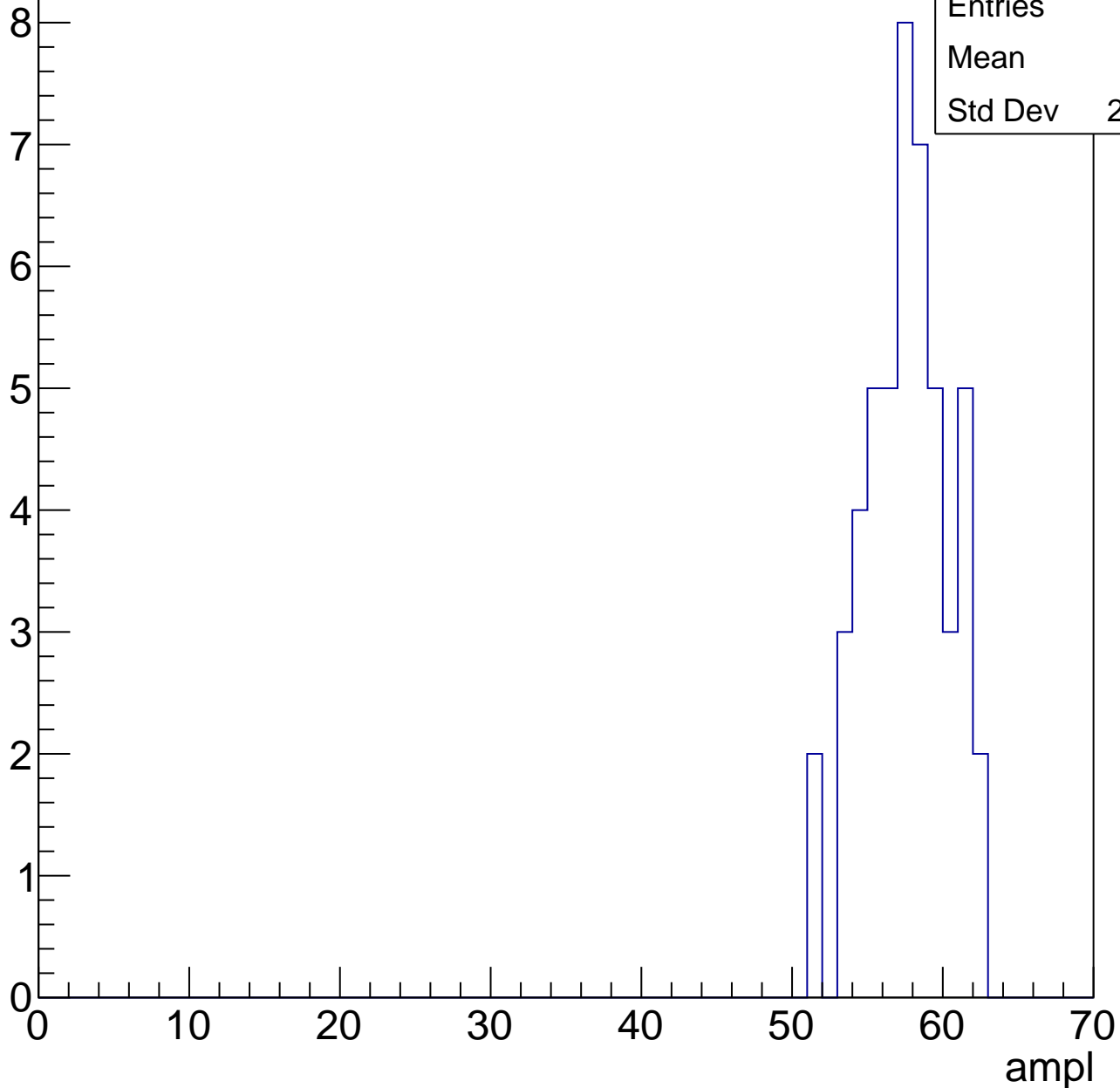


# B1L002S, U26-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	57.1
Std Dev	2.727



# B1L002S, U26-ch18, adc6

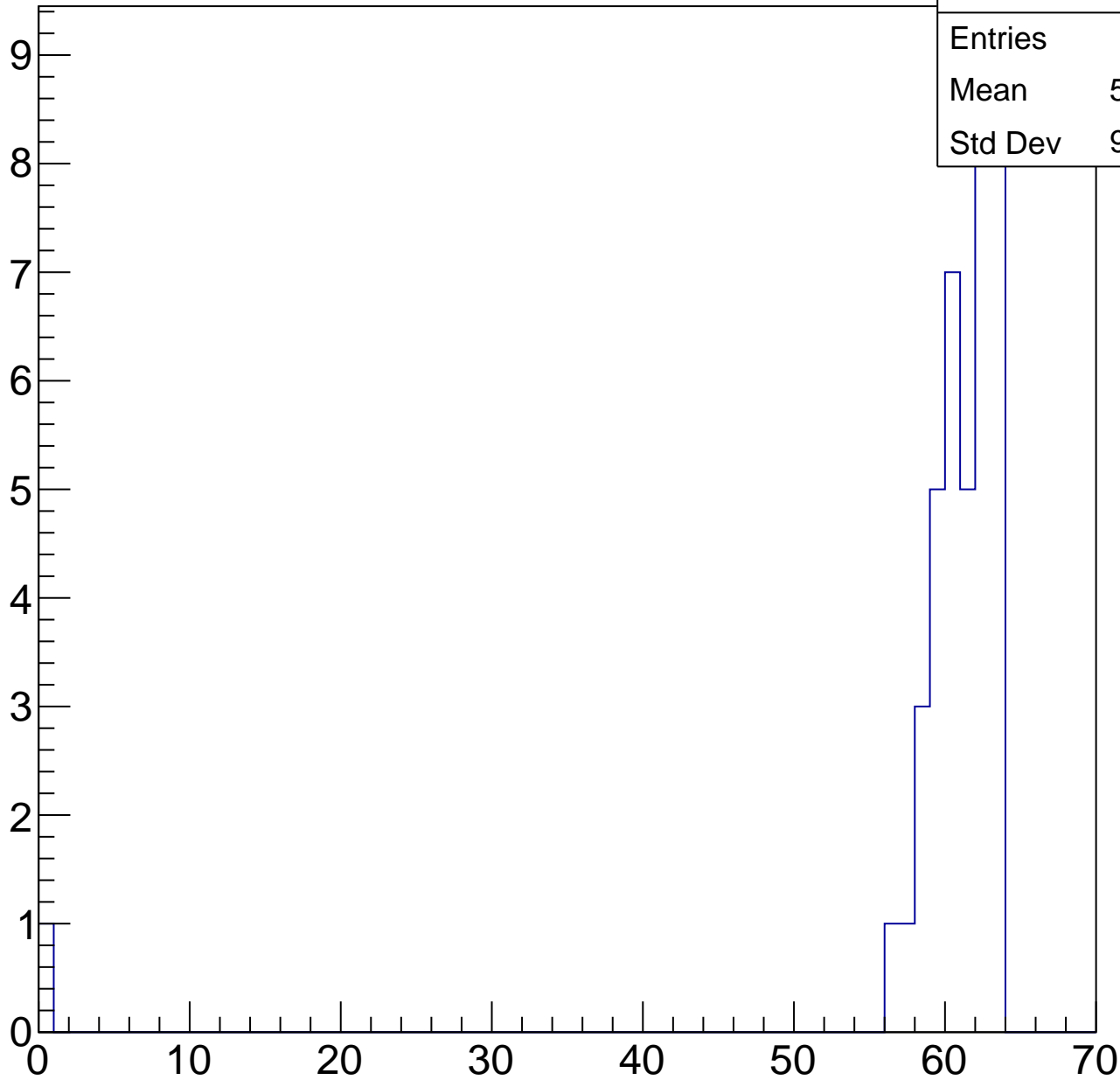
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	59.25
Std Dev	9.666

ampl





# B1L002S, U26-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch19, adc0

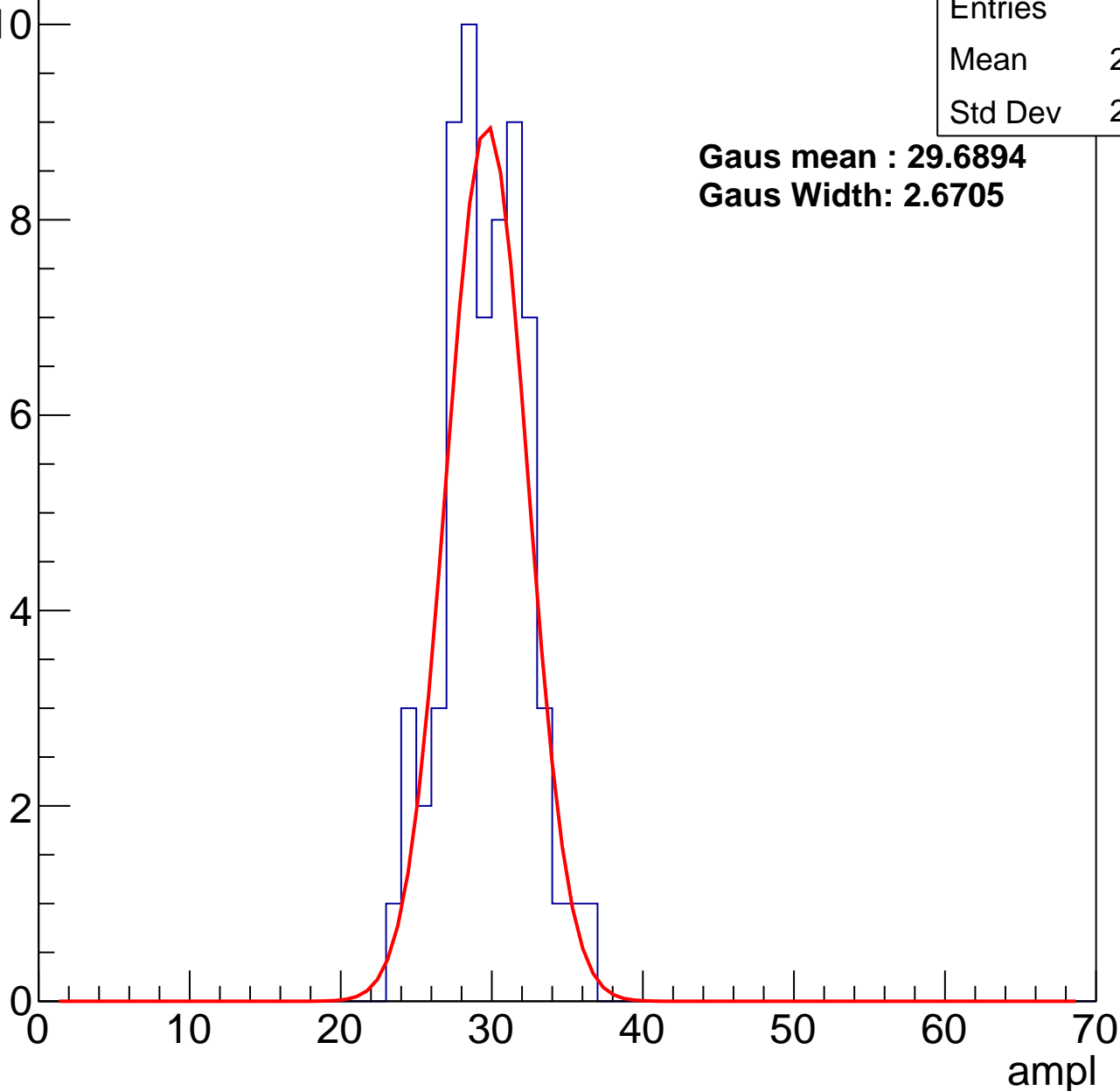
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	29.17
Std Dev	2.715

**Gaus mean : 29.6894**

**Gaus Width: 2.6705**



# B1L002S, U26-ch19, adc1

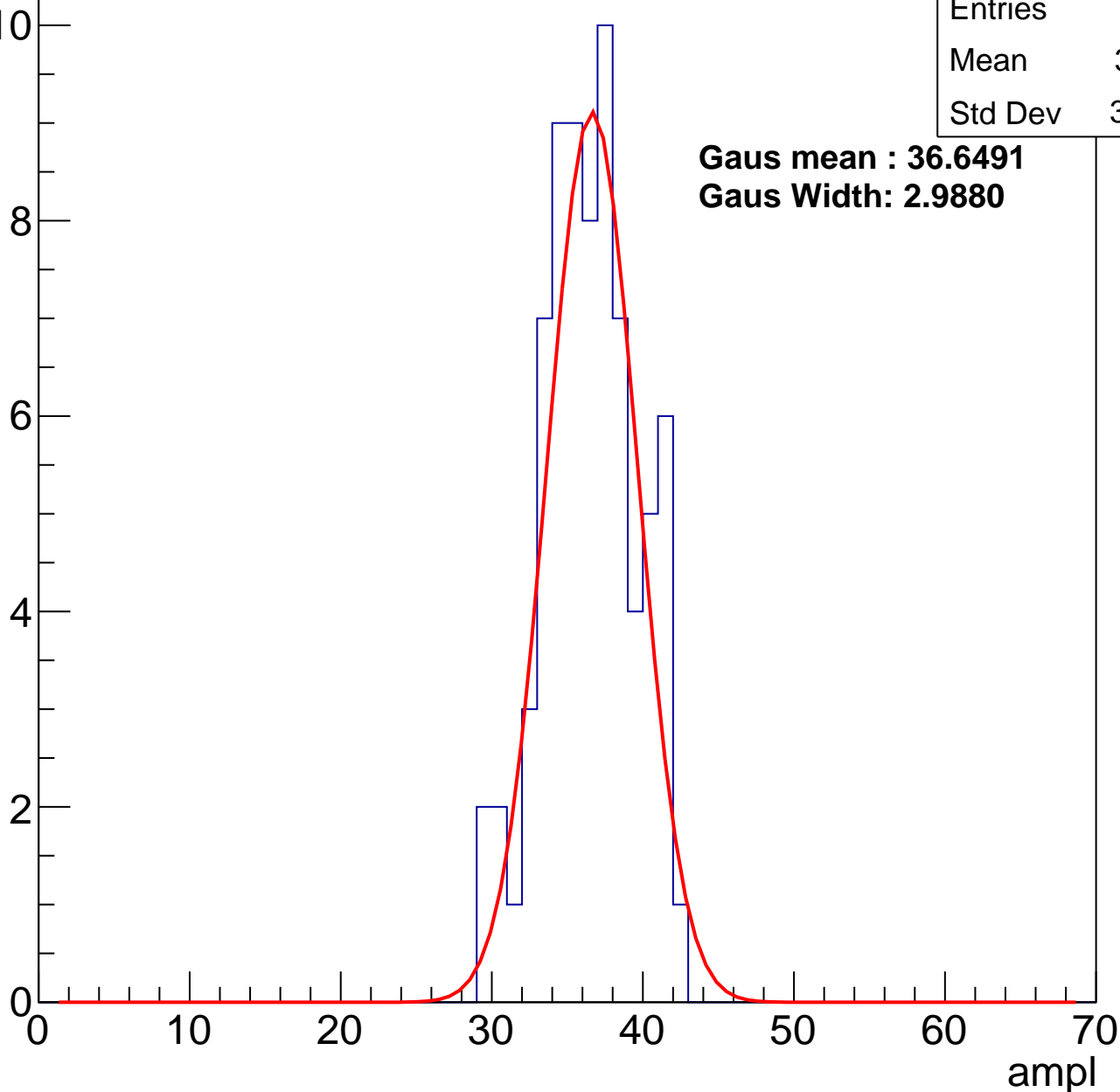
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	36.01
Std Dev	3.056

**Gaus mean : 36.6491**

**Gaus Width: 2.9880**



# B1L002S, U26-ch19, adc2

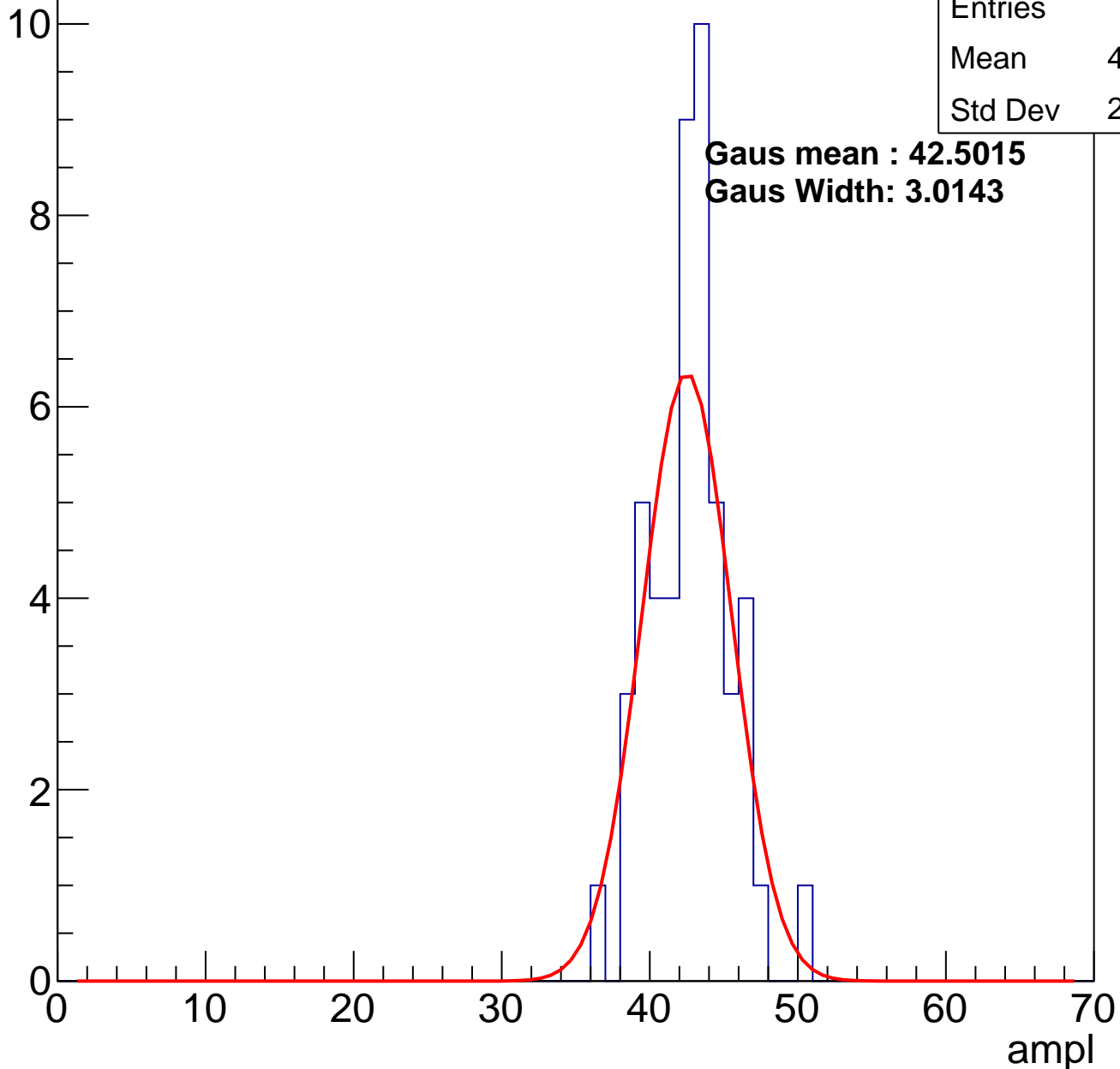
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	50
Mean	42.26
Std Dev	2.667

**Gaus mean : 42.5015**

**Gaus Width: 3.0143**

Entry

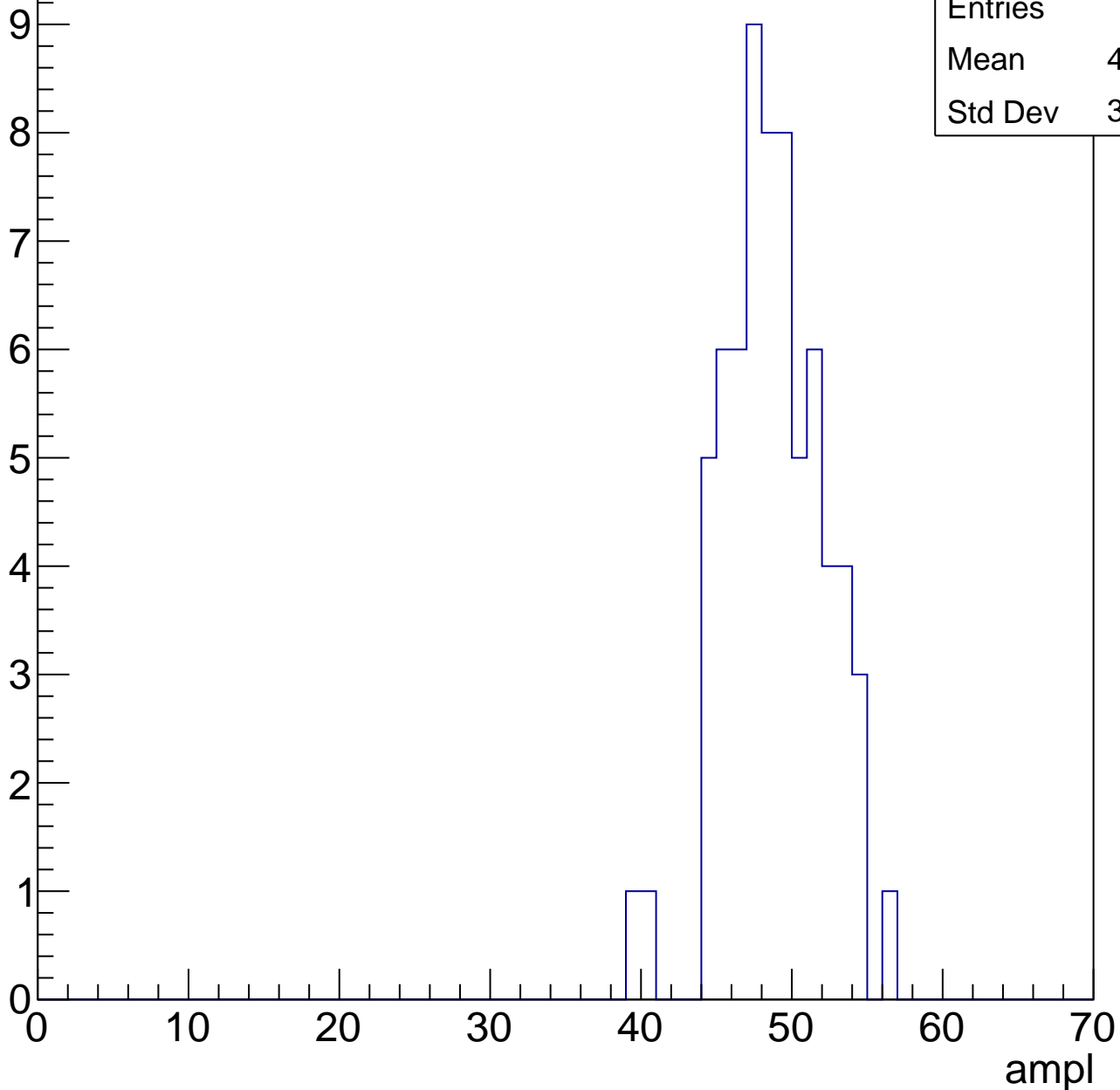


# B1L002S, U26-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.33
Std Dev	3.284

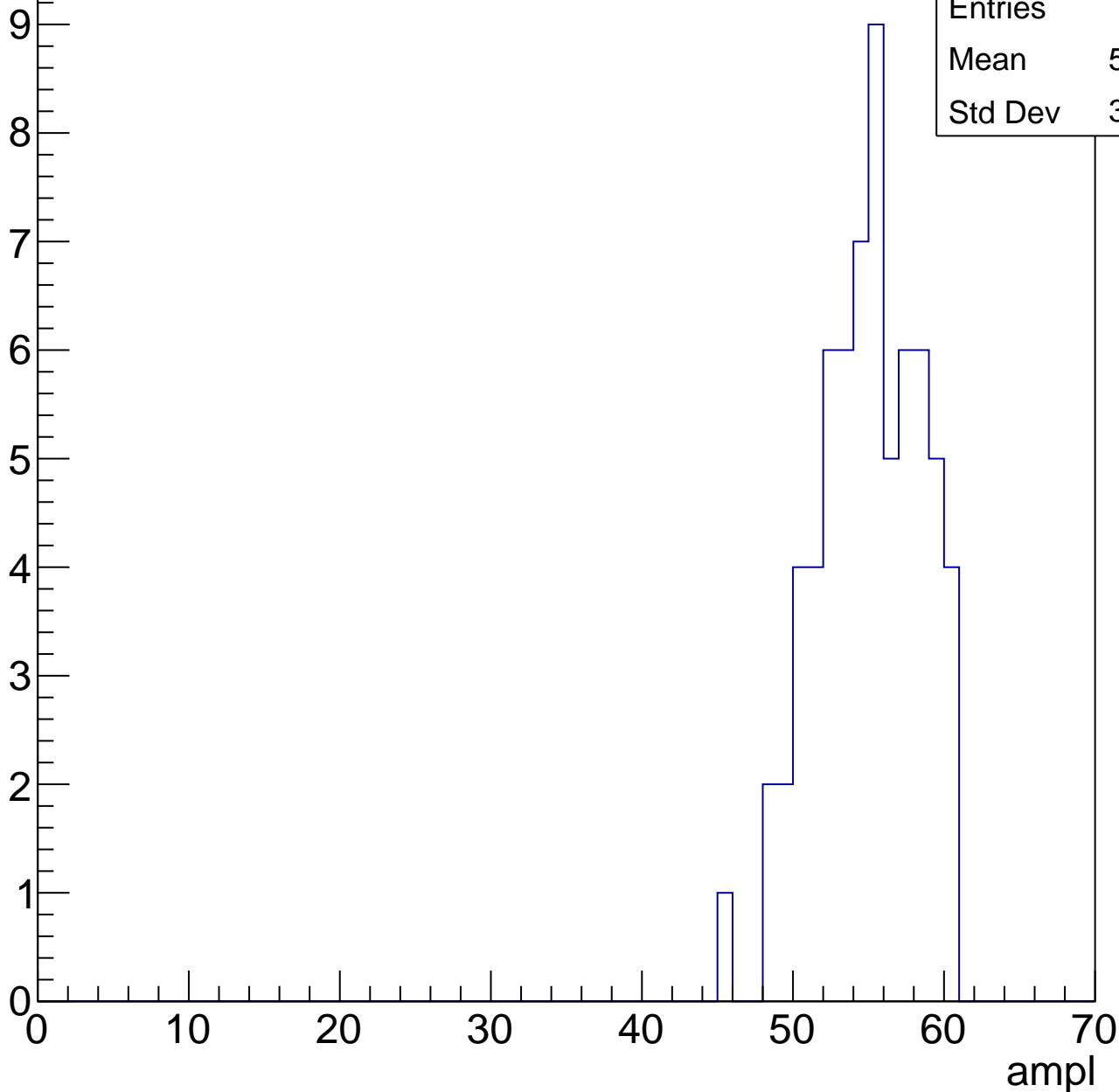


# B1L002S, U26-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	54.49
Std Dev	3.378

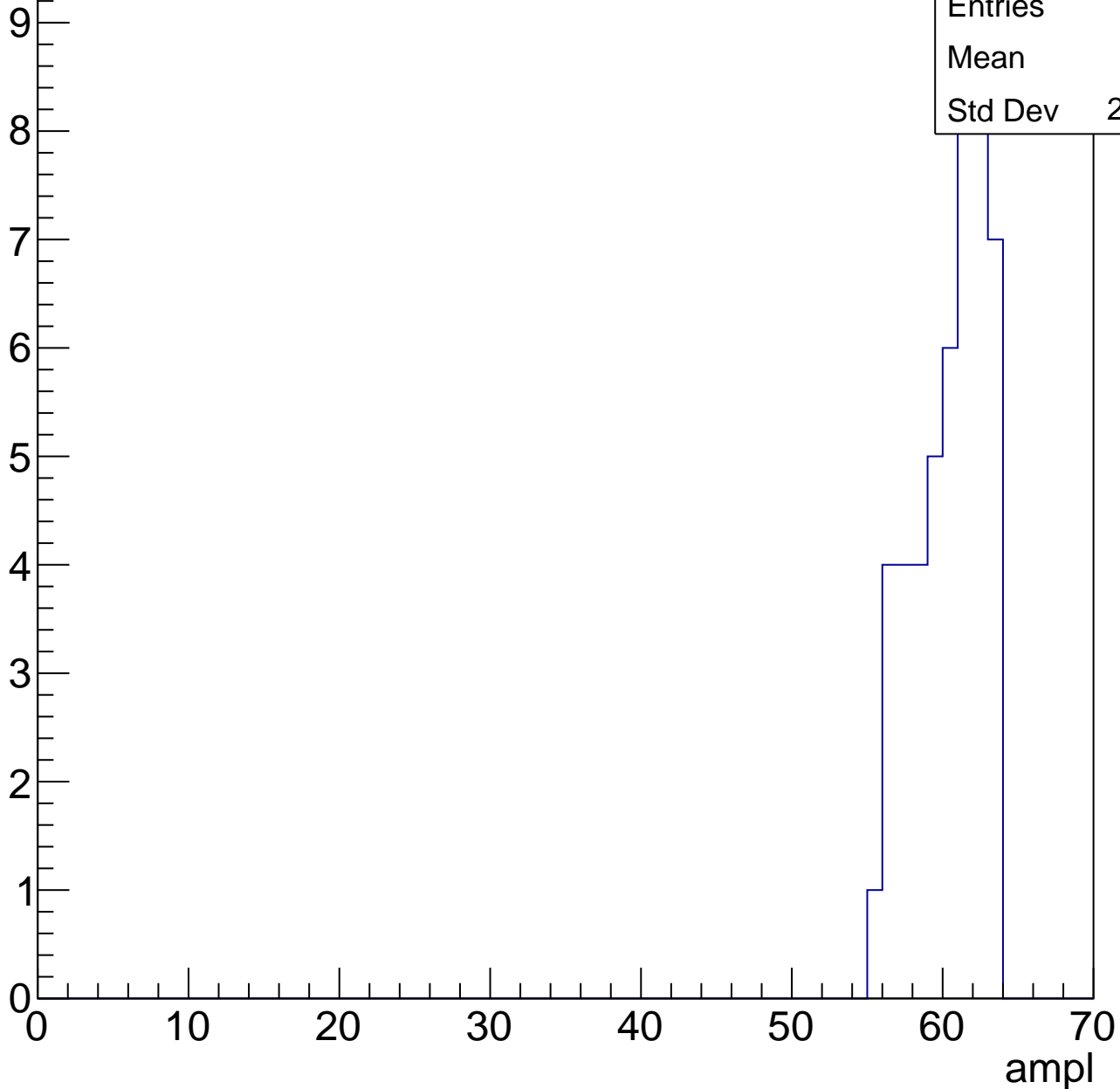


# B1L002S, U26-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	60
Std Dev	2.282



# B1L002S, U26-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

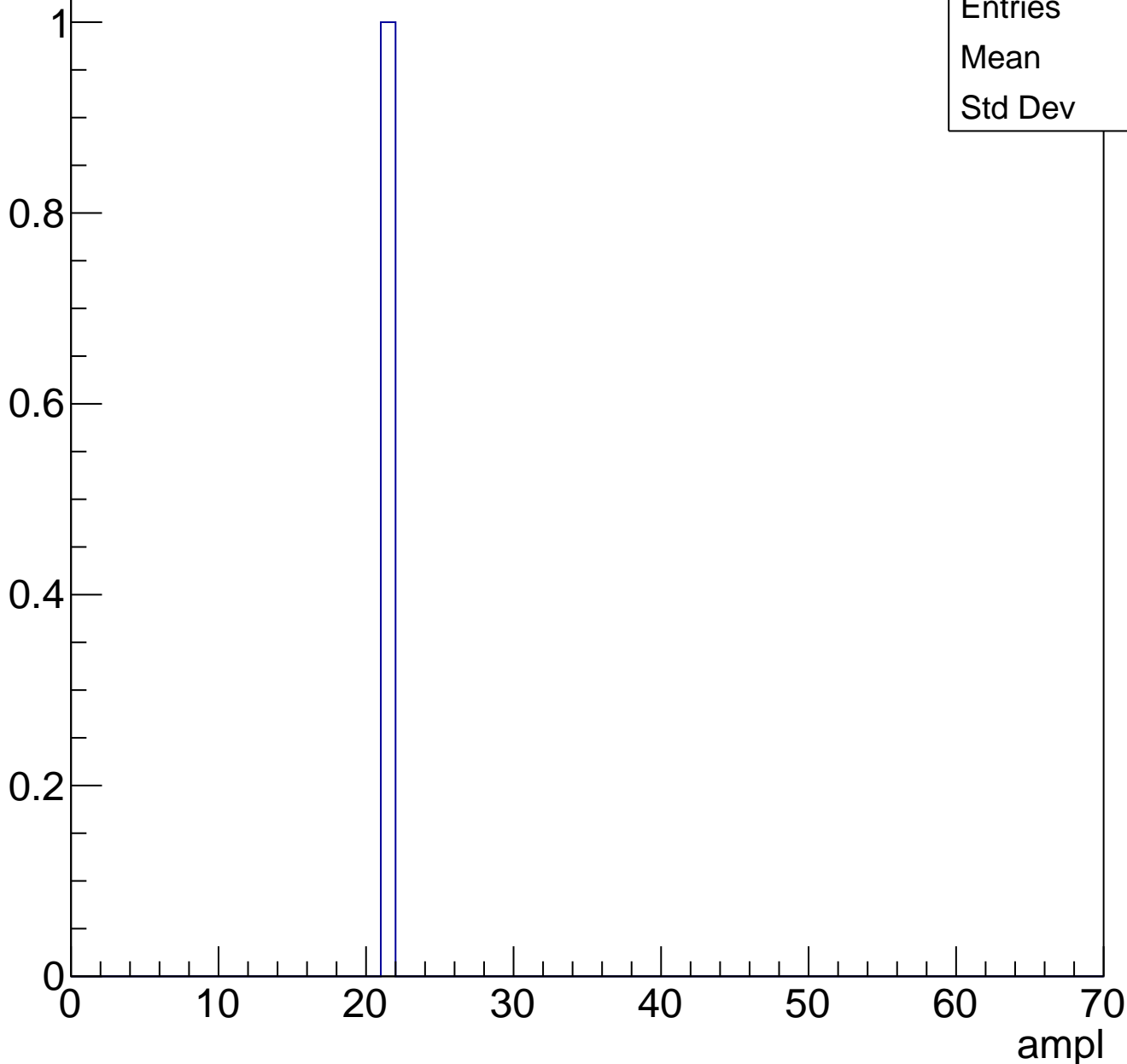




# B1L002S, U26-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch20, adc0

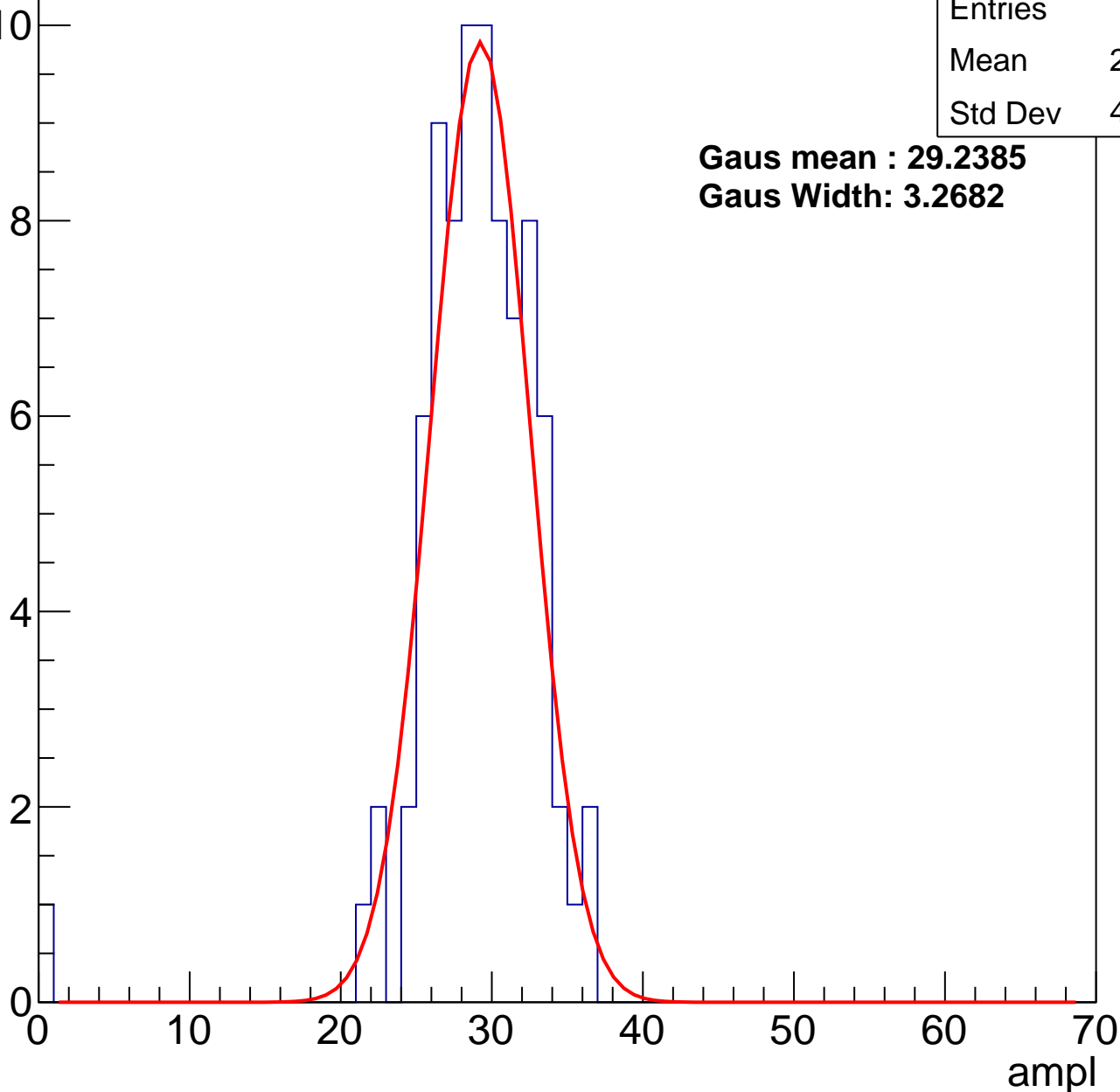
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	28.54
Std Dev	4.449

**Gaus mean : 29.2385**

**Gaus Width: 3.2682**



# B1L002S, U26-ch20, adc1

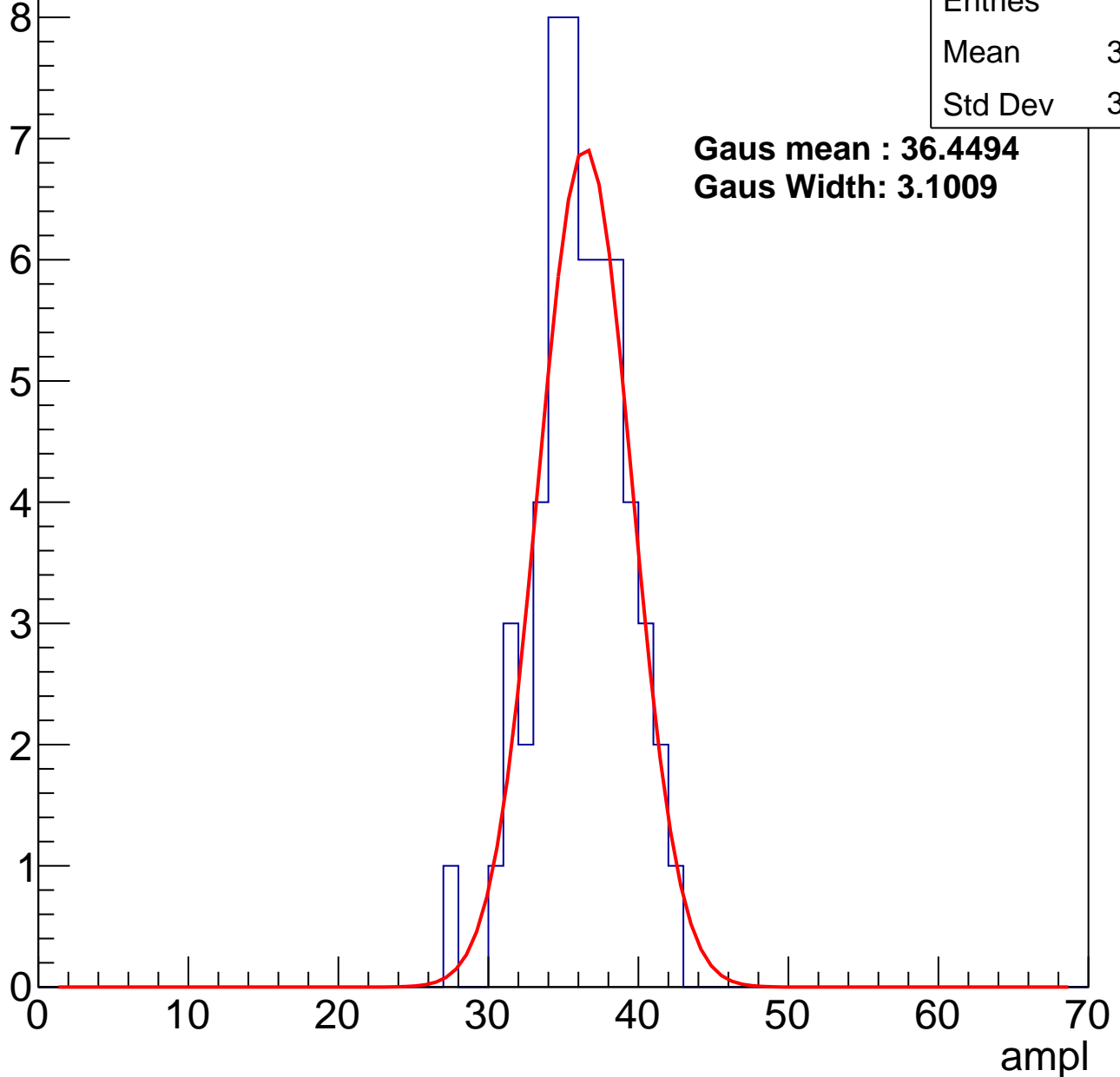
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	35.71
Std Dev	3.001

**Gaus mean : 36.4494**

**Gaus Width: 3.1009**



# B1L002S, U26-ch20, adc2

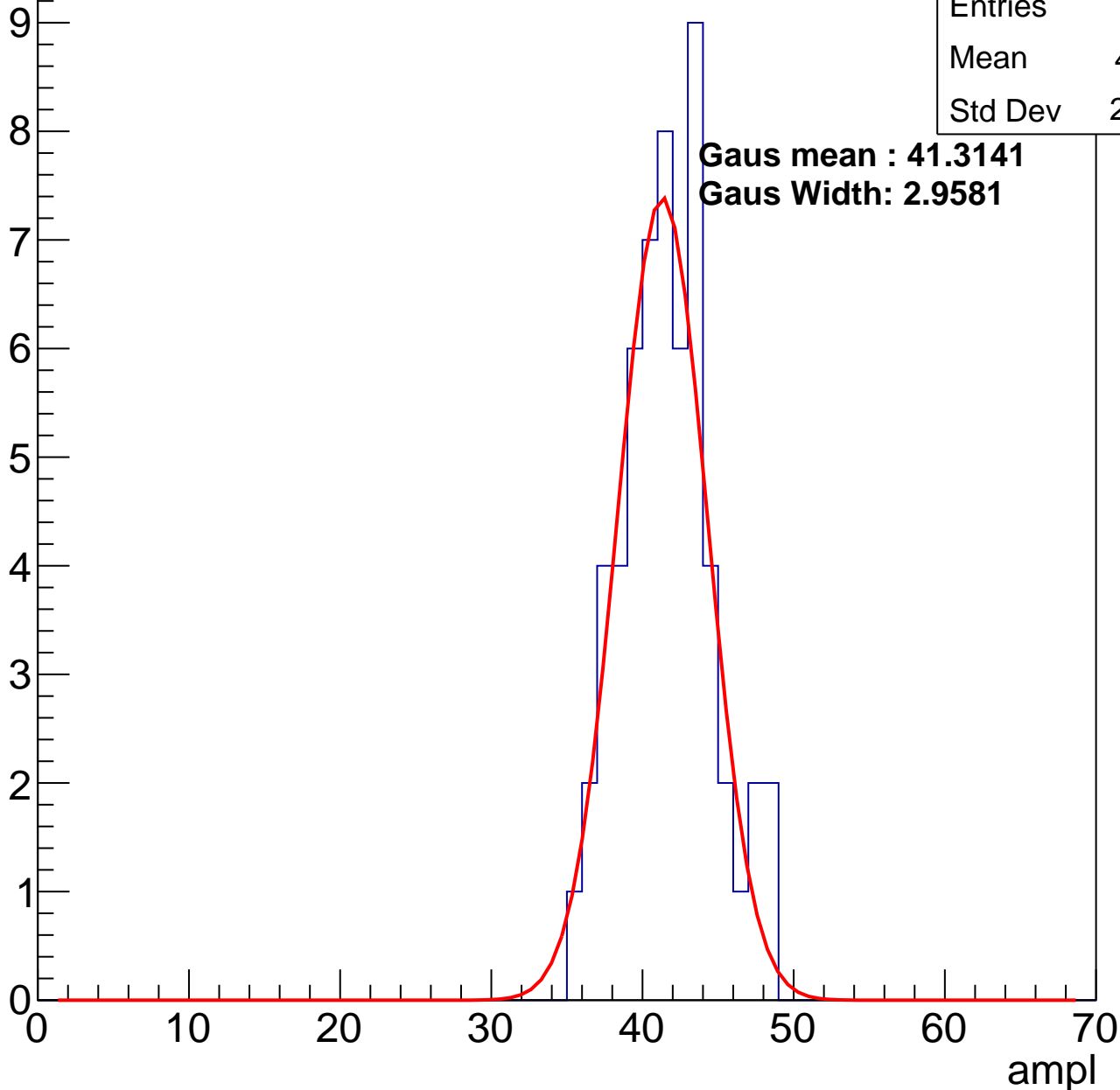
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.21
Std Dev	2.993

**Gaus mean : 41.3141**

**Gaus Width: 2.9581**

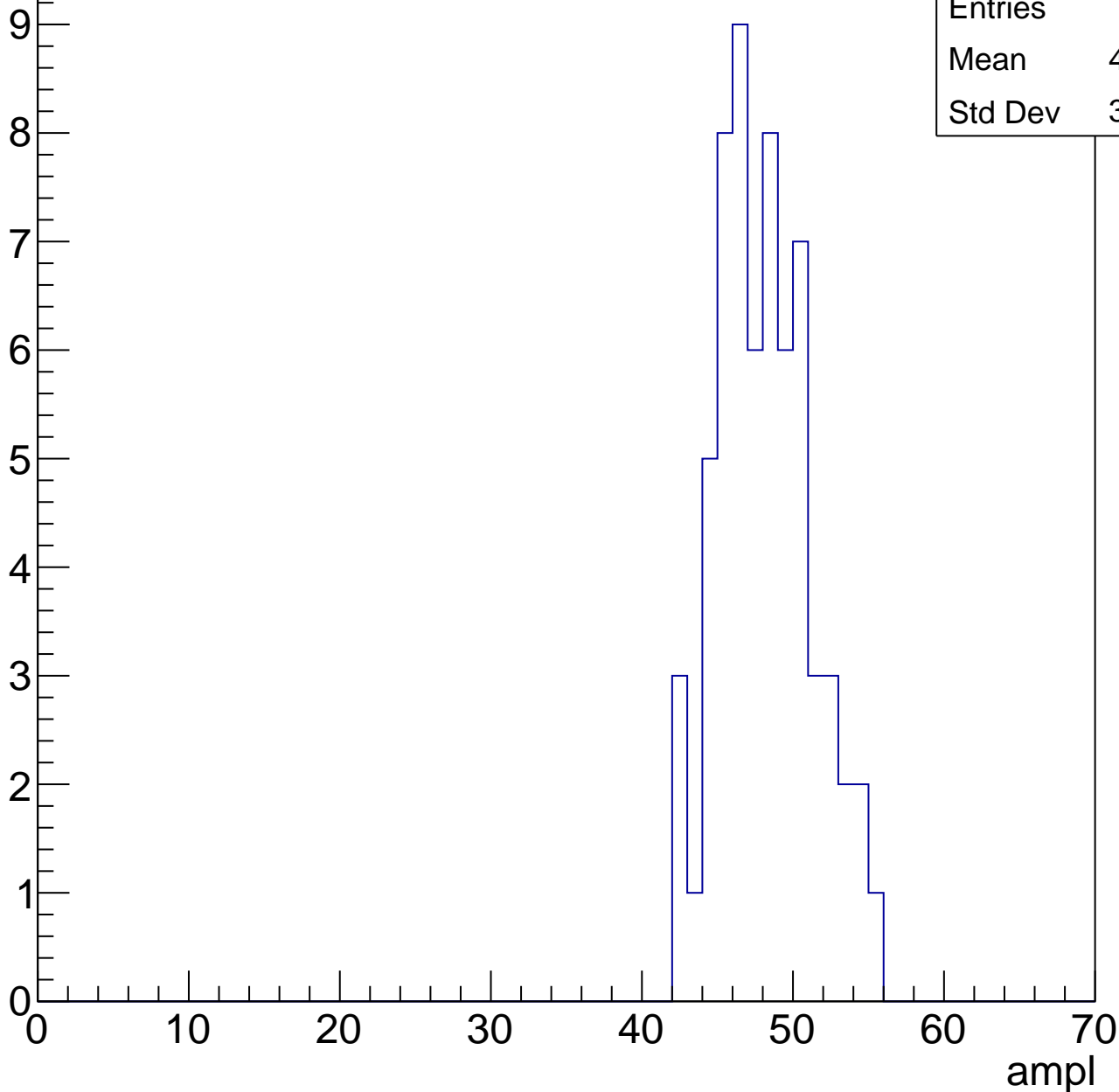


# B1L002S, U26-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	47.67
Std Dev	3.062

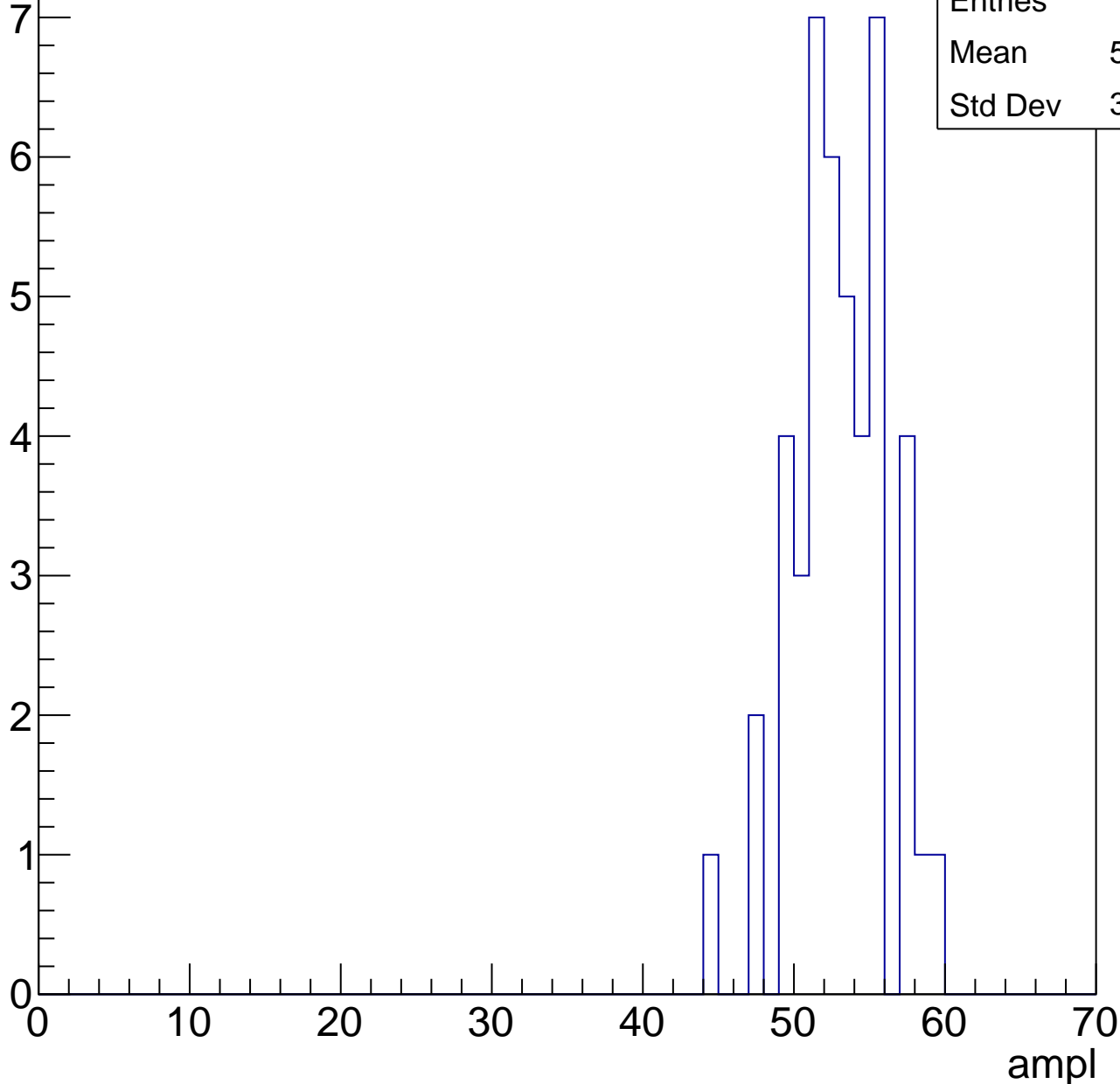


# B1L002S, U26-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	52.53
Std Dev	3.074

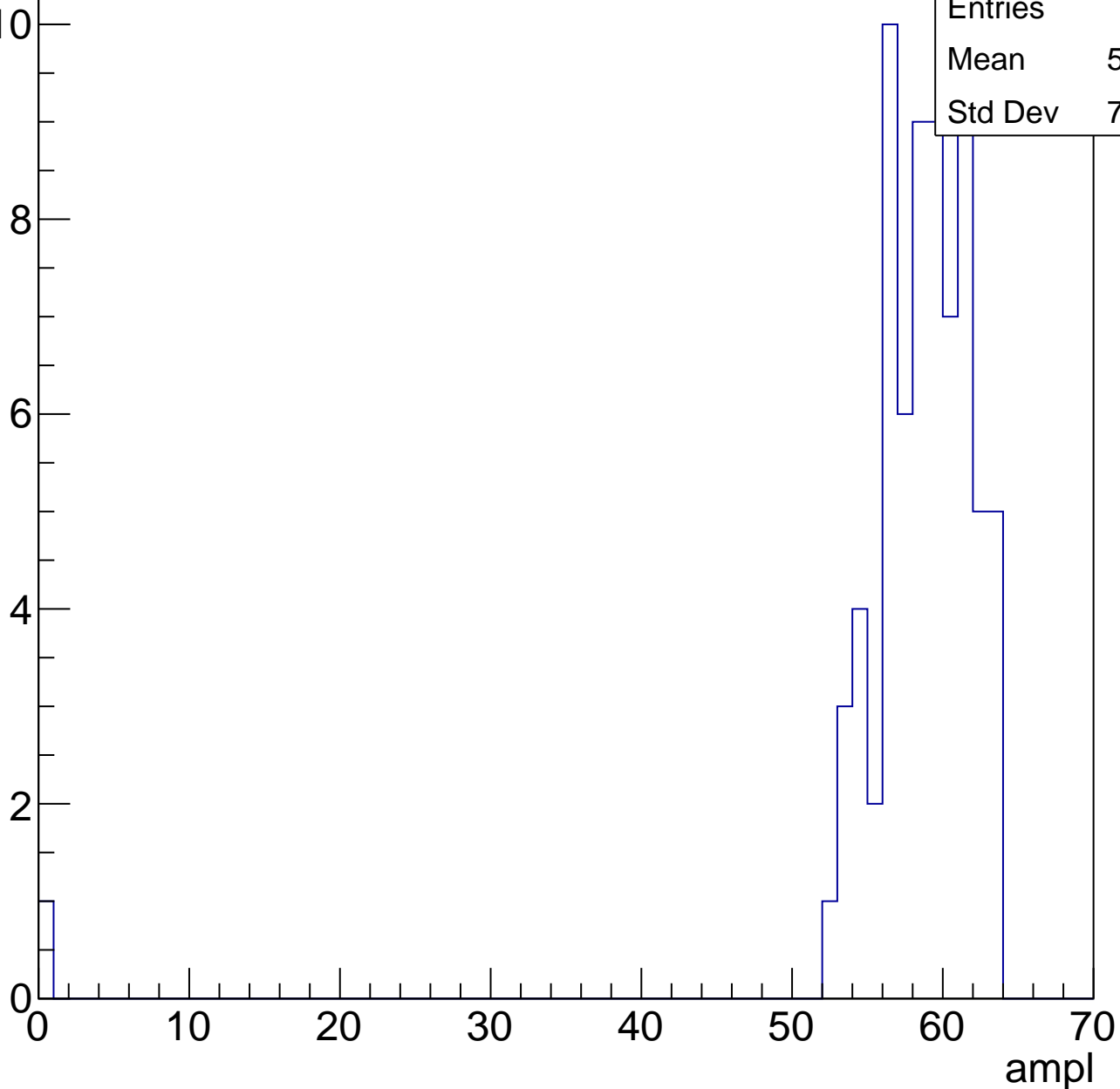


# B1L002S, U26-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	57.55
Std Dev	7.422

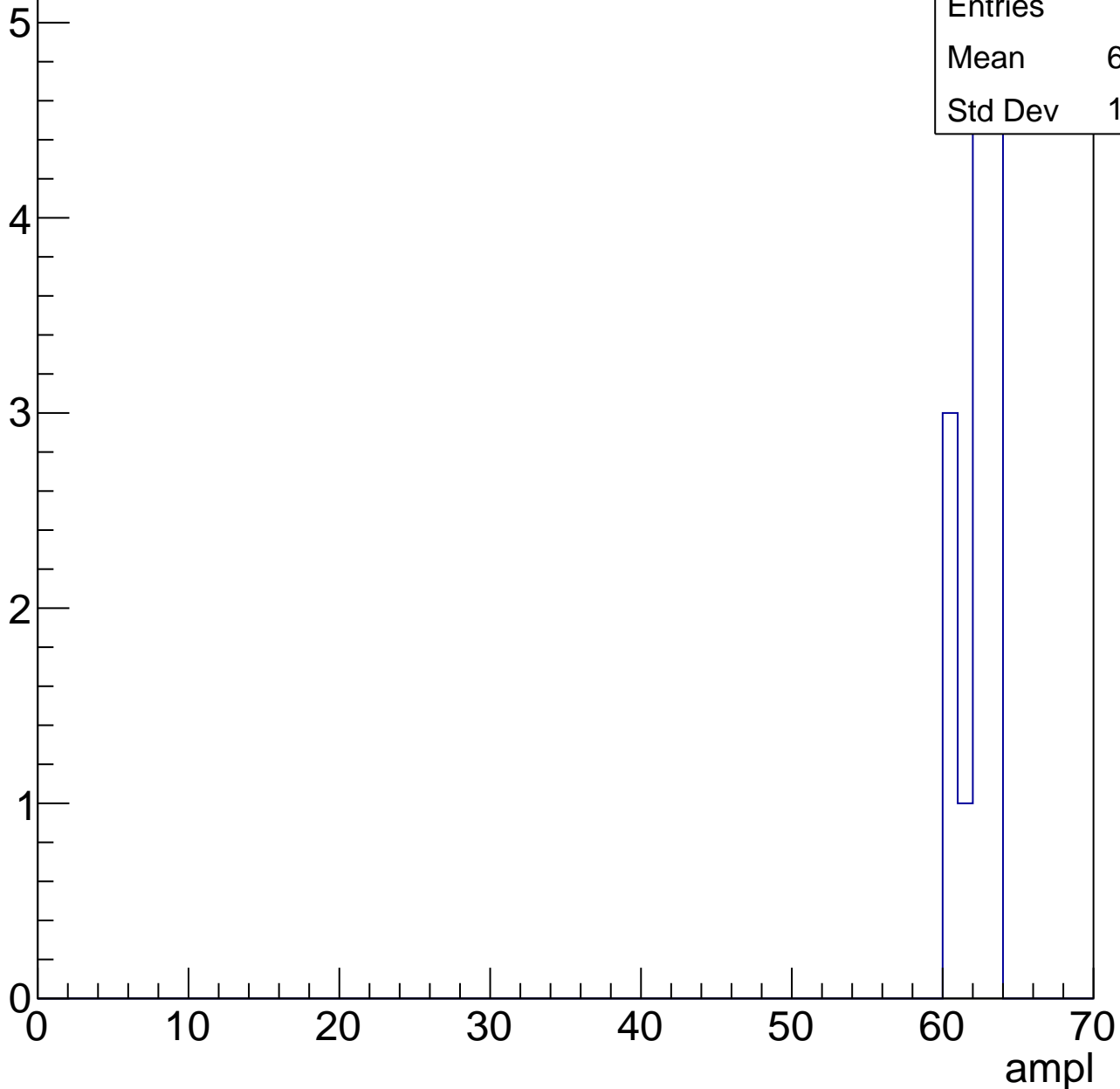


# B1L002S, U26-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.125





# B1L002S, U26-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch21, adc0

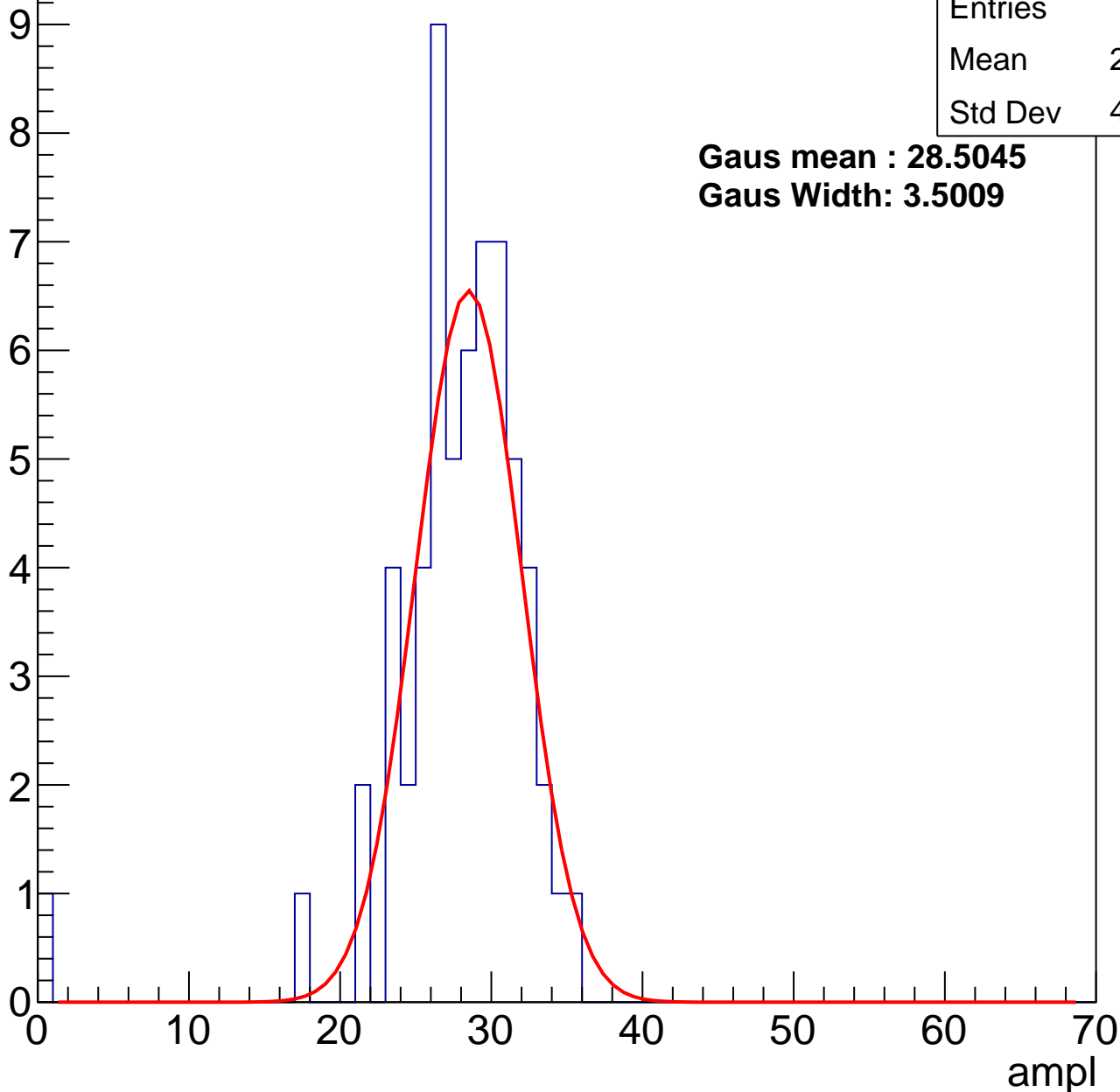
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.33
Std Dev	4.898

**Gaus mean : 28.5045**

**Gaus Width: 3.5009**



# B1L002S, U26-ch21, adc1

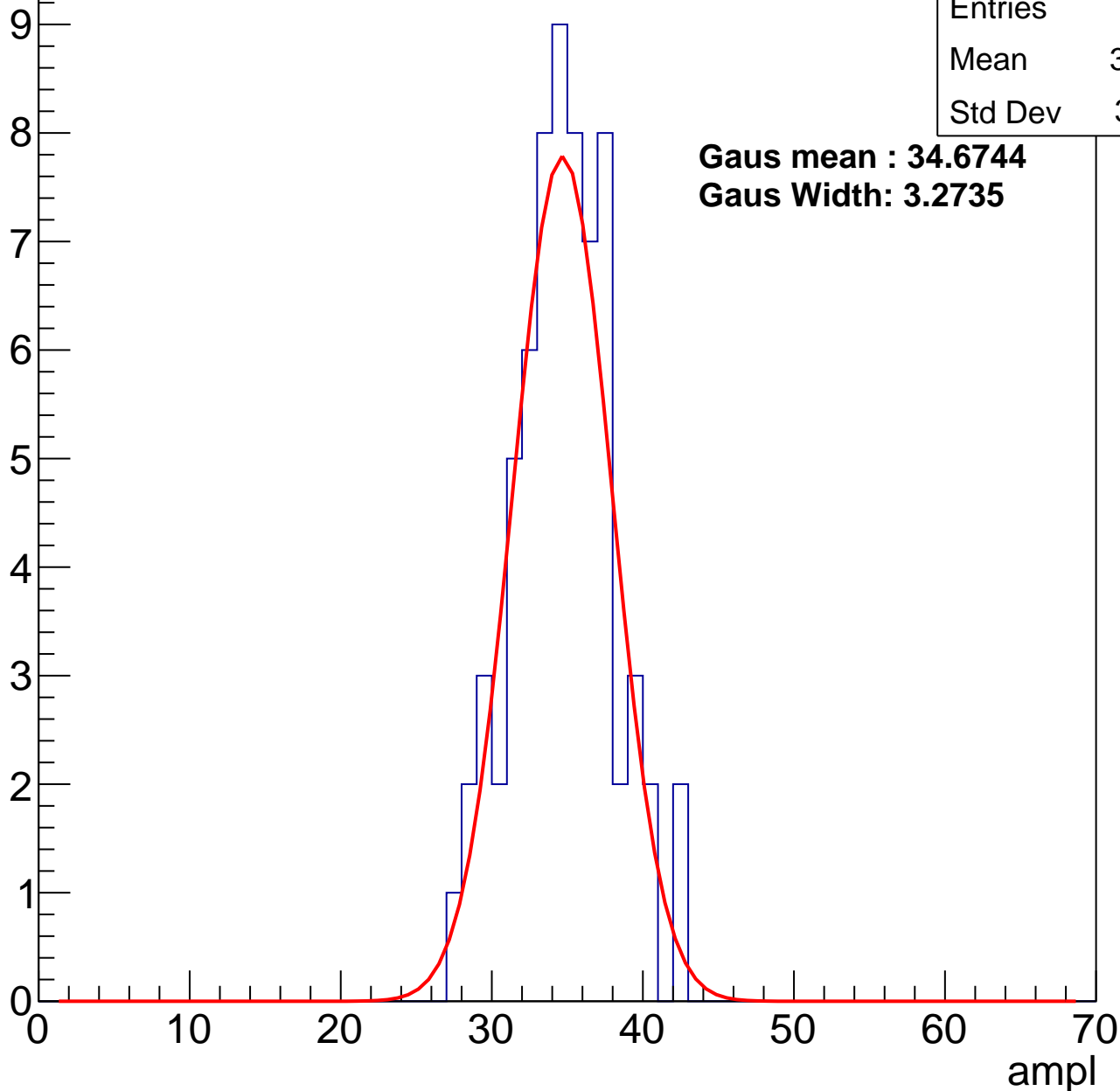
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.29
Std Dev	3.241

**Gaus mean : 34.6744**

**Gaus Width: 3.2735**



# B1L002S, U26-ch21, adc2

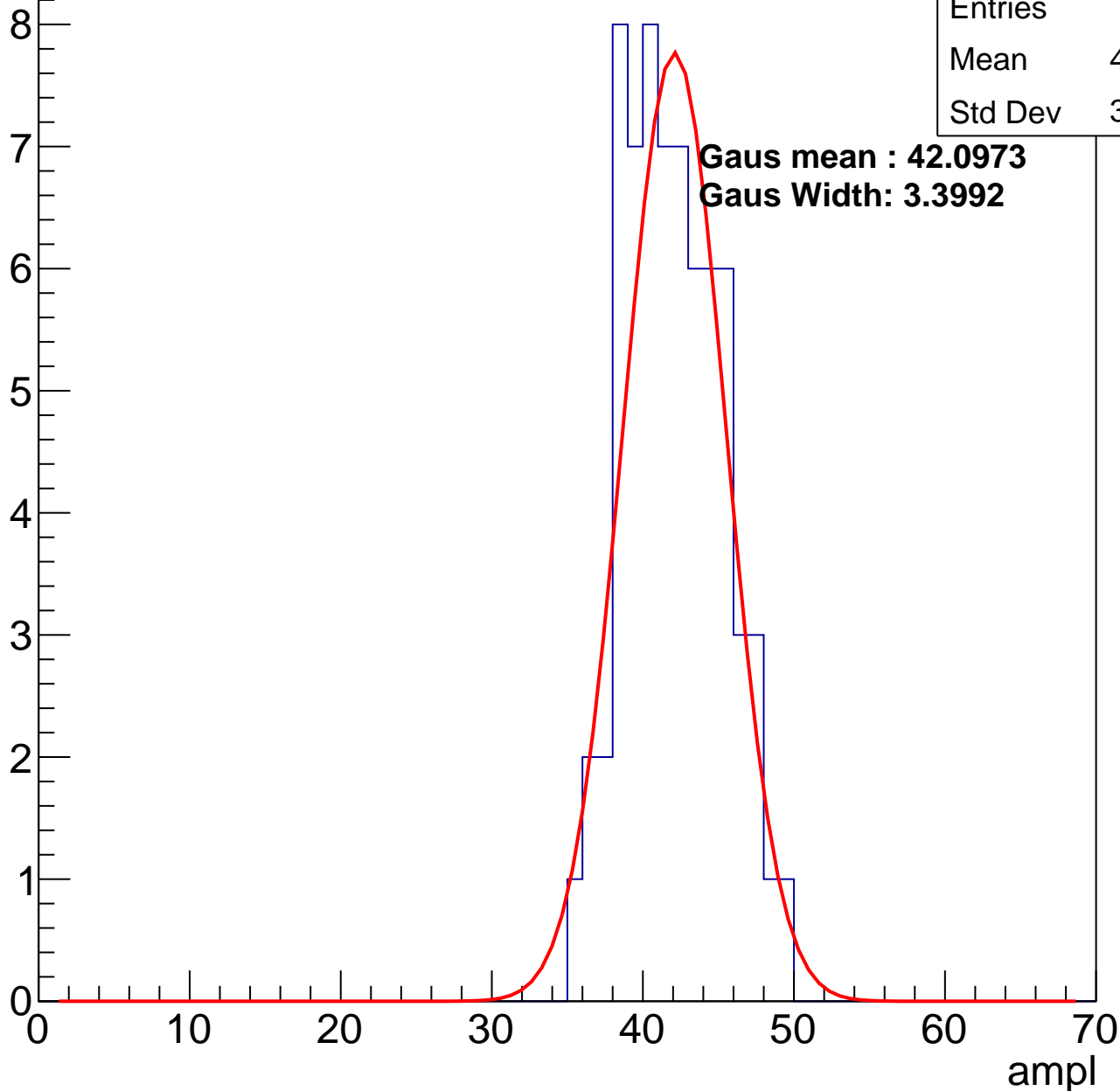
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.57
Std Dev	3.164

**Gaus mean : 42.0973**

**Gaus Width: 3.3992**

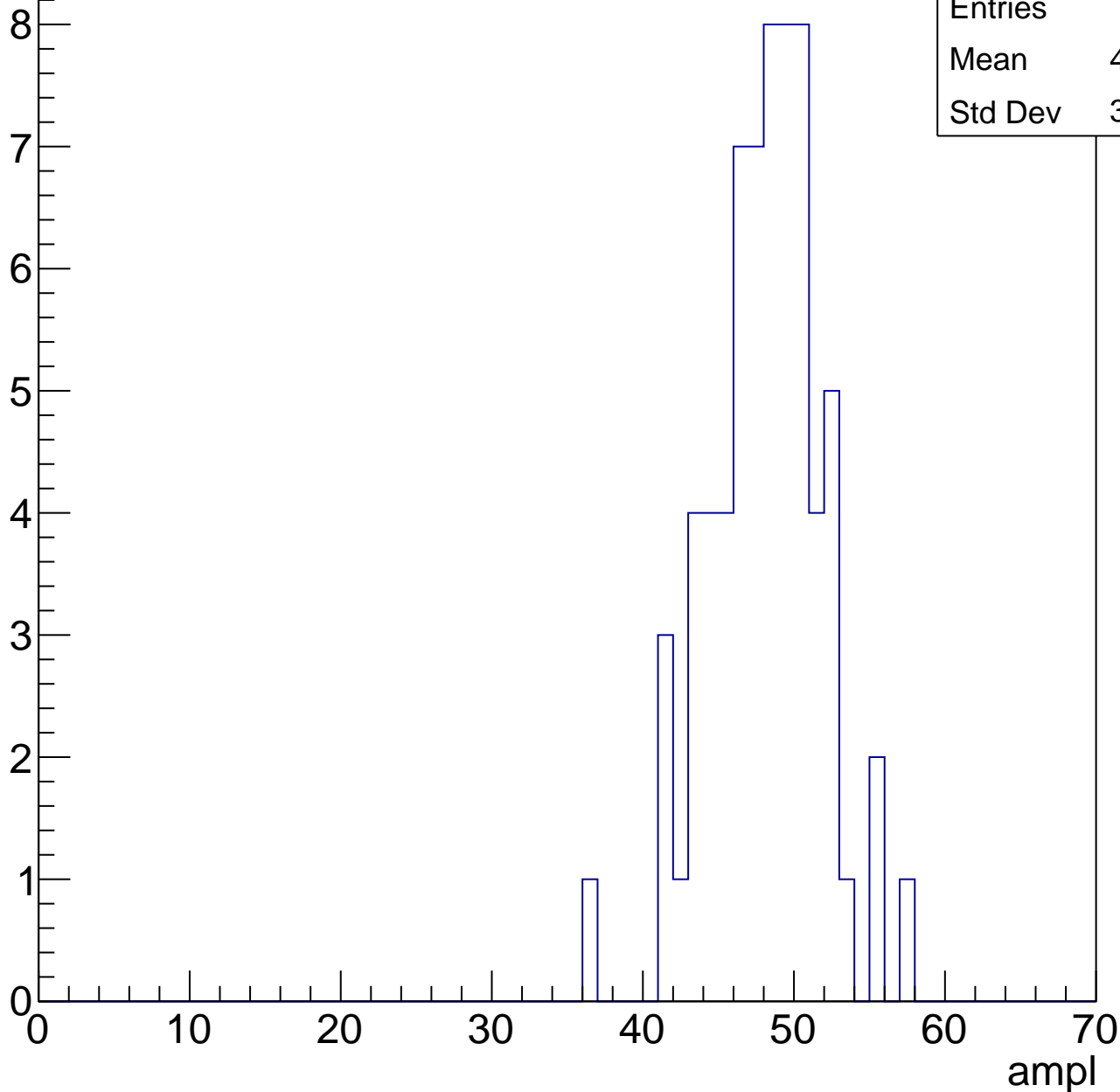


# B1L002S, U26-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

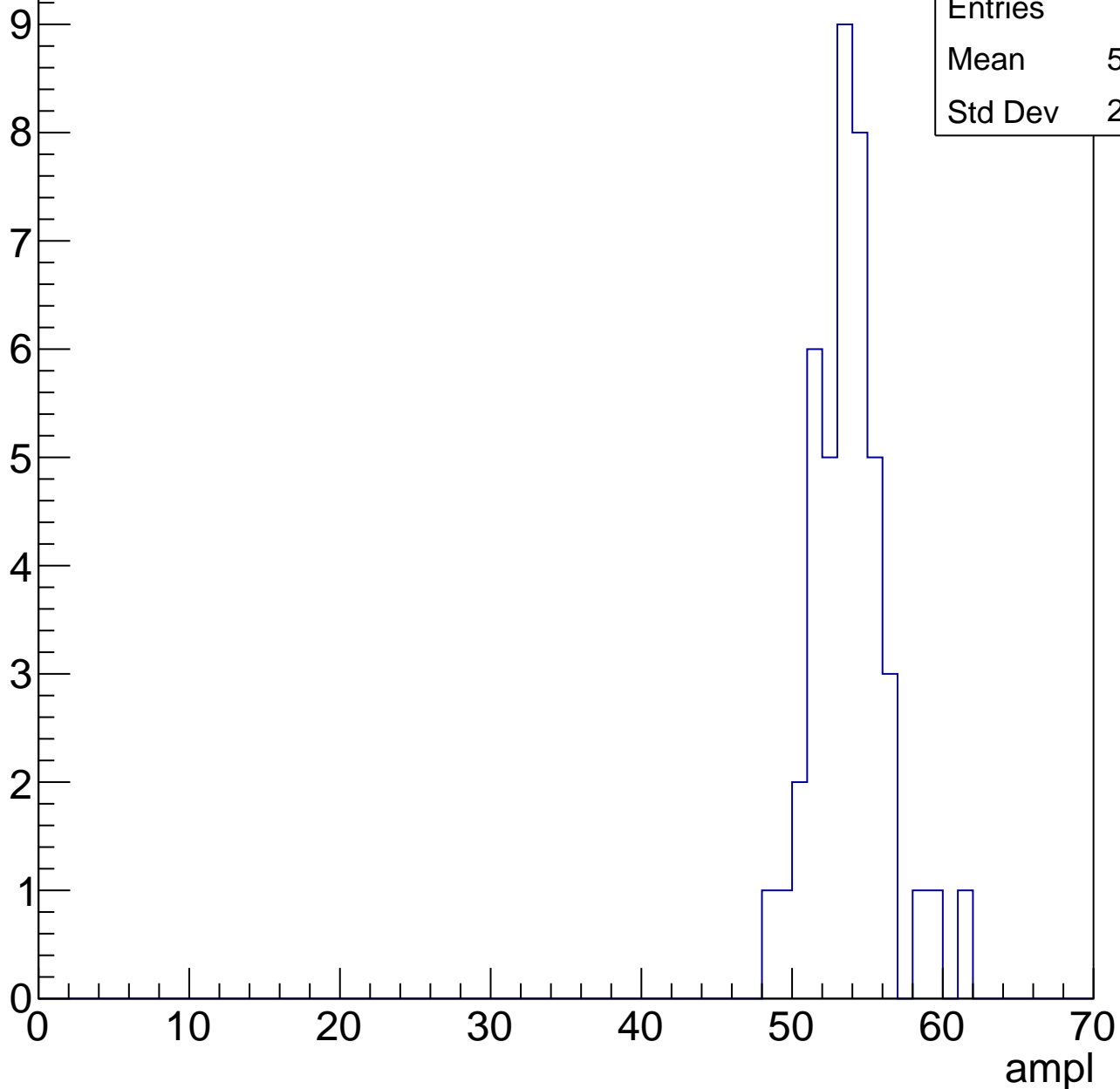
Entries	68
Mean	47.65
Std Dev	3.665



# B1L002S, U26-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



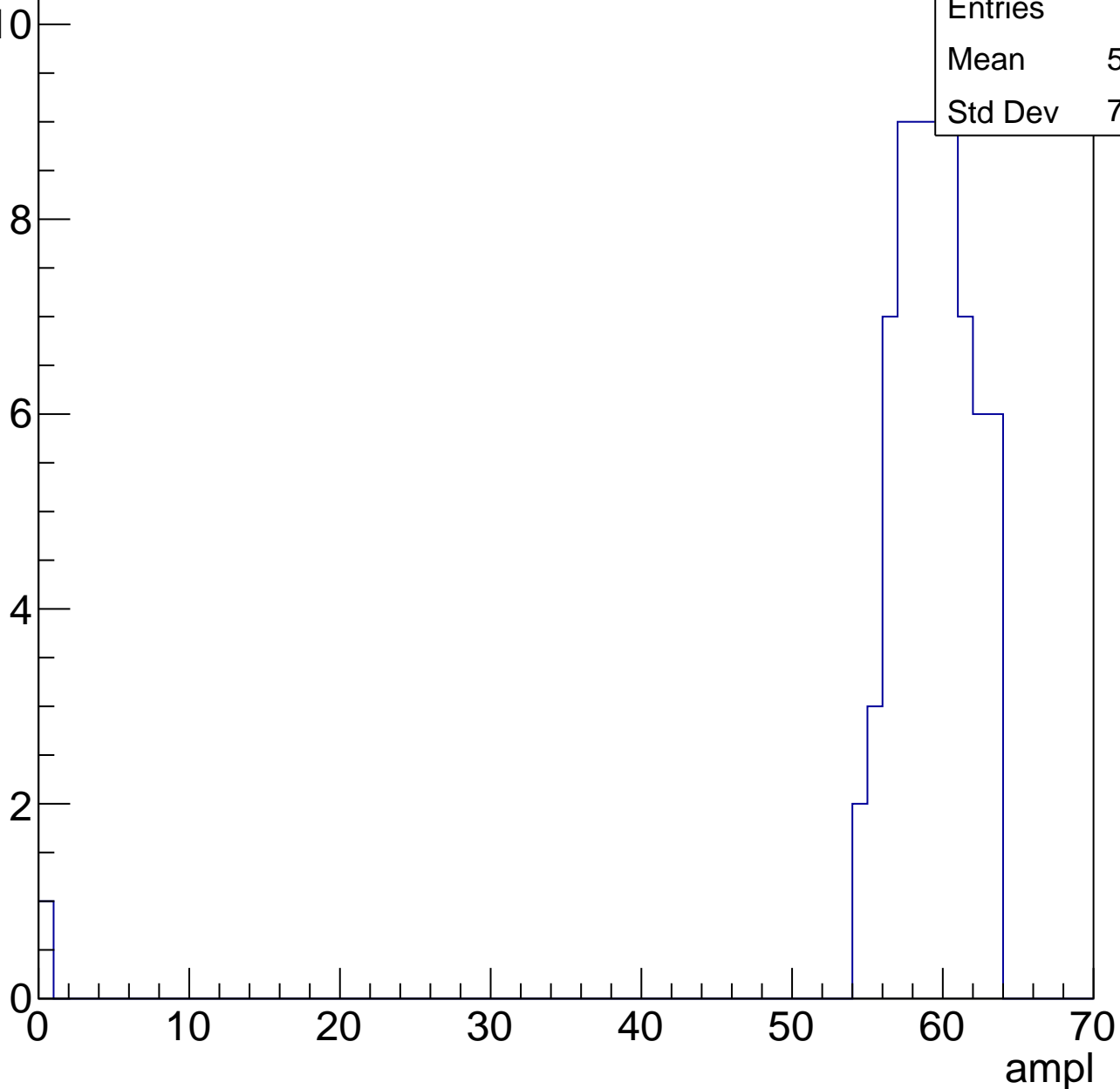
Entries	43
Mean	53.33
Std Dev	2.475

# B1L002S, U26-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

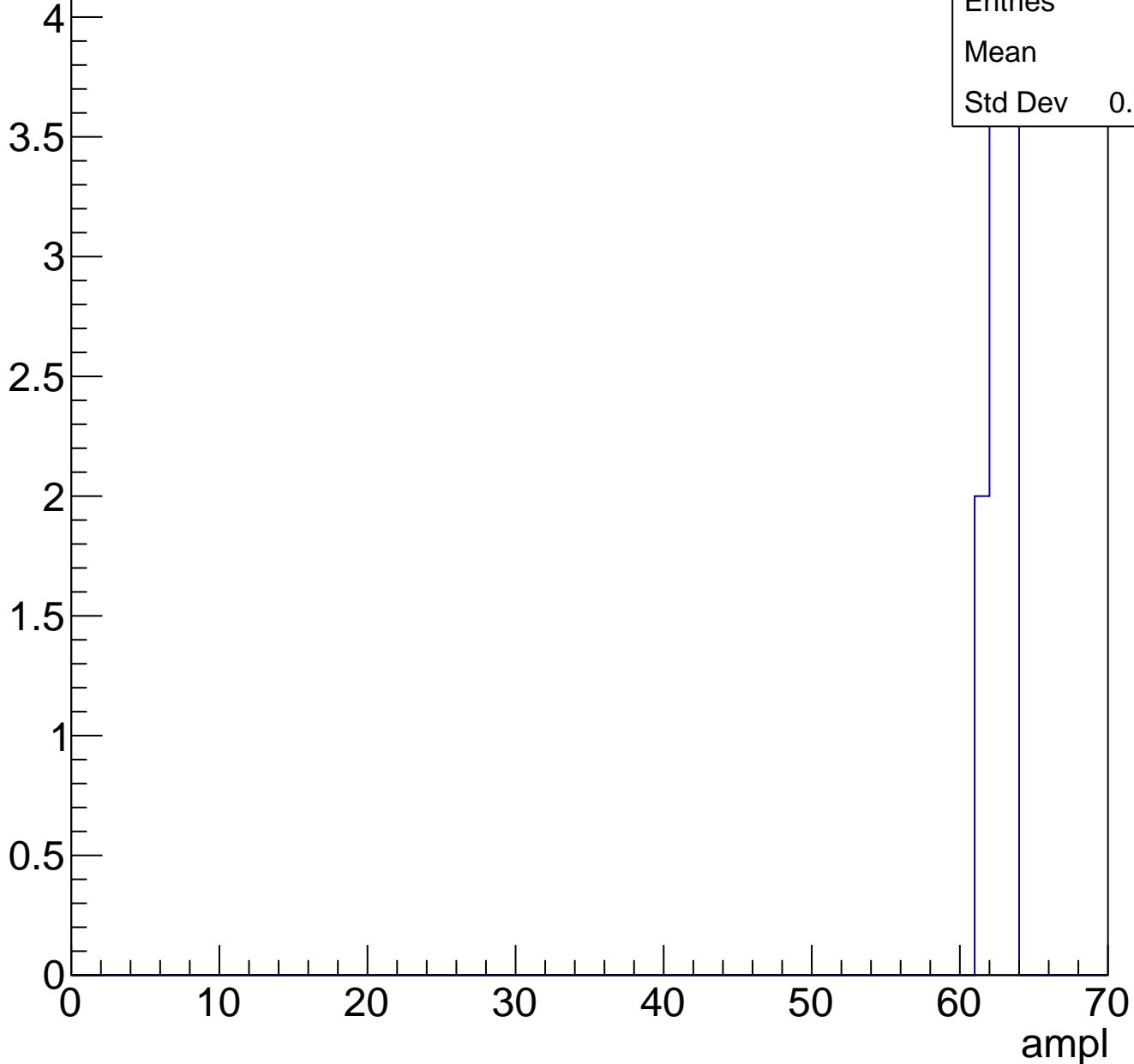
Entries	69
Mean	58.09
Std Dev	7.438



# B1L002S, U26-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	29.58
Std Dev	3.114

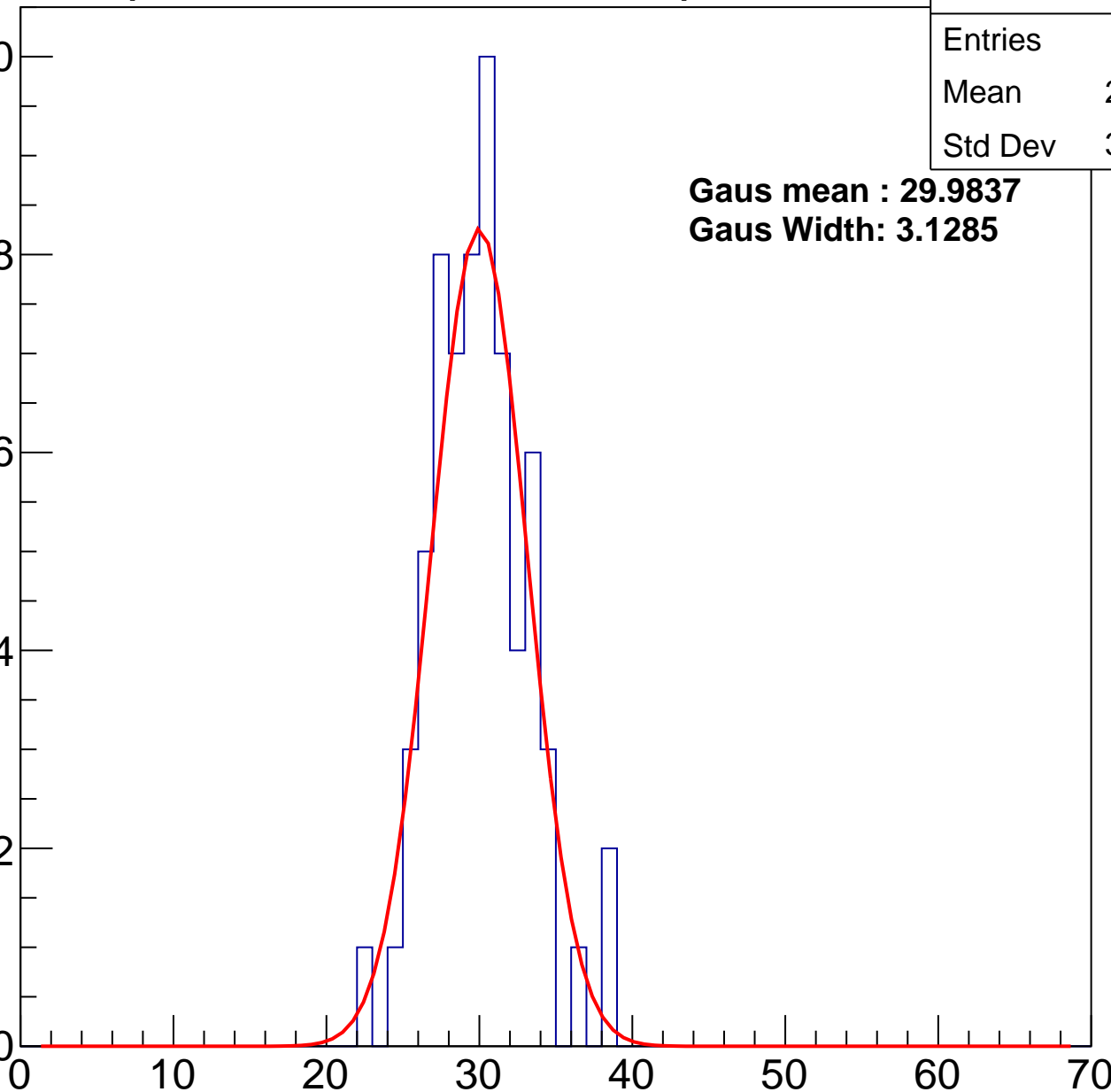
**Gaus mean : 29.9837**

**Gaus Width: 3.1285**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U26-ch22, adc1

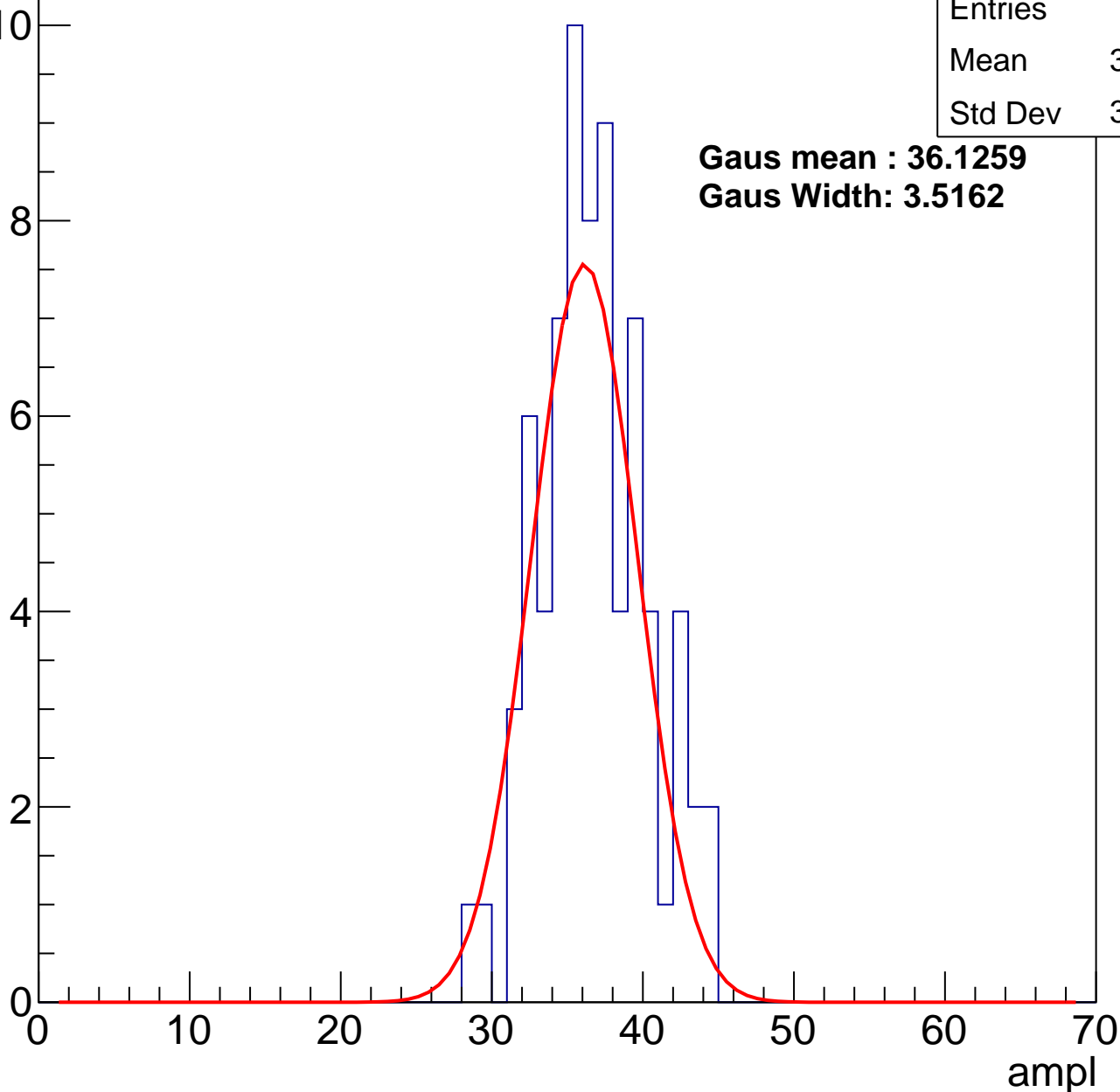
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.32
Std Dev	3.507

**Gaus mean : 36.1259**

**Gaus Width: 3.5162**



# B1L002S, U26-ch22, adc2

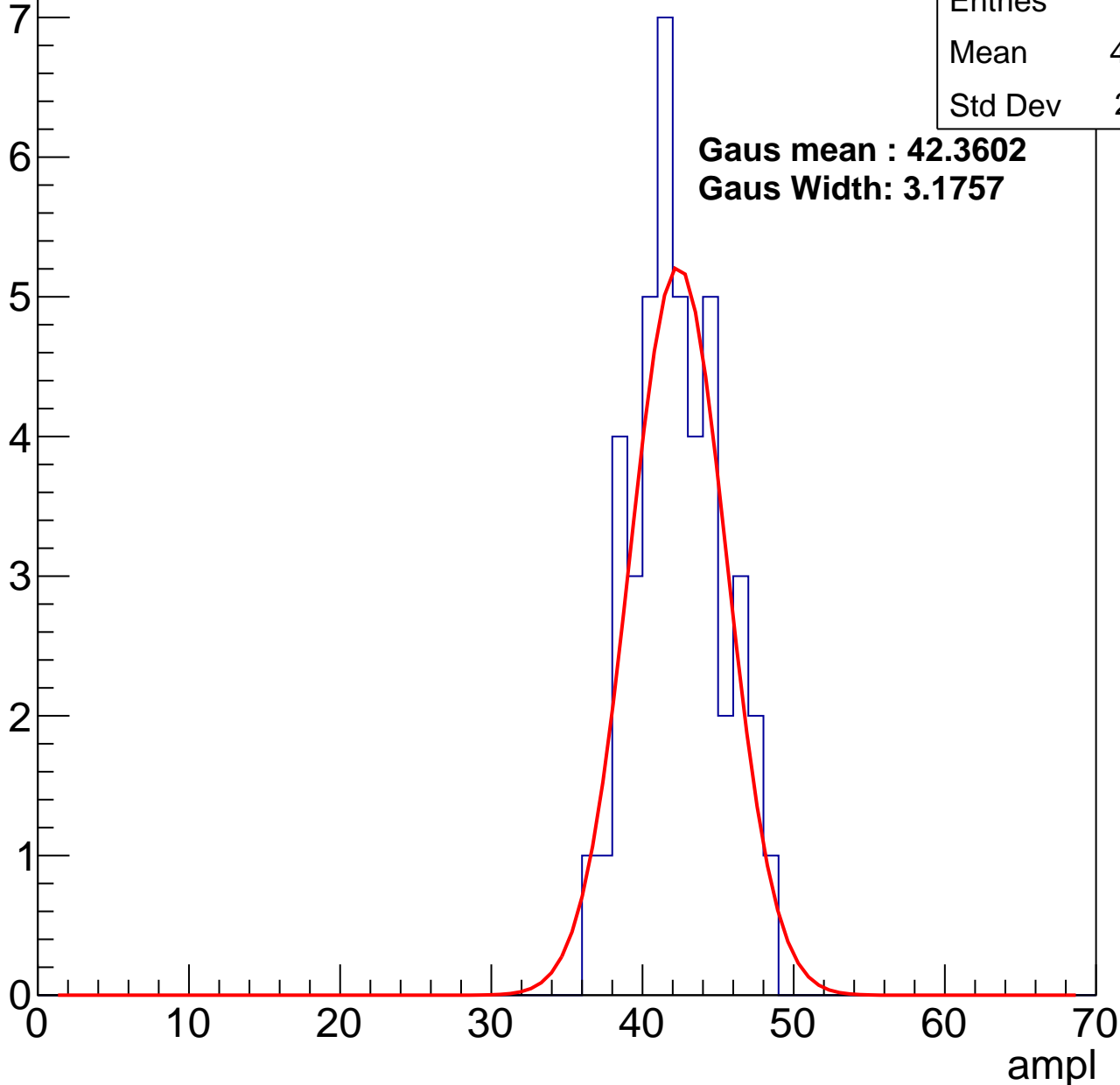
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	41.88
Std Dev	2.871

**Gaus mean : 42.3602**

**Gaus Width: 3.1757**

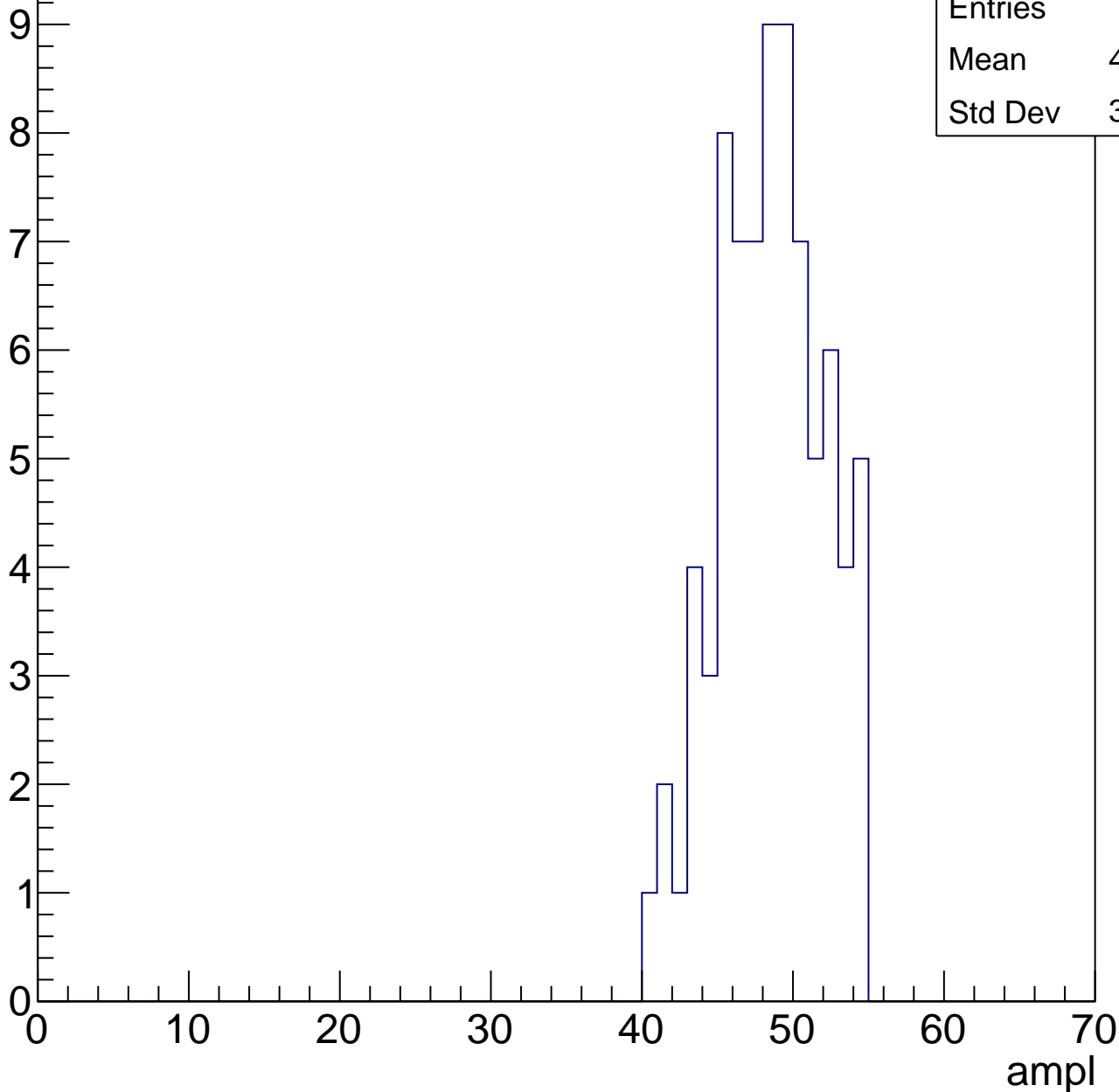


# B1L002S, U26-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

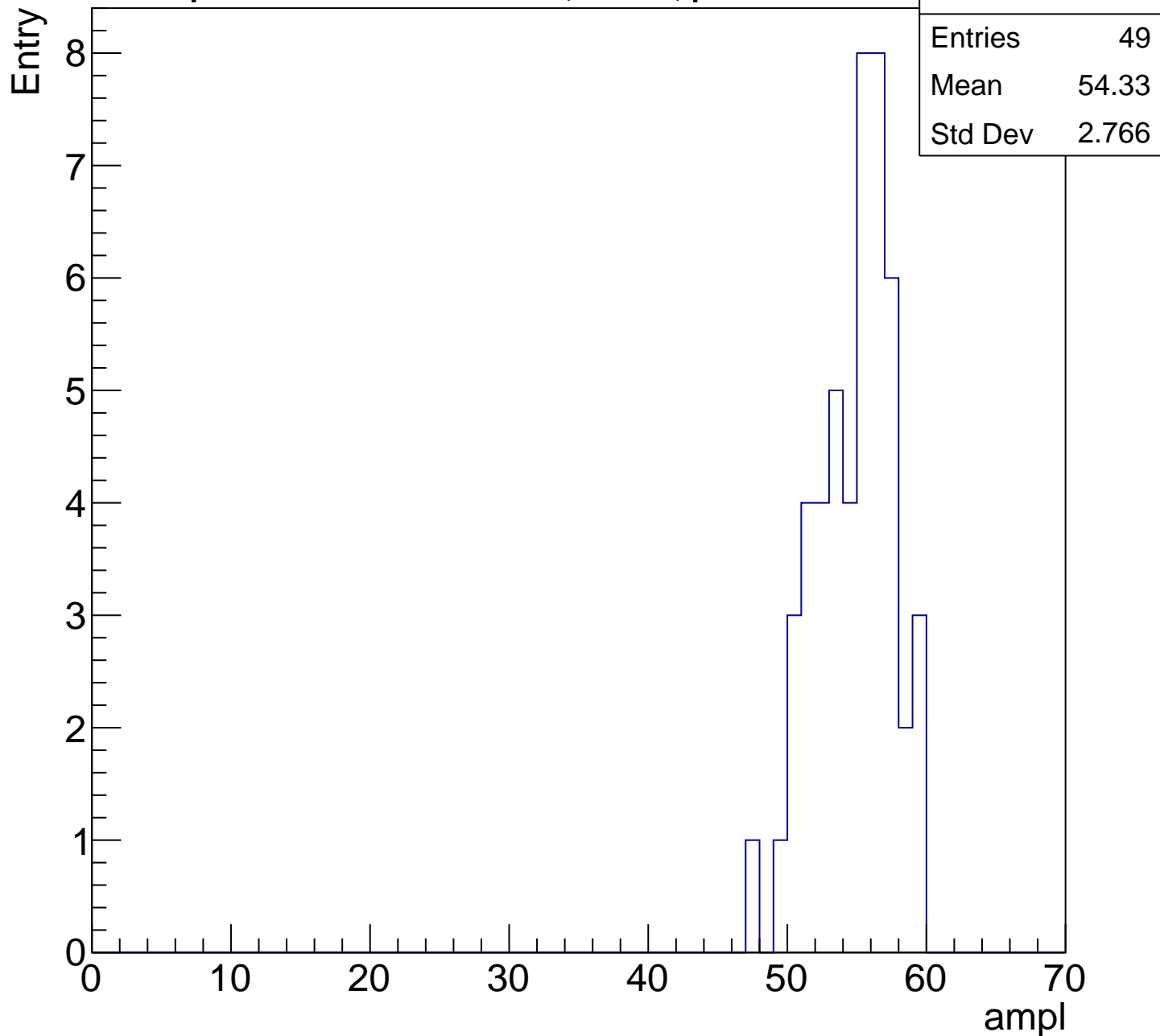
Entry

Entries	78
Mean	48.09
Std Dev	3.416



# B1L002S, U26-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

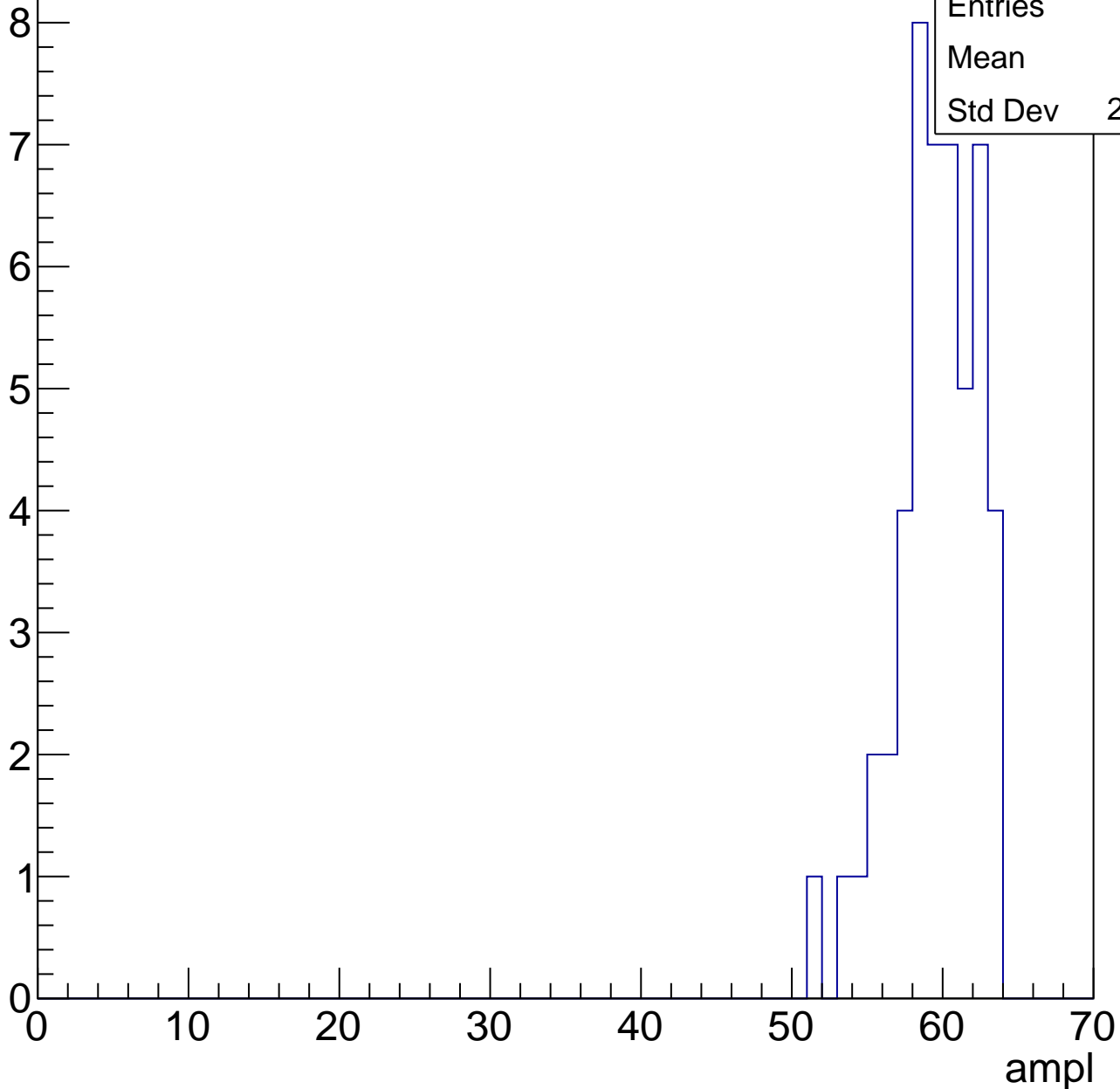


# B1L002S, U26-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	59.1
Std Dev	2.682

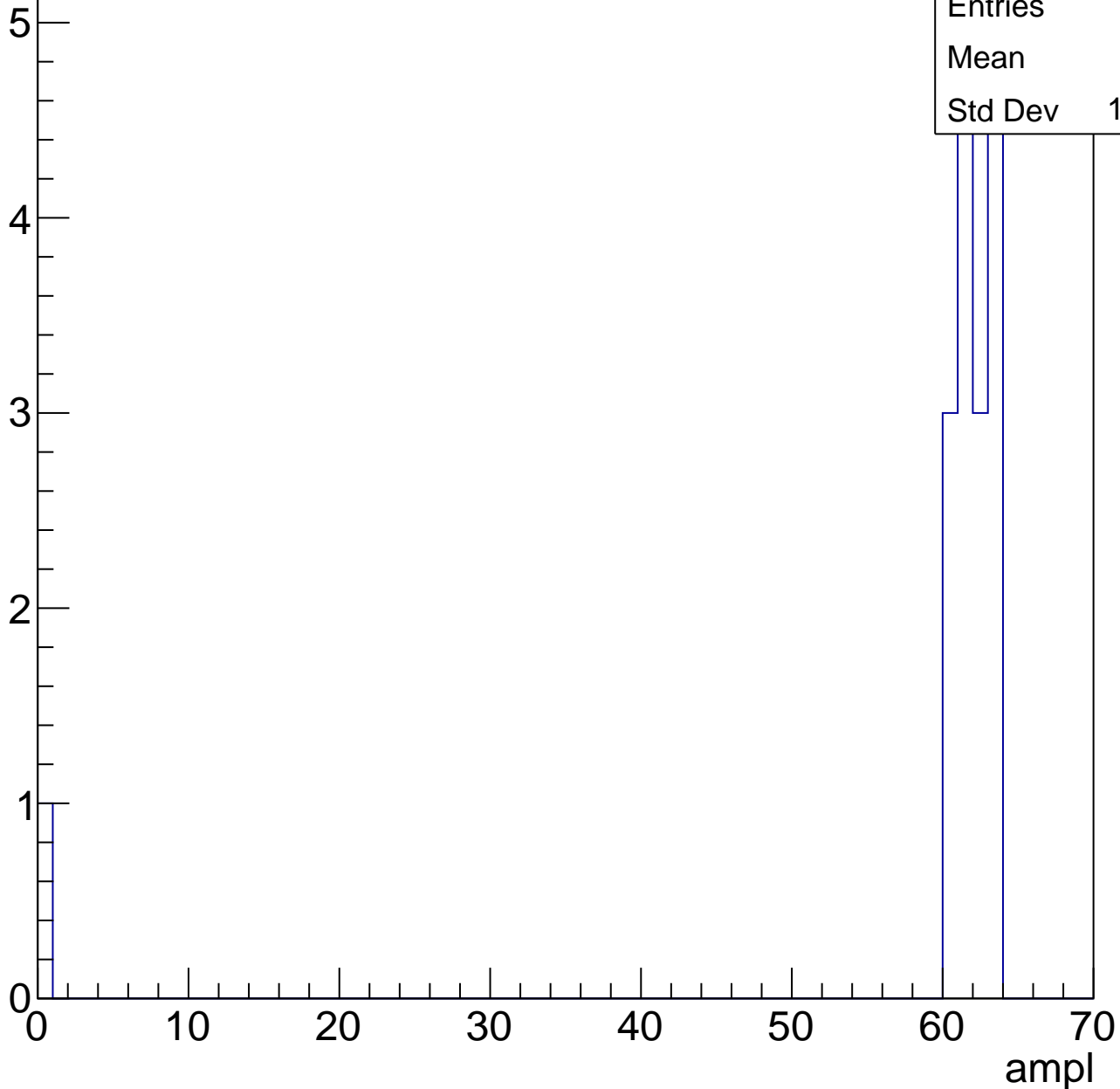


# B1L002S, U26-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58
Std Dev	14.54

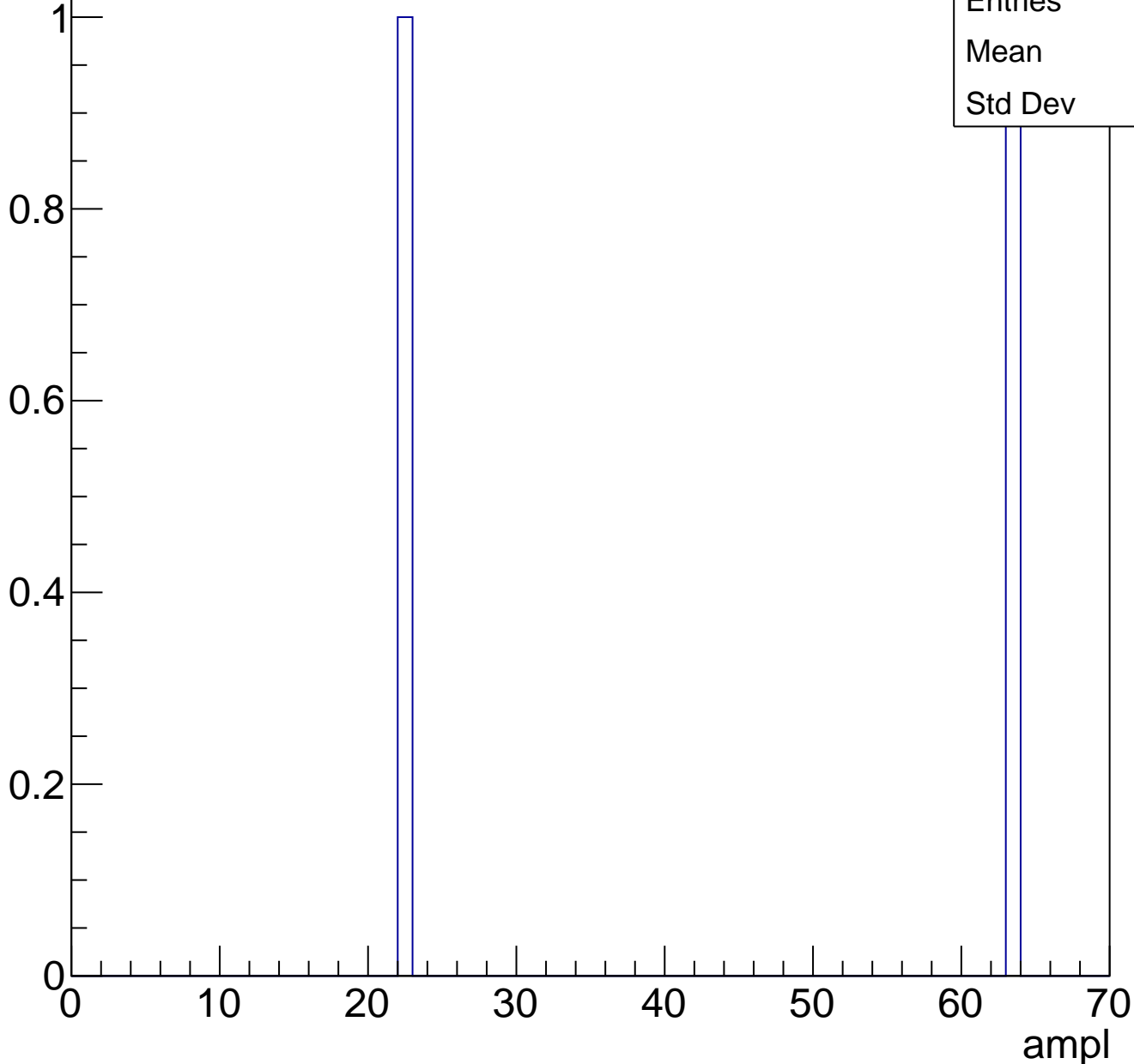




# B1L002S, U26-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch23, adc0

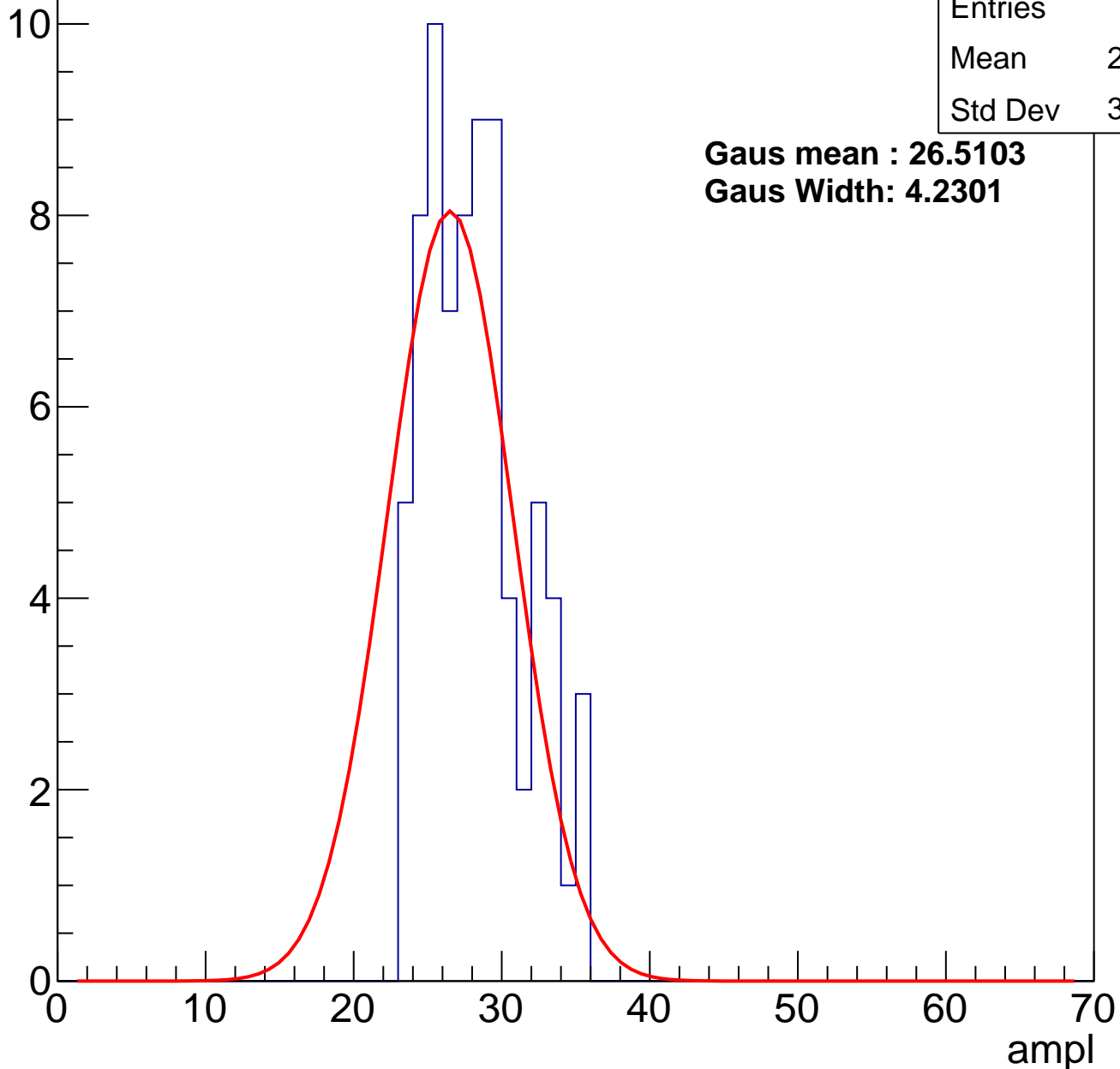
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	27.75
Std Dev	3.225

**Gaus mean : 26.5103**

**Gaus Width: 4.2301**

Entry



# B1L002S, U26-ch23, adc1

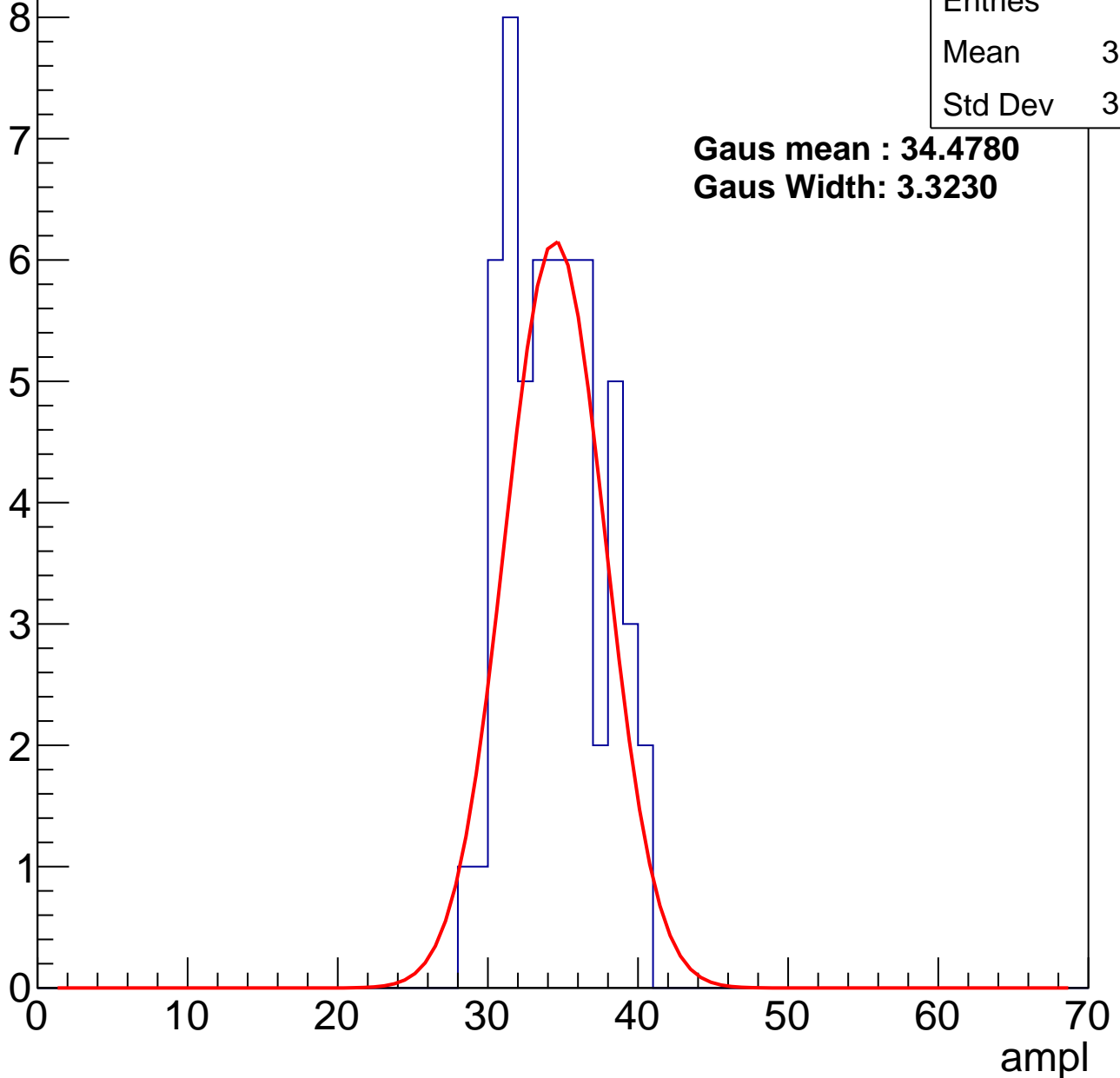
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	33.93
Std Dev	3.048

**Gaus mean : 34.4780**

**Gaus Width: 3.3230**



# B1L002S, U26-ch23, adc2

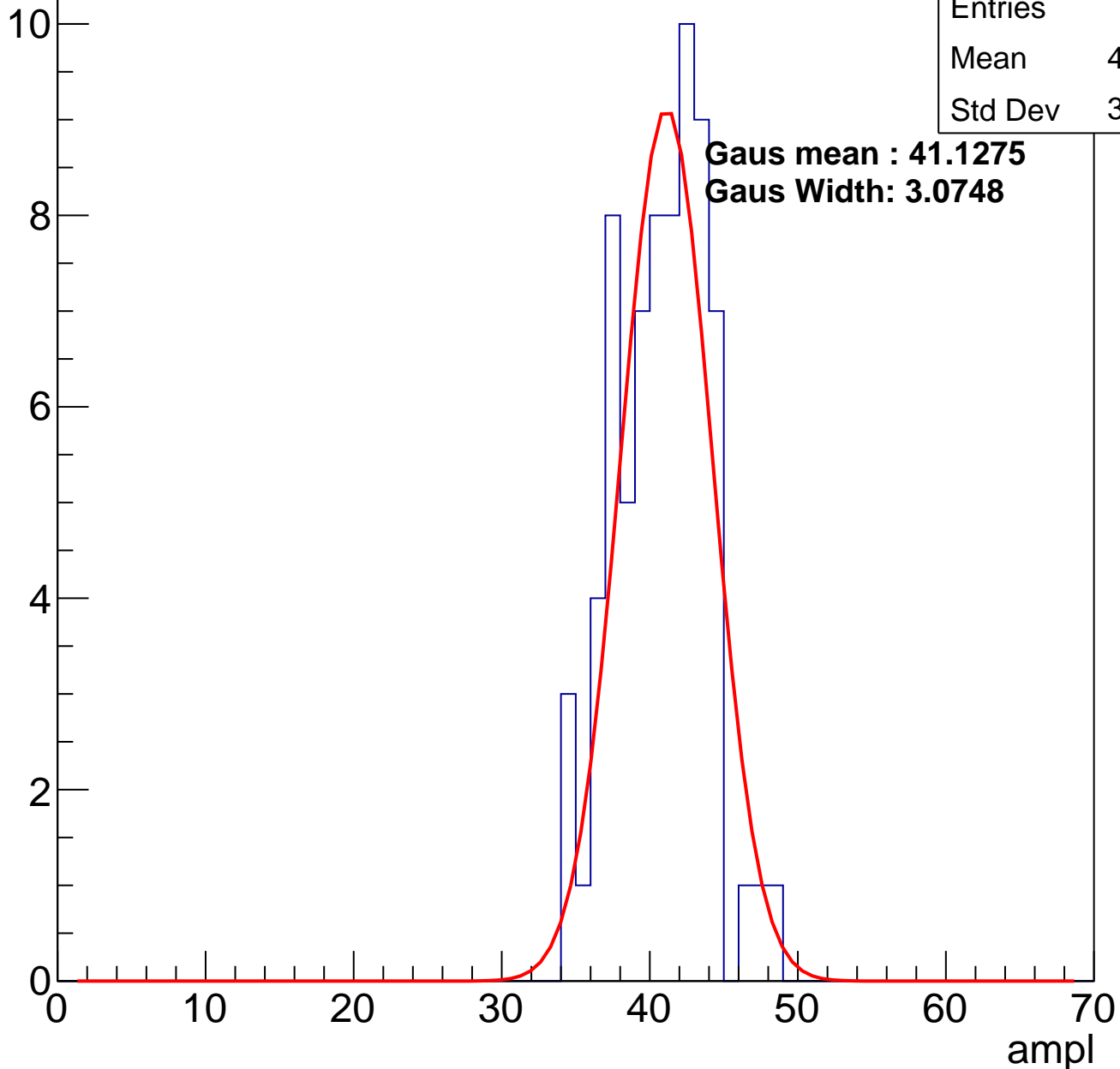
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	40.33
Std Dev	3.048

**Gaus mean : 41.1275**

**Gaus Width: 3.0748**

Entry



# B1L002S, U26-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	48.11
Std Dev	3.239

Entry

10

8

6

4

2

0

0

10

20

30

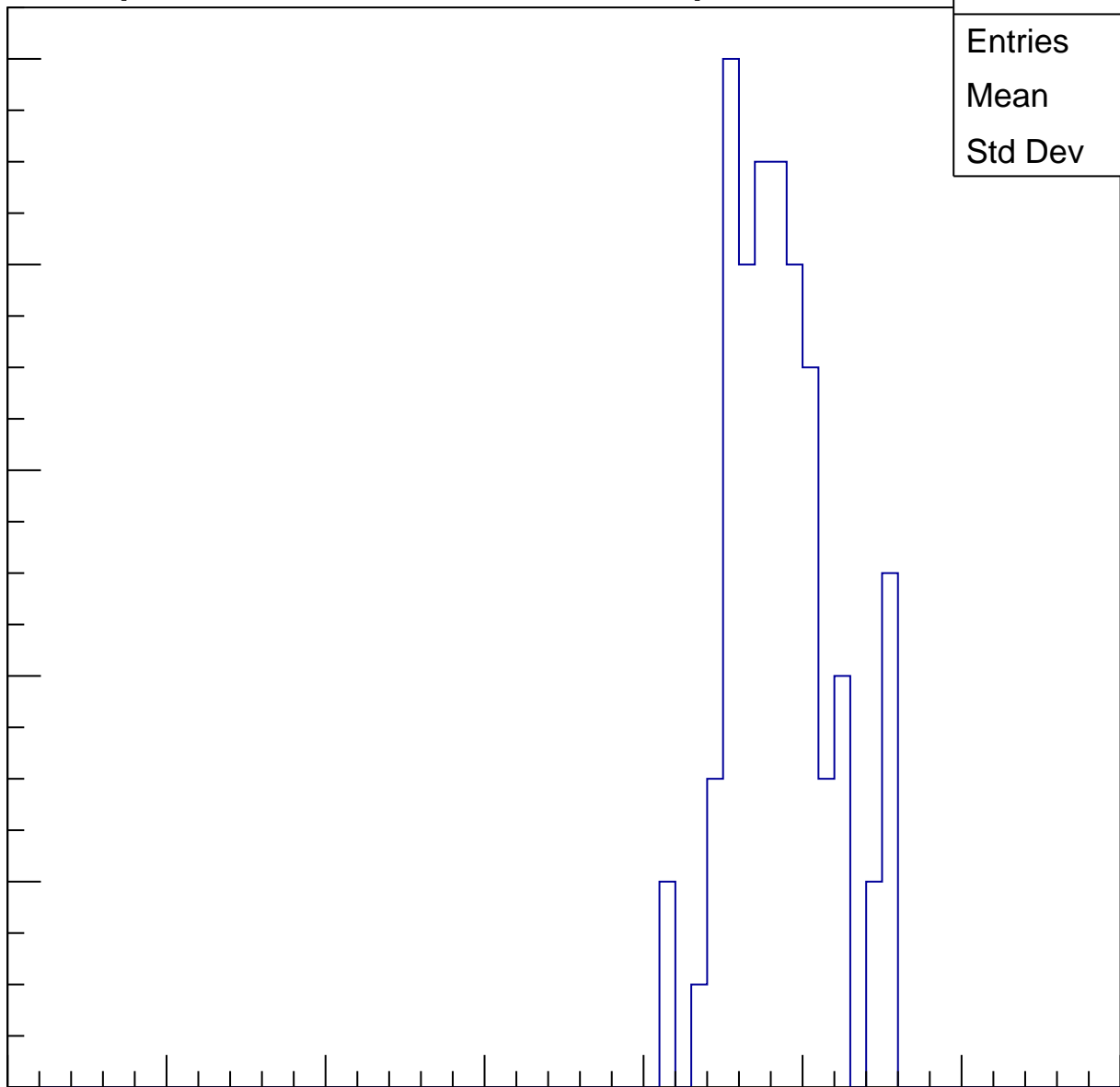
40

50

60

70

ampl

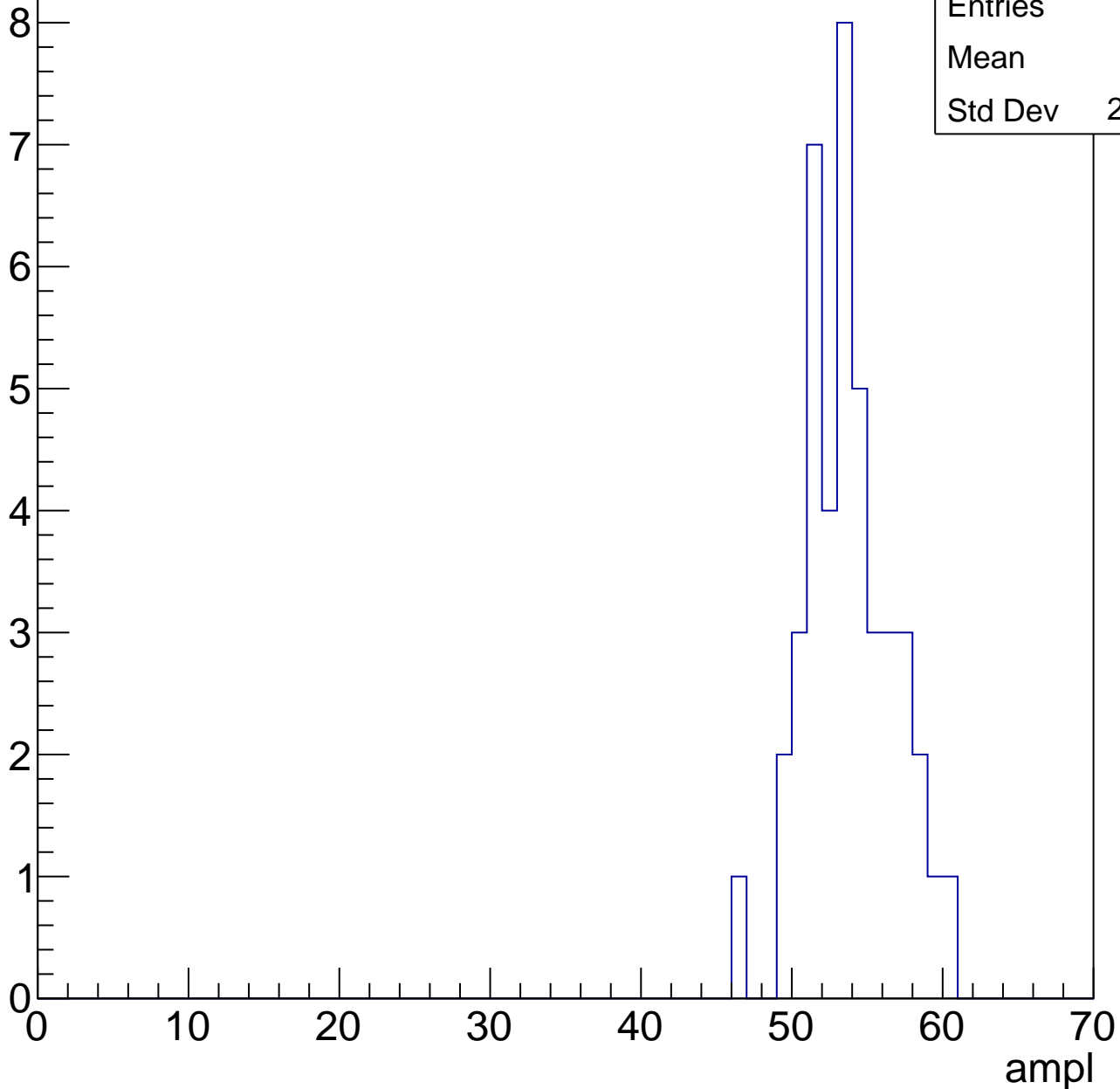


# B1L002S, U26-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	53.3
Std Dev	2.906



# B1L002S, U26-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

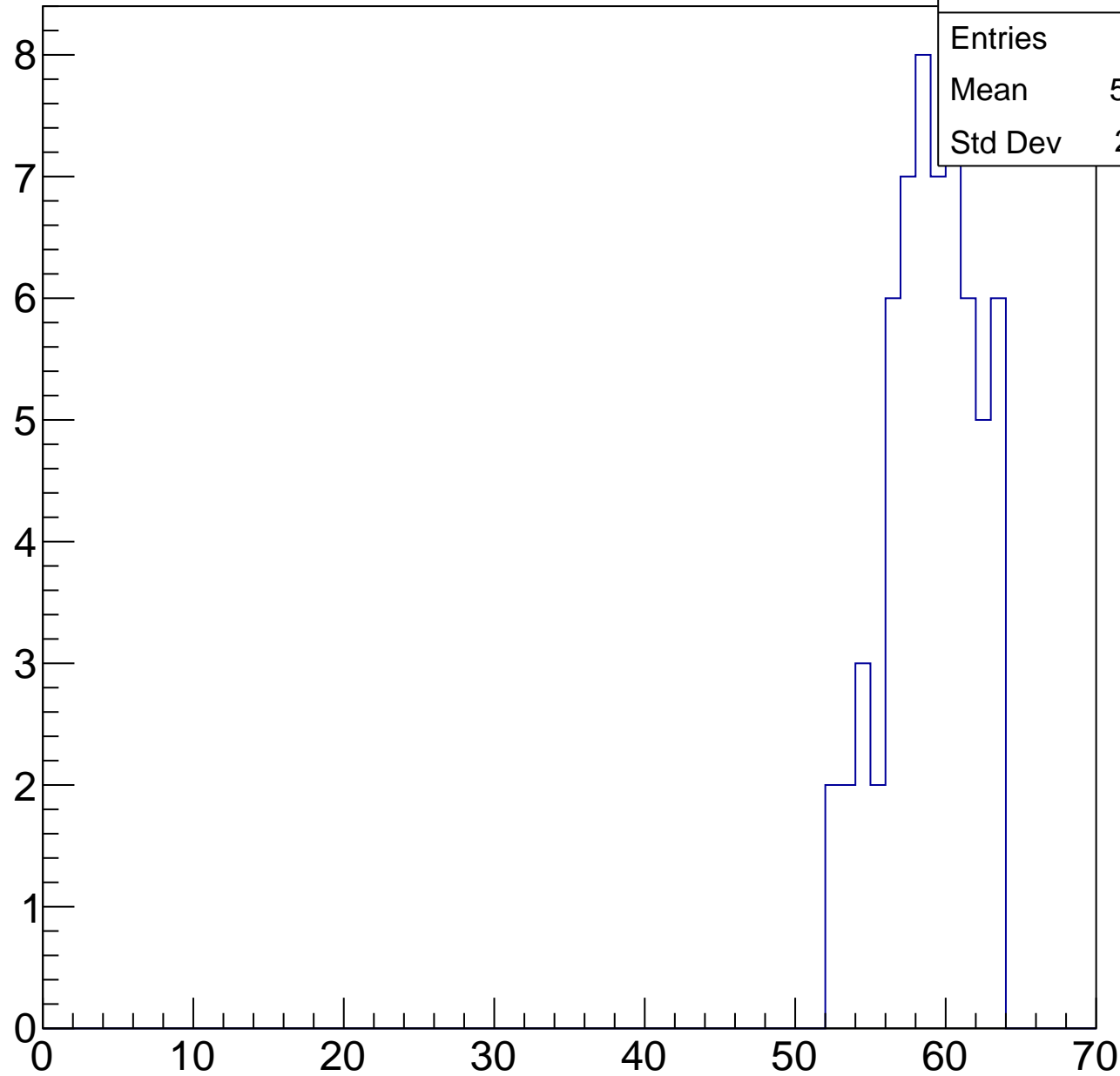
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	62
Mean	58.52
Std Dev	2.911

ampl

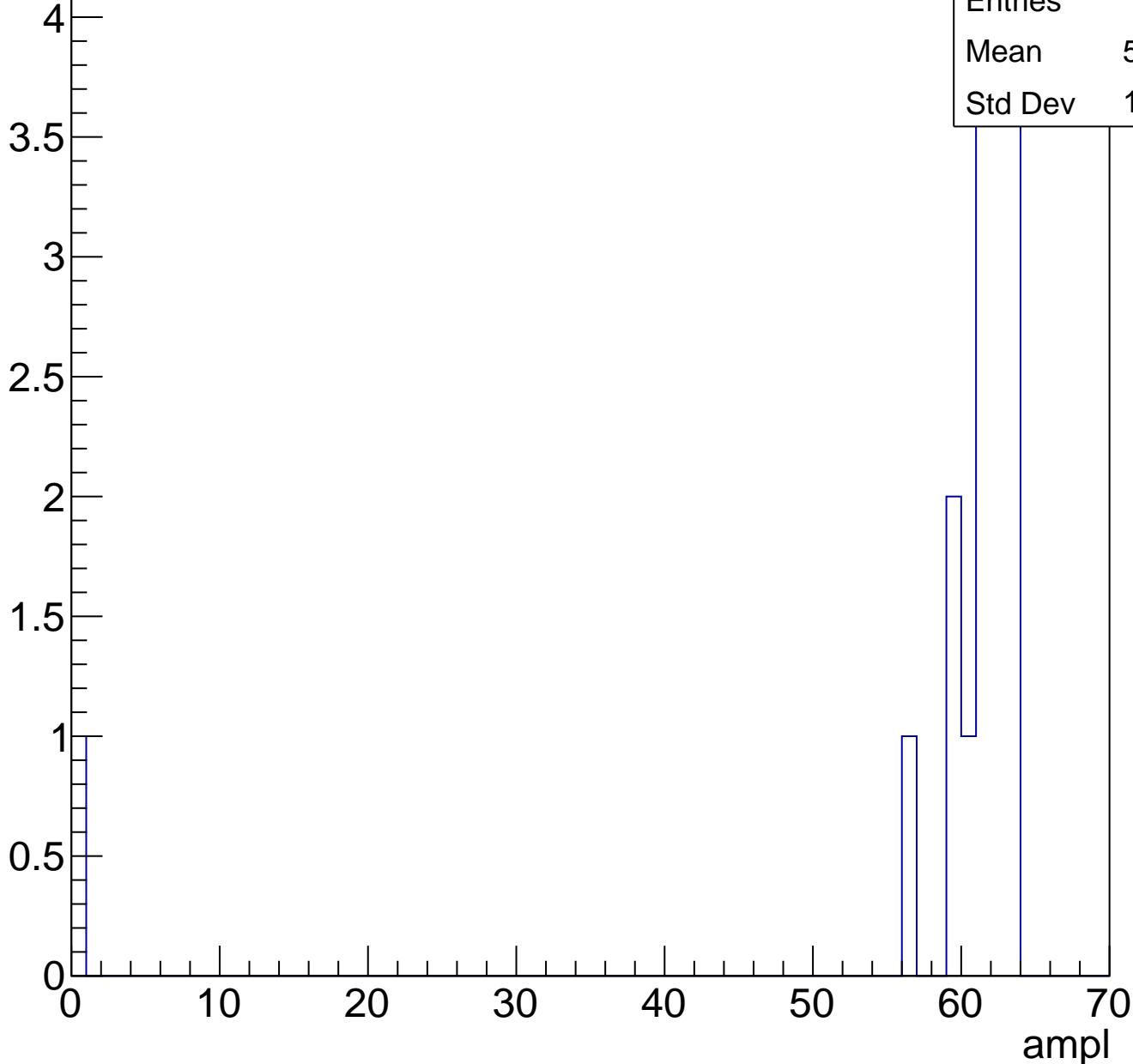
0 10 20 30 40 50 60 70



# B1L002S, U26-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

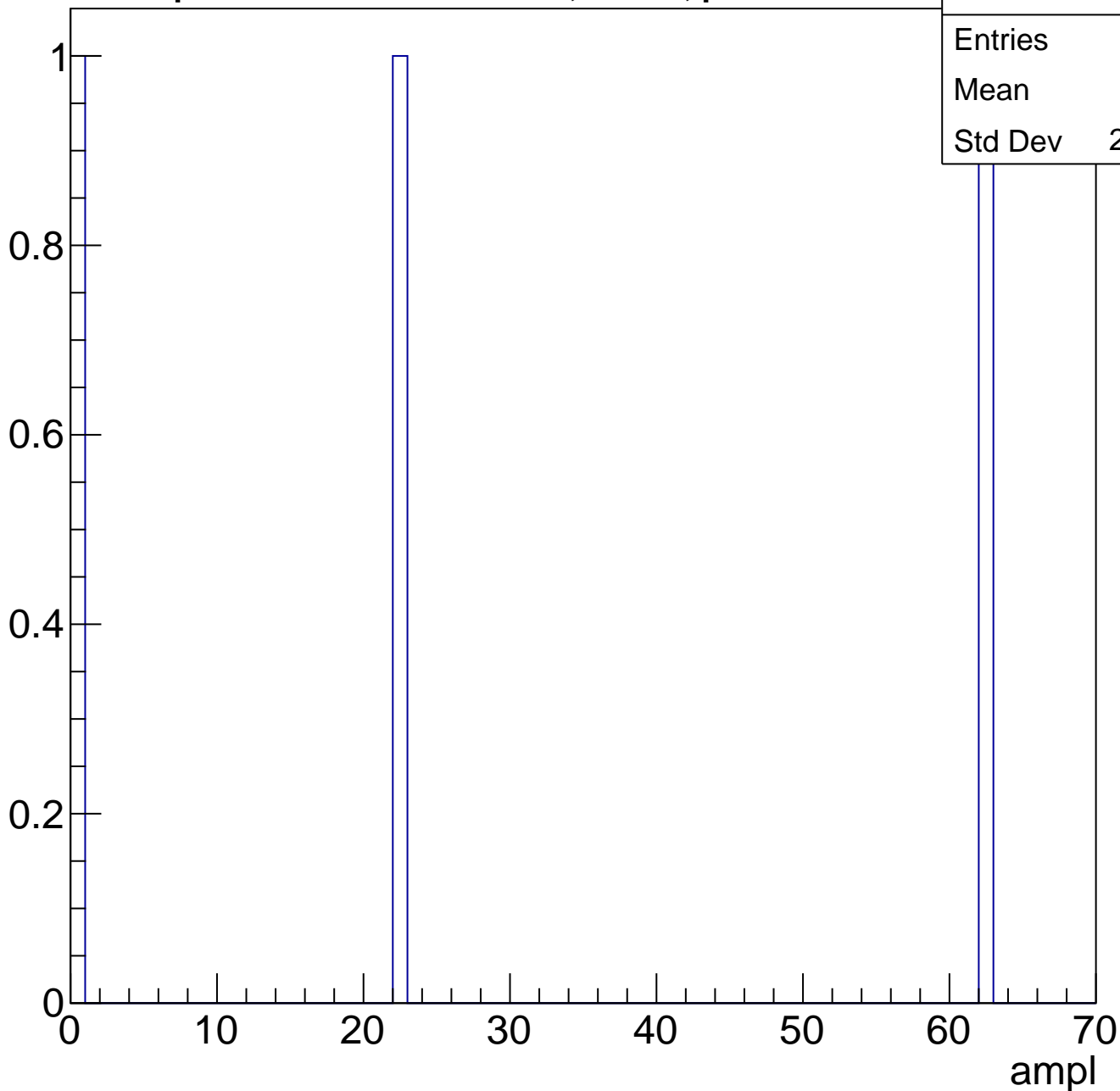




# B1L002S, U26-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	25.66

# B1L002S, U26-ch24, adc0

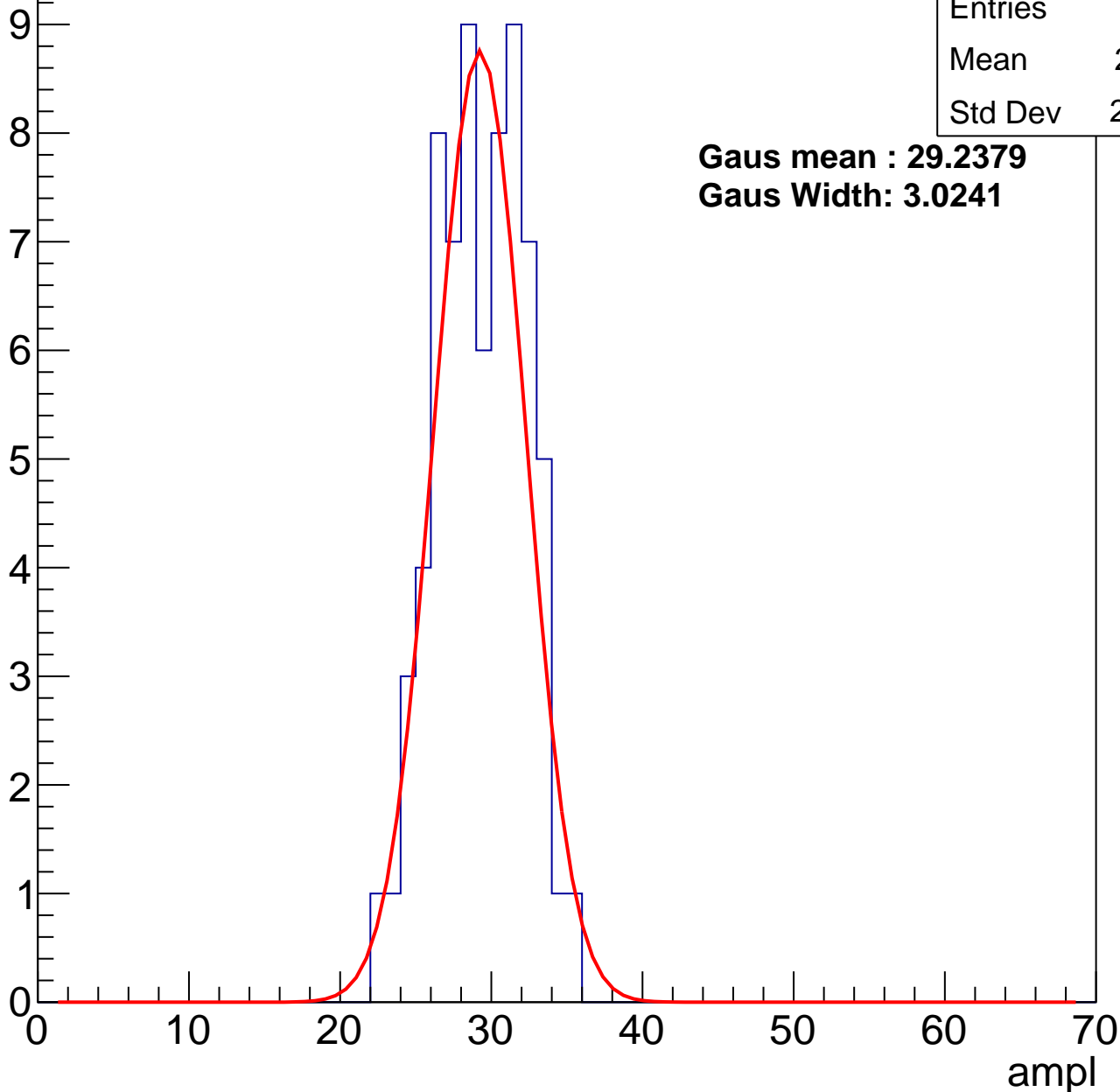
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.81
Std Dev	2.875

**Gaus mean : 29.2379**

**Gaus Width: 3.0241**



# B1L002S, U26-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	36.42
Std Dev	3.005

**Gaus mean : 37.2842**

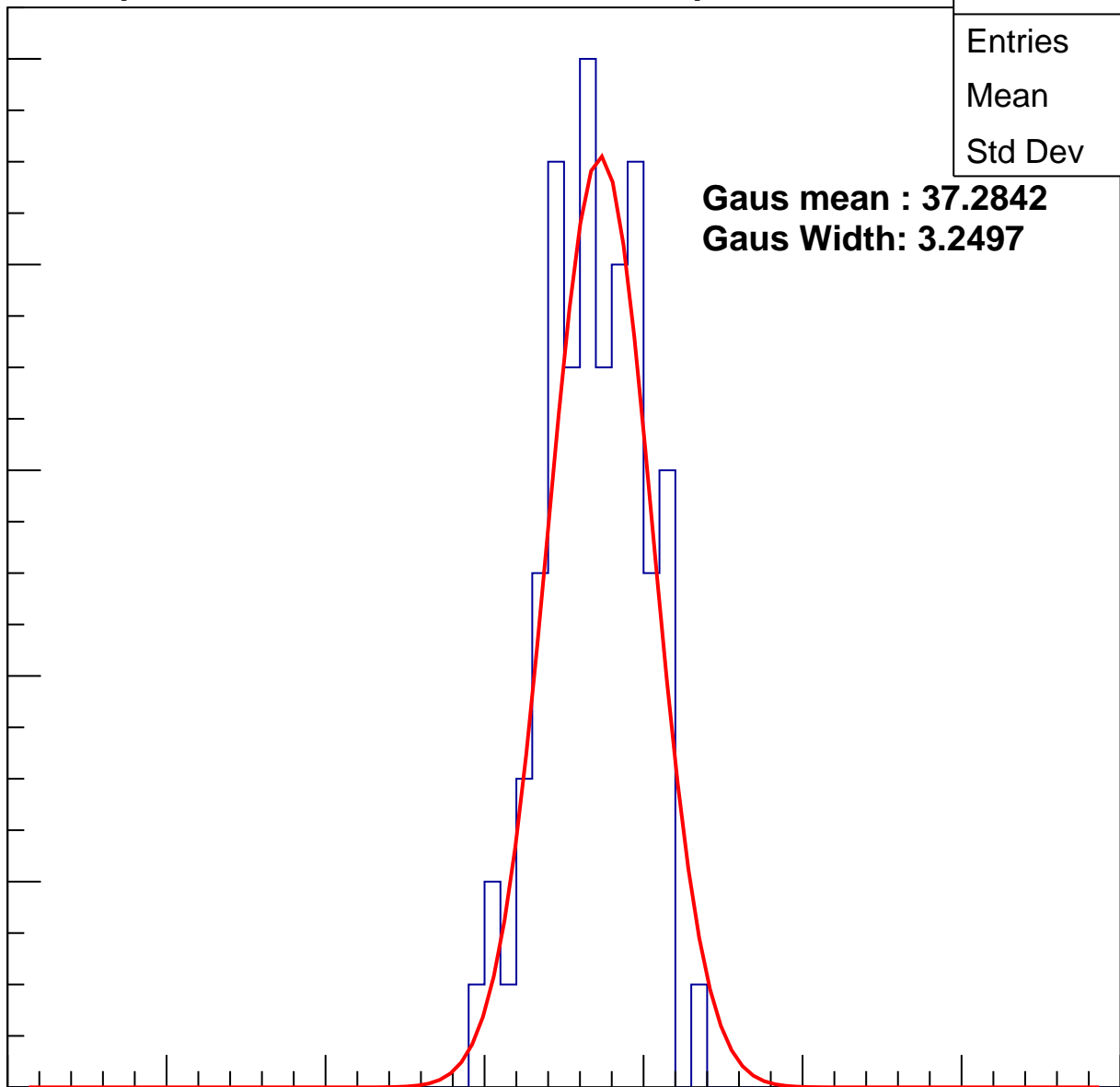
**Gaus Width: 3.2497**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U26-ch24, adc2

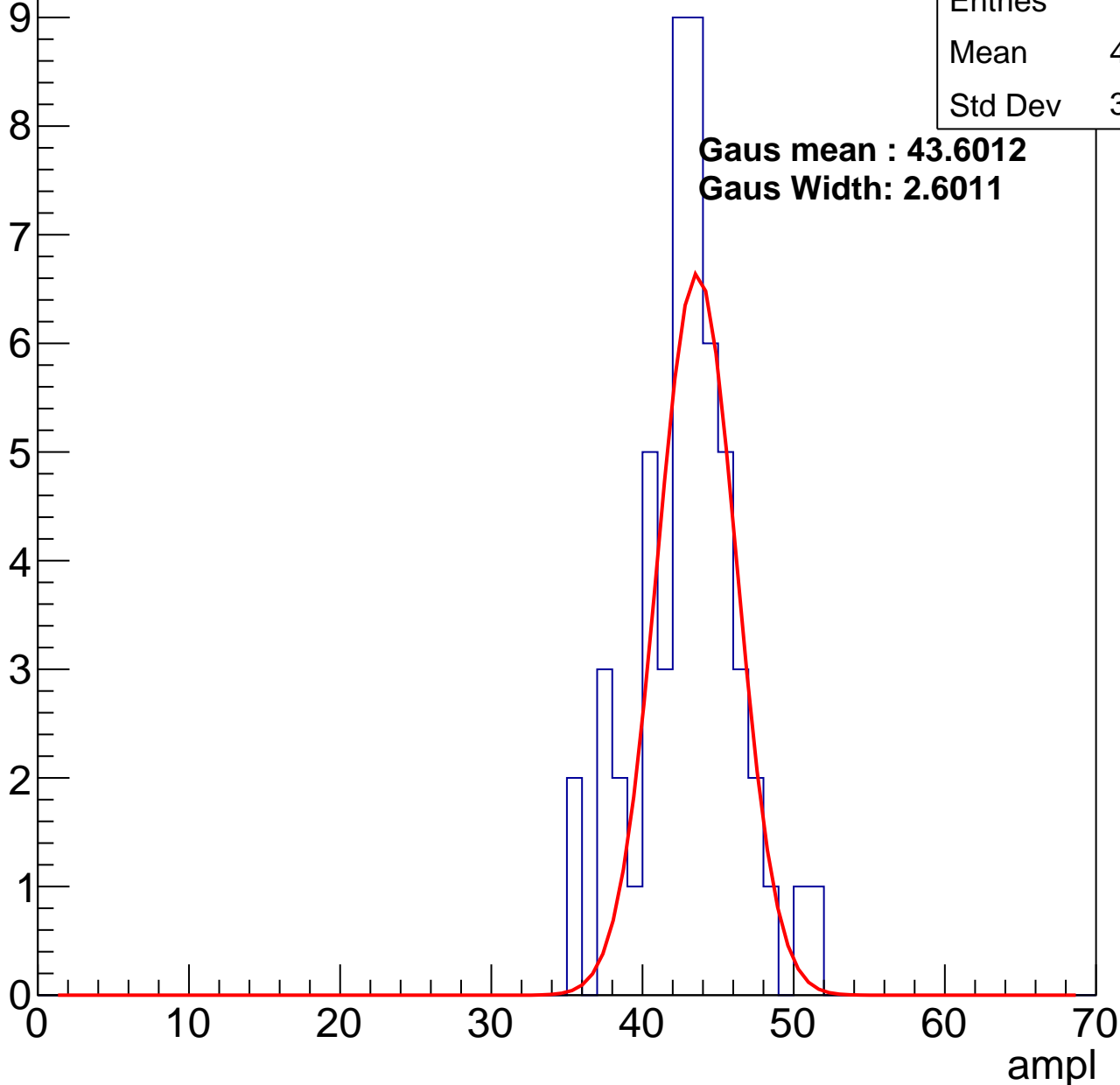
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	42.53
Std Dev	3.306

**Gaus mean : 43.6012**

**Gaus Width: 2.6011**

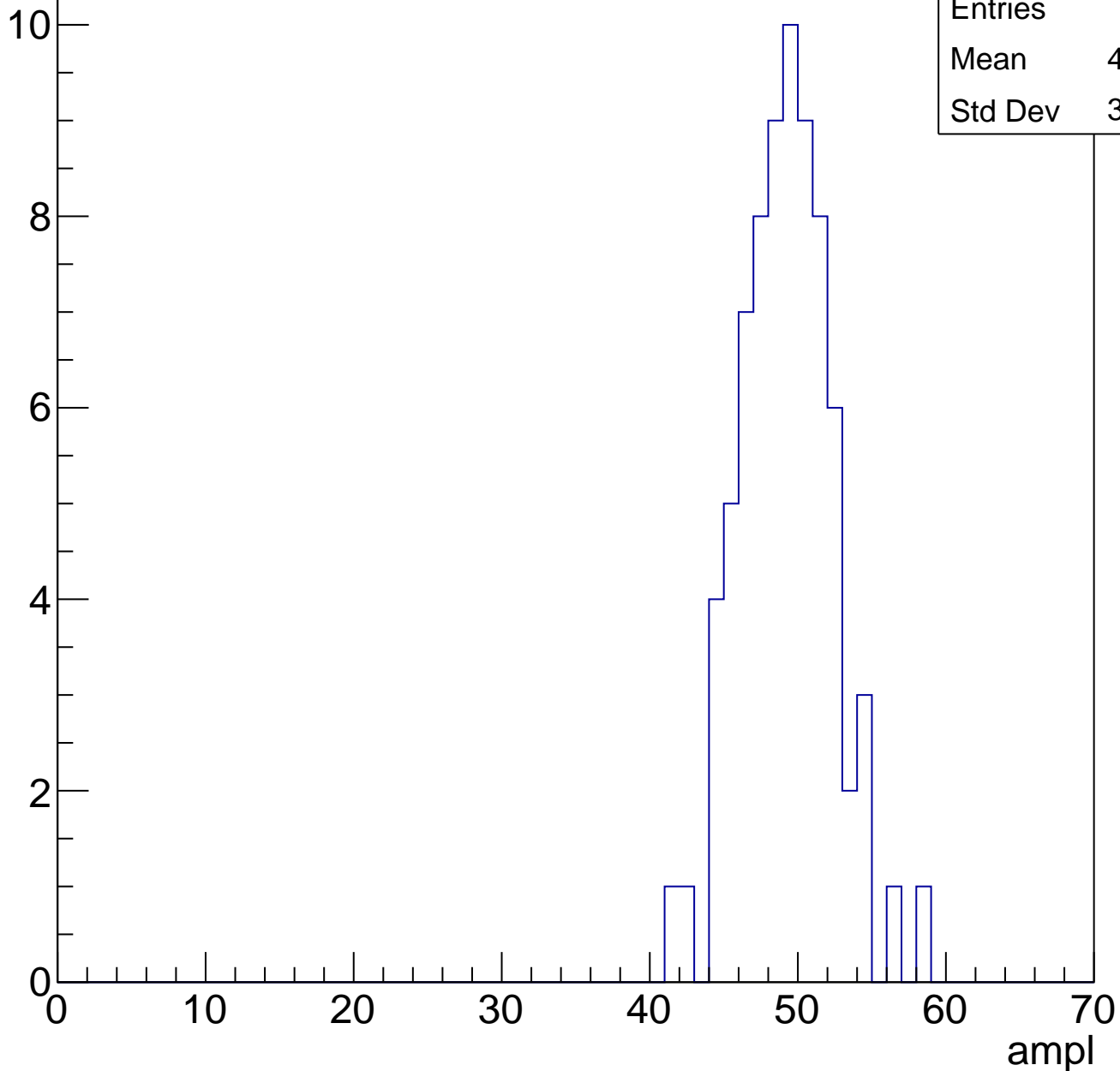


# B1L002S, U26-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	48.75
Std Dev	3.107

Entry

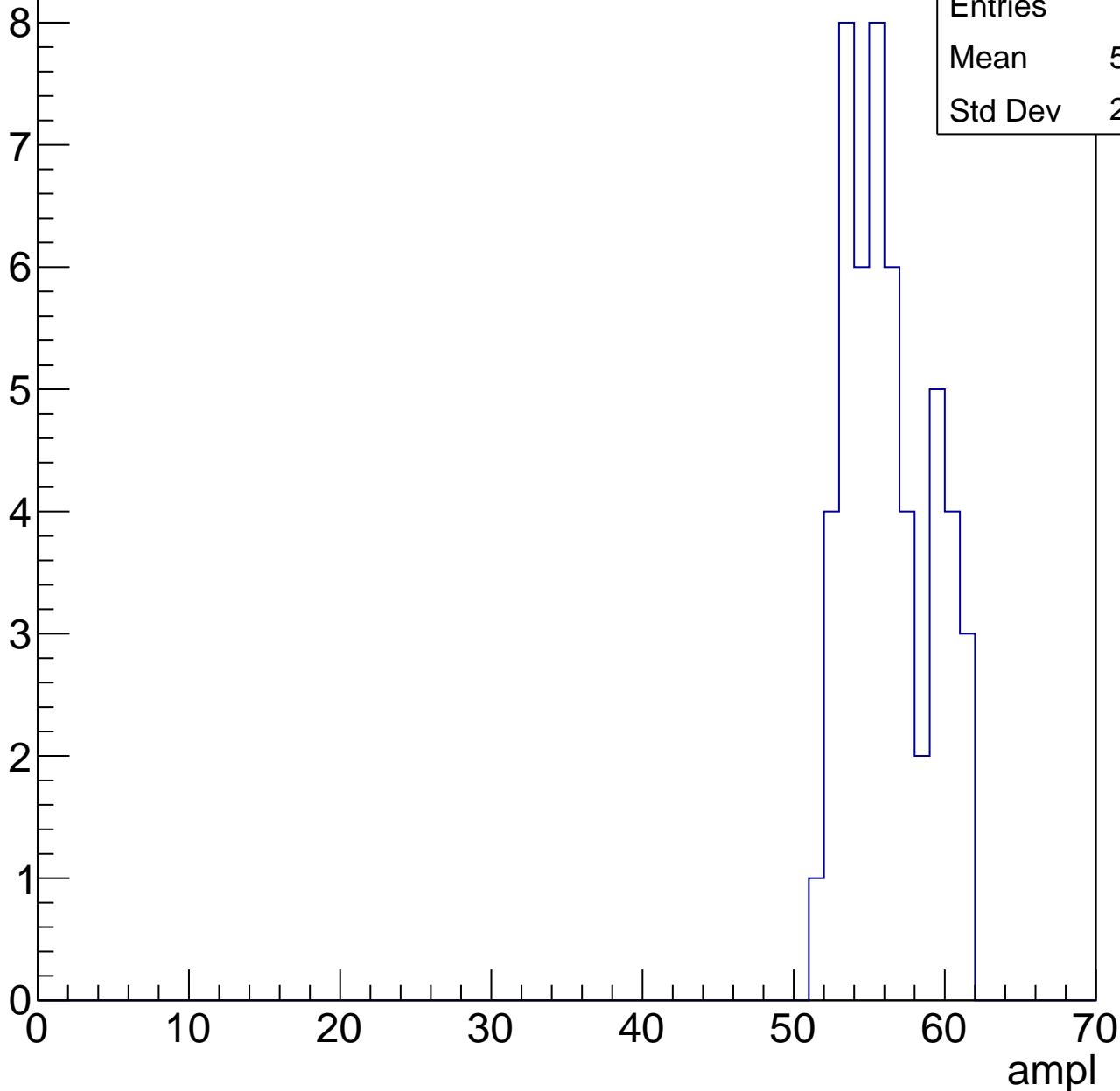


# B1L002S, U26-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	55.78
Std Dev	2.753

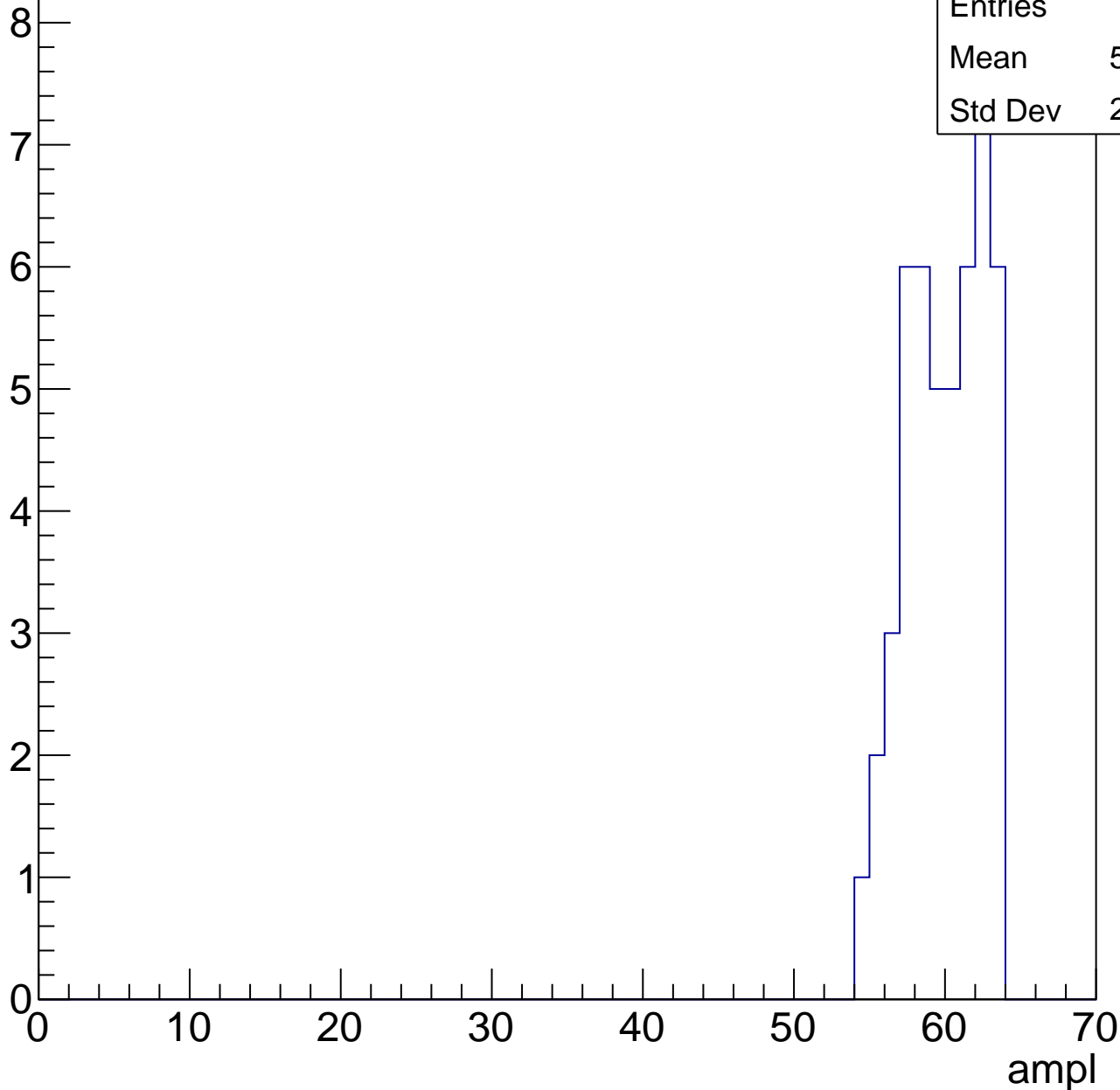


# B1L002S, U26-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

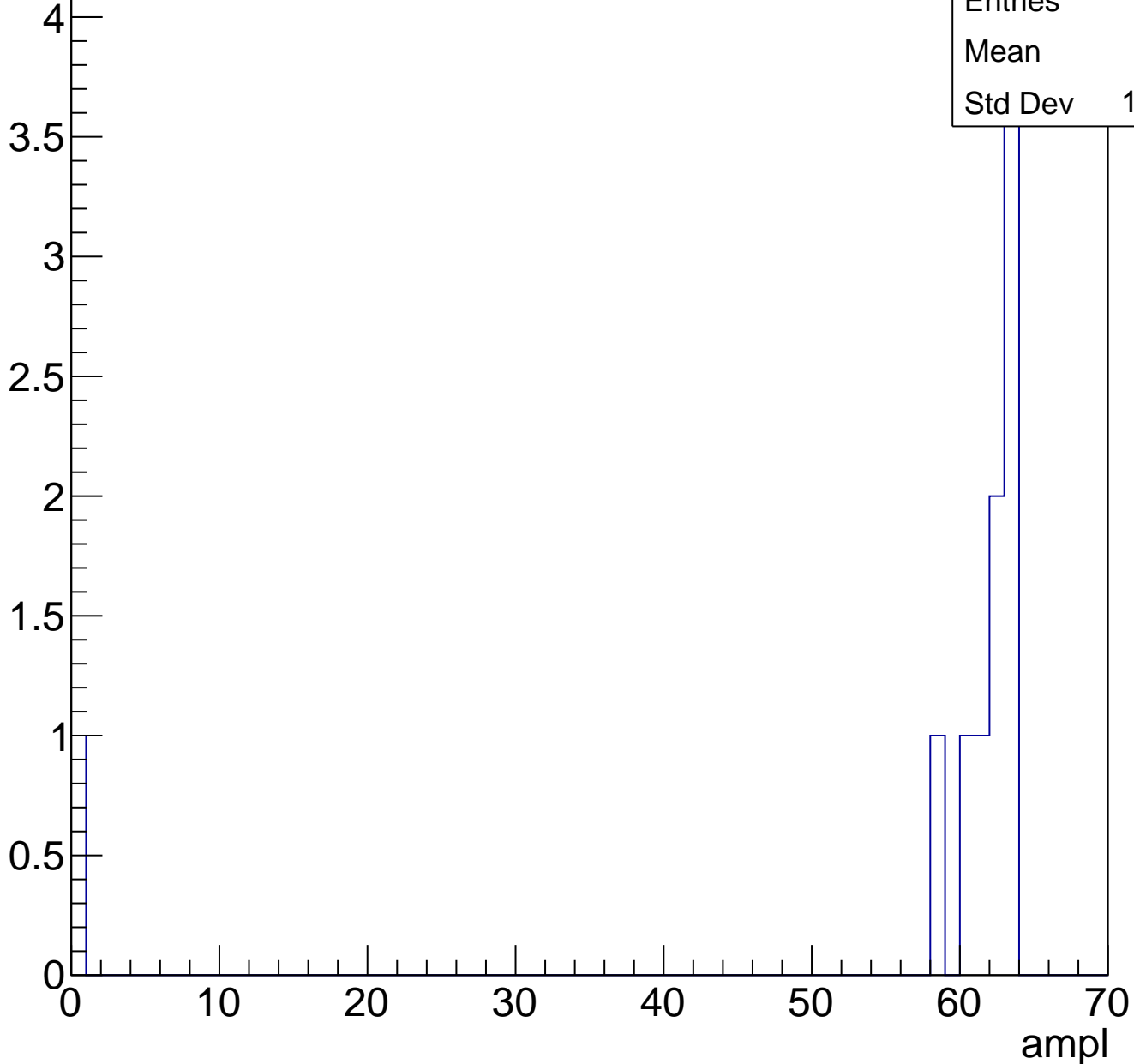
Entries	48
Mean	59.52
Std Dev	2.492



# B1L002S, U26-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U26-ch25, adc0

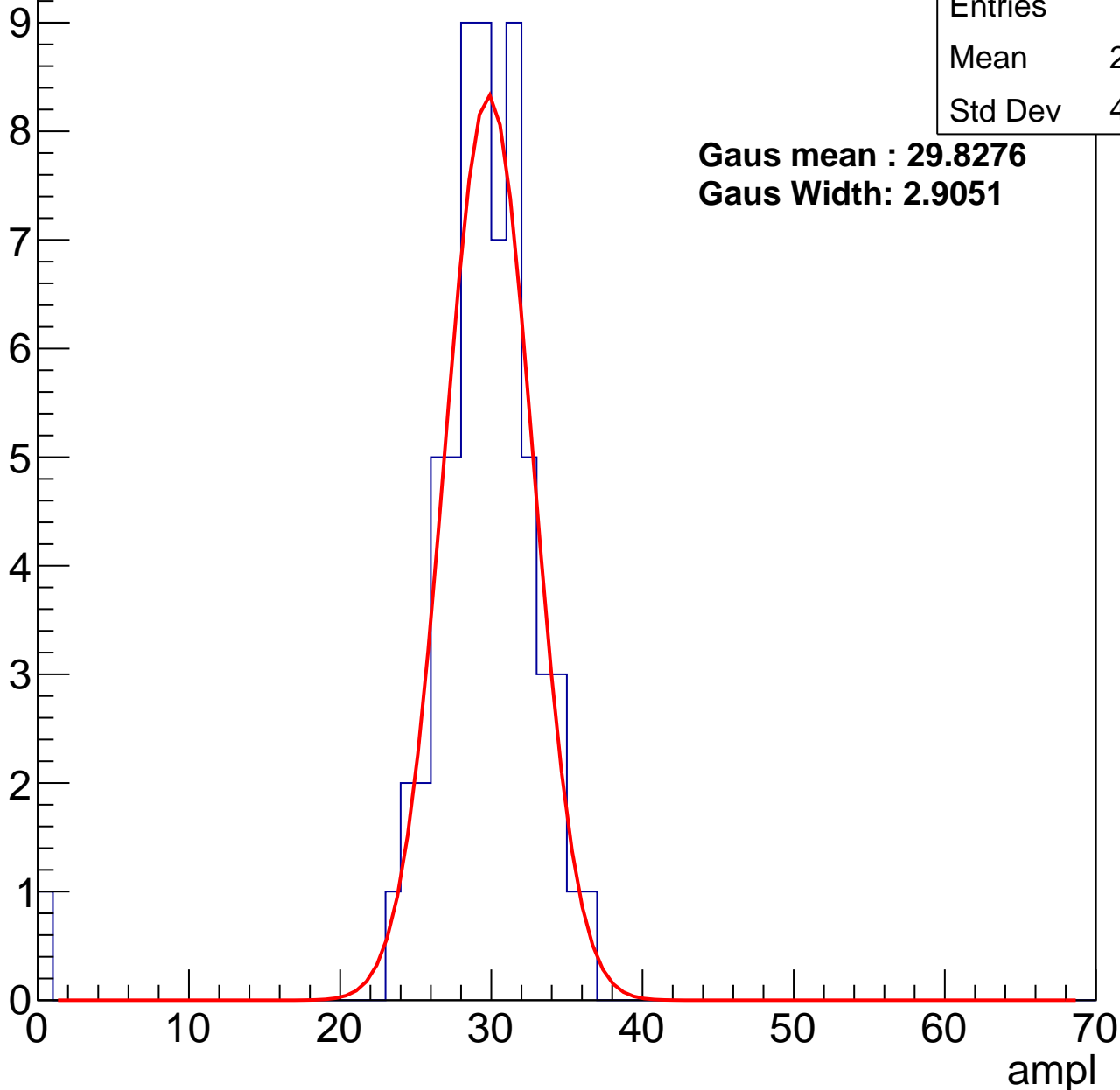
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.89
Std Dev	4.592

**Gaus mean : 29.8276**

**Gaus Width: 2.9051**



# B1L002S, U26-ch25, adc1

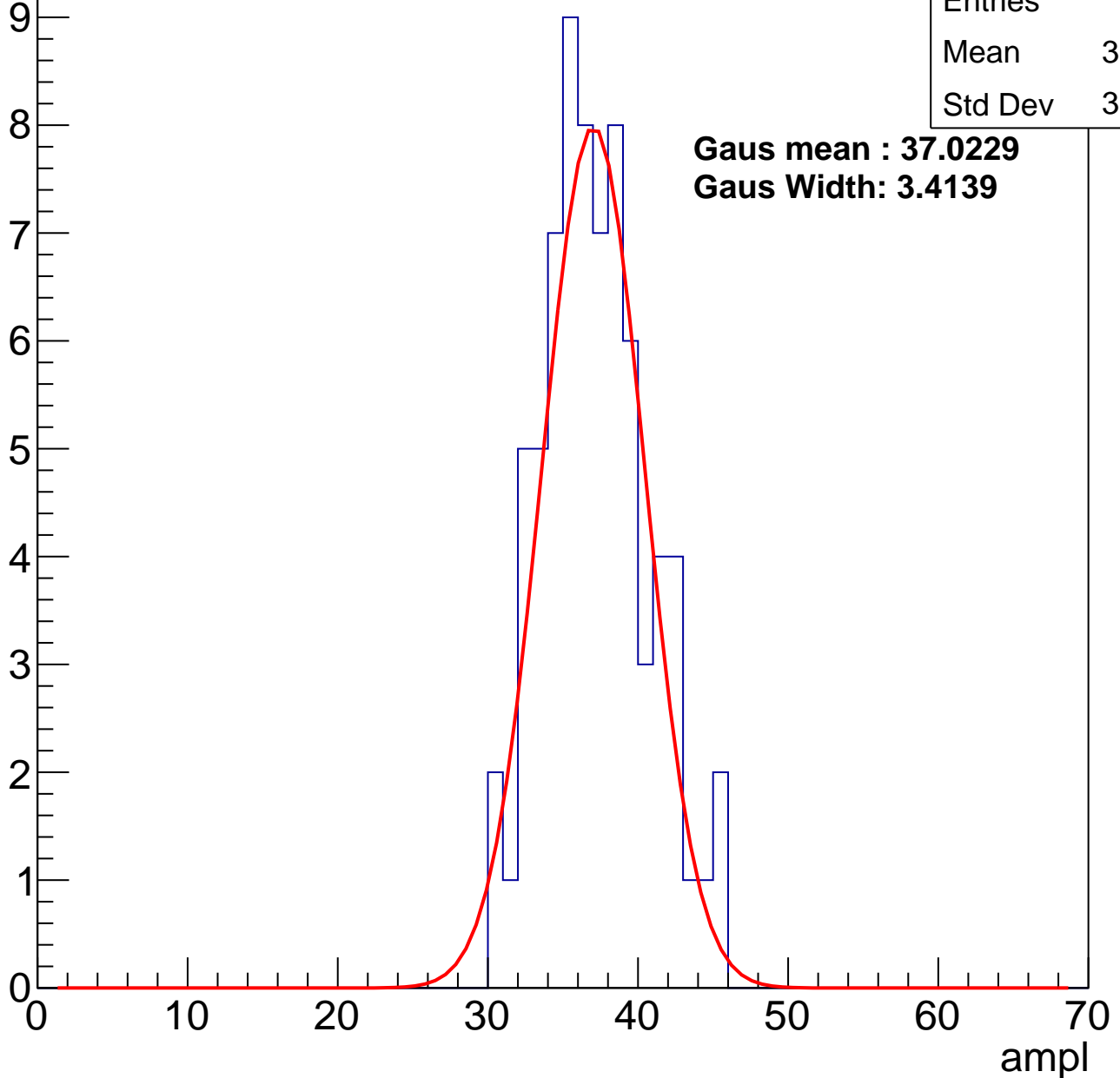
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.75
Std Dev	3.467

**Gaus mean : 37.0229**

**Gaus Width: 3.4139**



# B1L002S, U26-ch25, adc2

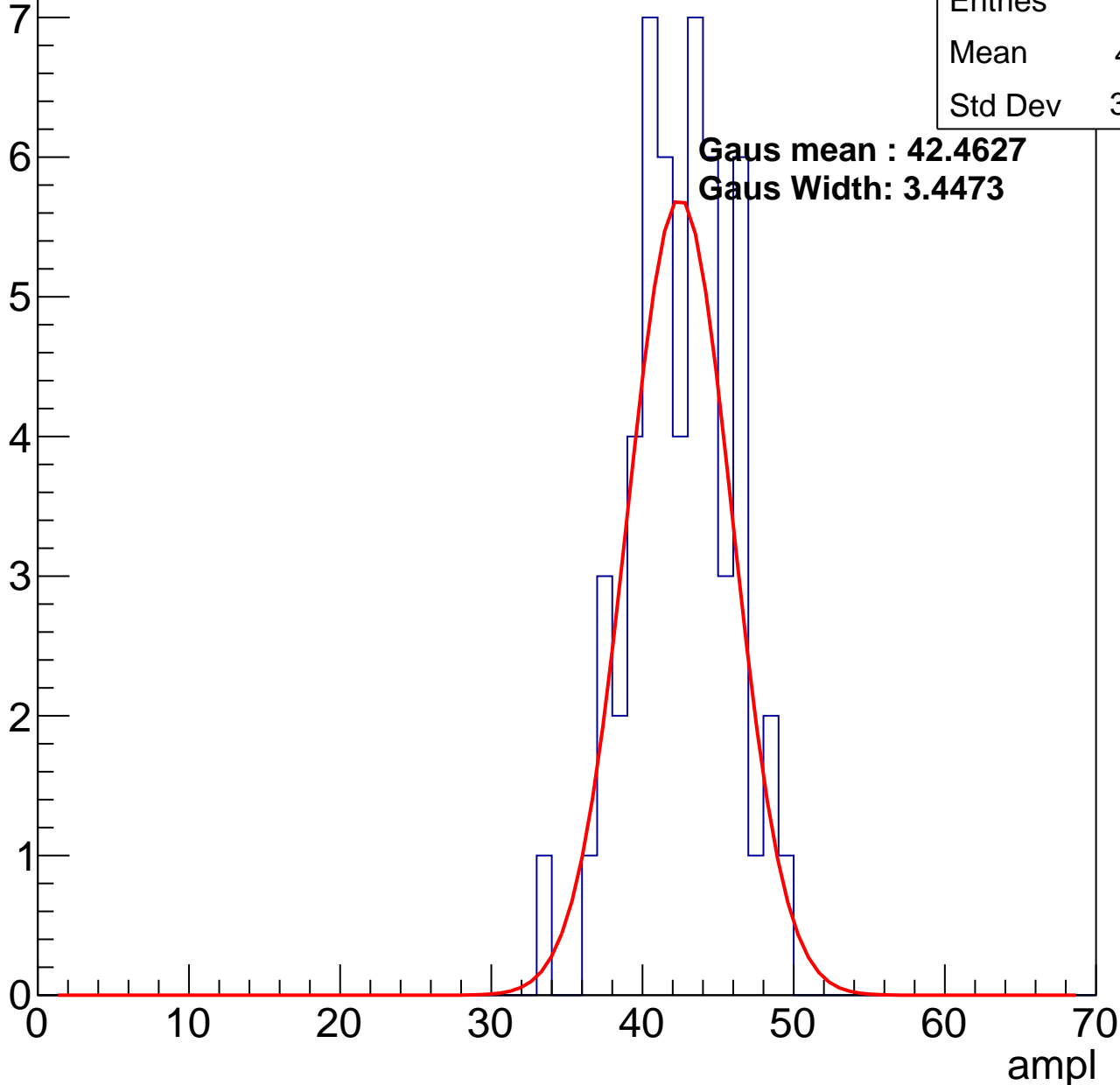
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	42.11
Std Dev	3.315

**Gaus mean : 42.4627**

**Gaus Width: 3.4473**

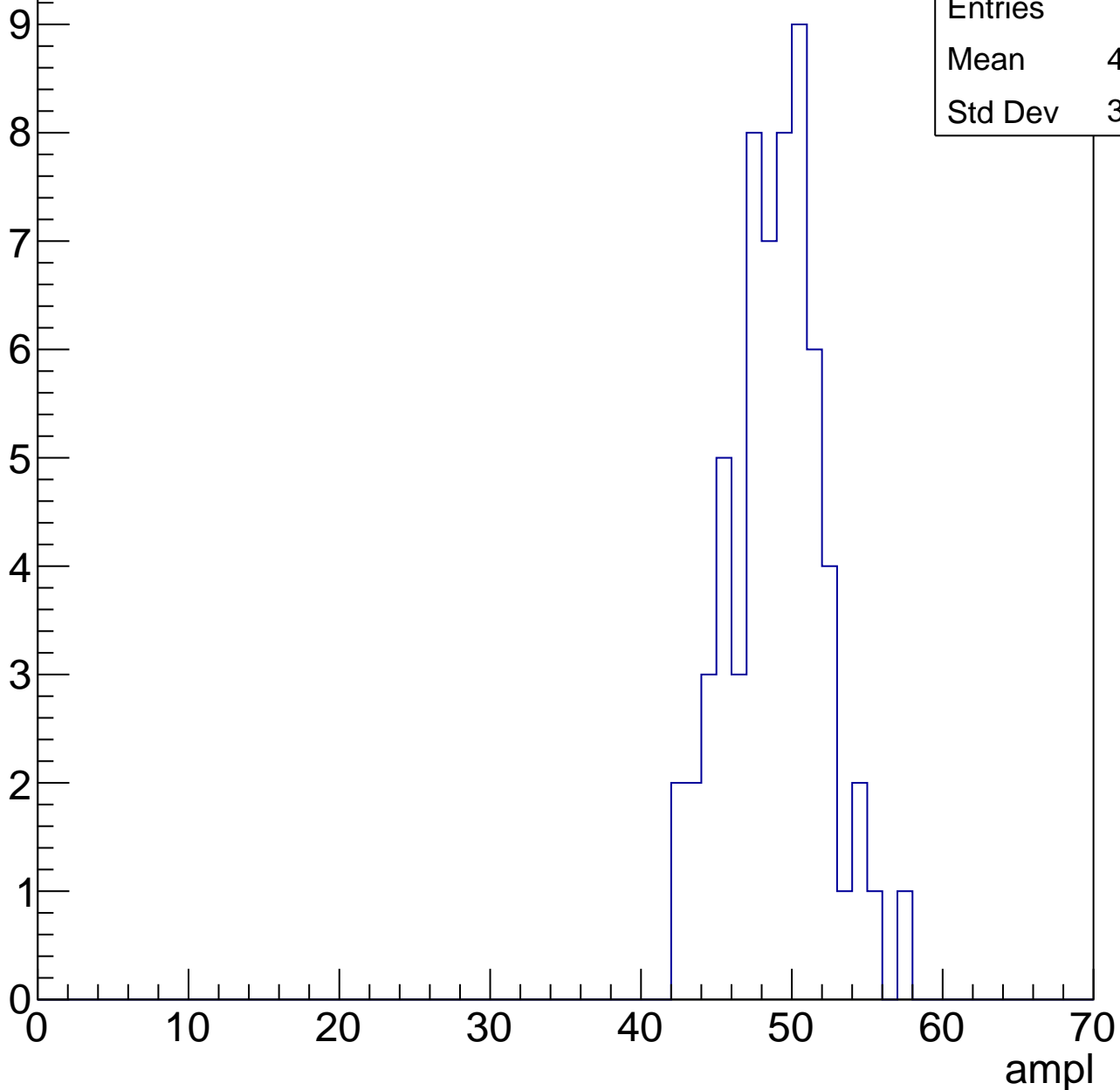


# B1L002S, U26-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

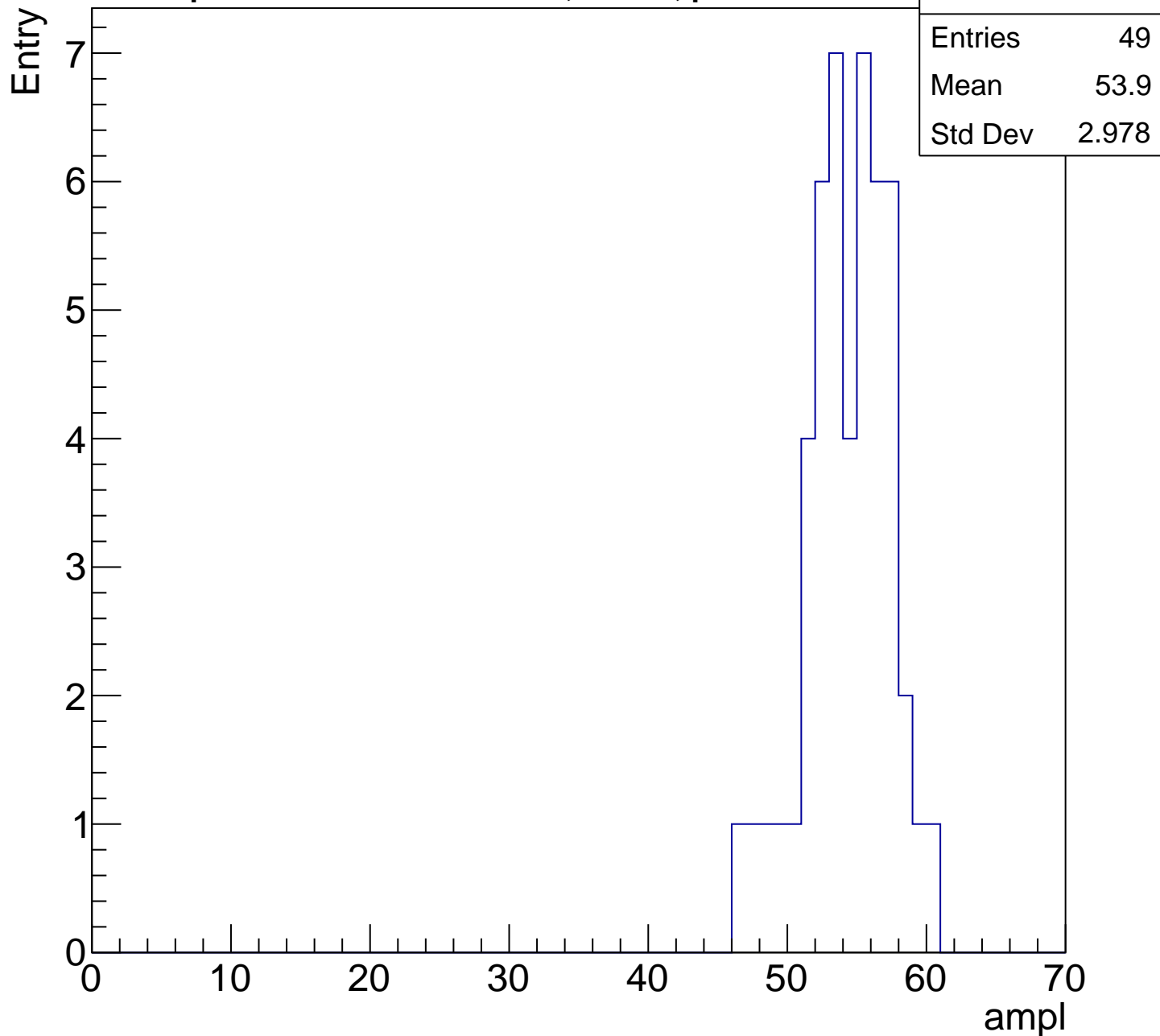
Entry

Entries	62
Mean	48.48
Std Dev	3.135



# B1L002S, U26-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch25, adc5

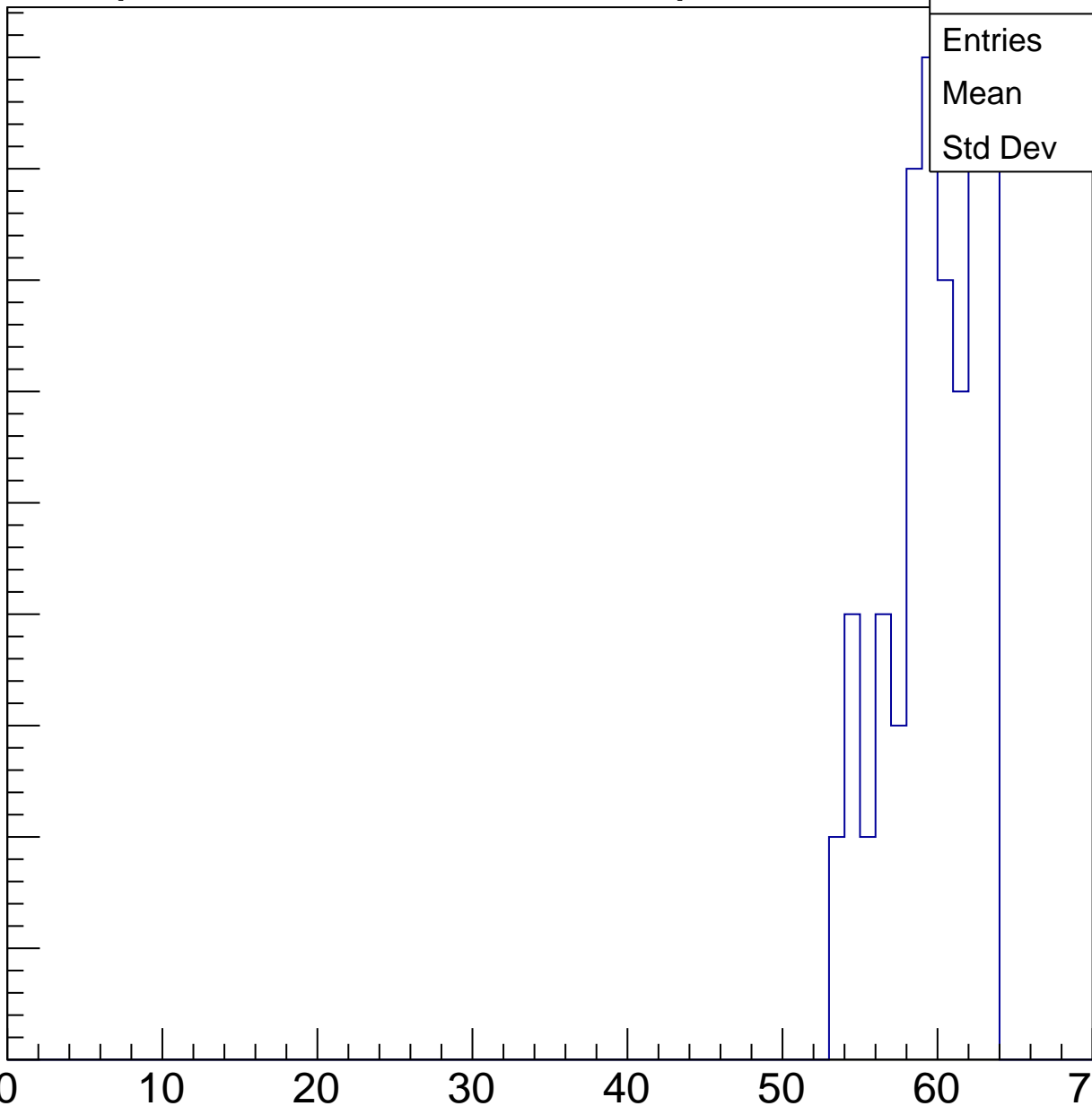
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	59.15
Std Dev	2.833

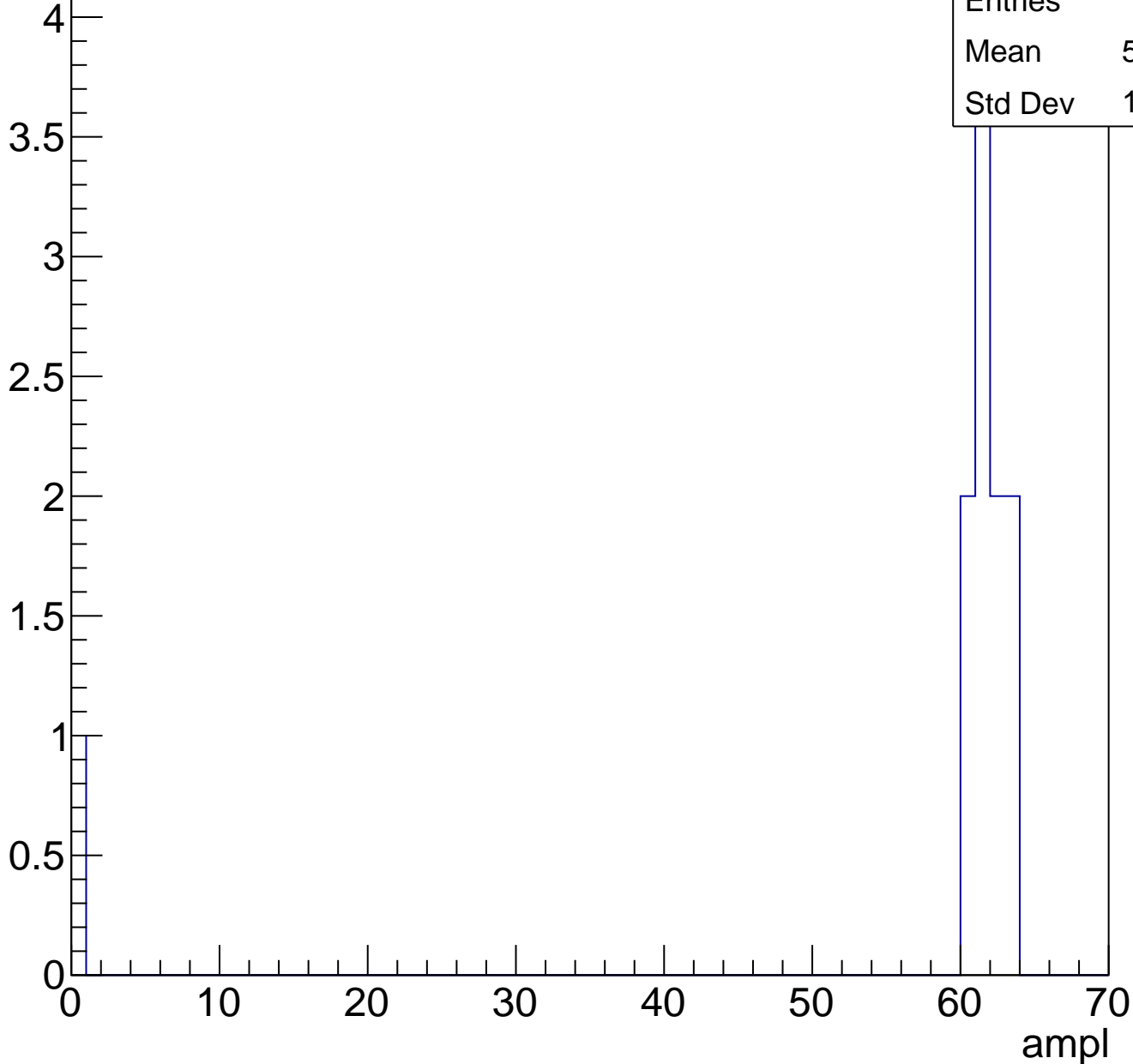
ampl



# B1L002S, U26-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	11
Mean	55.82
Std Dev	17.68



# B1L002S, U26-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch26, adc0

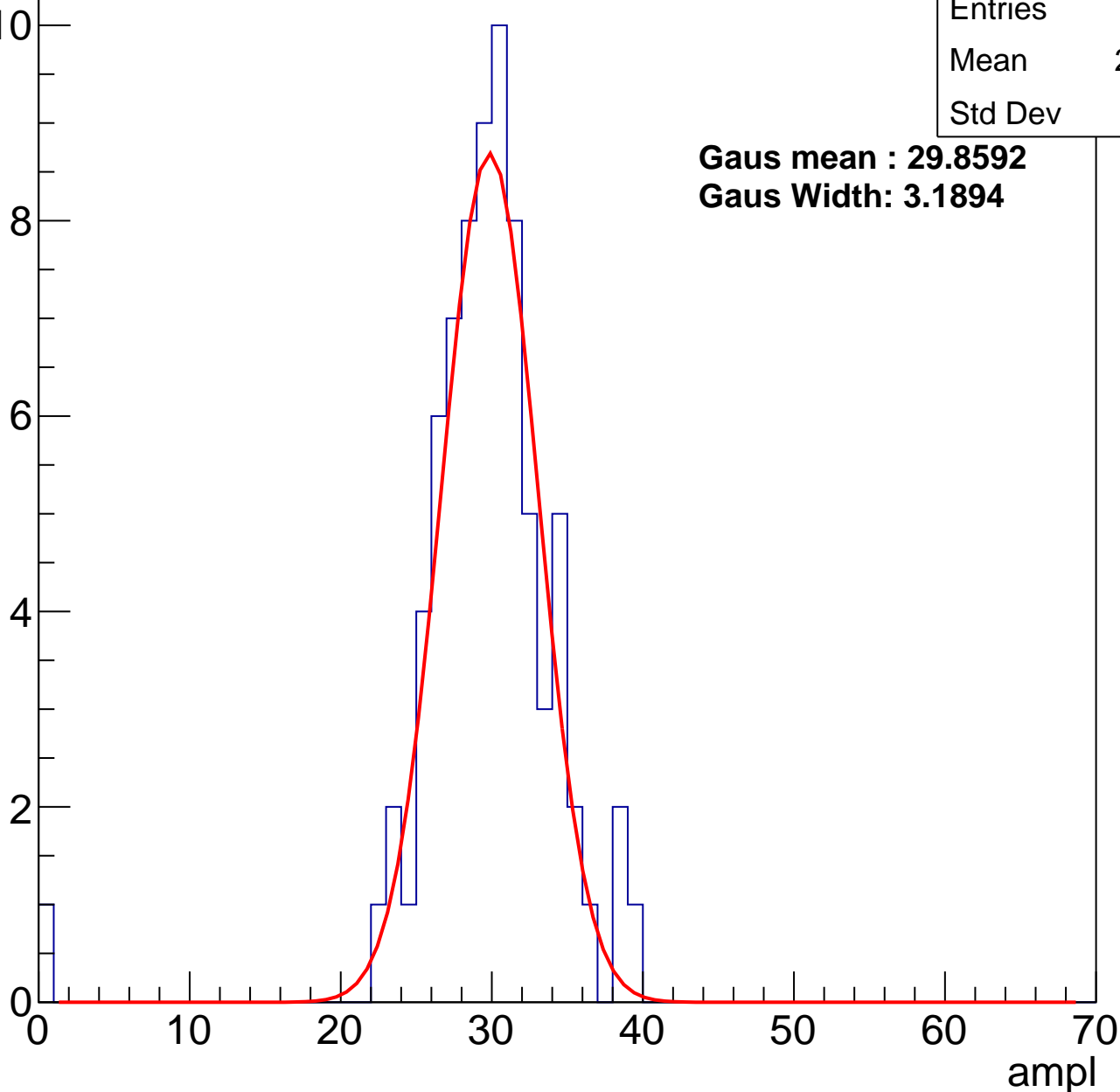
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	29.21
Std Dev	4.84

**Gaus mean : 29.8592**

**Gaus Width: 3.1894**



# B1L002S, U26-ch26, adc1

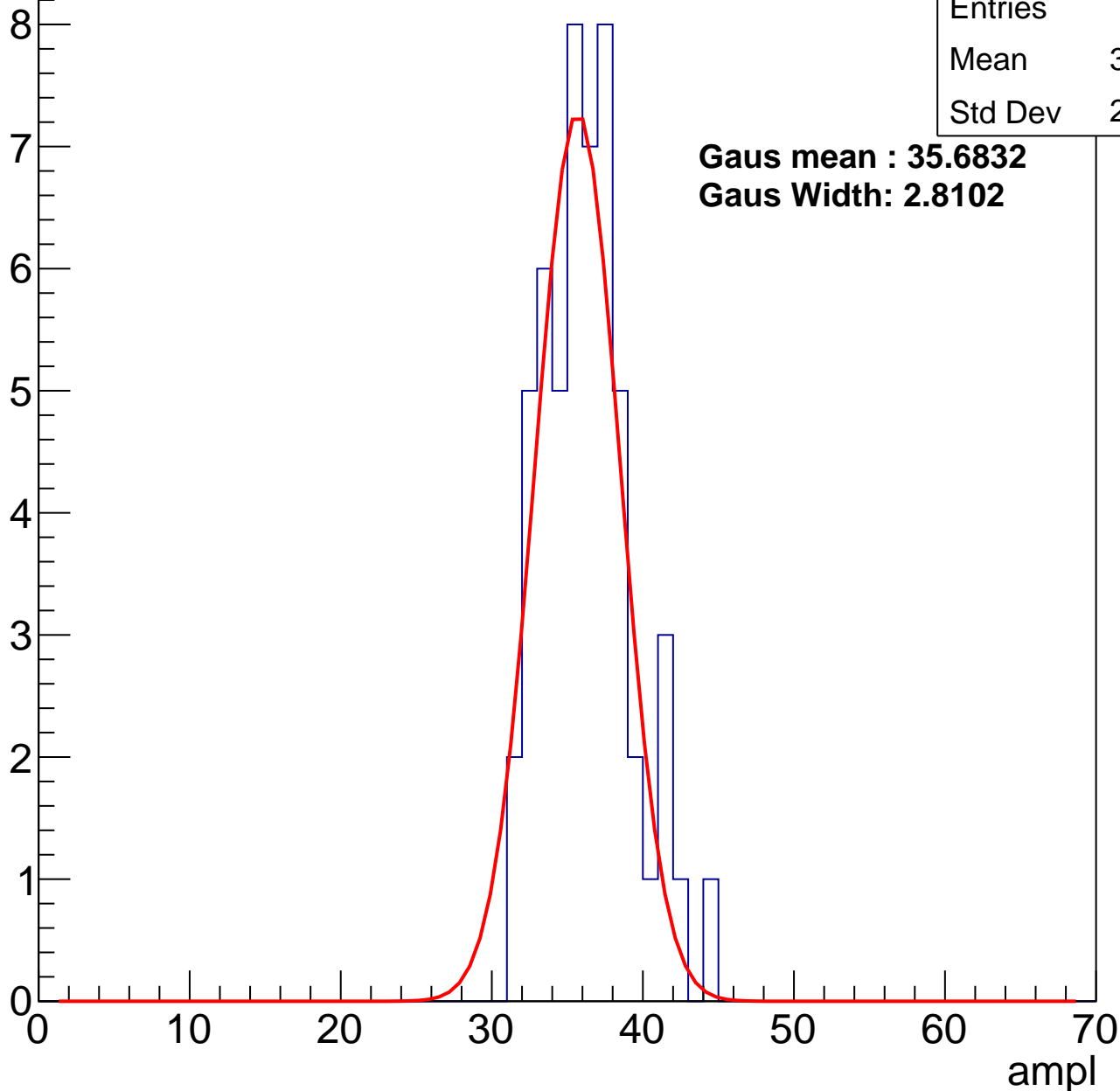
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	35.83
Std Dev	2.879

**Gaus mean : 35.6832**

**Gaus Width: 2.8102**



# B1L002S, U26-ch26, adc2

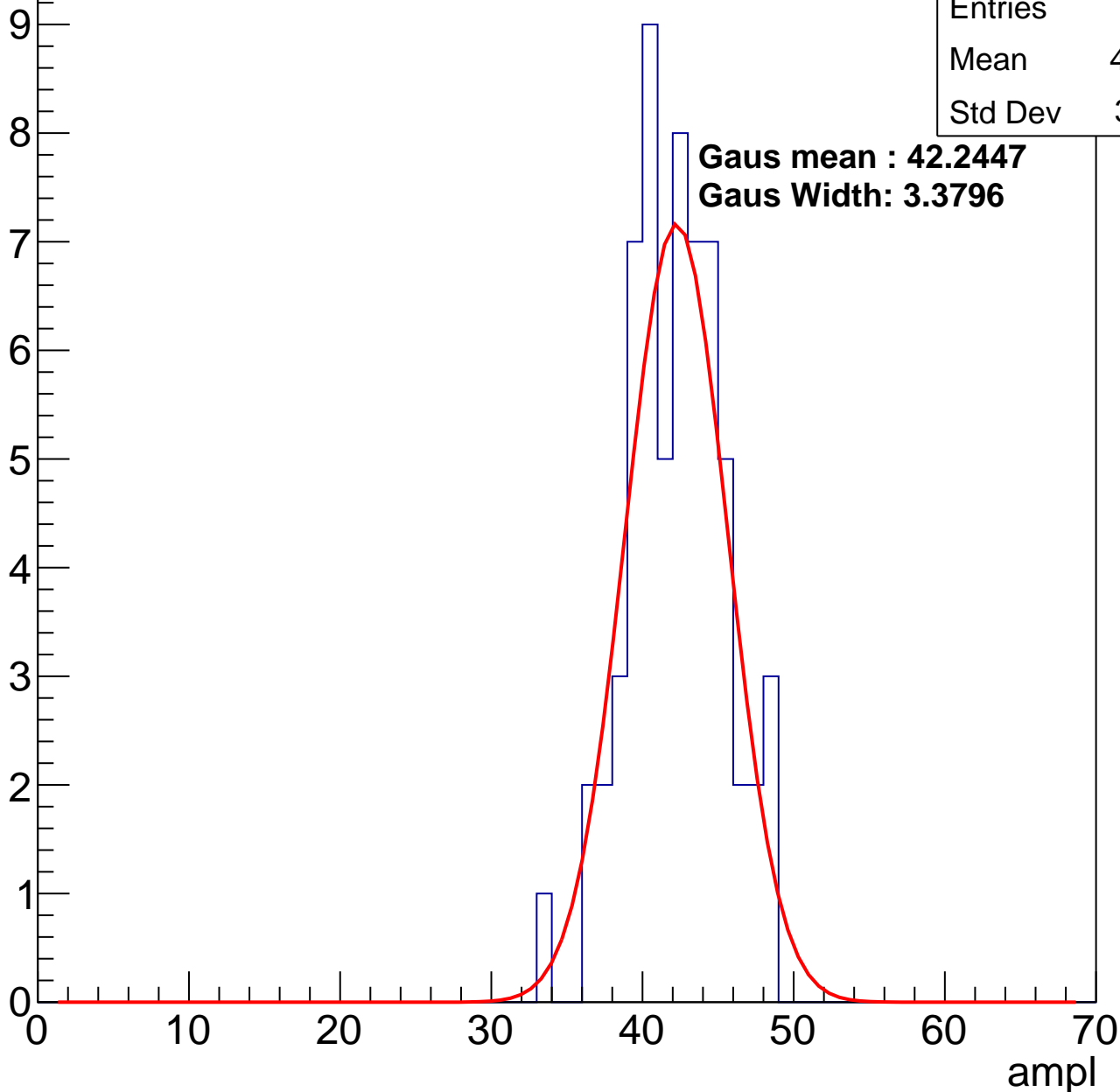
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.76
Std Dev	3.141

**Gaus mean : 42.2447**

**Gaus Width: 3.3796**

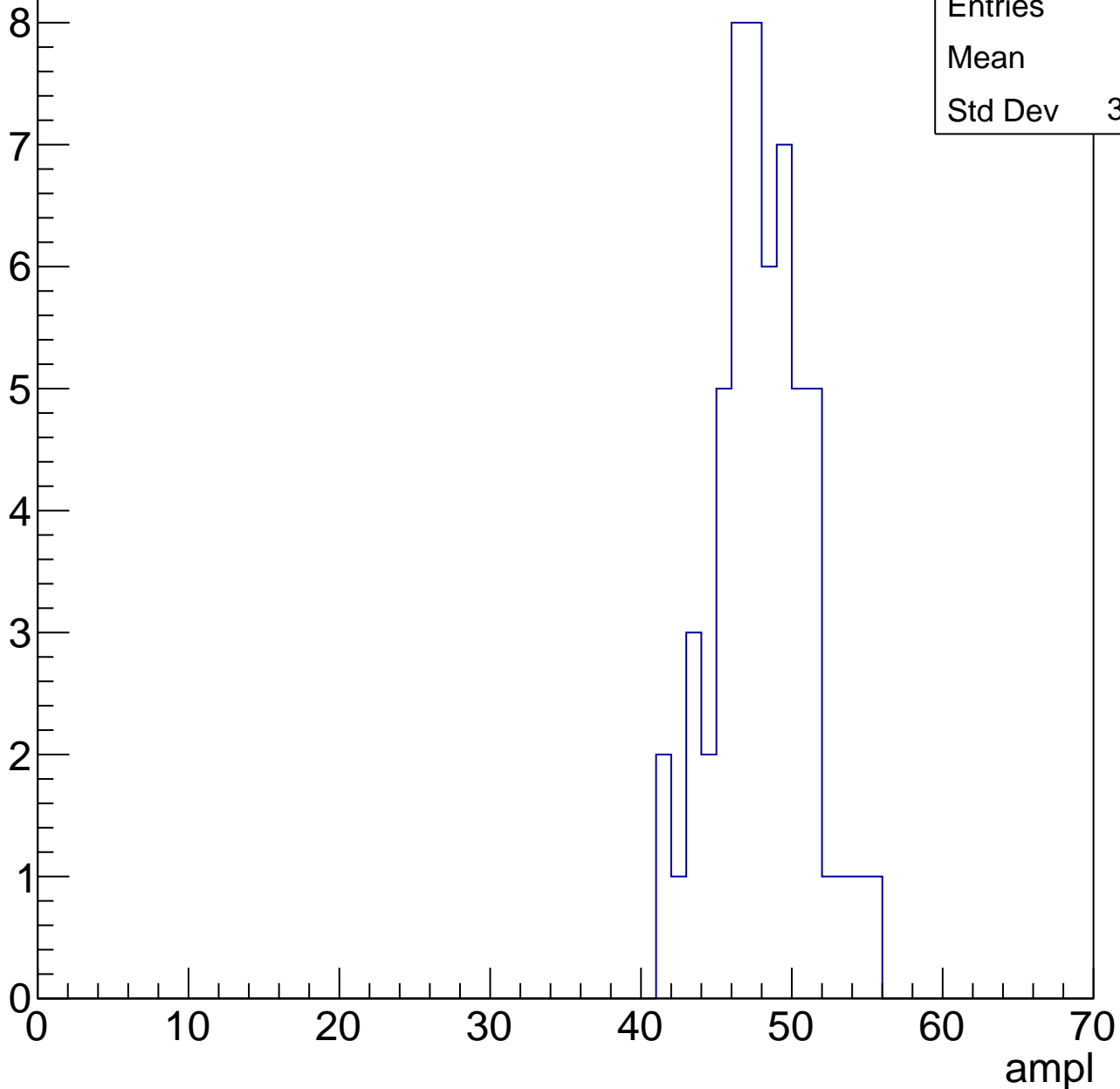


# B1L002S, U26-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	47.5
Std Dev	3.018

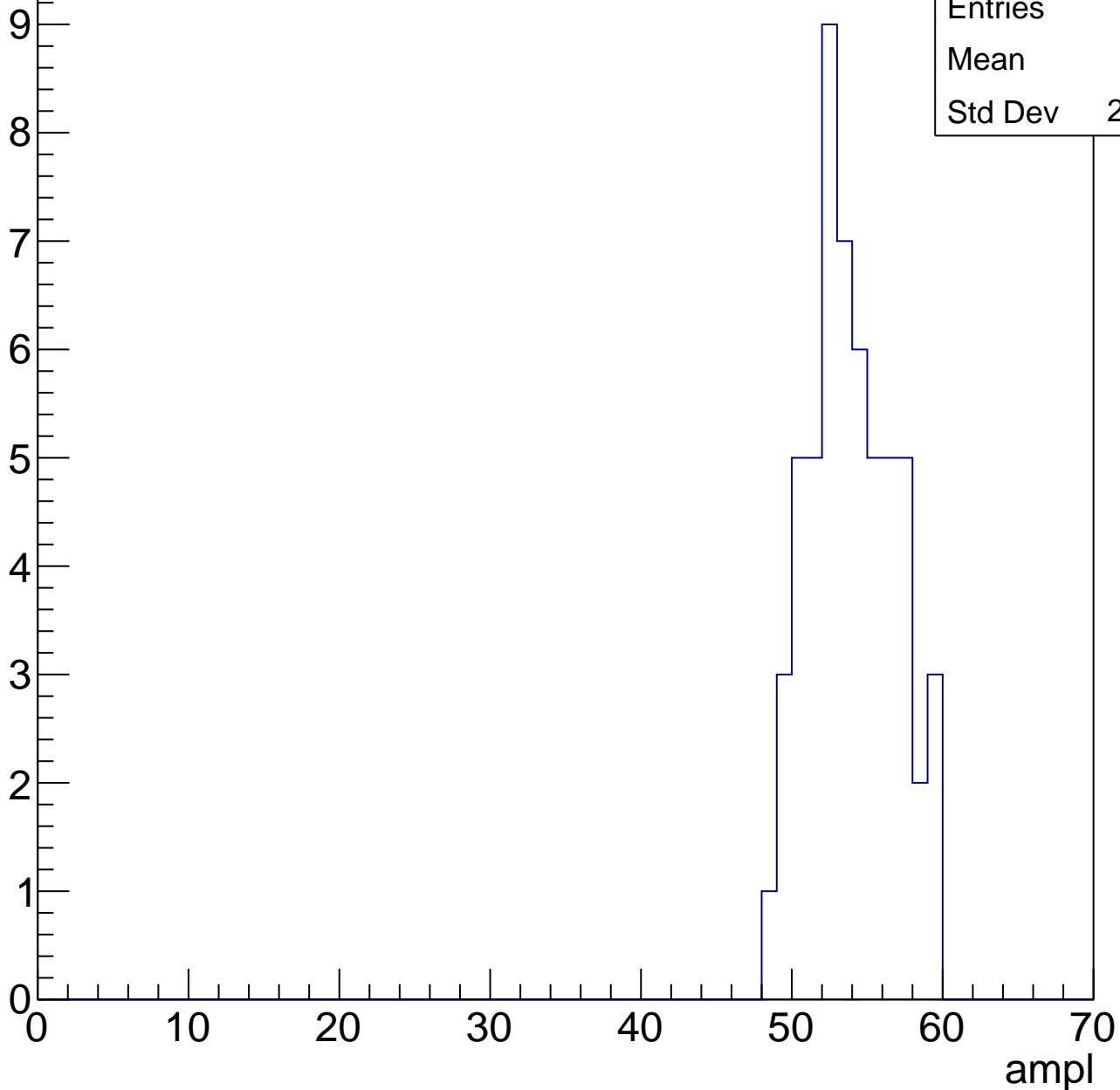


# B1L002S, U26-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.5
Std Dev	2.809

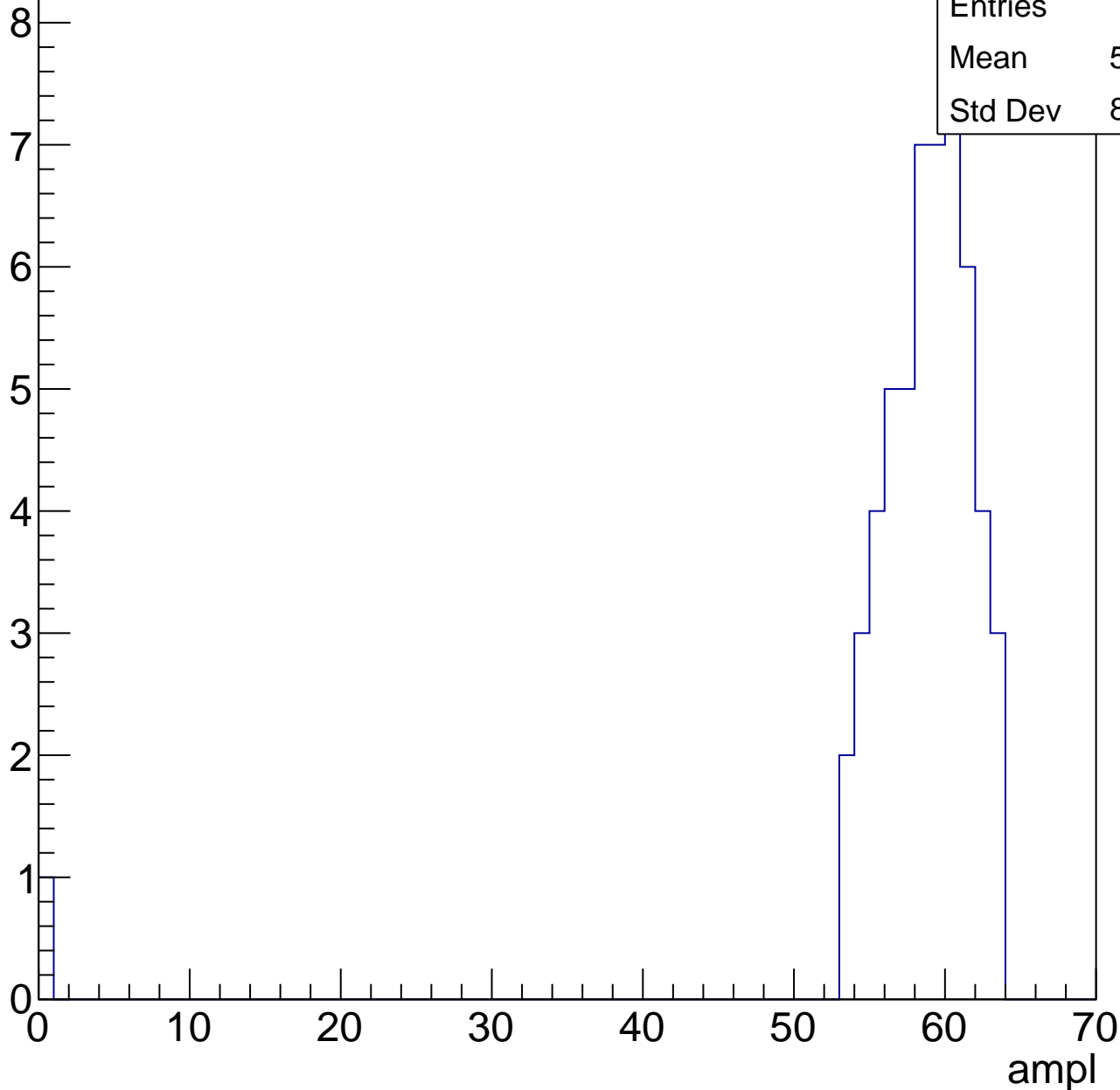


# B1L002S, U26-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.36
Std Dev	8.238

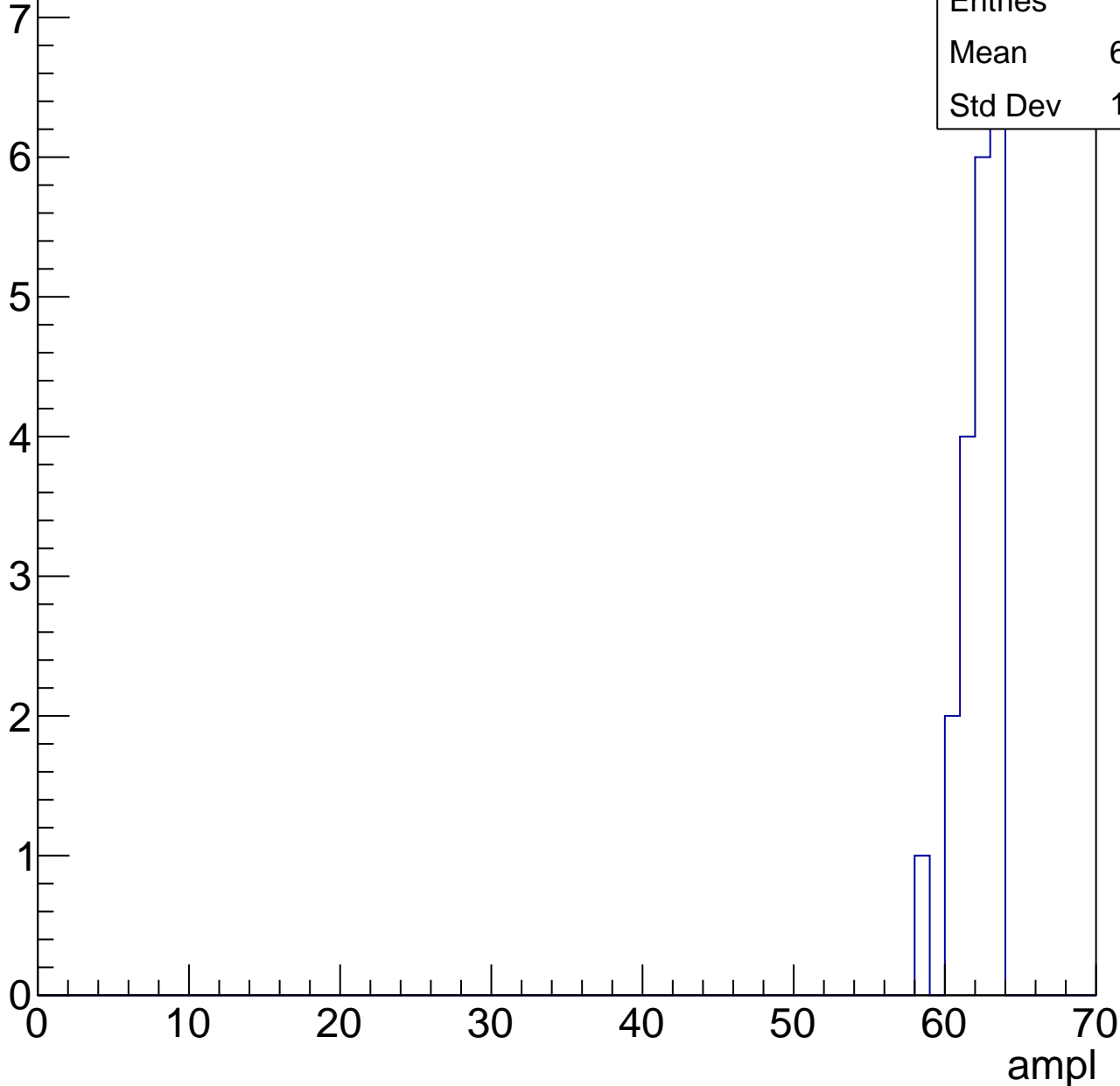


# B1L002S, U26-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.75
Std Dev	1.299





# B1L002S, U26-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch27, adc0

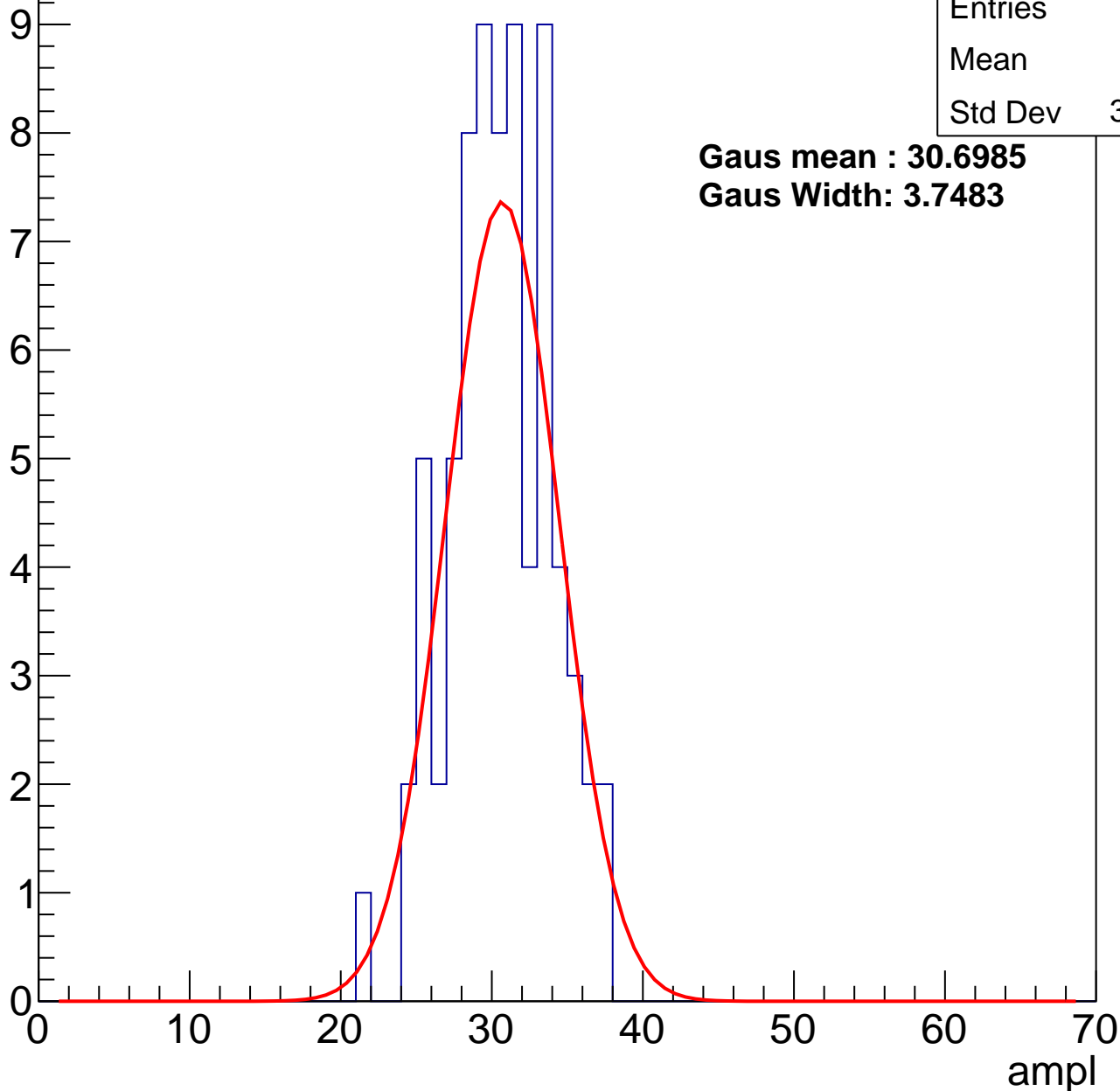
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	30.1
Std Dev	3.332

**Gaus mean : 30.6985**

**Gaus Width: 3.7483**



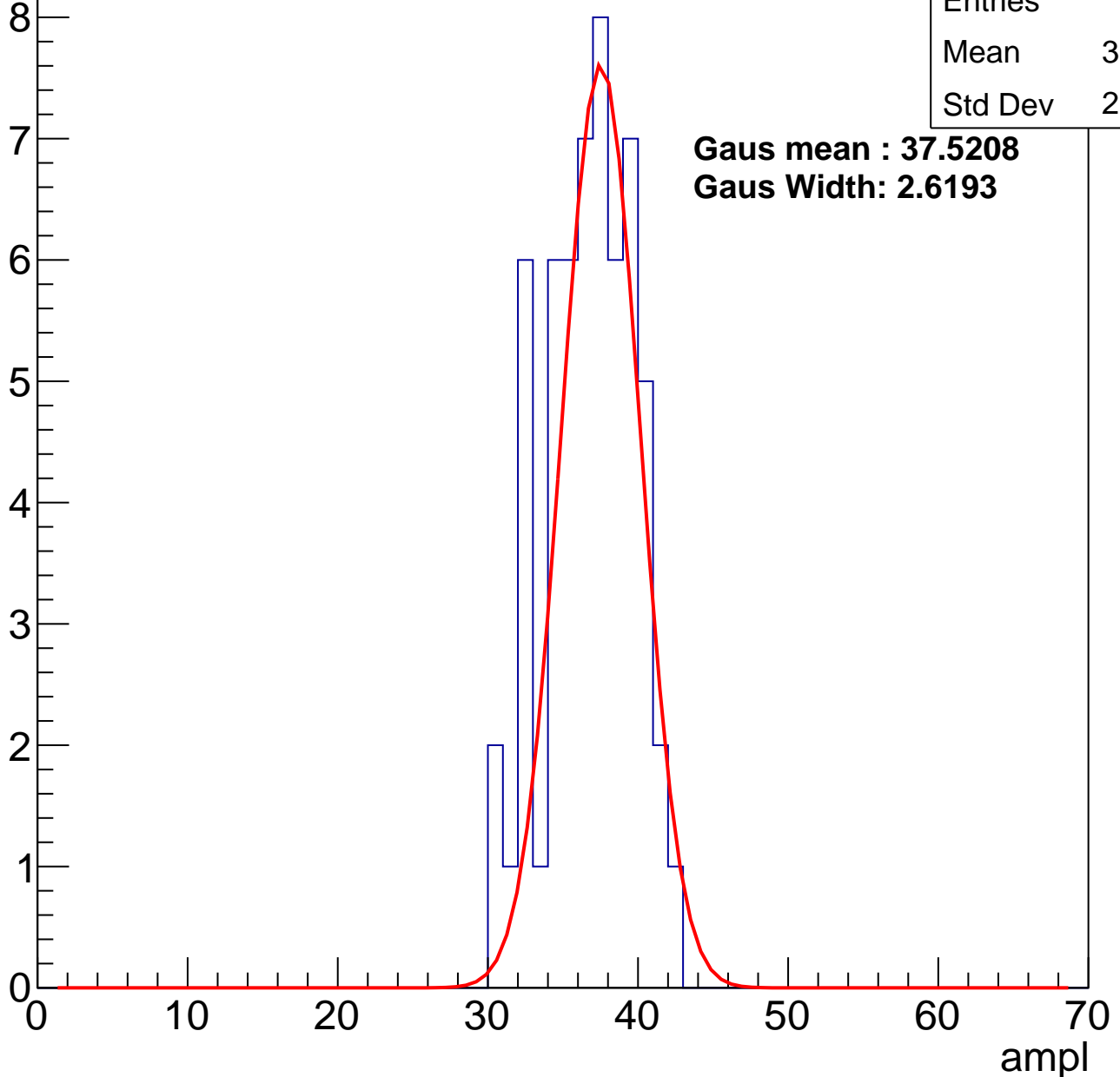
# B1L002S, U26-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	36.26
Std Dev	2.904

**Gaus mean : 37.5208**  
**Gaus Width: 2.6193**



# B1L002S, U26-ch27, adc2

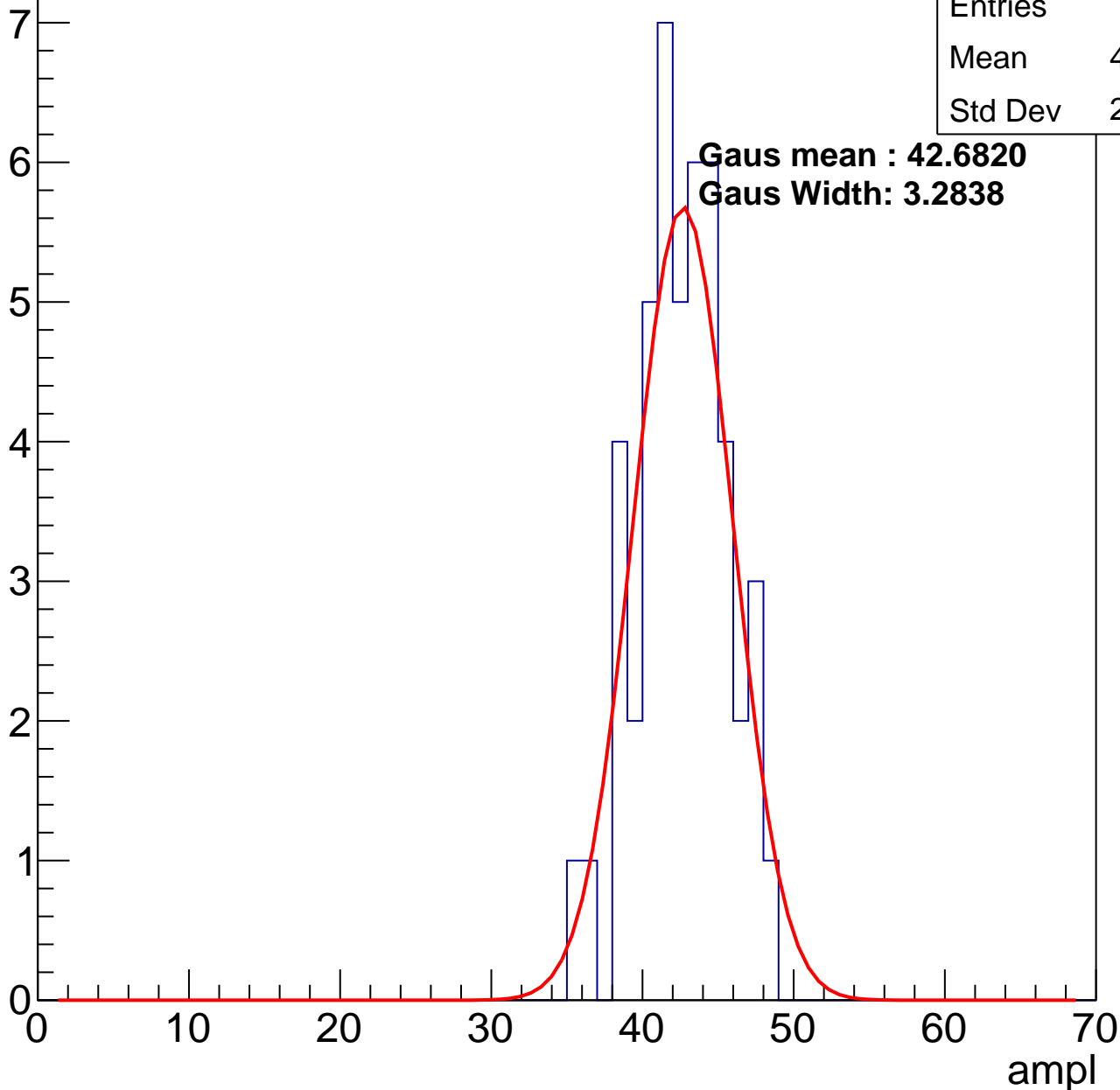
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	42.15
Std Dev	2.924

**Gaus mean : 42.6820**

**Gaus Width: 3.2838**

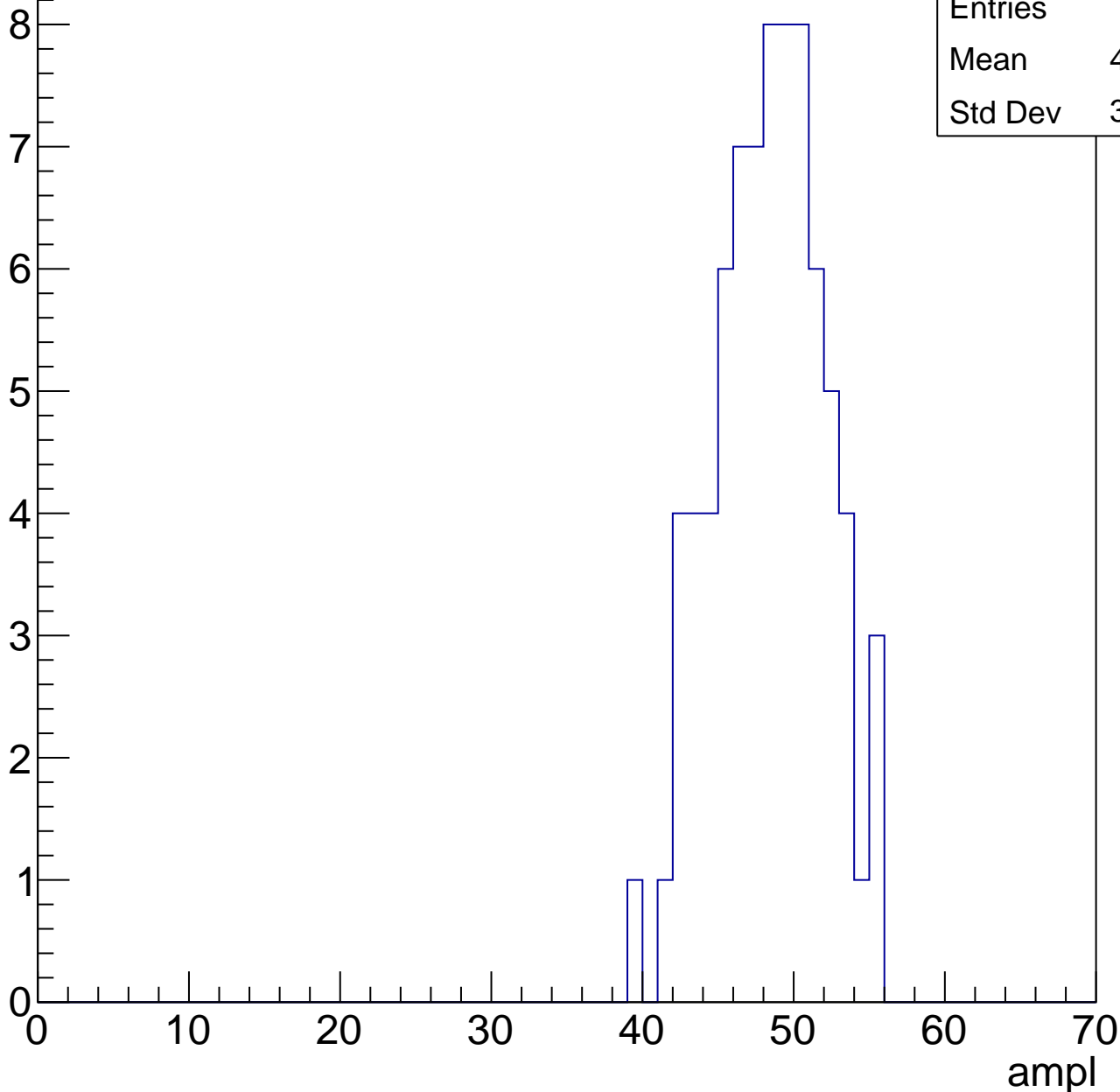


# B1L002S, U26-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

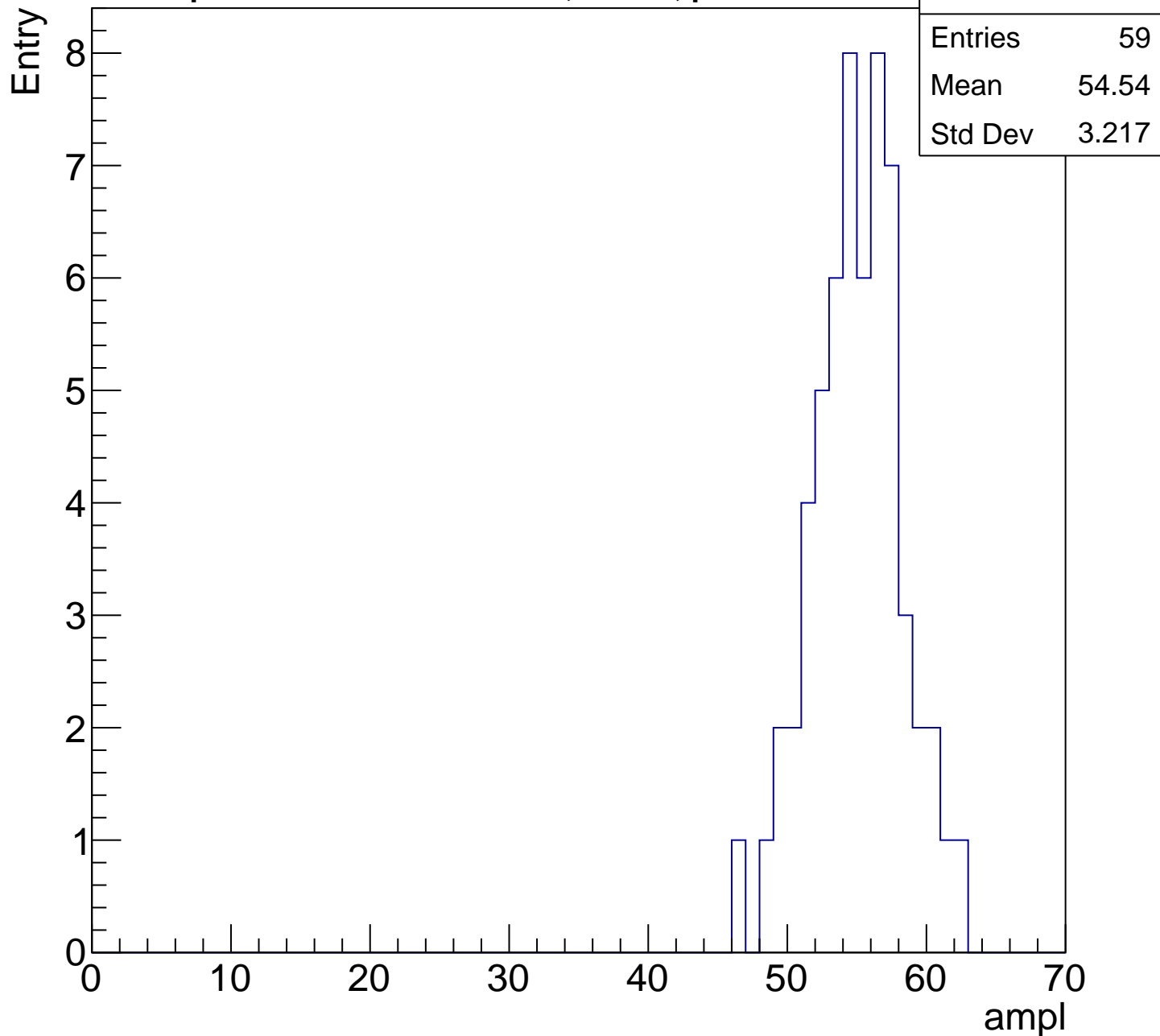
Entry

Entries	77
Mean	47.92
Std Dev	3.574



# B1L002S, U26-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	39
Mean	59.41
Std Dev	2.216

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

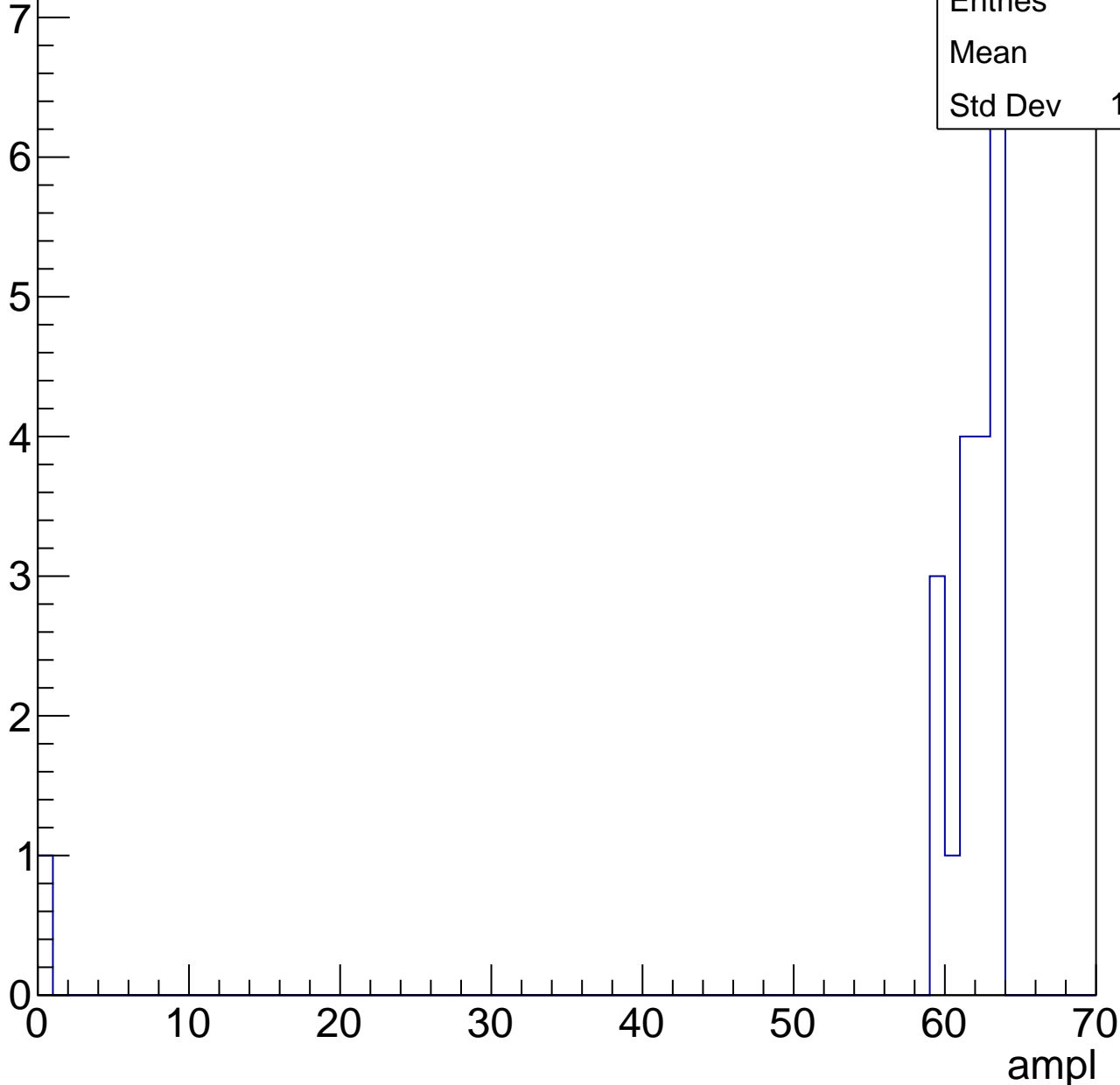
7

# B1L002S, U26-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.5
Std Dev	13.49





# B1L002S, U26-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L002S, U26-ch28, adc0

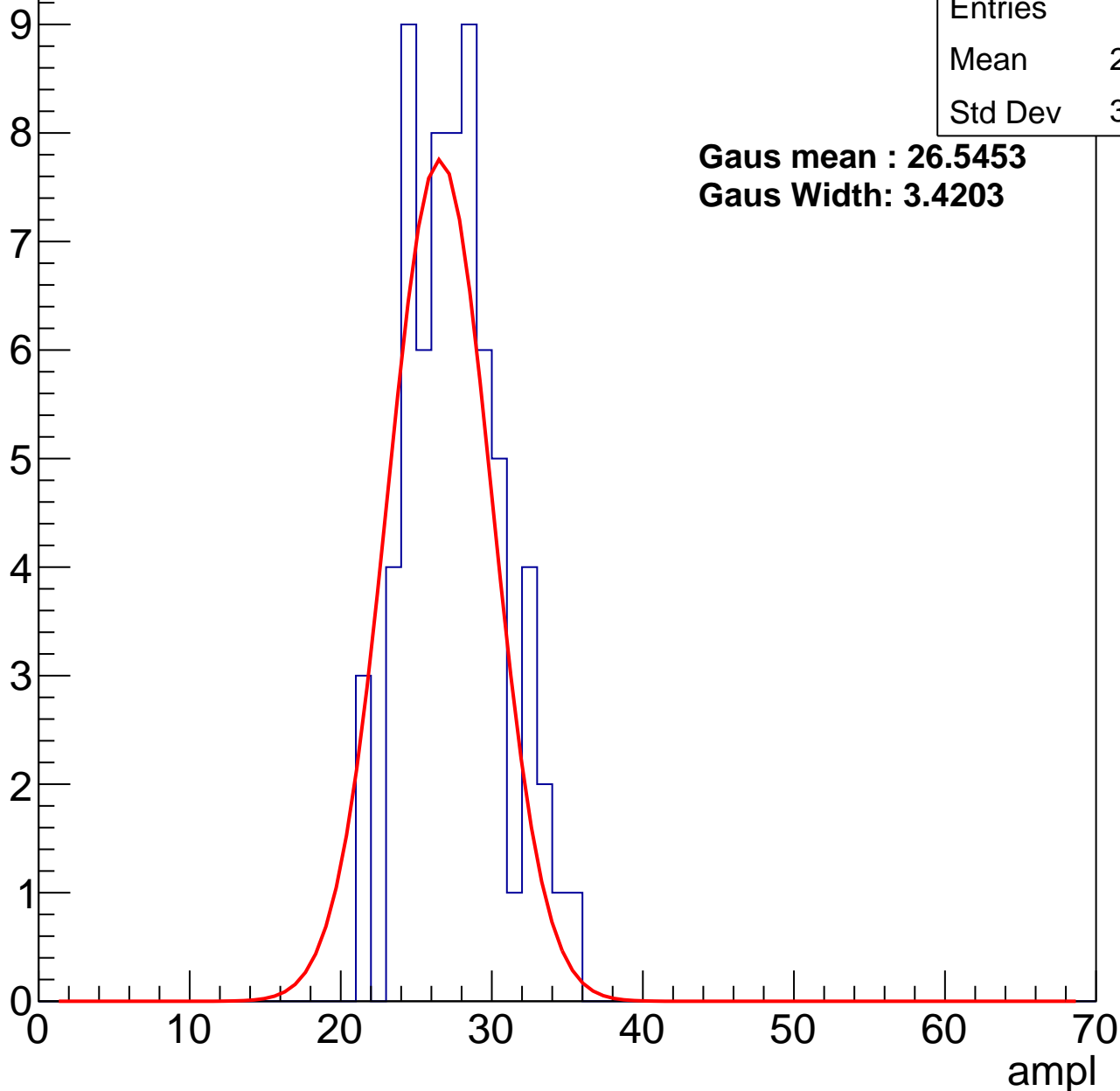
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.09
Std Dev	3.147

**Gaus mean : 26.5453**

**Gaus Width: 3.4203**



# B1L002S, U26-ch28, adc1

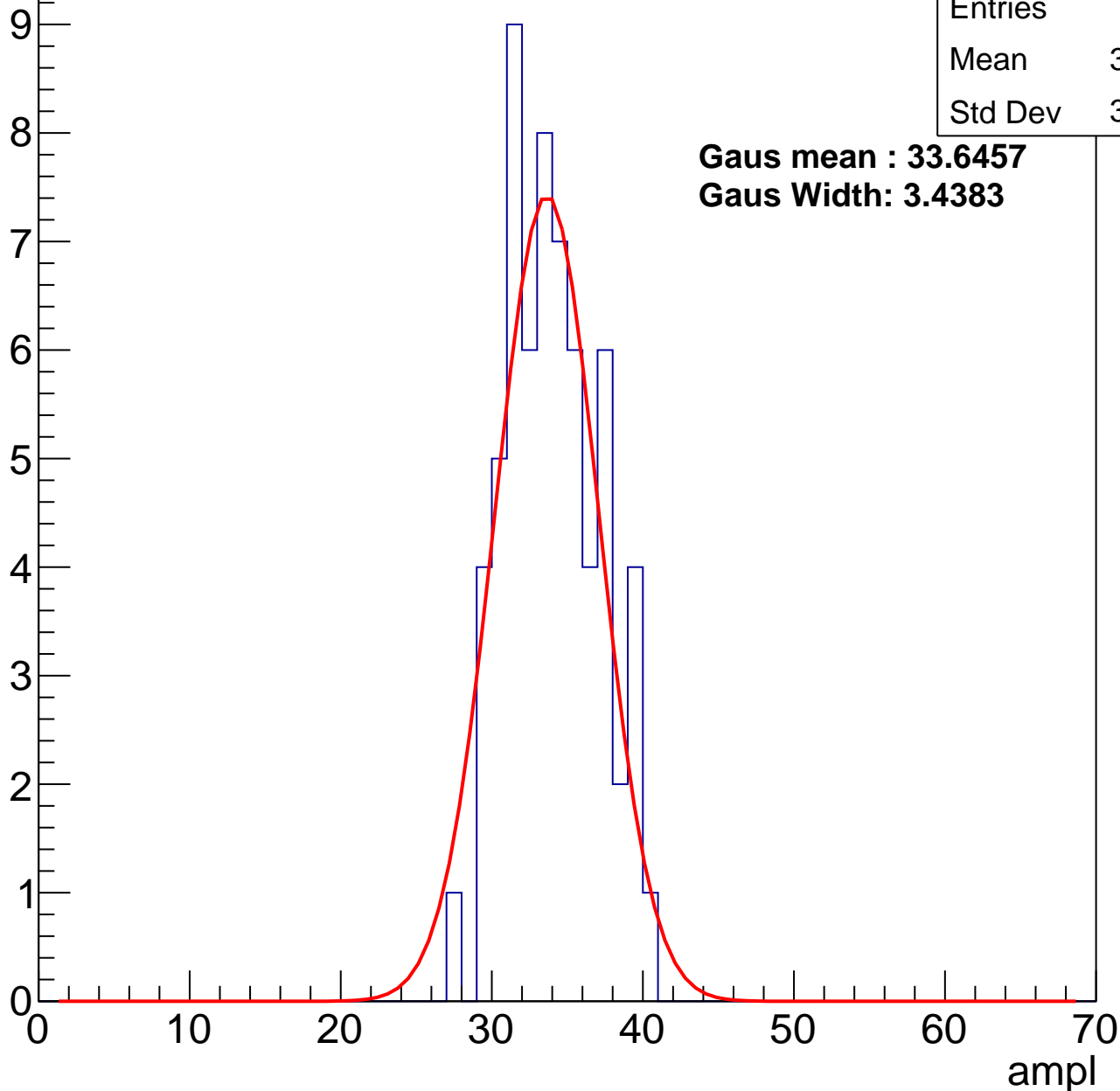
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.56
Std Dev	3.017

**Gaus mean : 33.6457**

**Gaus Width: 3.4383**



# B1L002S, U26-ch28, adc2

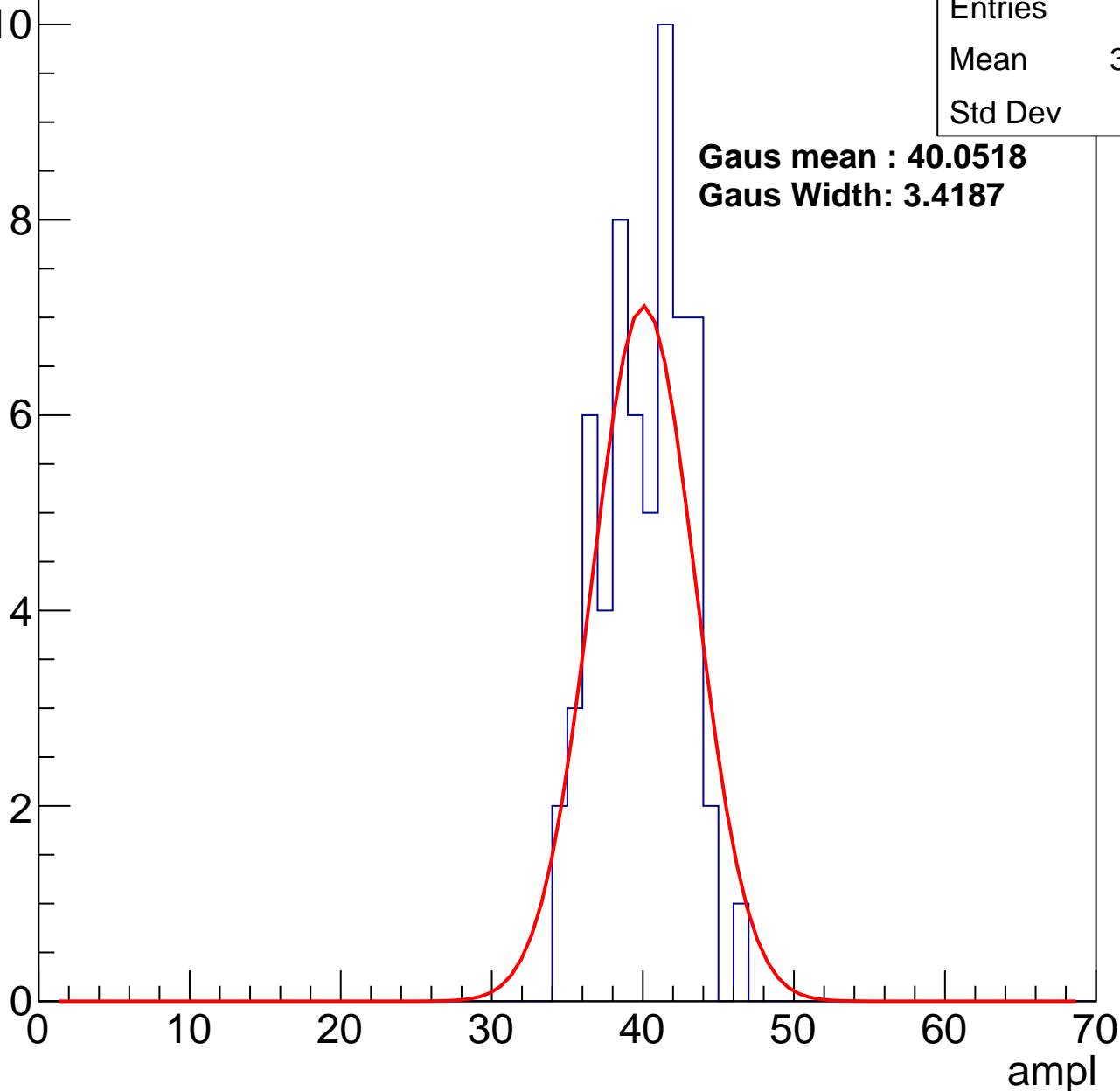
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.57
Std Dev	2.79

**Gaus mean : 40.0518**

**Gaus Width: 3.4187**

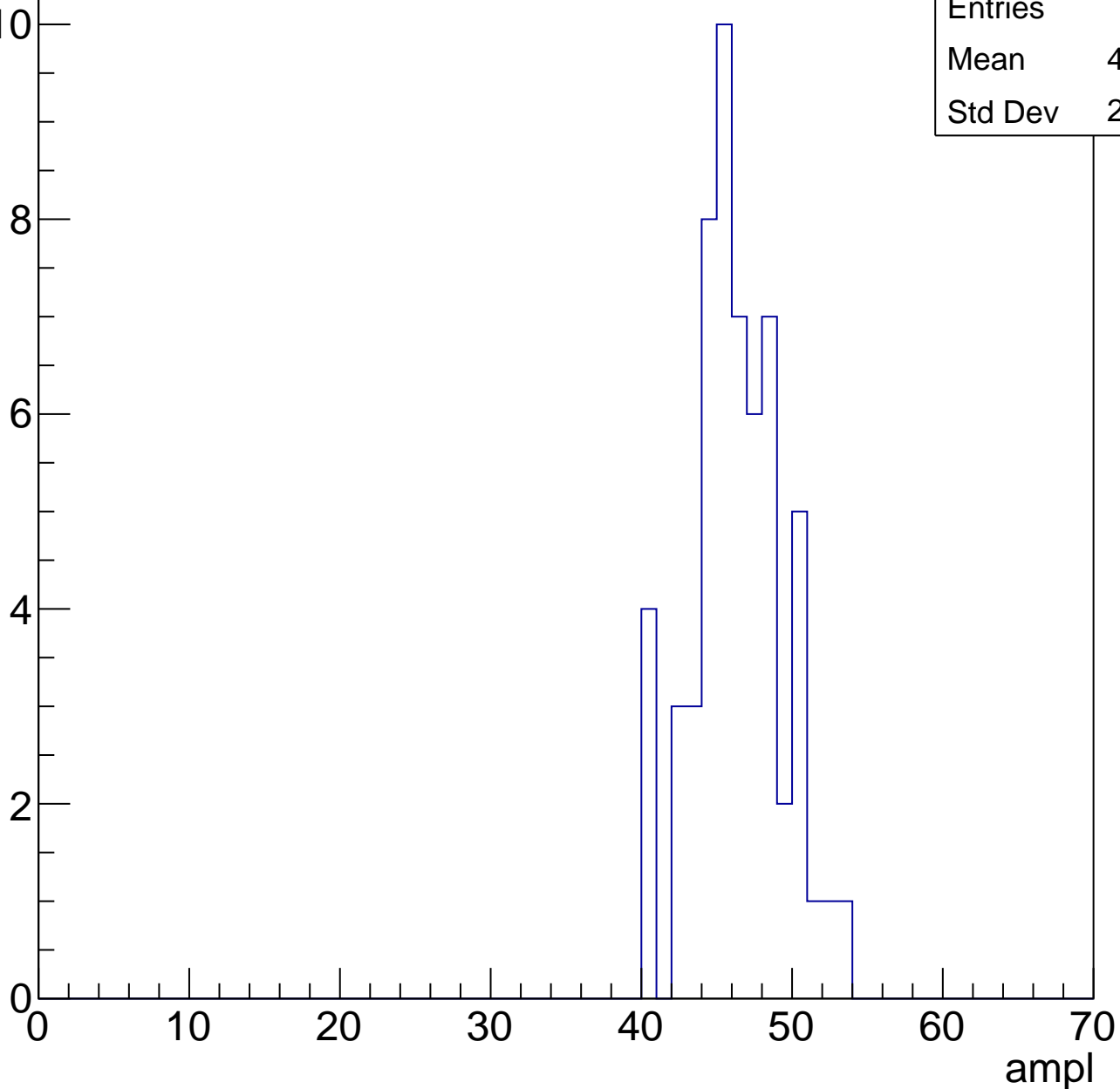


# B1L002S, U26-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.88
Std Dev	2.942

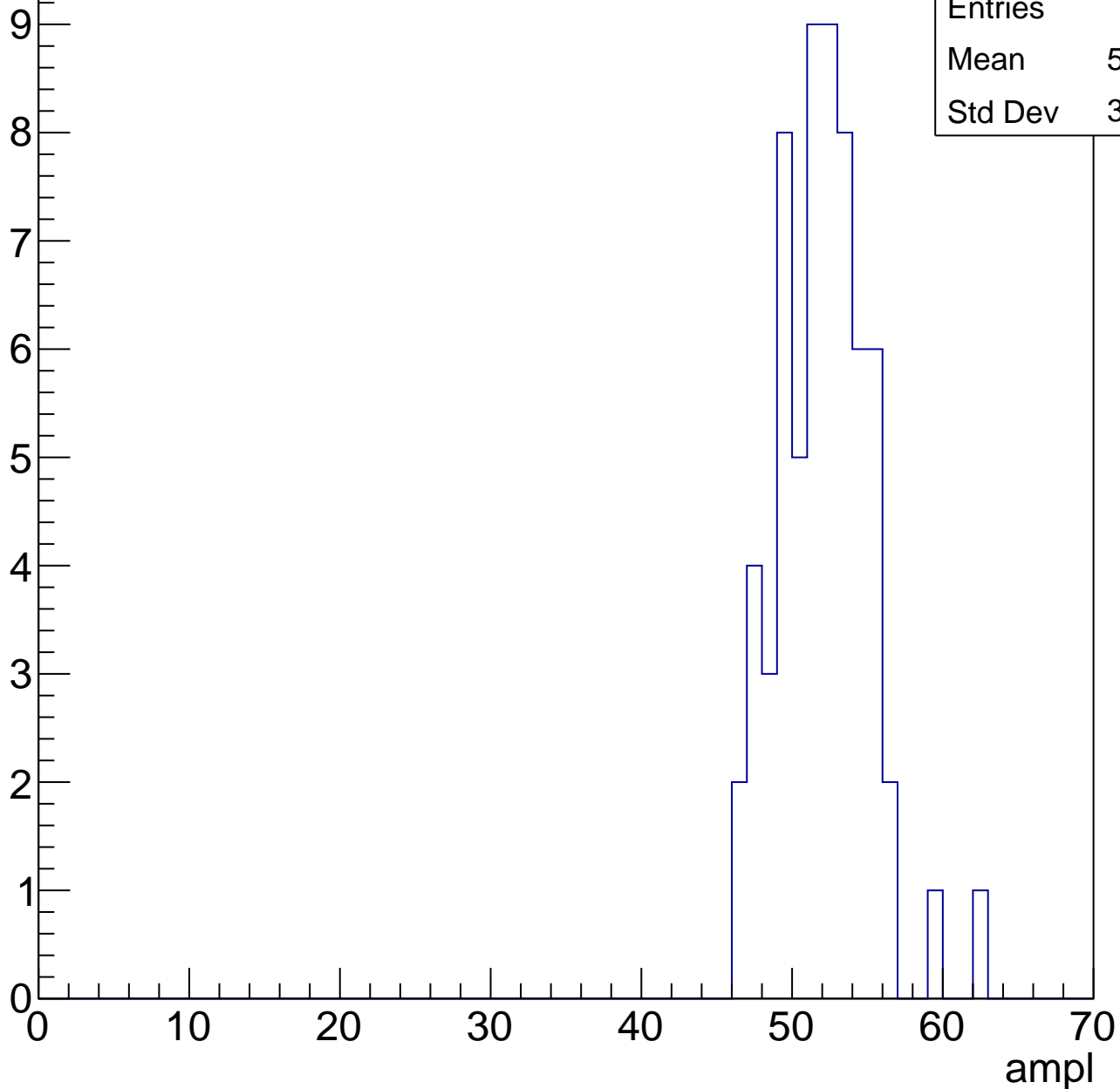


# B1L002S, U26-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	51.62
Std Dev	3.008

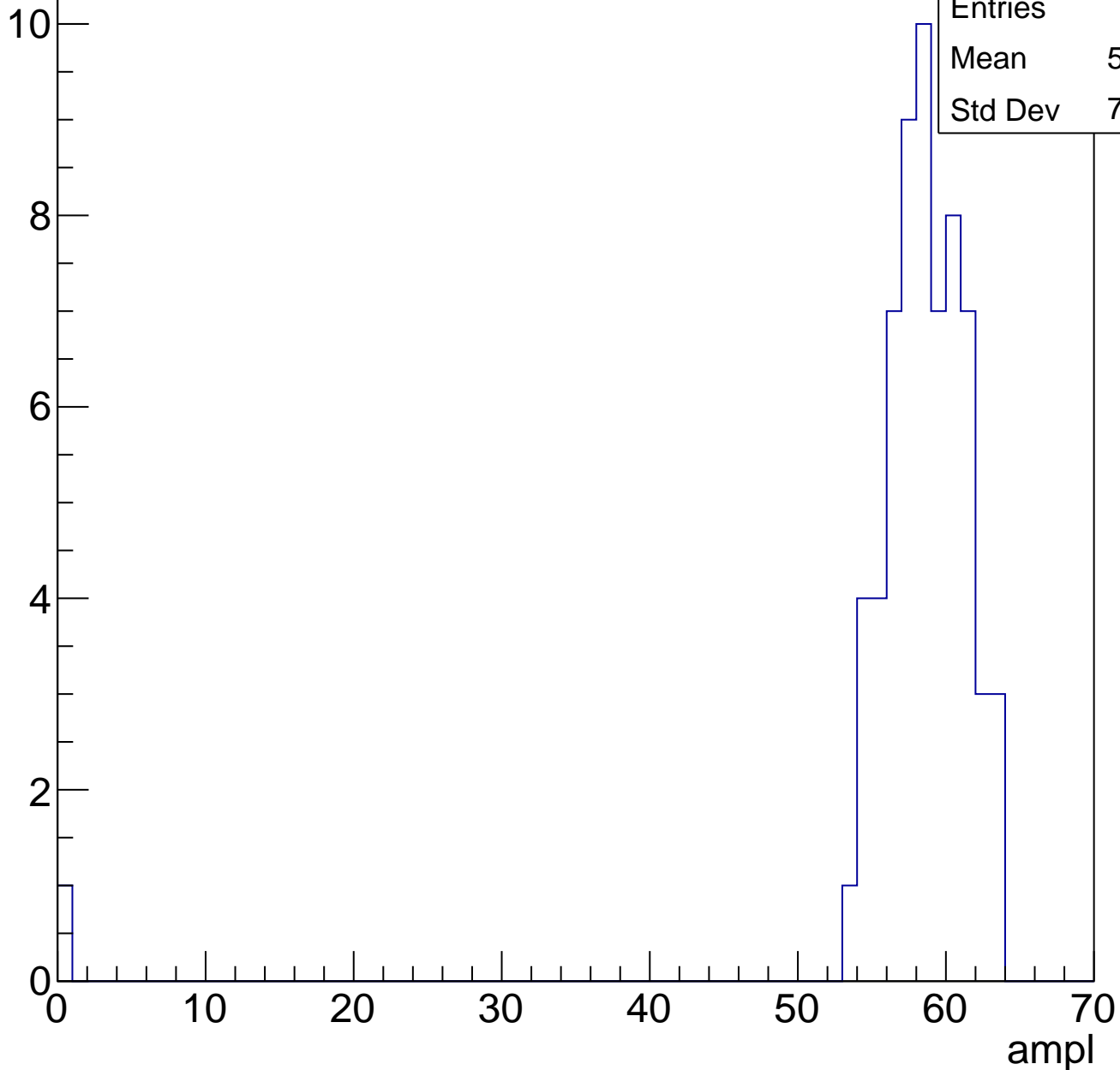


# B1L002S, U26-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	57.33
Std Dev	7.626

Entry

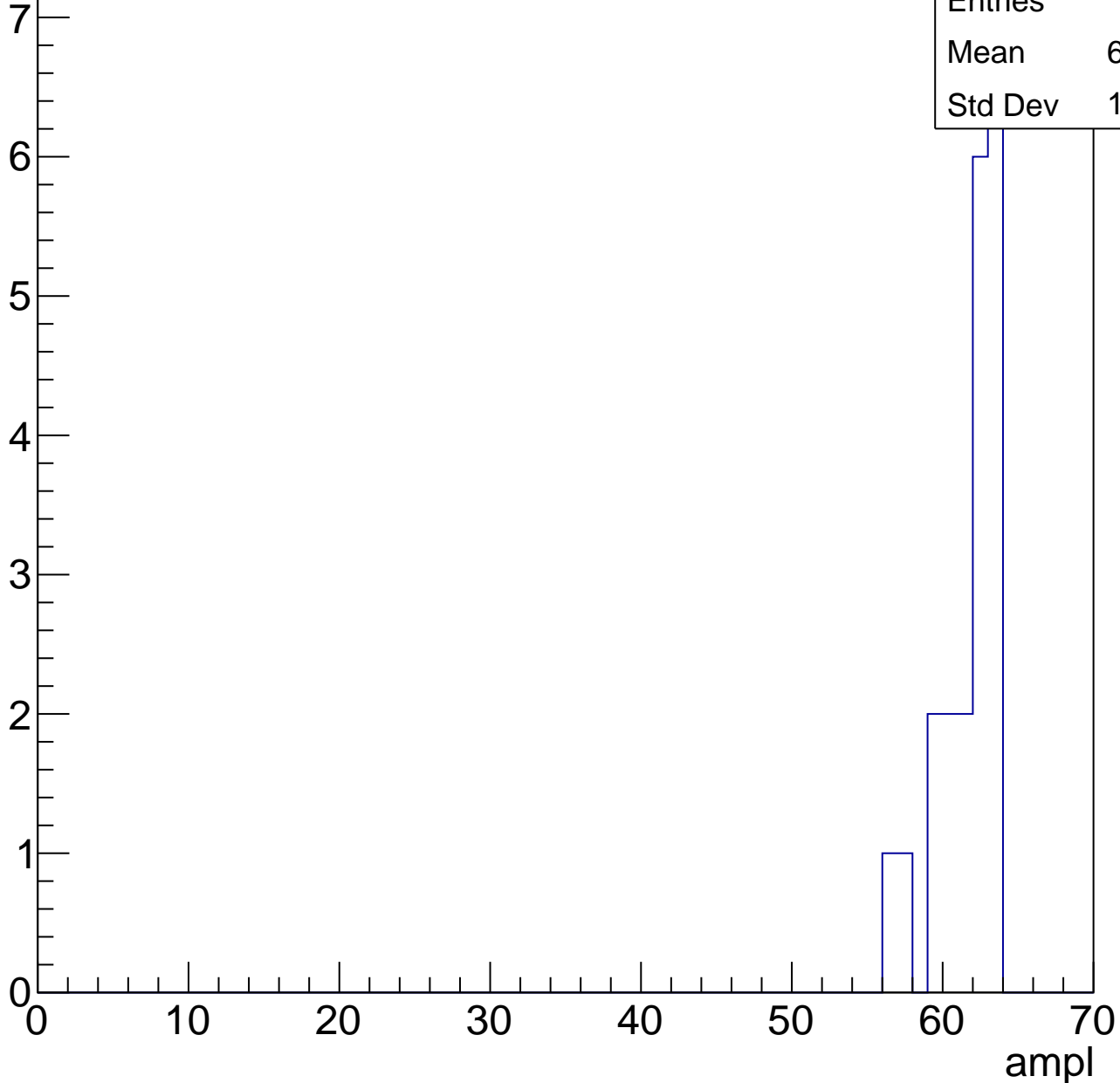


# B1L002S, U26-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.24
Std Dev	1.998

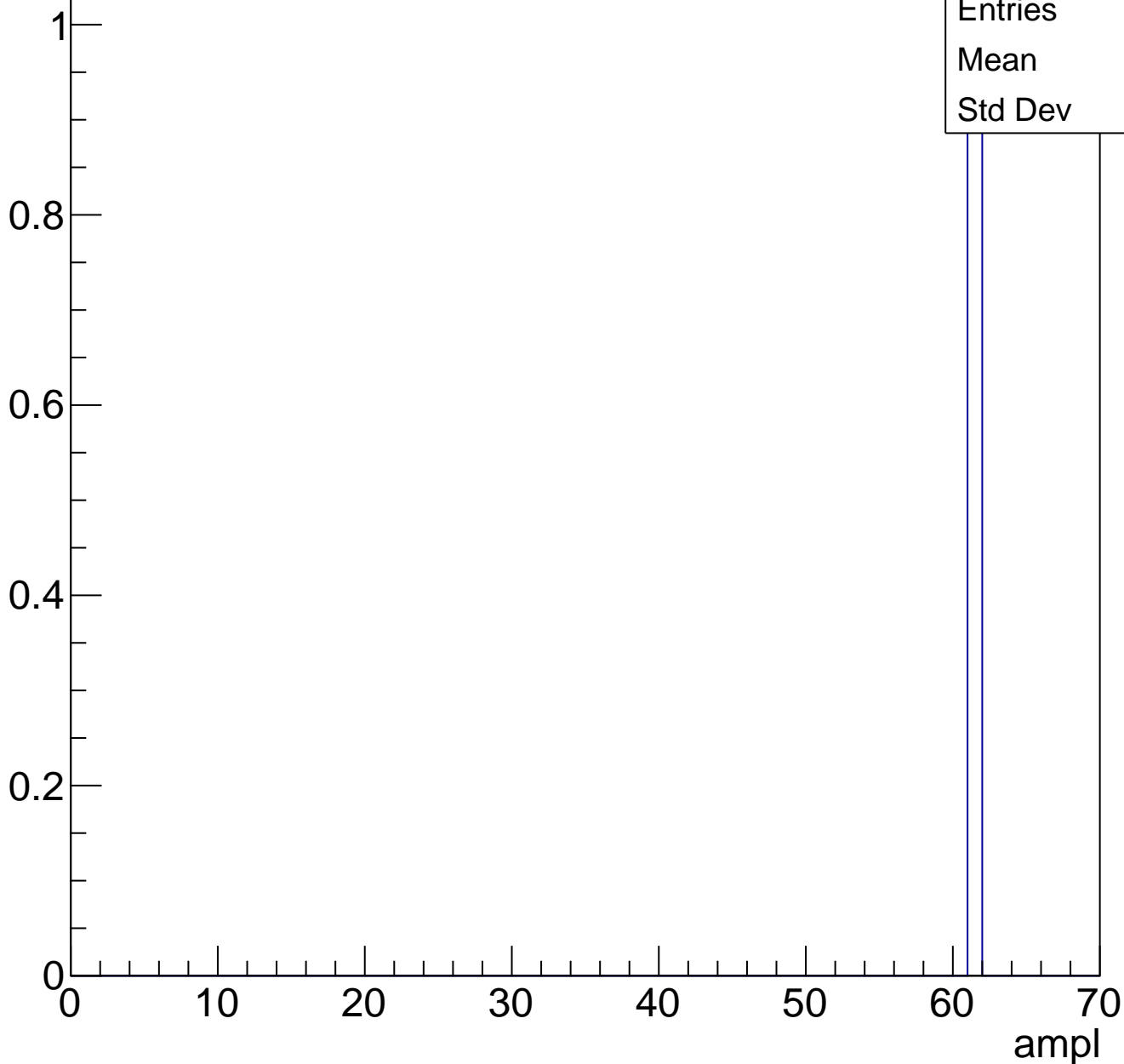




# B1L002S, U26-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch29, adc0

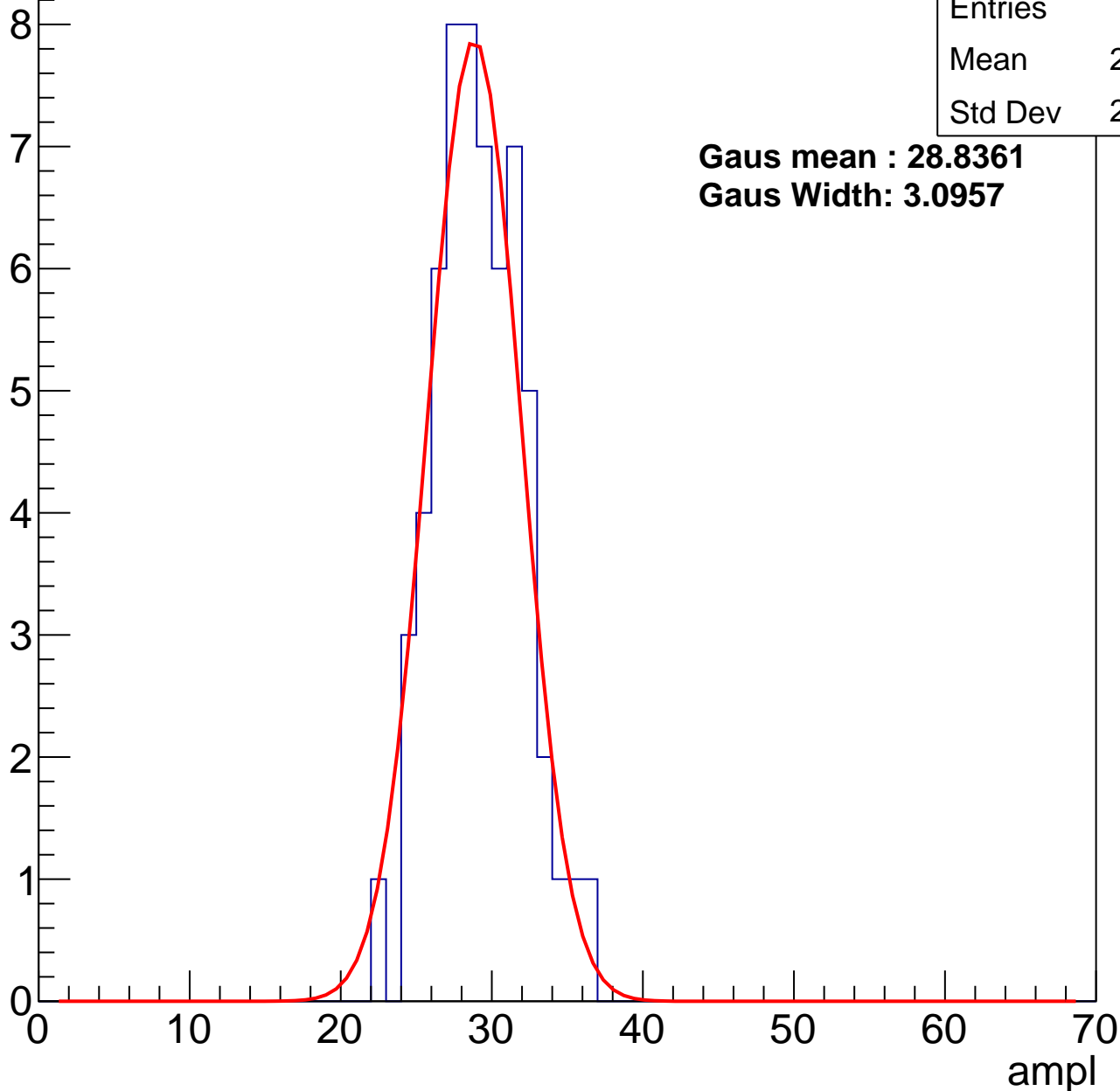
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.68
Std Dev	2.878

**Gaus mean : 28.8361**

**Gaus Width: 3.0957**



# B1L002S, U26-ch29, adc1

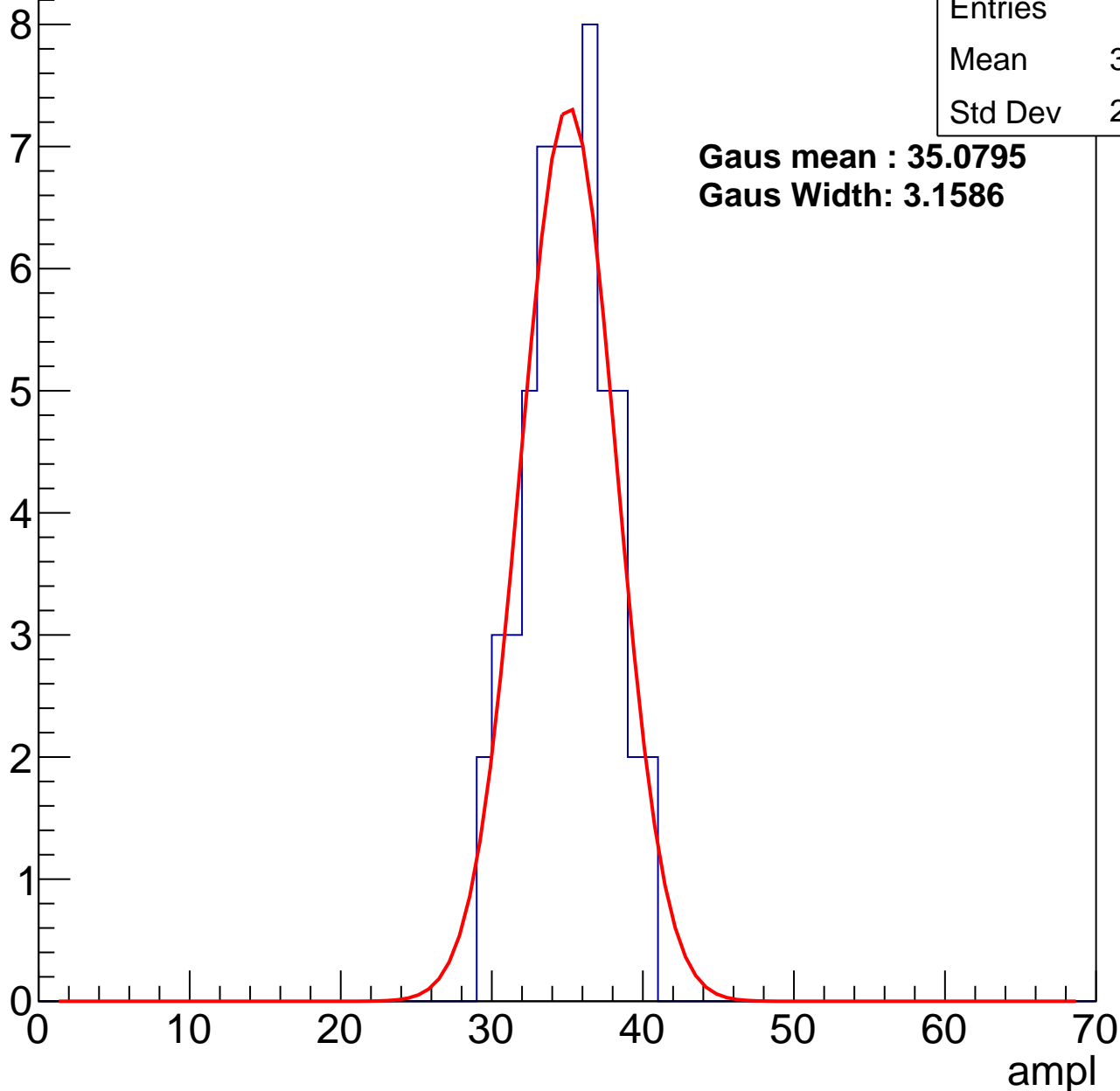
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.57
Std Dev	2.738

**Gaus mean : 35.0795**

**Gaus Width: 3.1586**



# B1L002S, U26-ch29, adc2

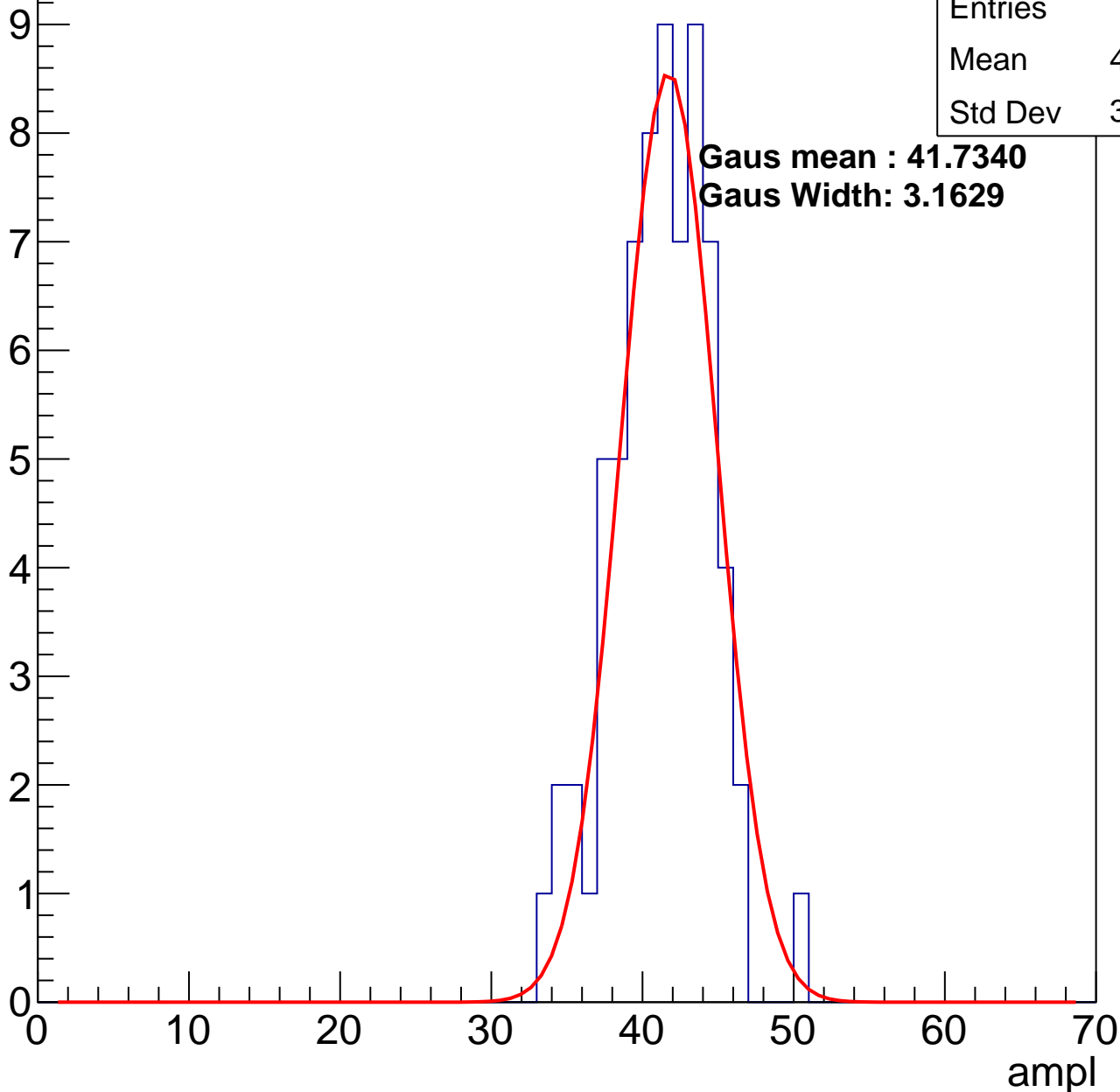
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.79
Std Dev	3.202

**Gaus mean : 41.7340**

**Gaus Width: 3.1629**

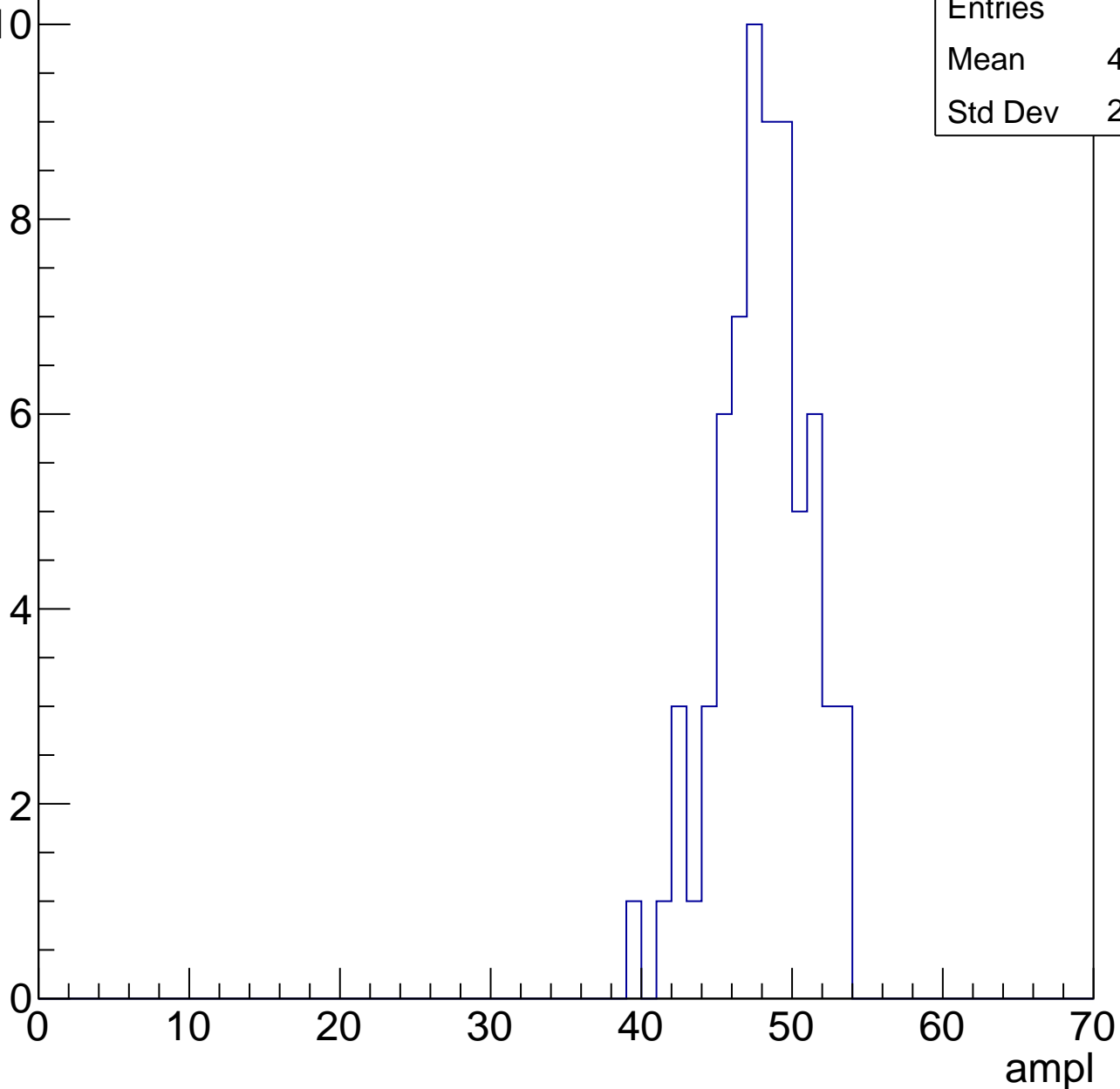


# B1L002S, U26-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	47.57
Std Dev	2.984

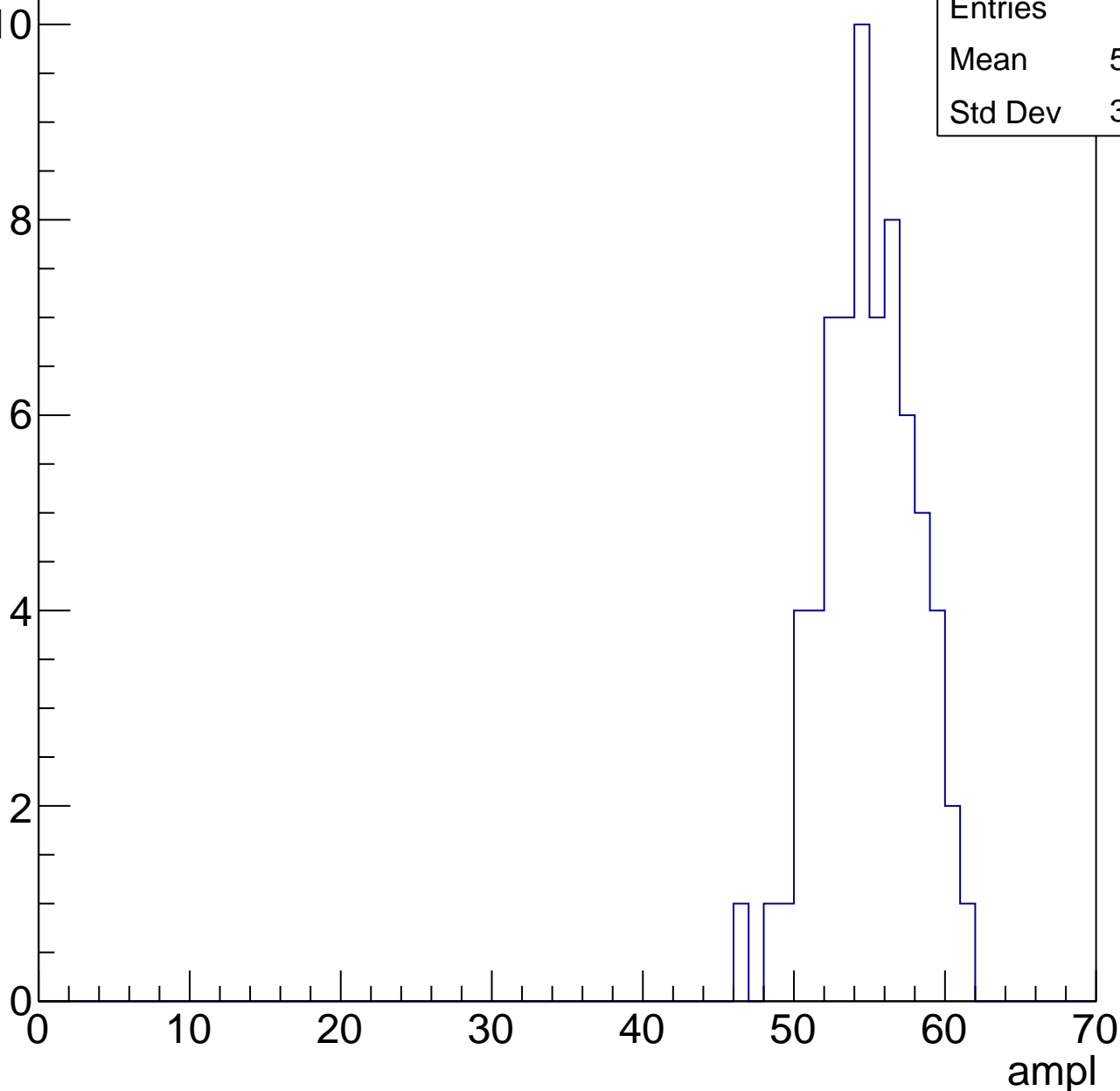


# B1L002S, U26-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	54.47
Std Dev	3.065

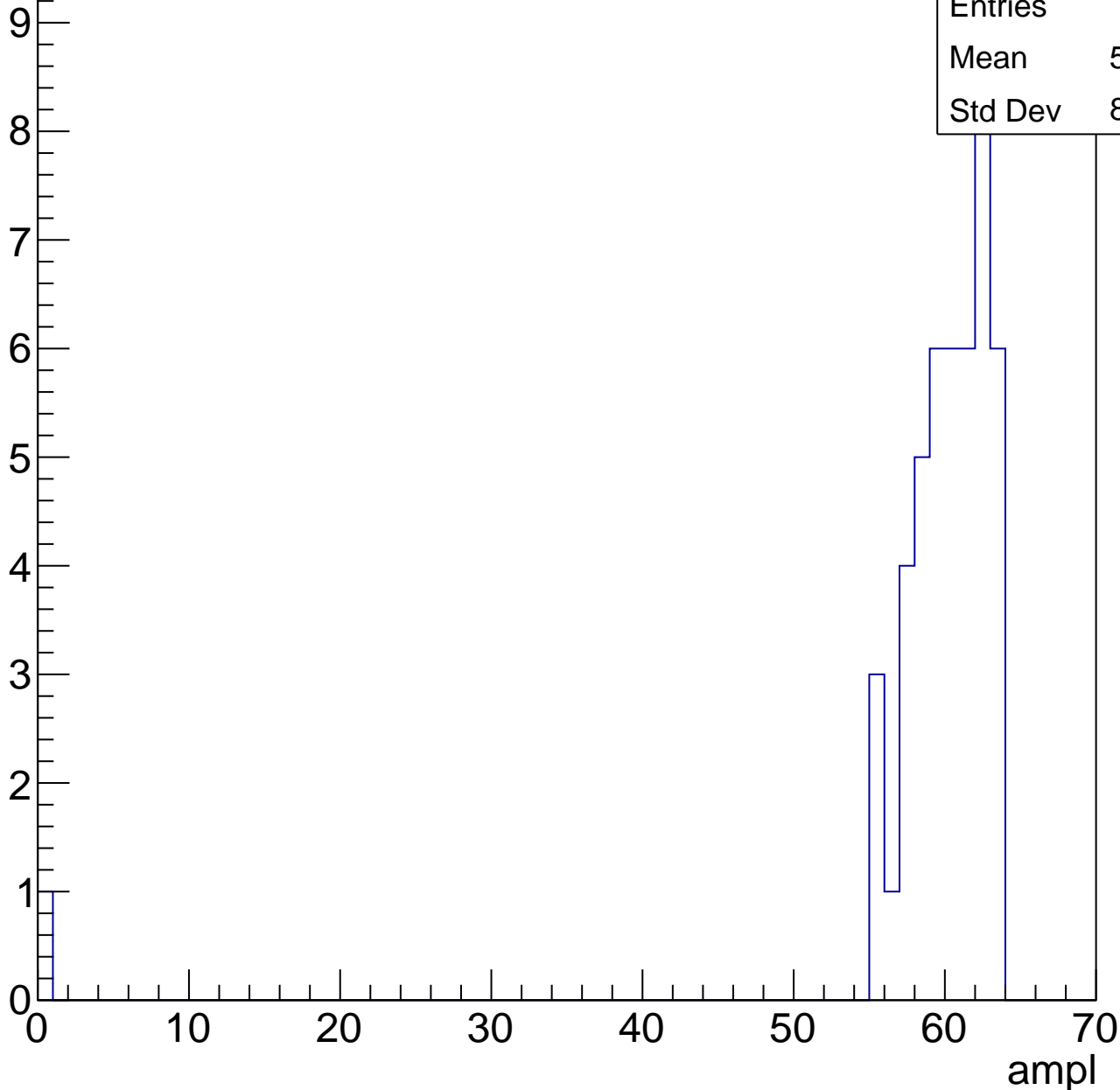


# B1L002S, U26-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

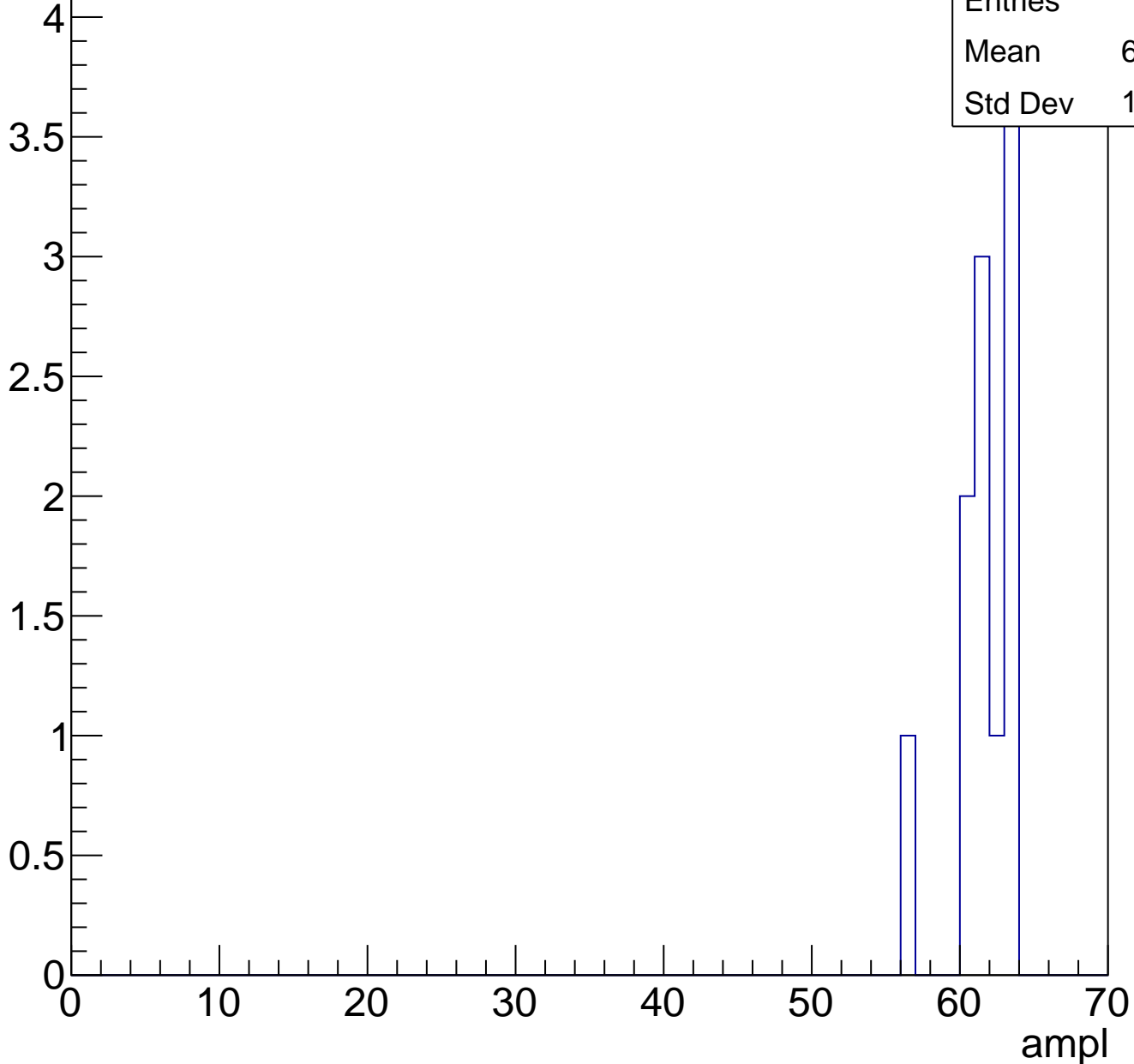
Entries	47
Mean	58.62
Std Dev	8.943



# B1L002S, U26-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U26-ch30, adc0

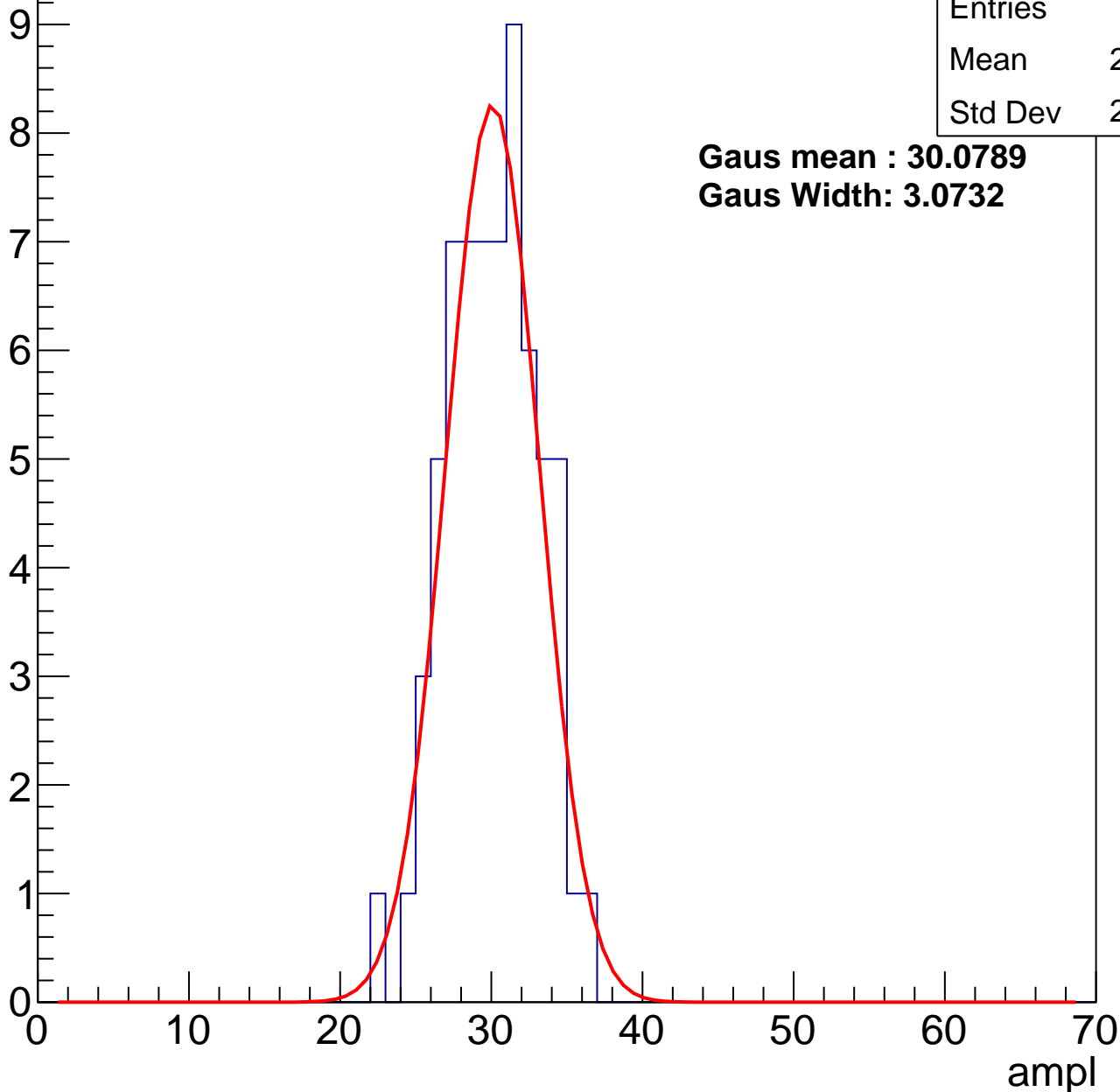
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	29.63
Std Dev	2.943

**Gaus mean : 30.0789**

**Gaus Width: 3.0732**



# B1L002S, U26-ch30, adc1

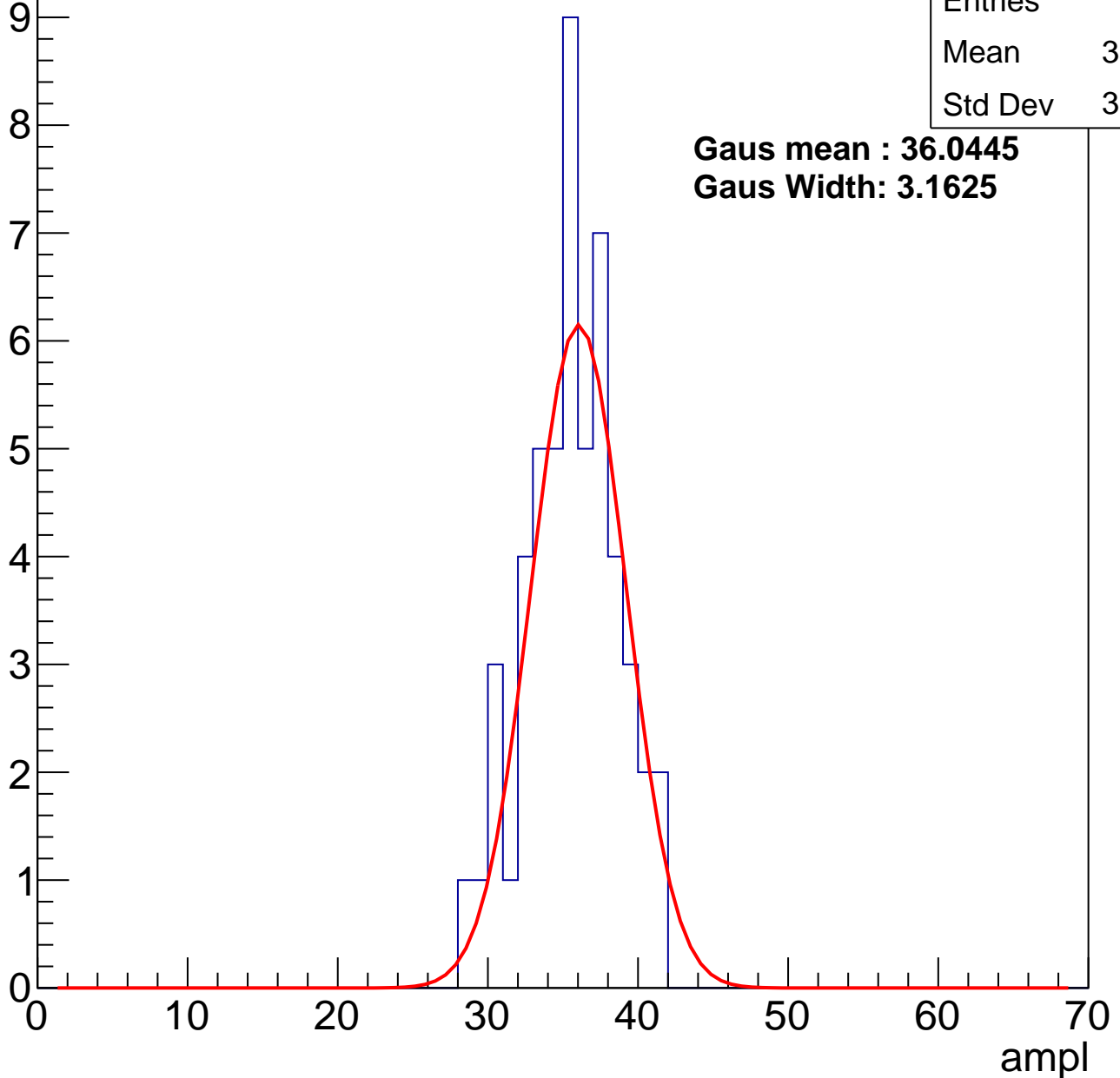
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	35.12
Std Dev	3.023

**Gaus mean : 36.0445**

**Gaus Width: 3.1625**



# B1L002S, U26-ch30, adc2

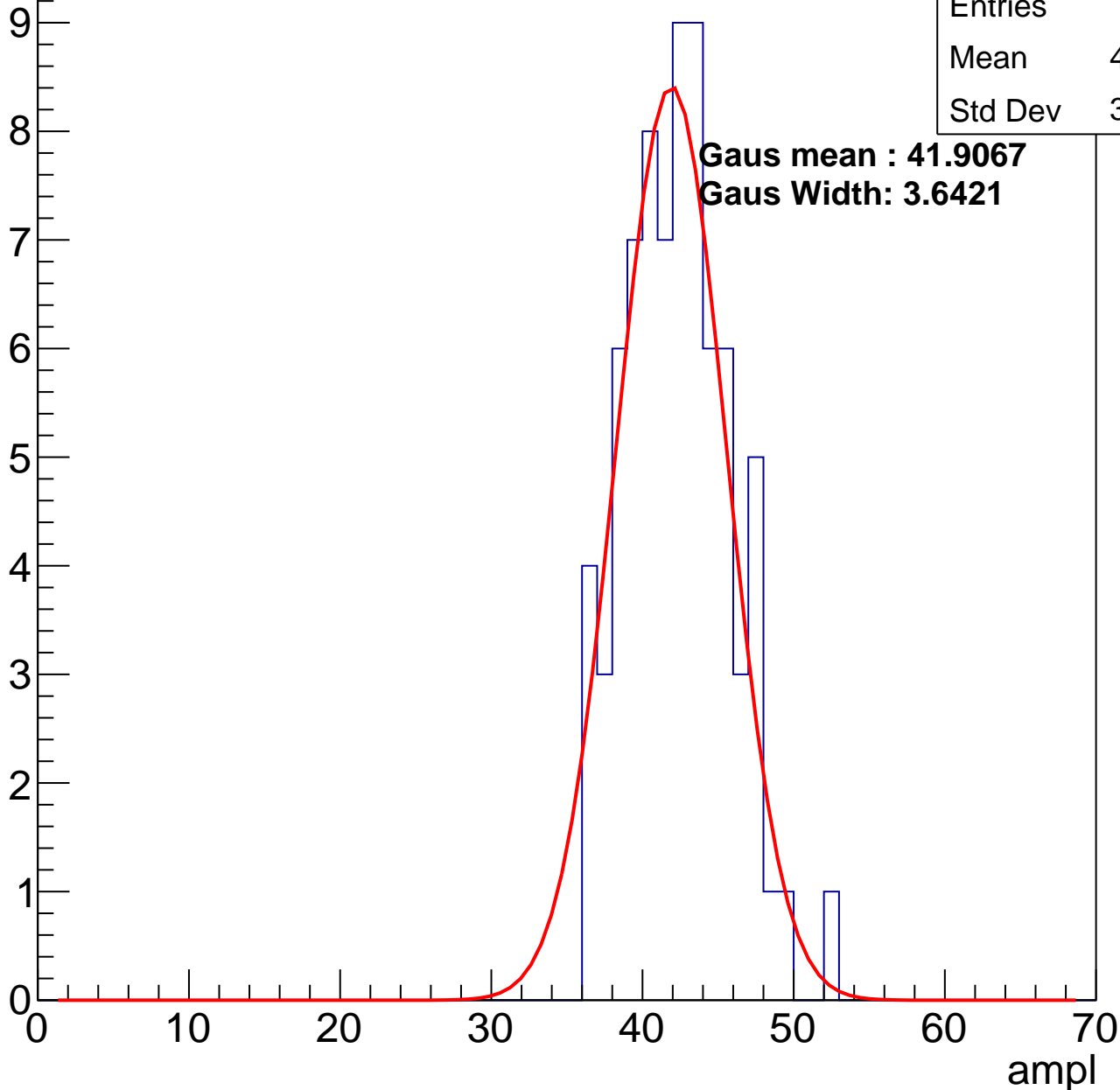
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	41.89
Std Dev	3.366

**Gaus mean : 41.9067**

**Gaus Width: 3.6421**

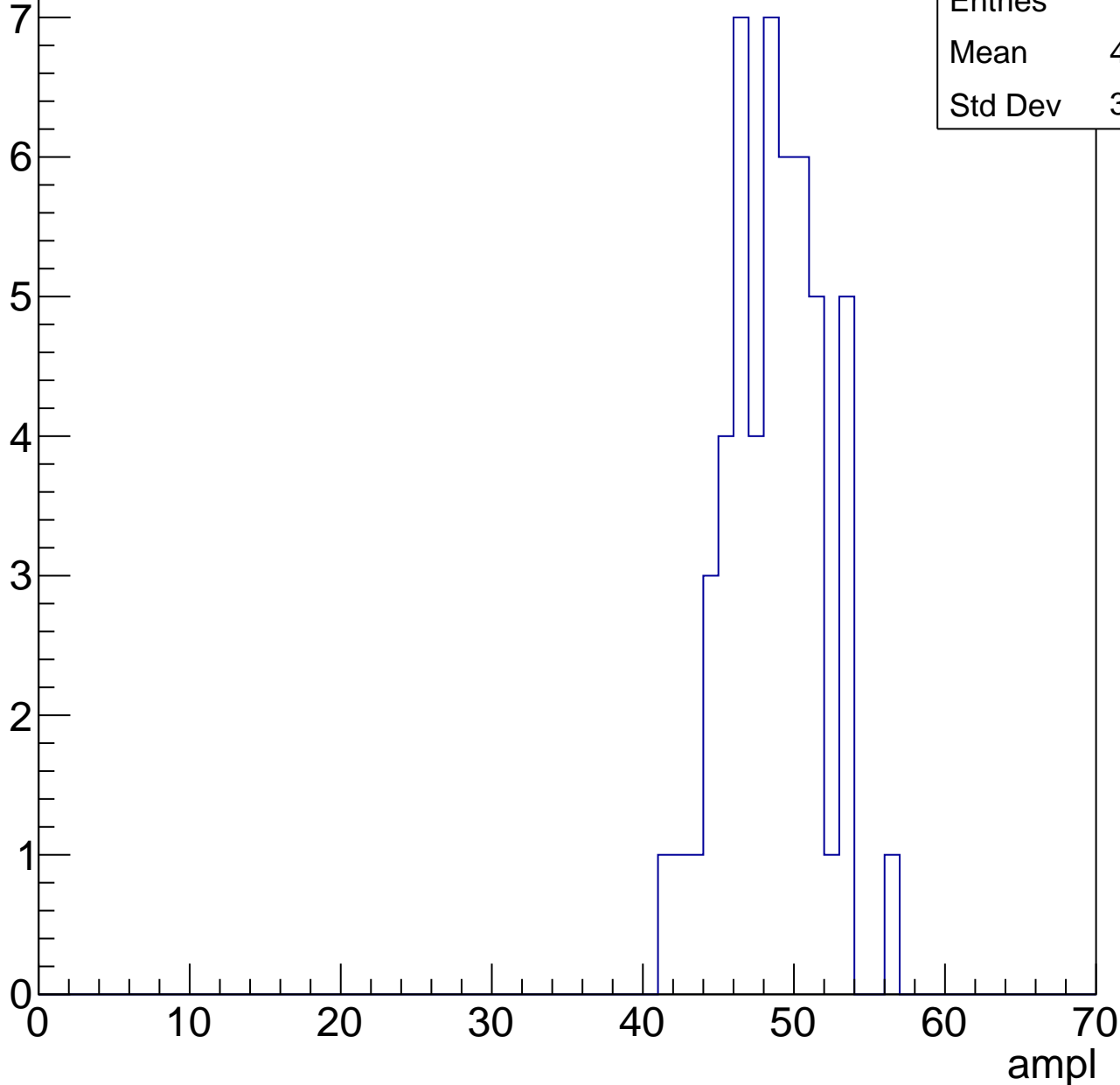


# B1L002S, U26-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	48.19
Std Dev	3.113

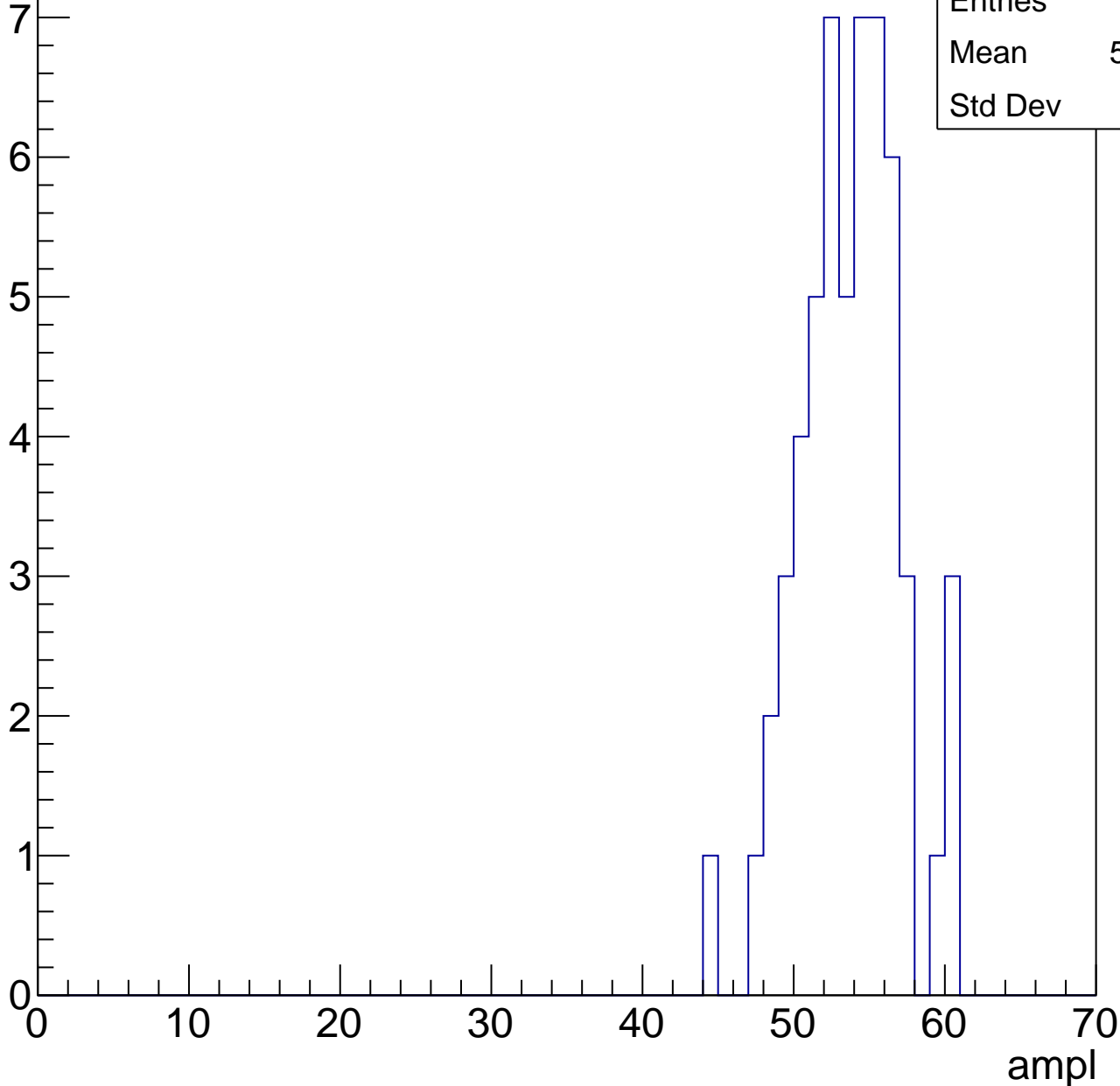


# B1L002S, U26-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	53.22
Std Dev	3.29

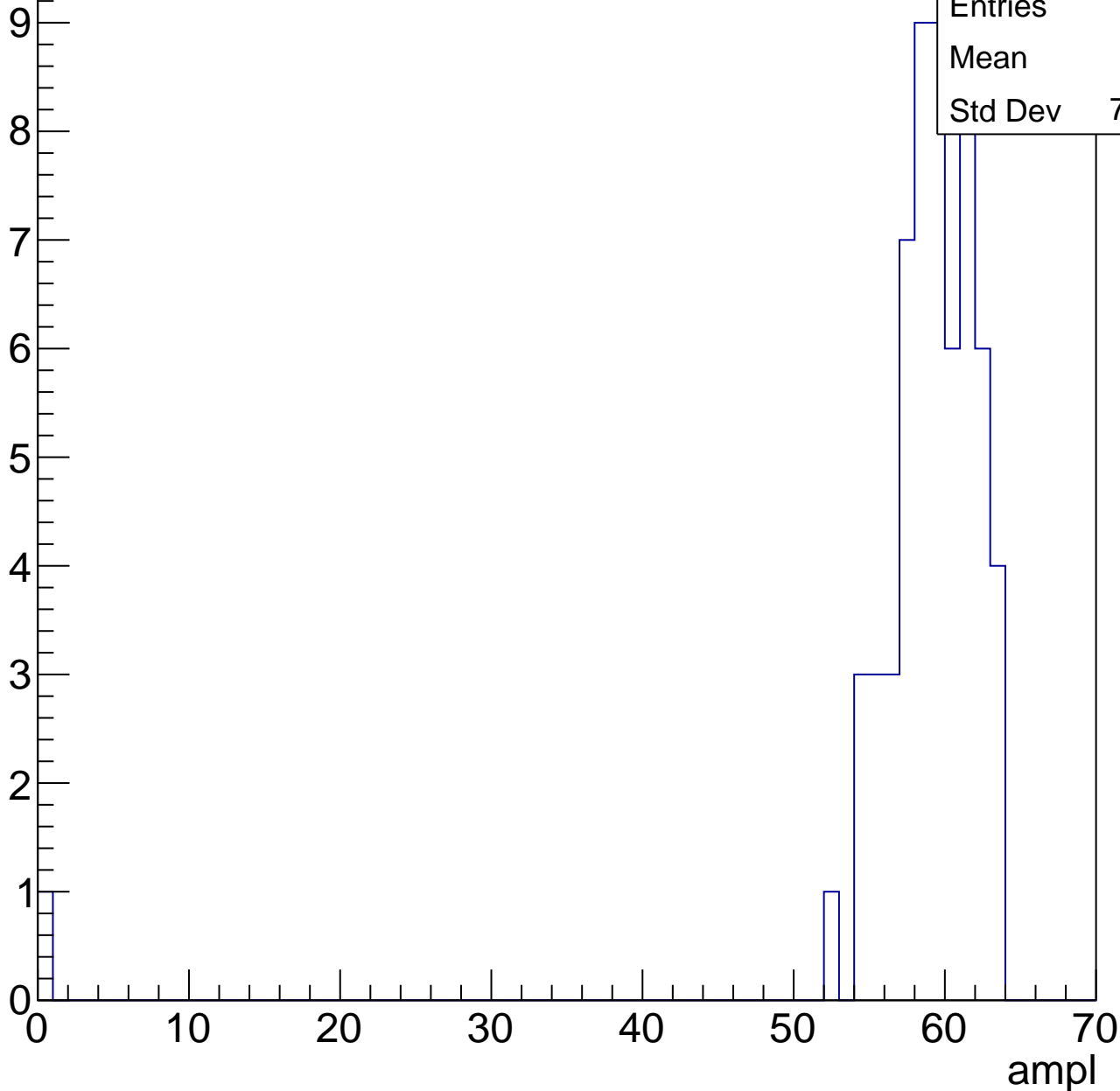


# B1L002S, U26-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.9
Std Dev	7.896

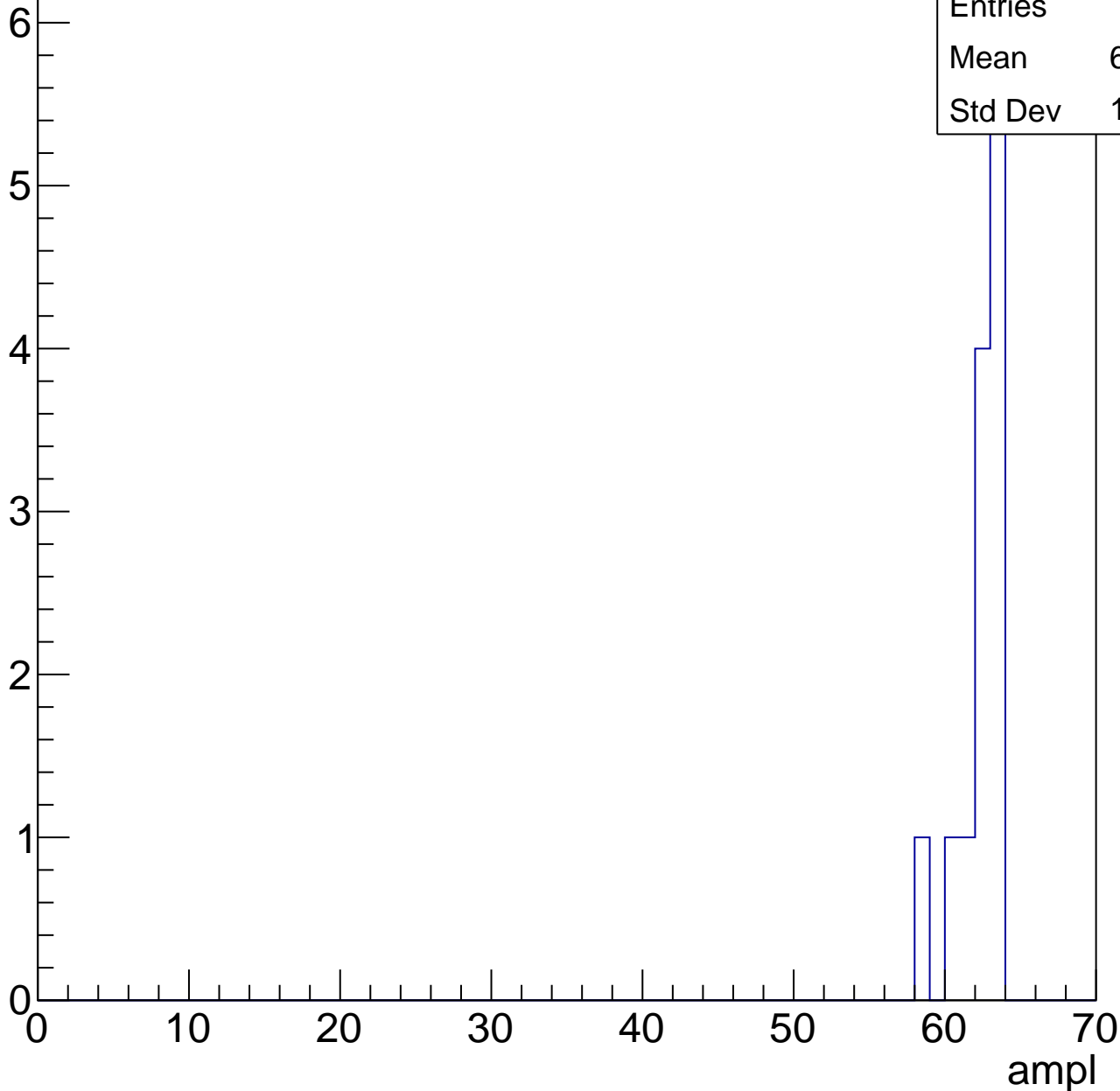


# B1L002S, U26-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.92
Std Dev	1.439





# B1L002S, U26-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U26-ch31, adc0

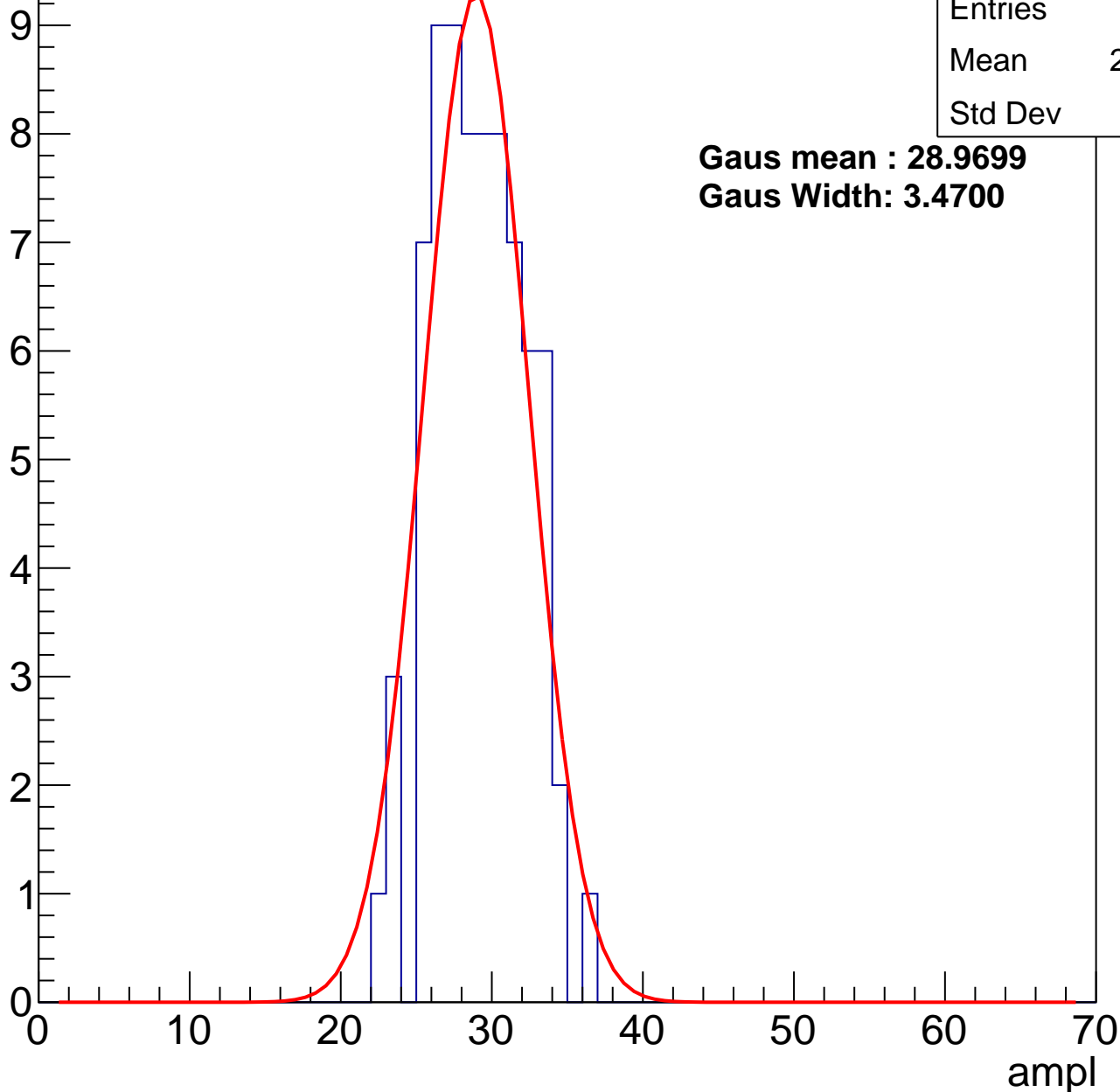
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	28.67
Std Dev	2.99

**Gaus mean : 28.9699**

**Gaus Width: 3.4700**



# B1L002S, U26-ch31, adc1

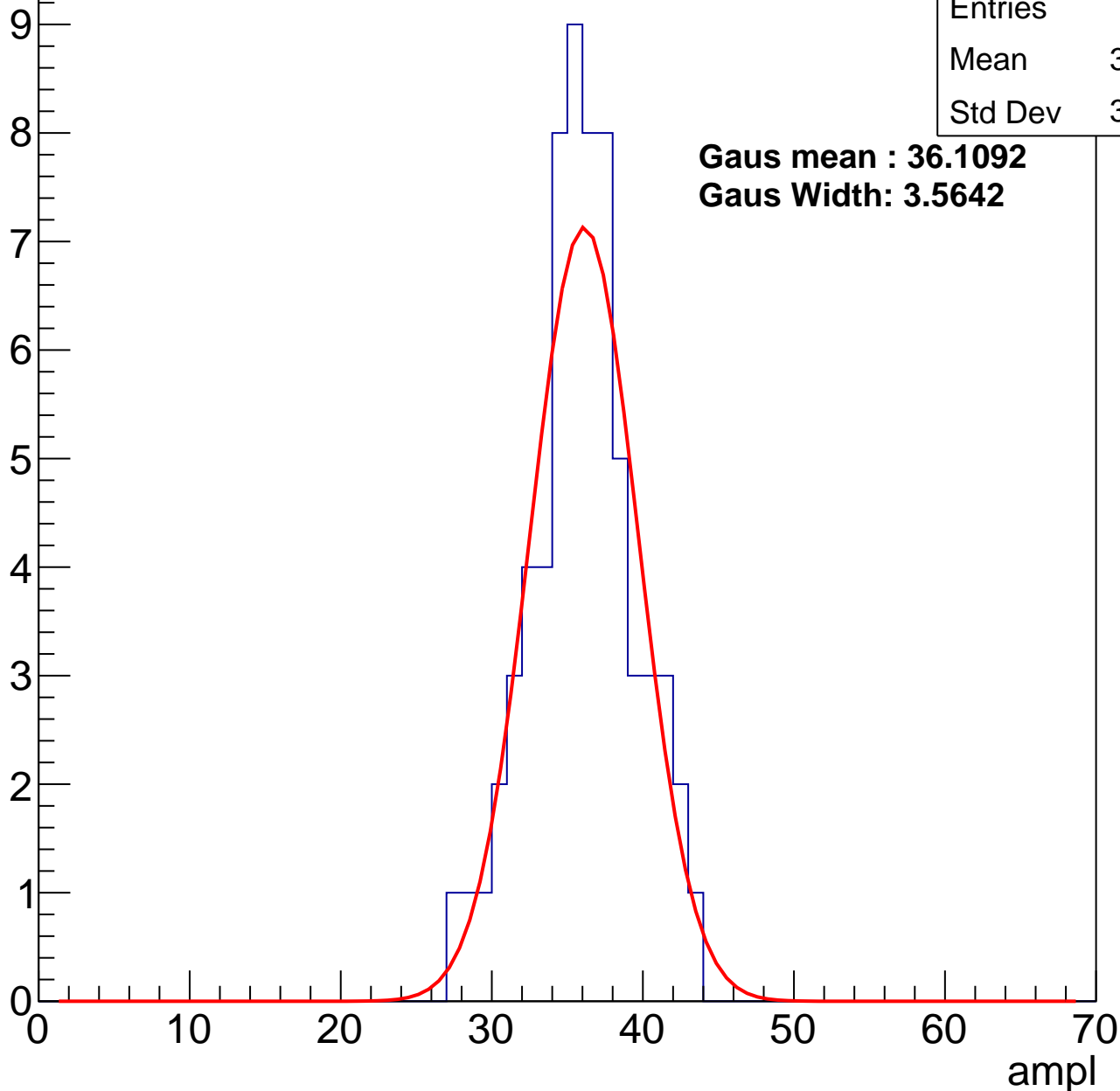
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	35.53
Std Dev	3.408

**Gaus mean : 36.1092**

**Gaus Width: 3.5642**

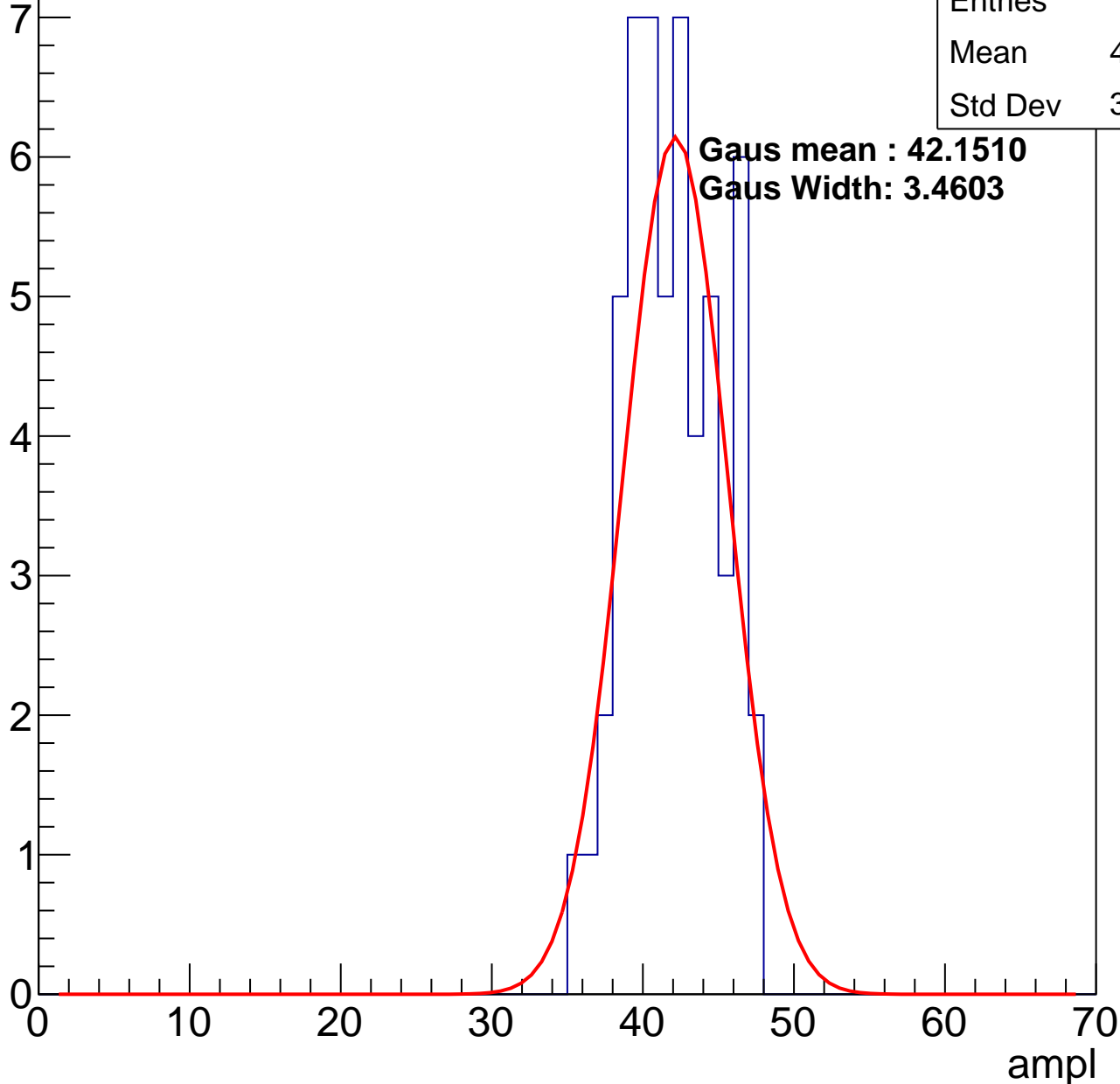


# B1L002S, U26-ch31, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.53
Std Dev	3.002

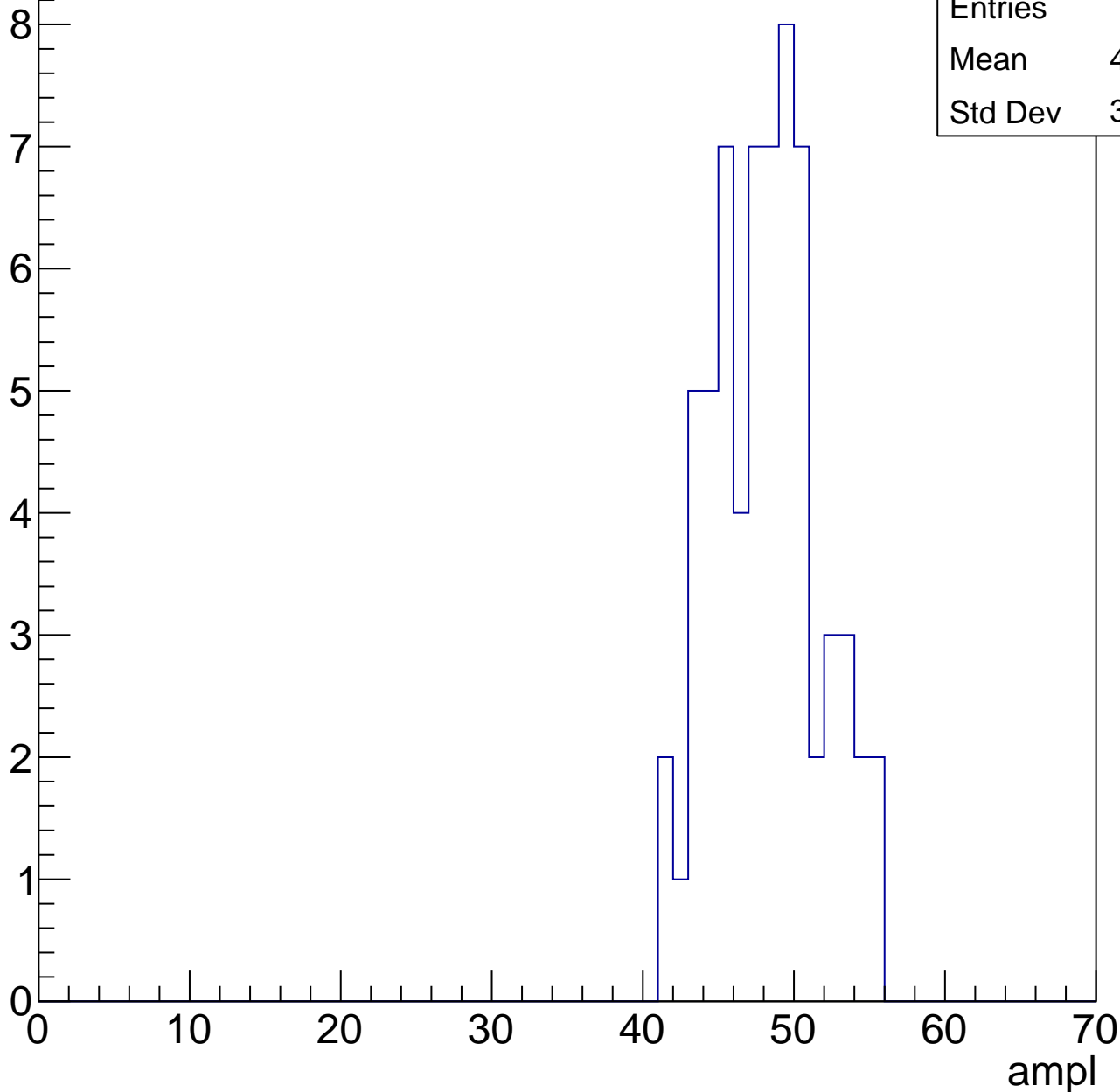


# B1L002S, U26-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	47.69
Std Dev	3.433

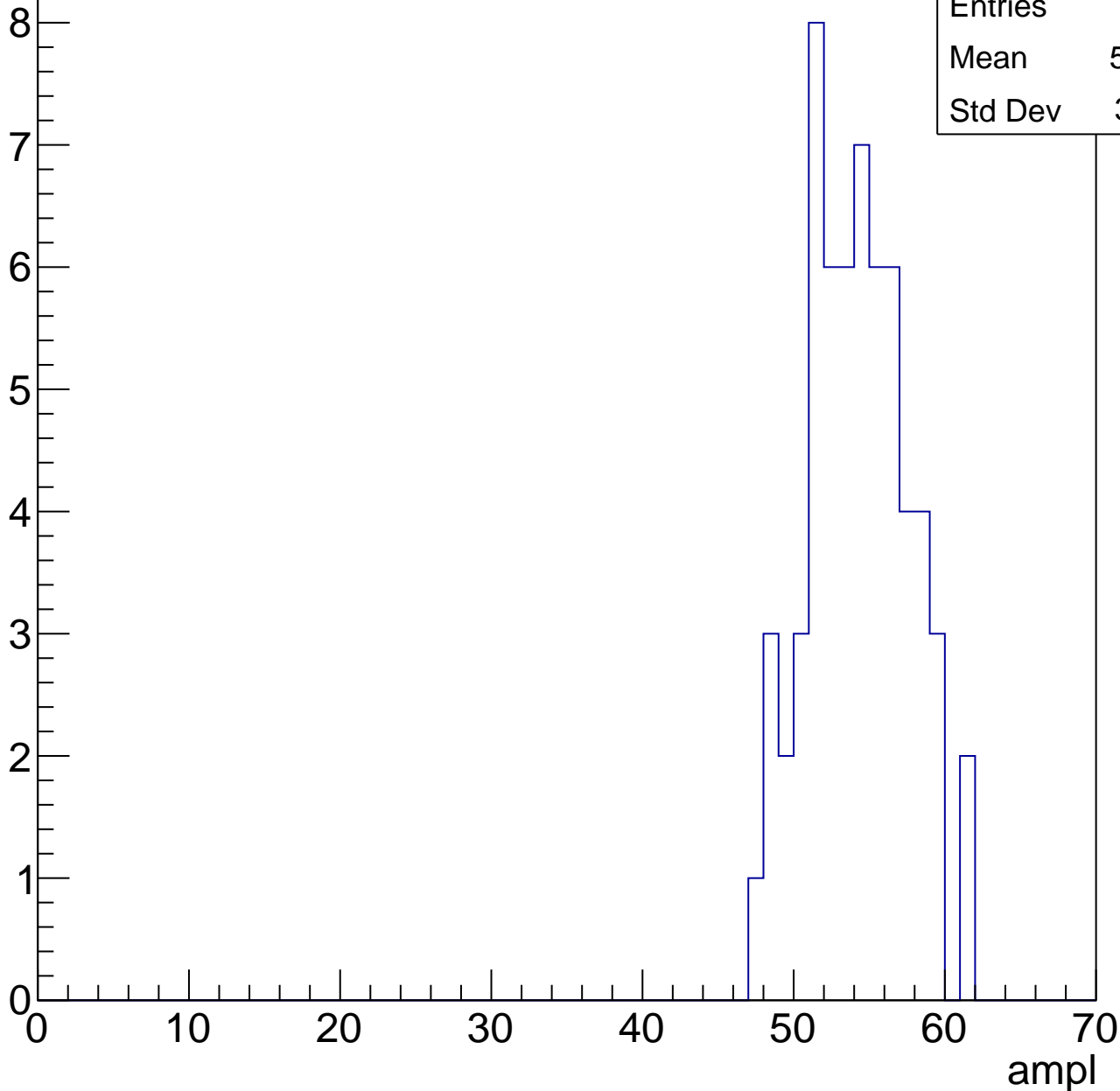


# B1L002S, U26-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	53.77
Std Dev	3.281

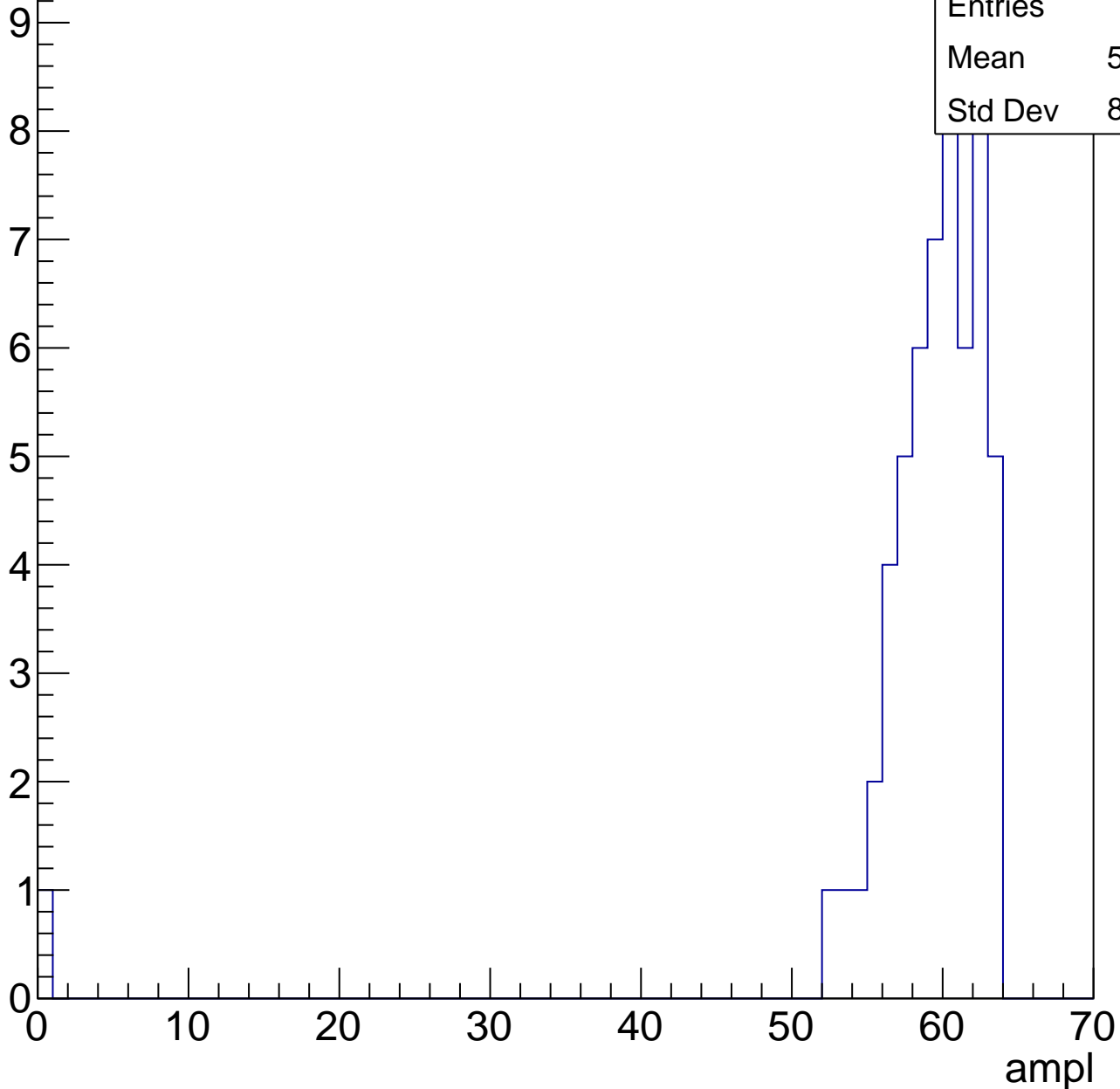


# B1L002S, U26-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	58.14
Std Dev	8.269

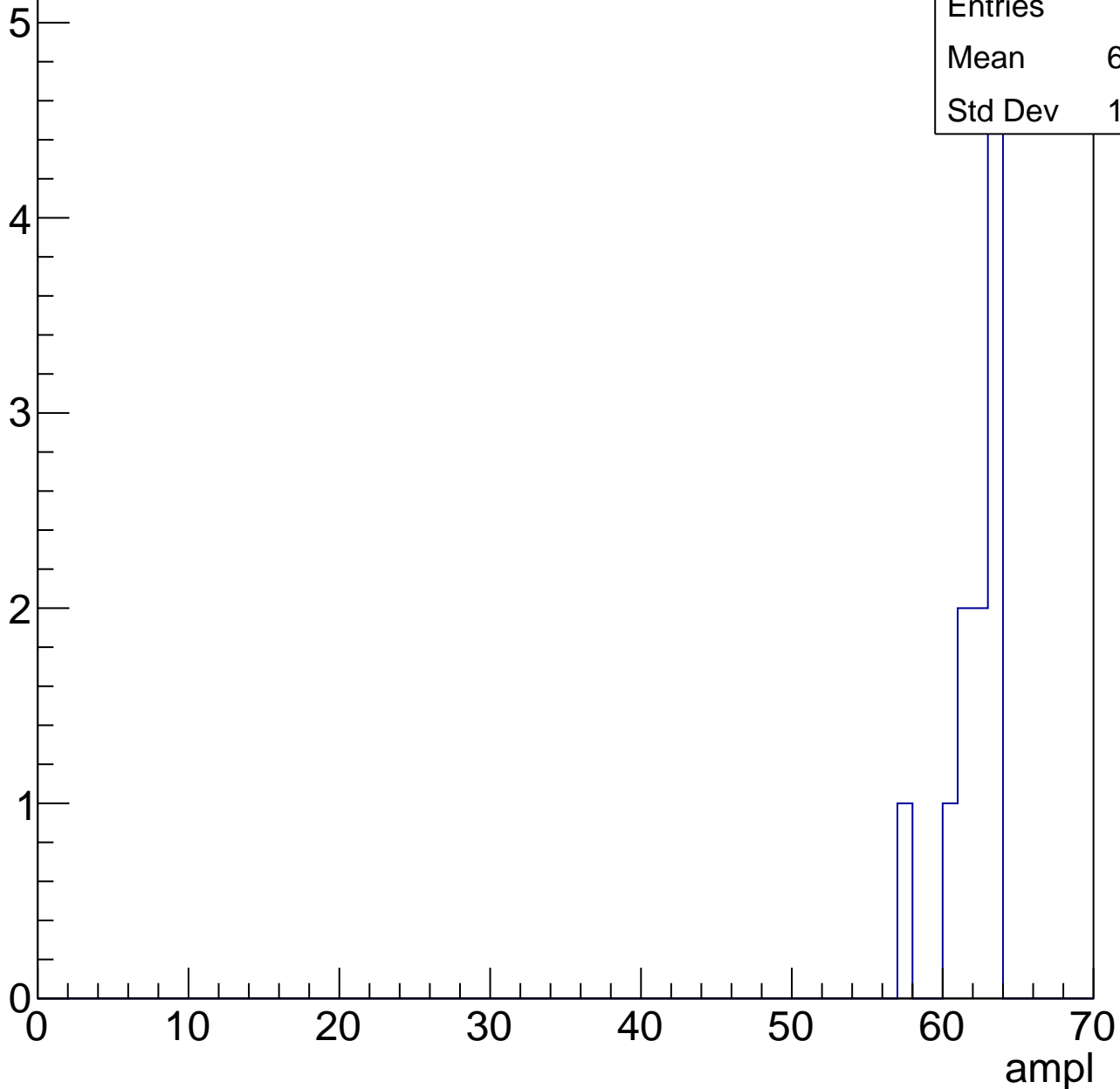


# B1L002S, U26-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	61.64
Std Dev	1.772





# B1L002S, U26-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch32, adc0

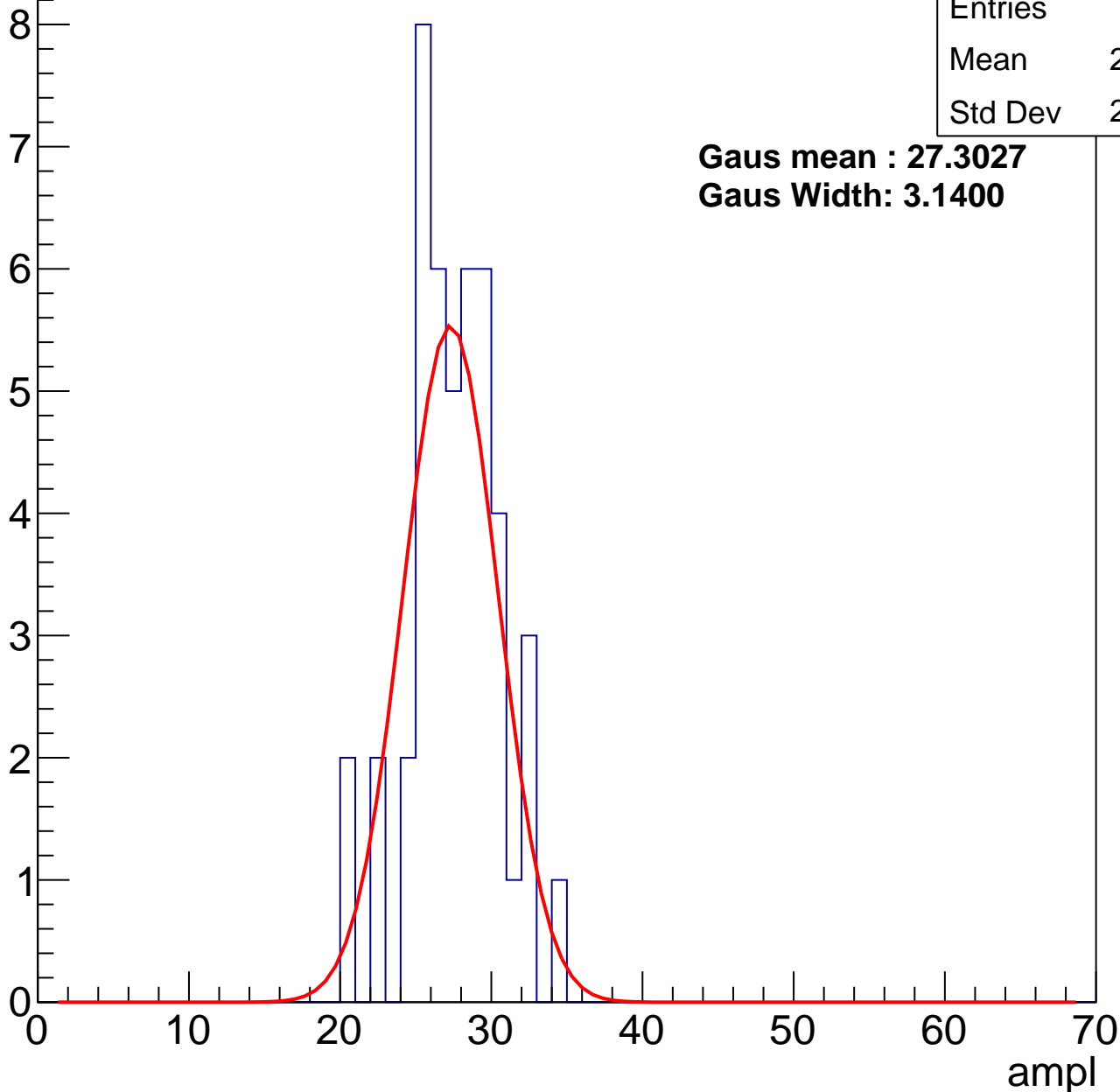
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	27.09
Std Dev	2.984

**Gaus mean : 27.3027**

**Gaus Width: 3.1400**



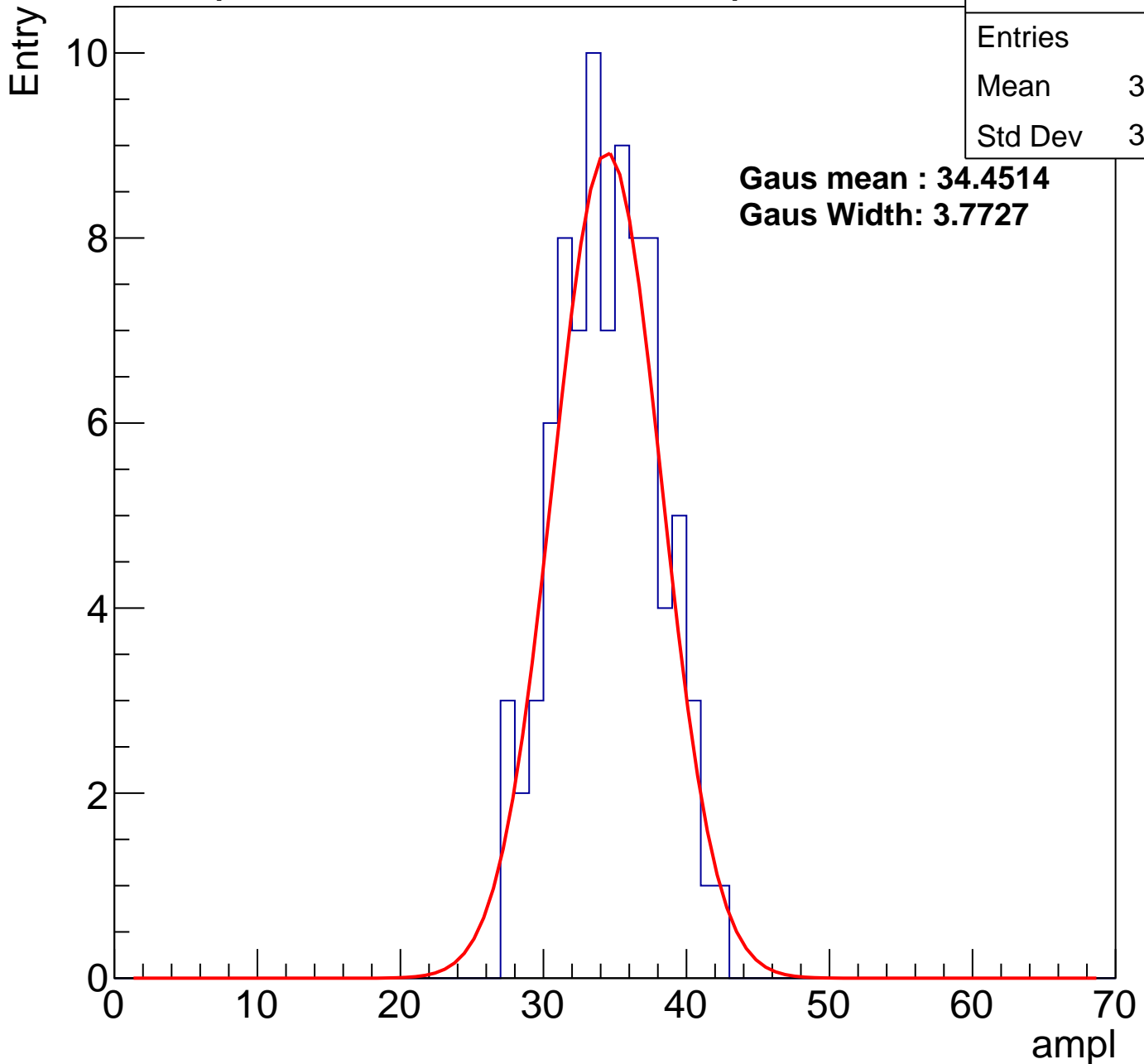
# B1L002S, U26-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	85
Mean	34.04
Std Dev	3.469

**Gaus mean : 34.4514**

**Gaus Width: 3.7727**



# B1L002S, U26-ch32, adc2

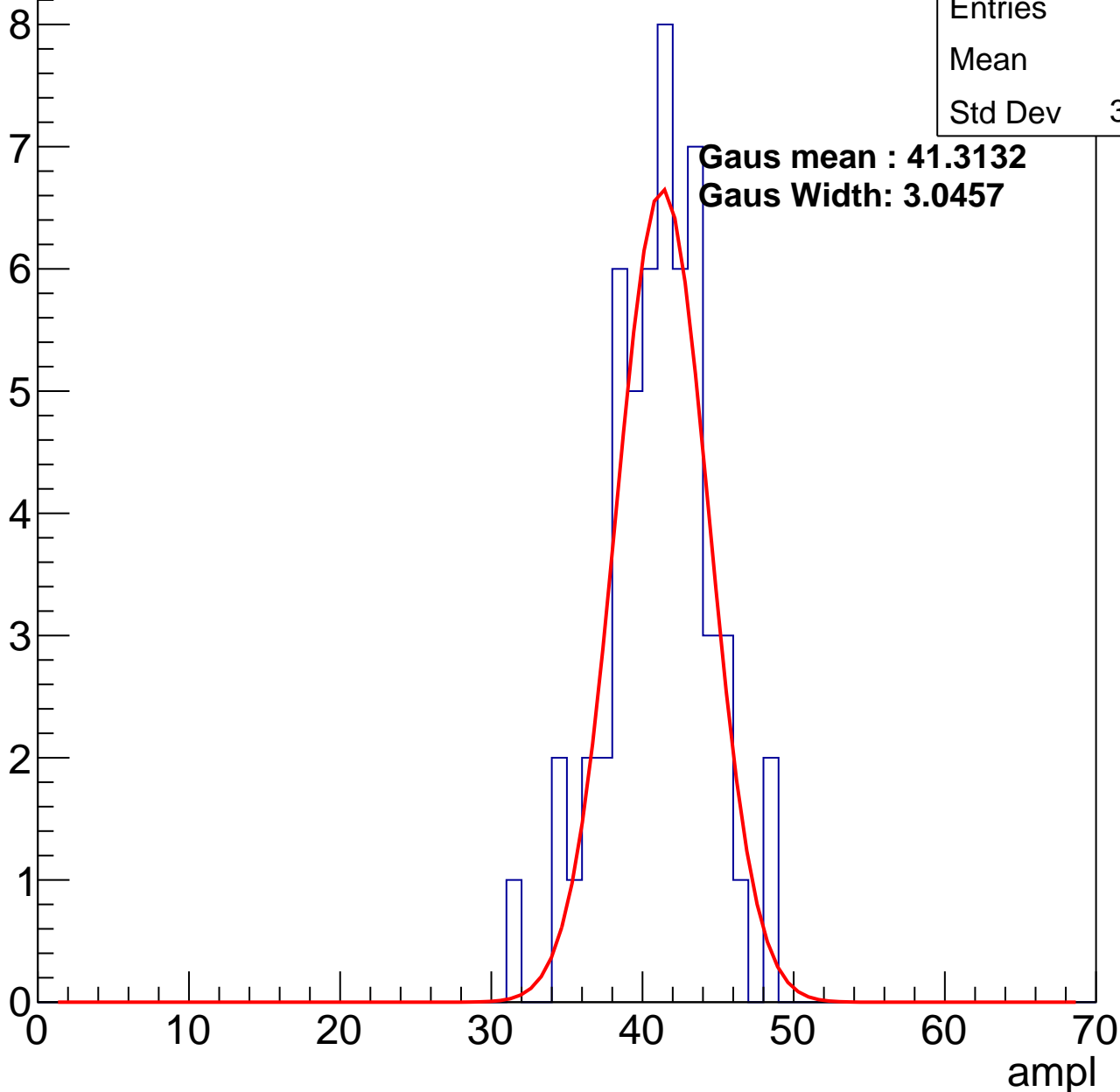
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.6
Std Dev	3.355

**Gaus mean : 41.3132**

**Gaus Width: 3.0457**

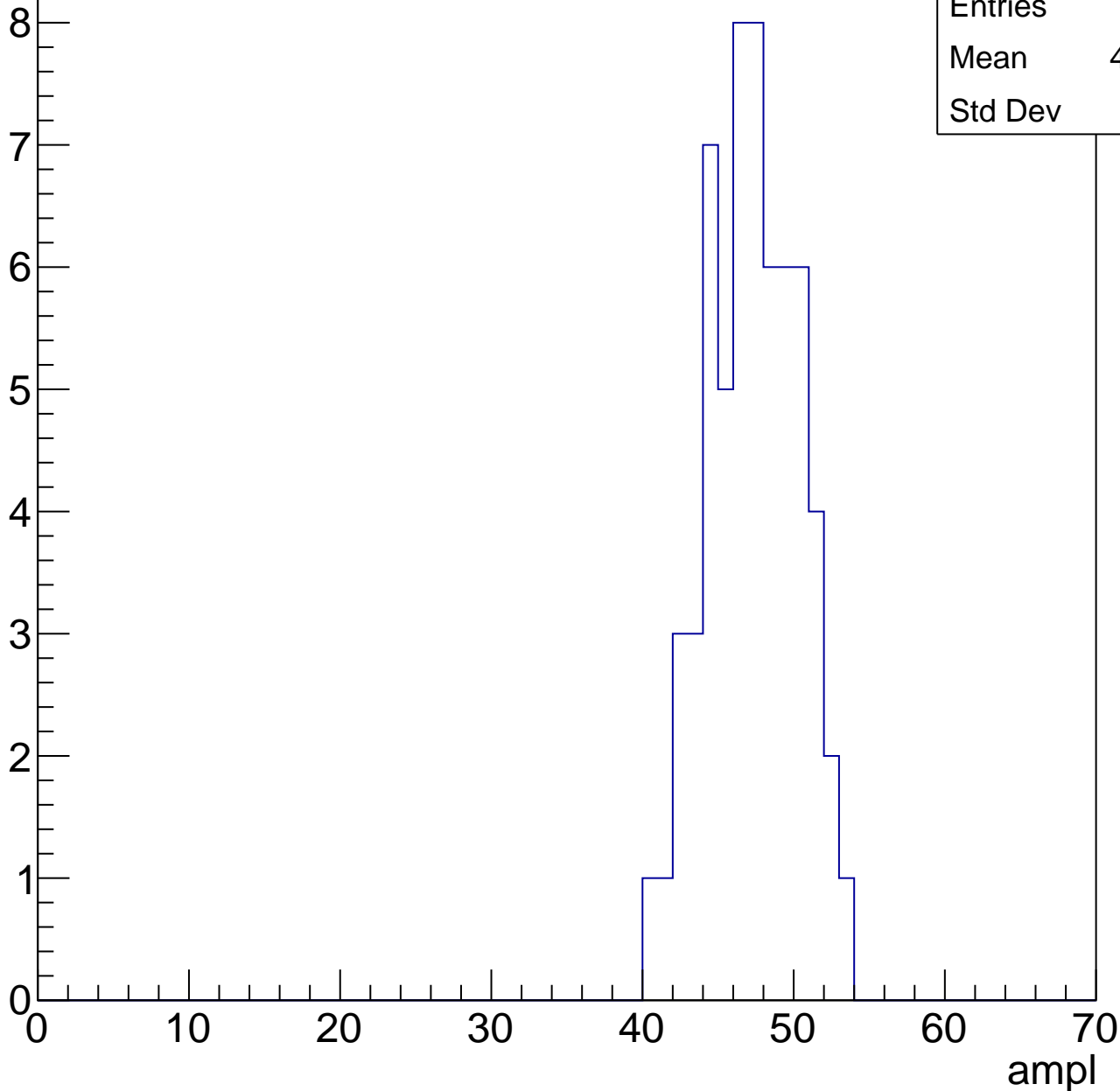


# B1L002S, U26-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.82
Std Dev	2.95

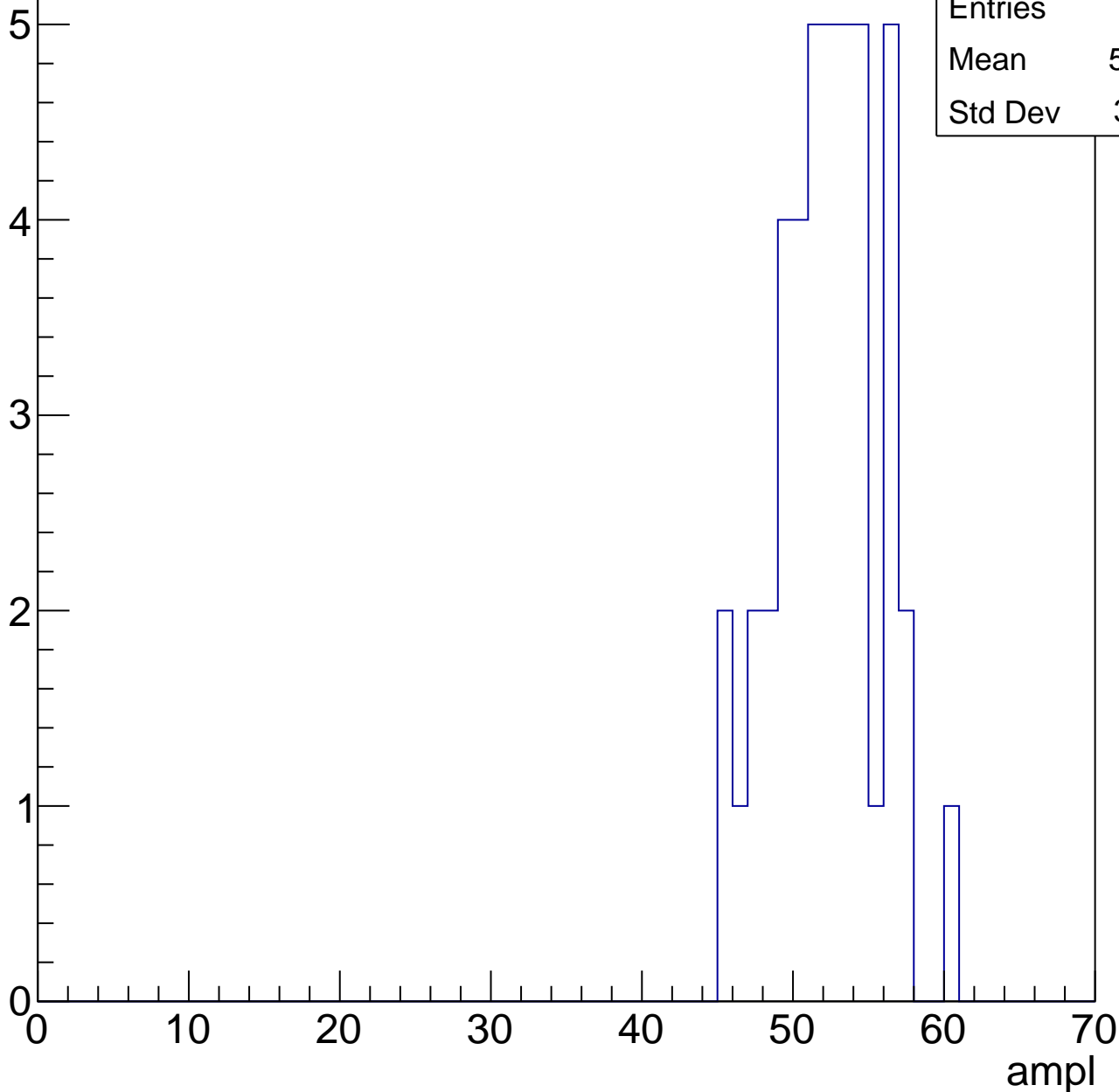


# B1L002S, U26-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

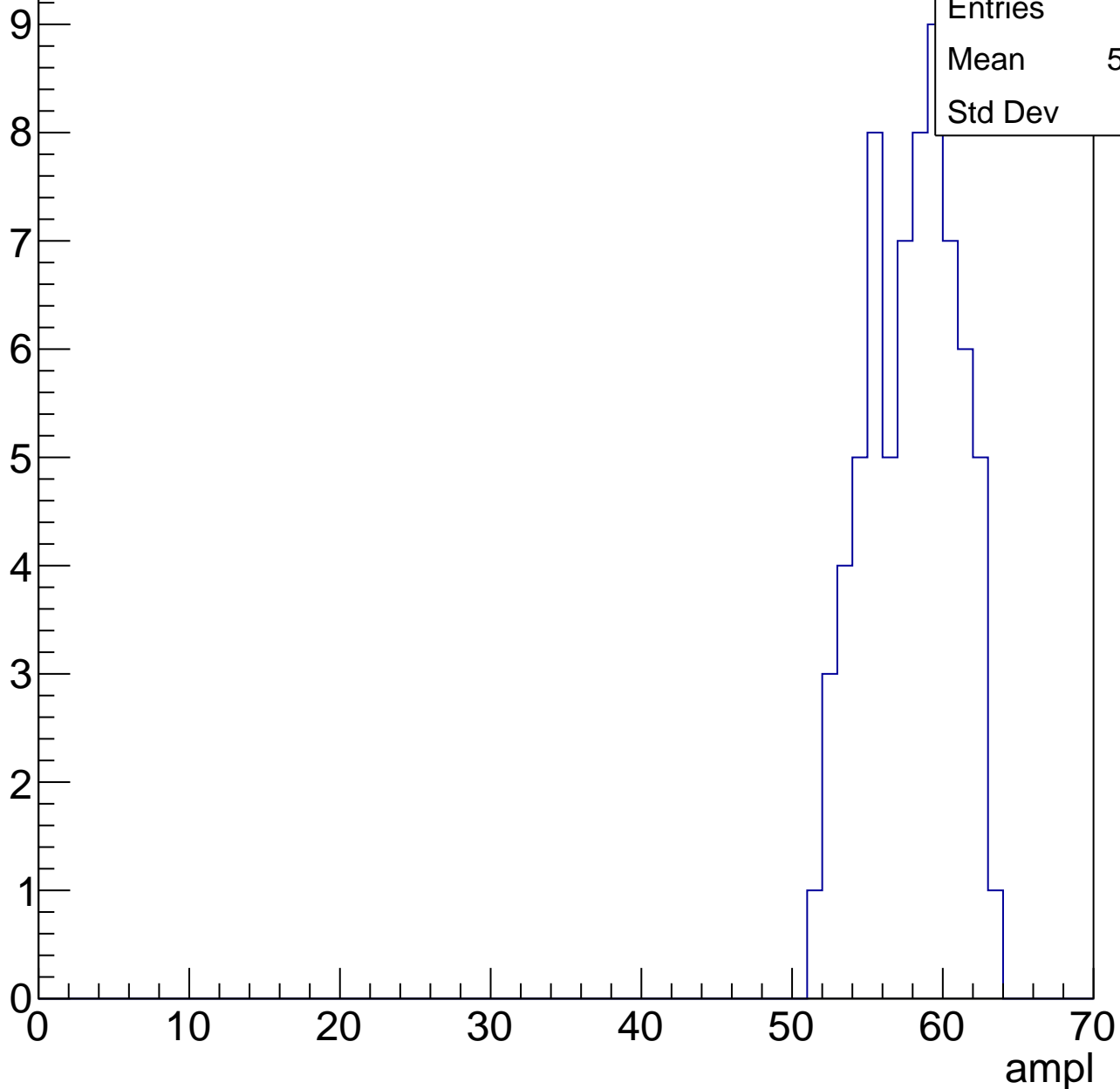
Entries	44
Mean	51.84
Std Dev	3.371



# B1L002S, U26-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

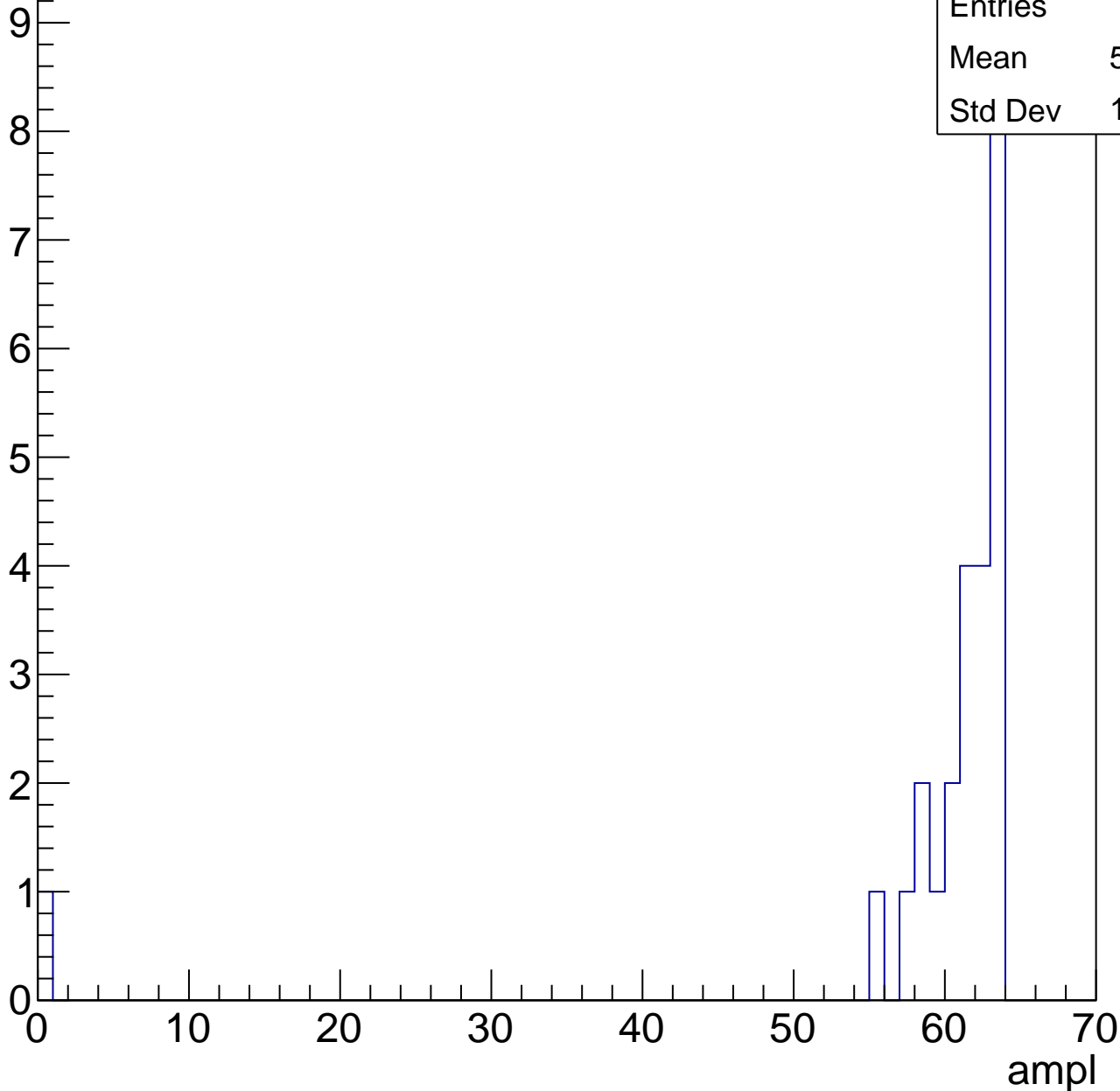


# B1L002S, U26-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.64
Std Dev	12.16





# B1L002S, U26-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch33, adc0

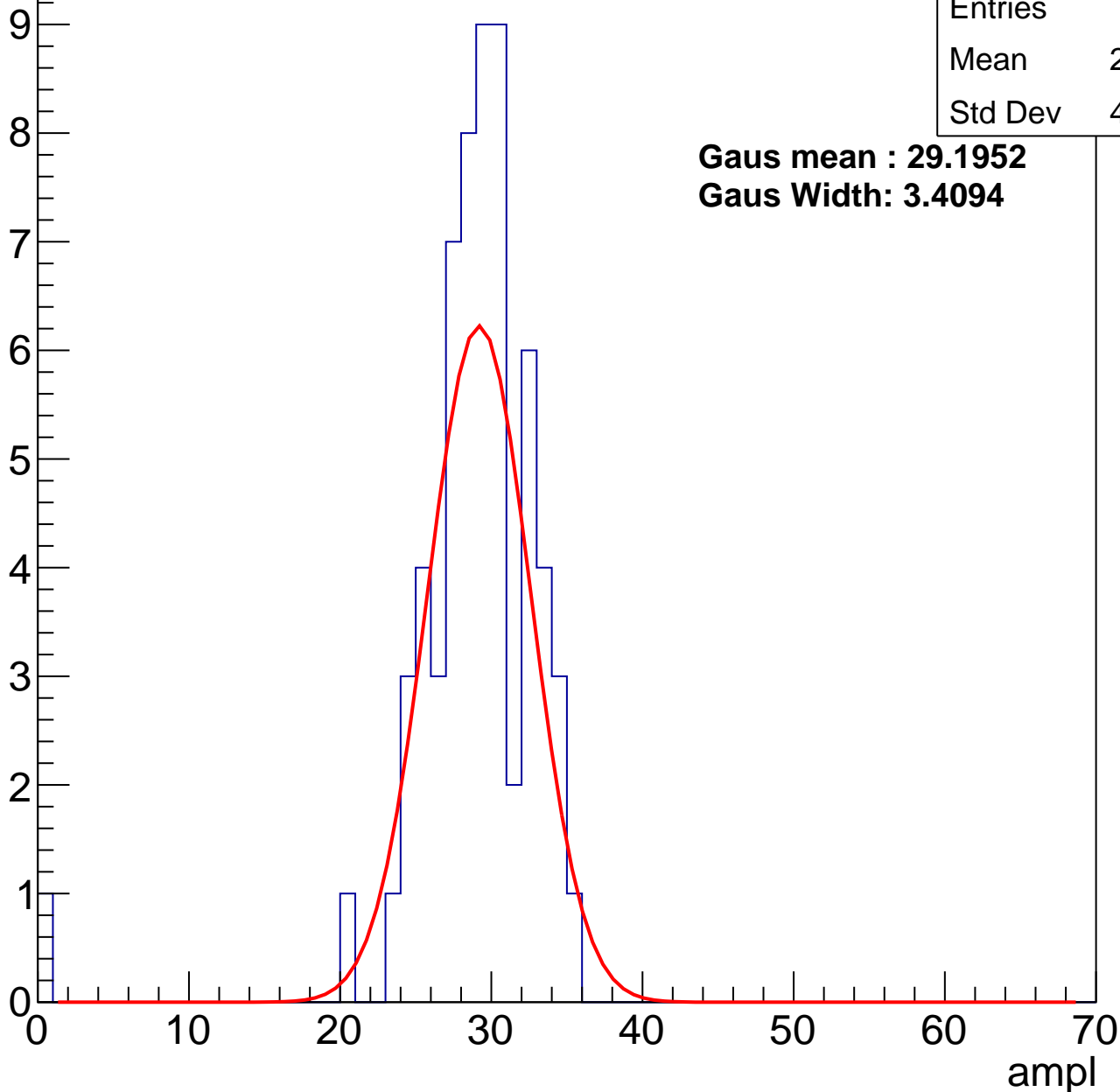
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.39
Std Dev	4.722

**Gaus mean : 29.1952**

**Gaus Width: 3.4094**



# B1L002S, U26-ch33, adc1

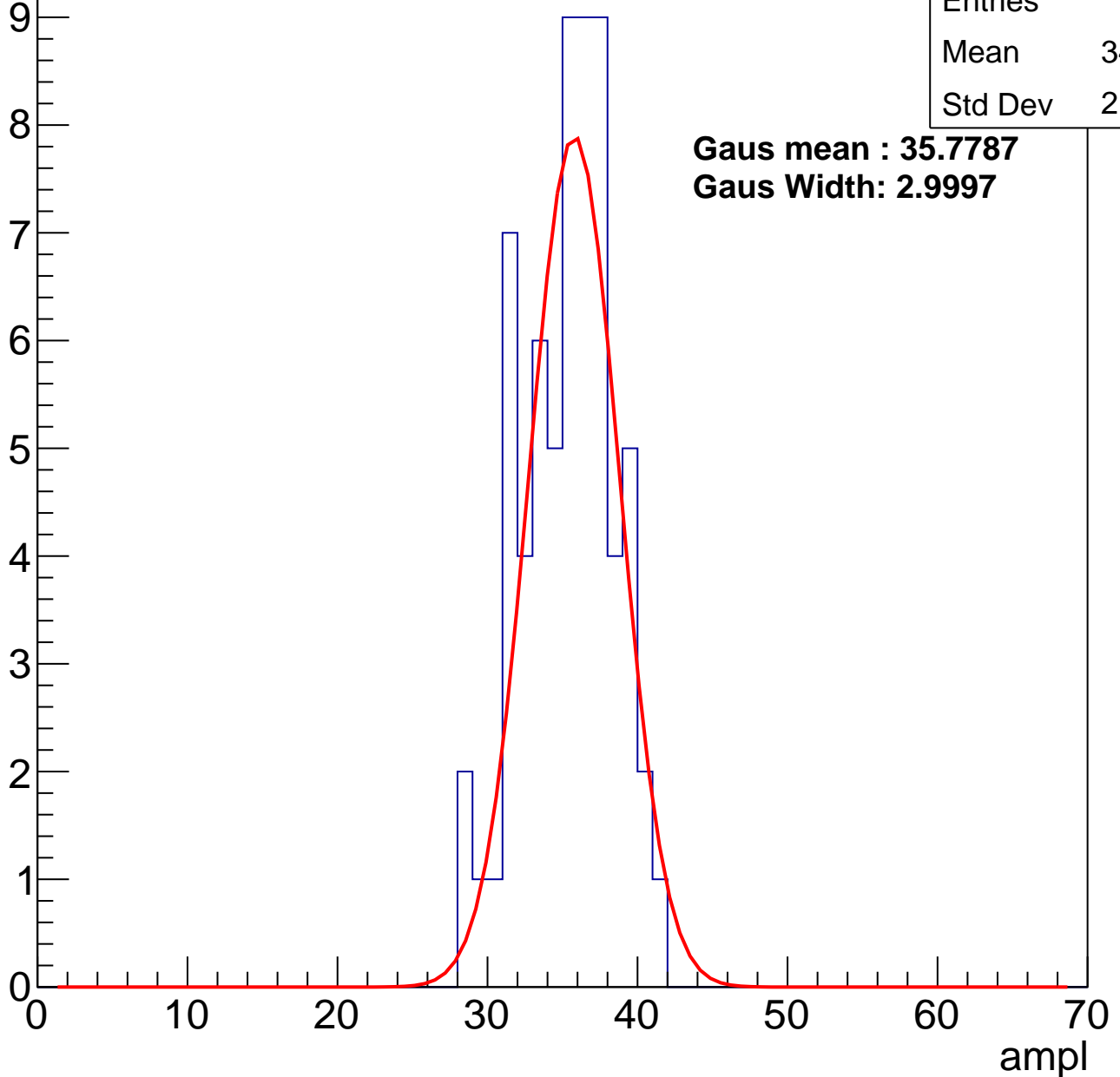
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.89
Std Dev	2.993

**Gaus mean : 35.7787**

**Gaus Width: 2.9997**



# B1L002S, U26-ch33, adc2

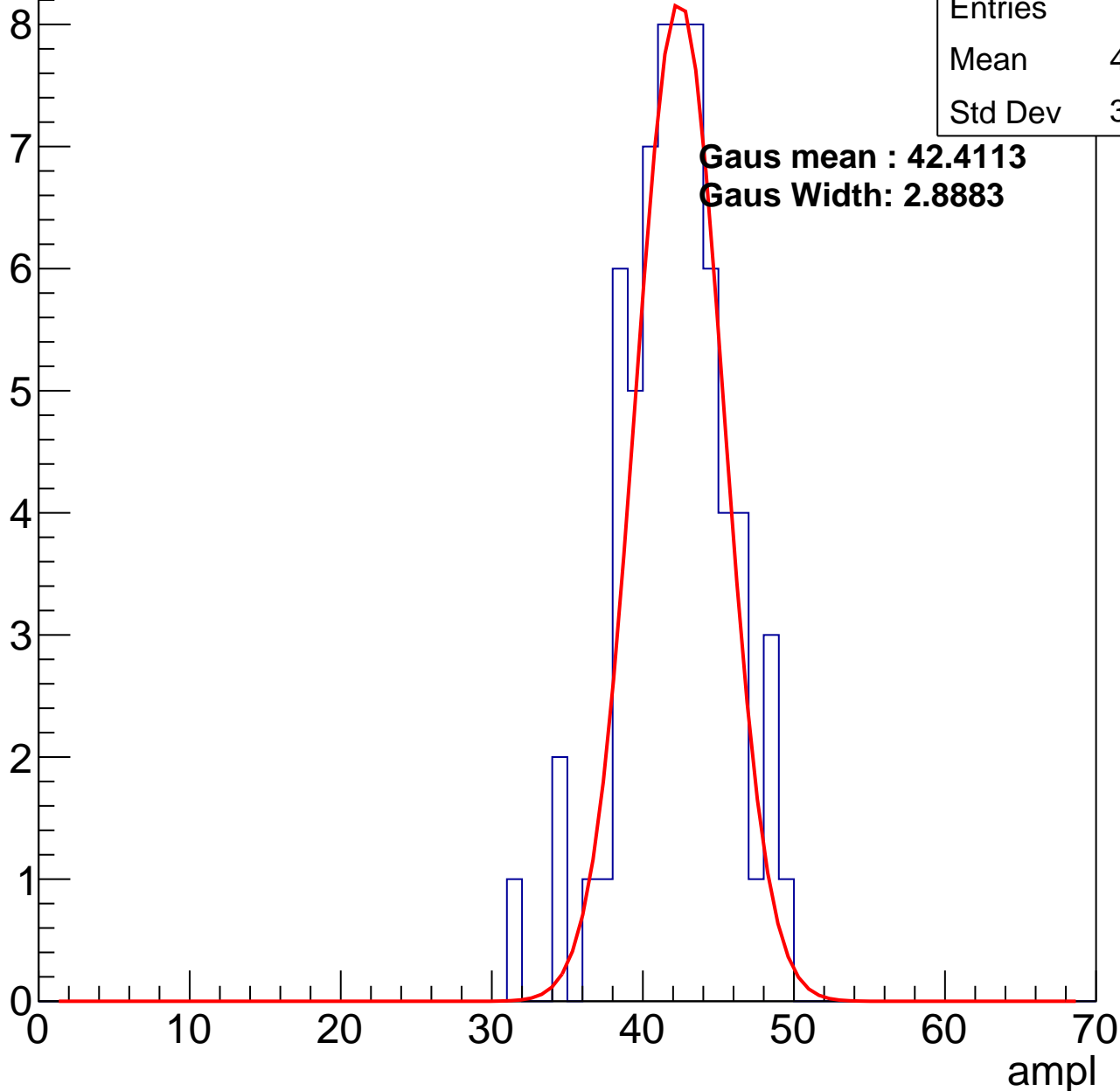
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.68
Std Dev	3.469

**Gaus mean : 42.4113**

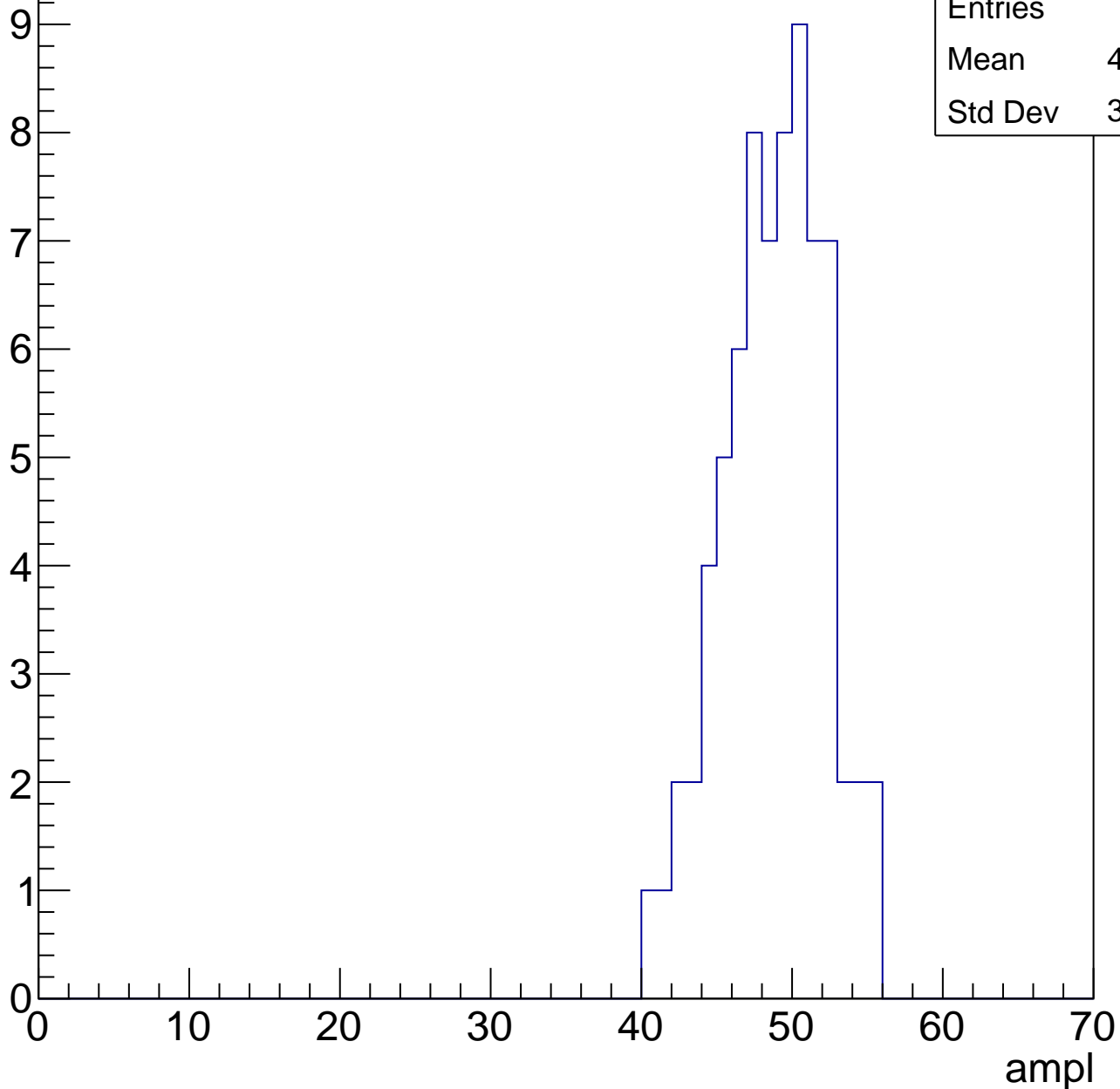
**Gaus Width: 2.8883**



# B1L002S, U26-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

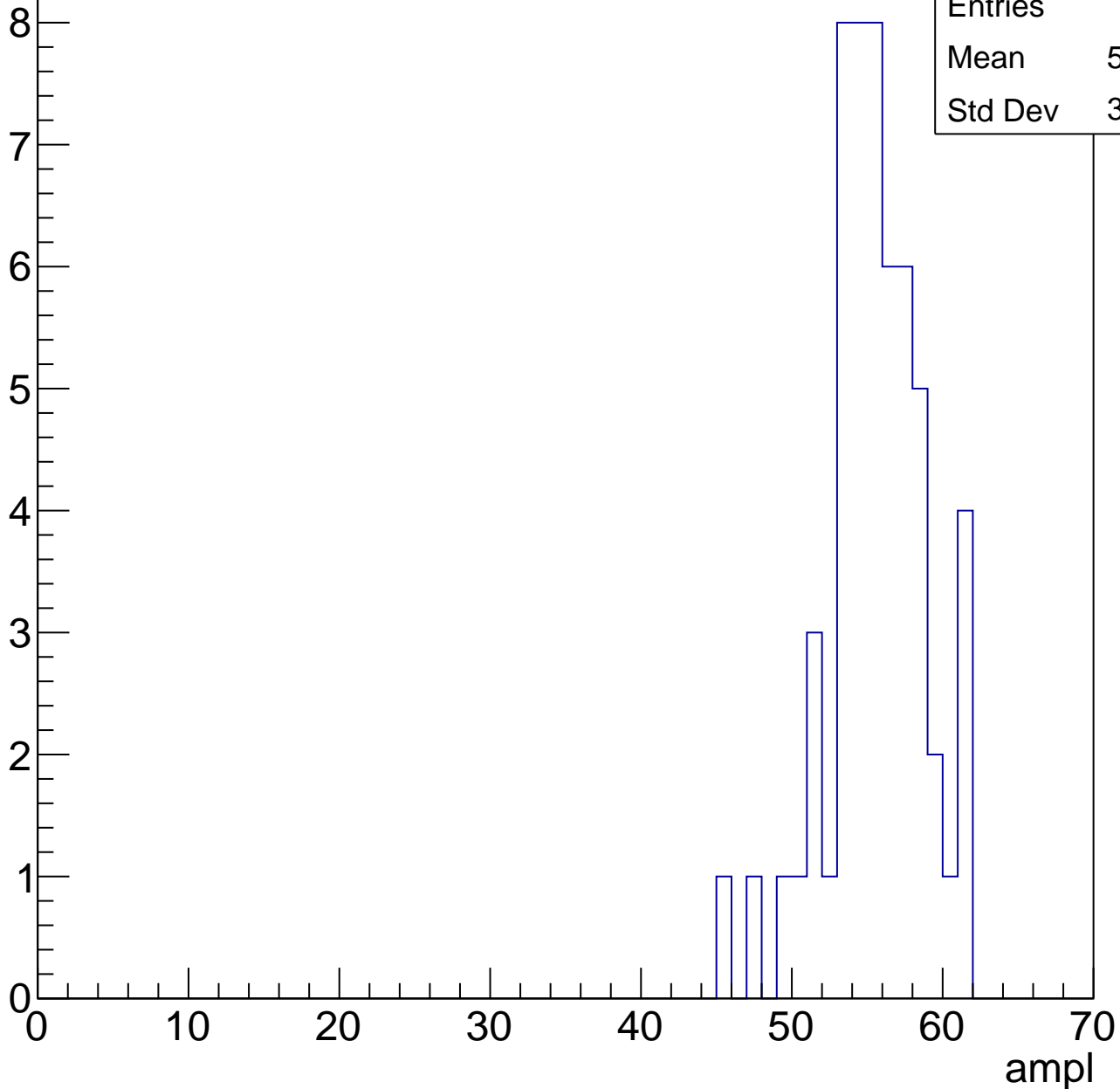


# B1L002S, U26-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	55.04
Std Dev	3.268

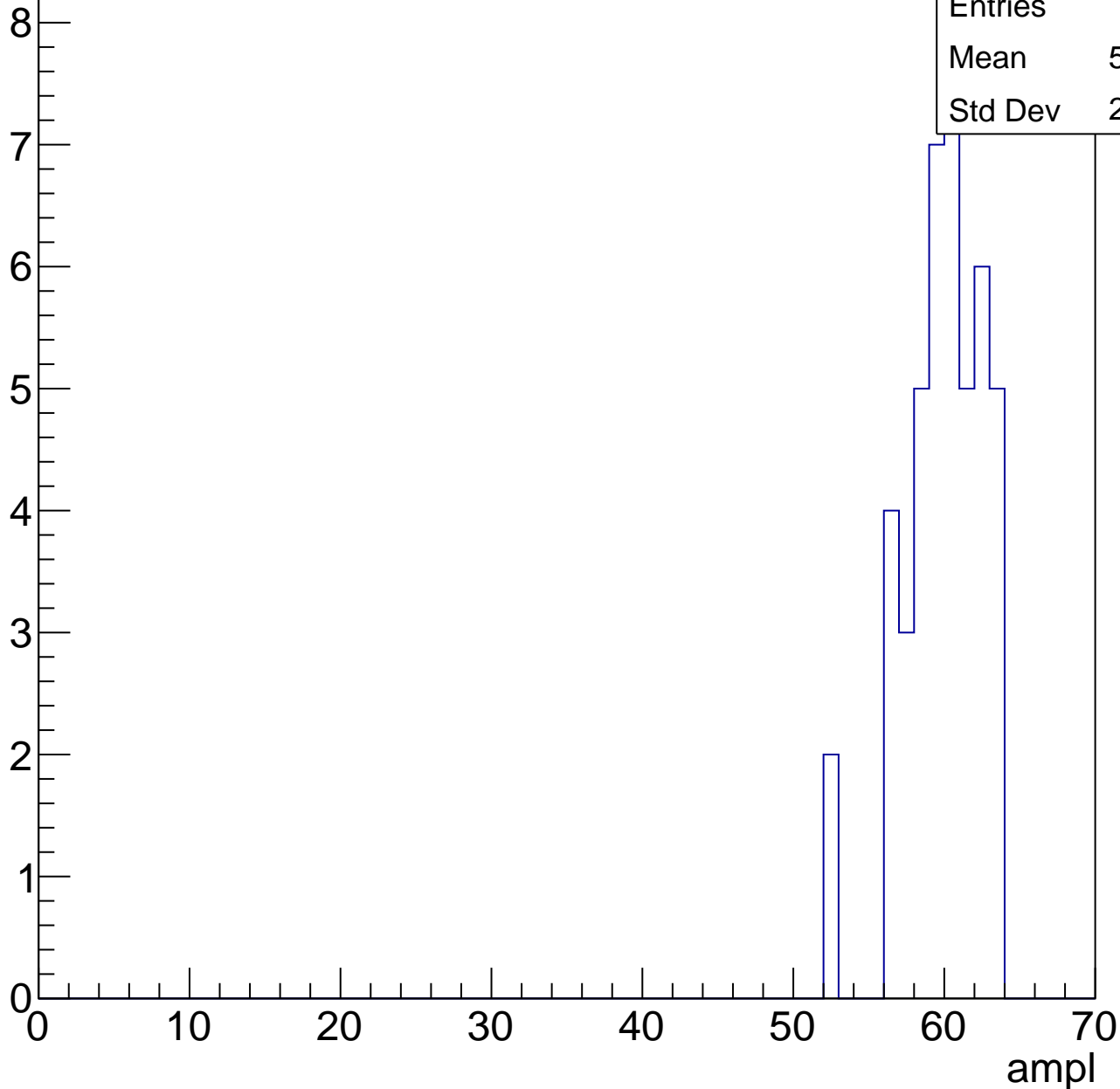


# B1L002S, U26-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

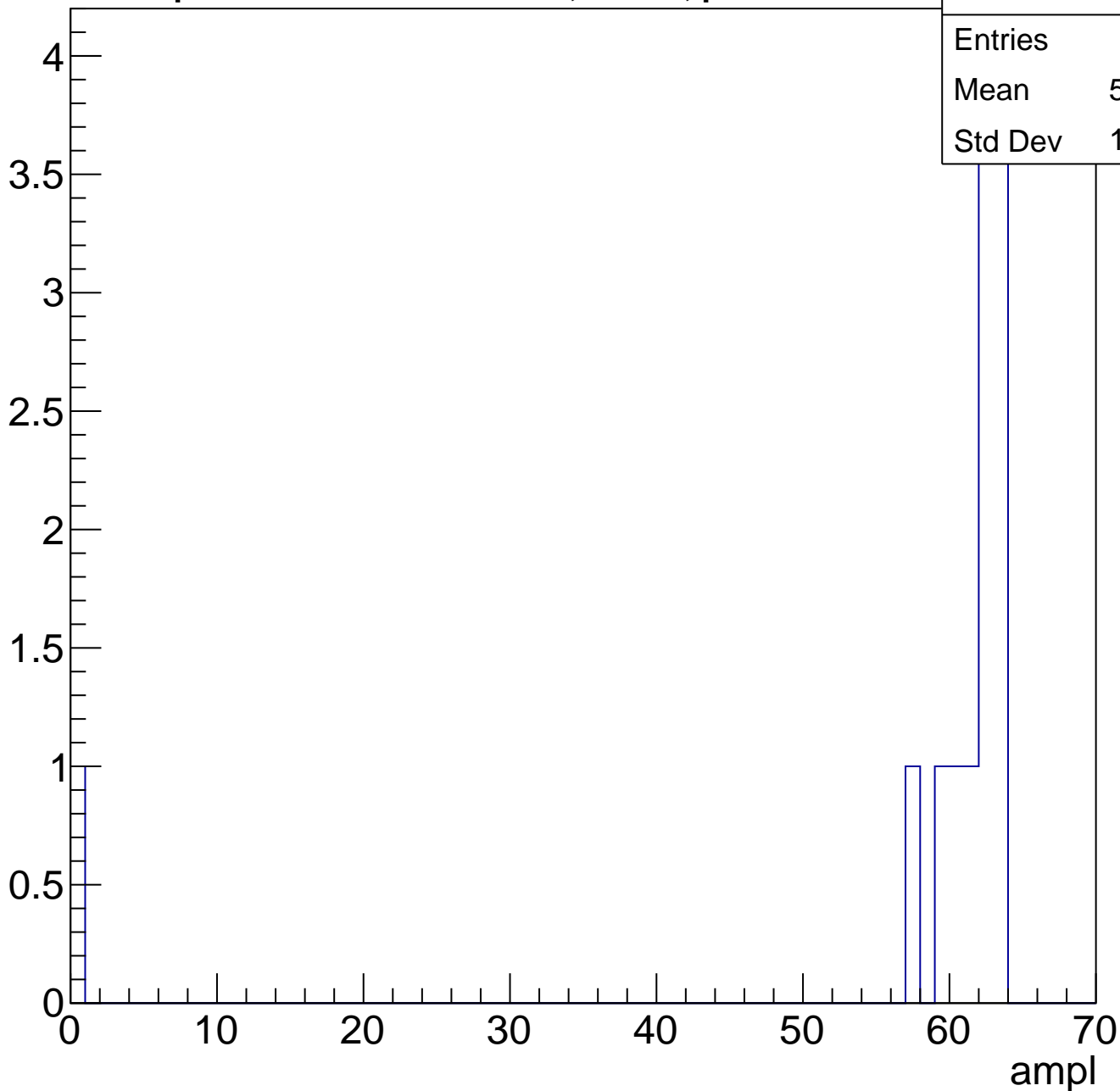
Entries	45
Mean	59.42
Std Dev	2.603



# B1L002S, U26-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	27.71
Std Dev	4.599

**Gaus mean : 28.7303**

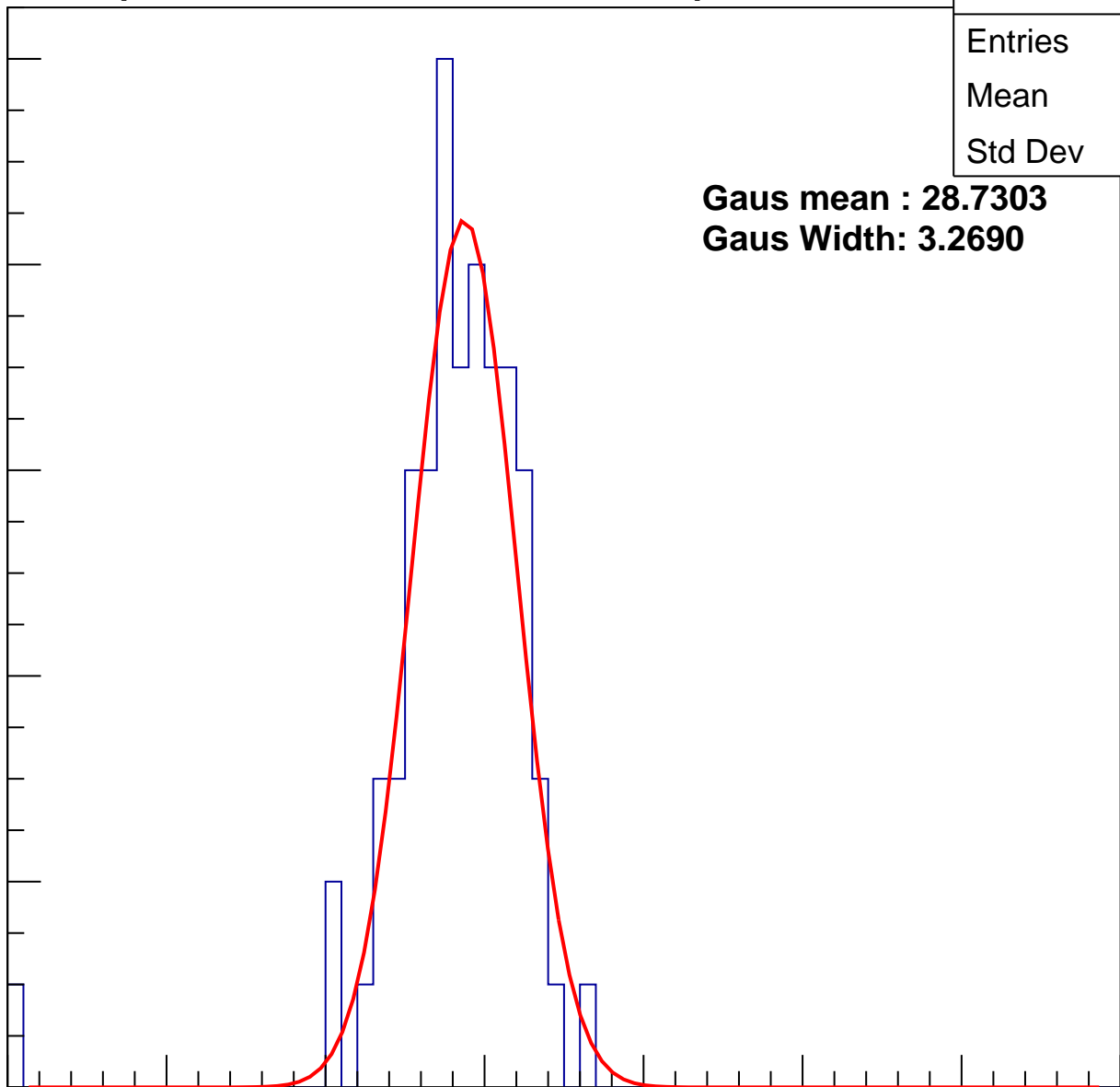
**Gaus Width: 3.2690**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch34, adc1

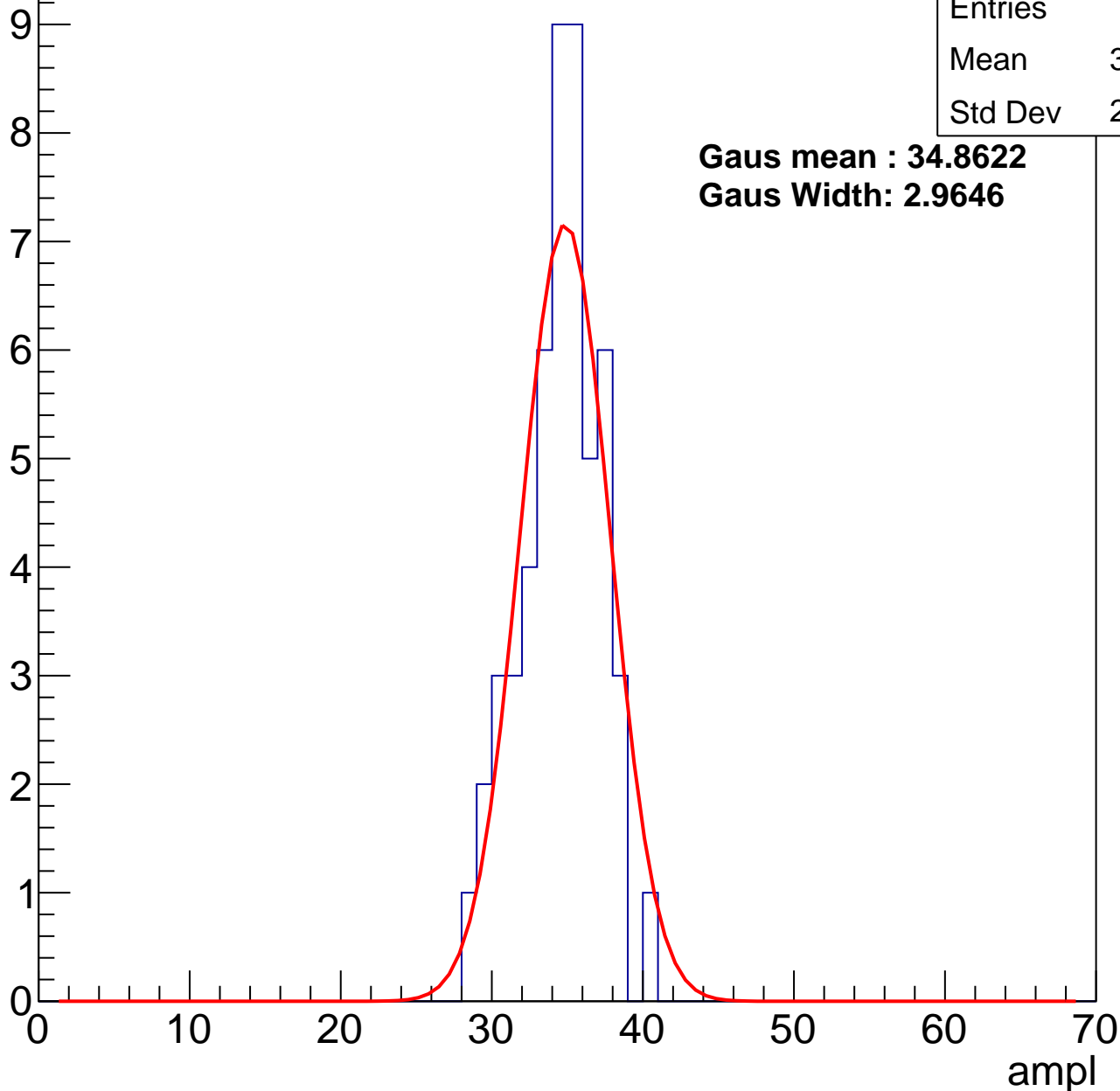
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	34.08
Std Dev	2.593

**Gaus mean : 34.8622**

**Gaus Width: 2.9646**



# B1L002S, U26-ch34, adc2

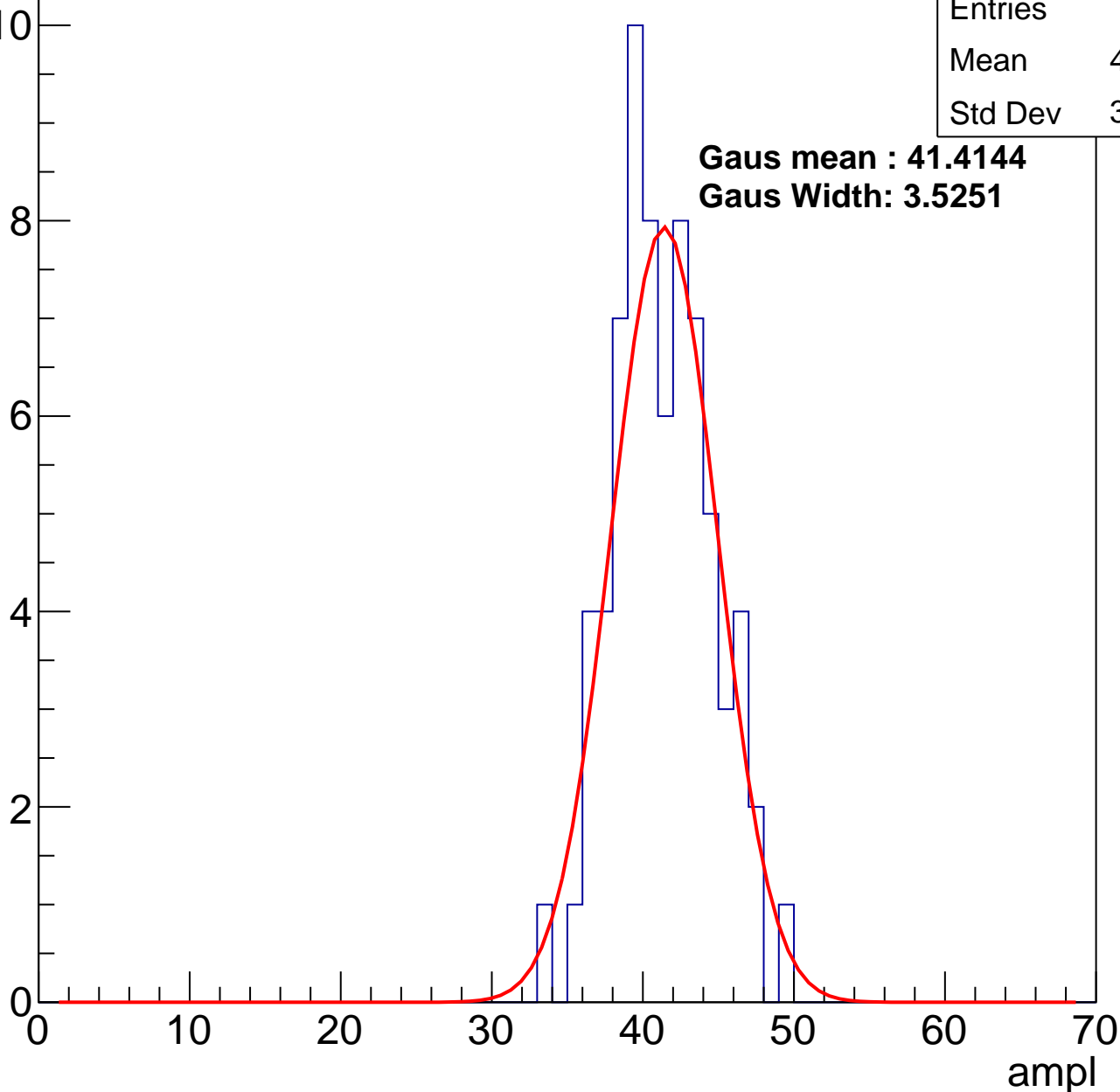
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.86
Std Dev	3.225

**Gaus mean : 41.4144**

**Gaus Width: 3.5251**

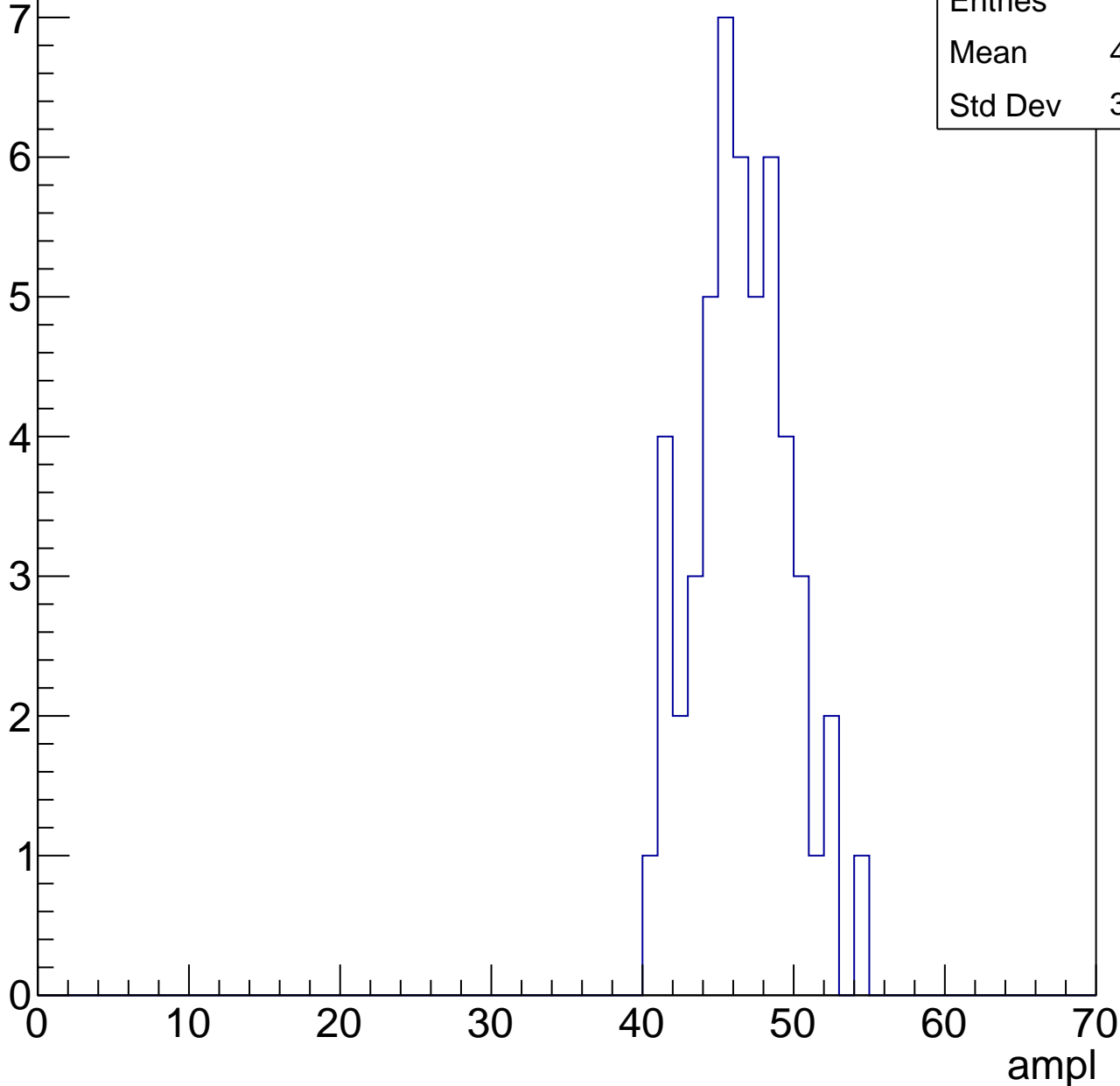


# B1L002S, U26-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

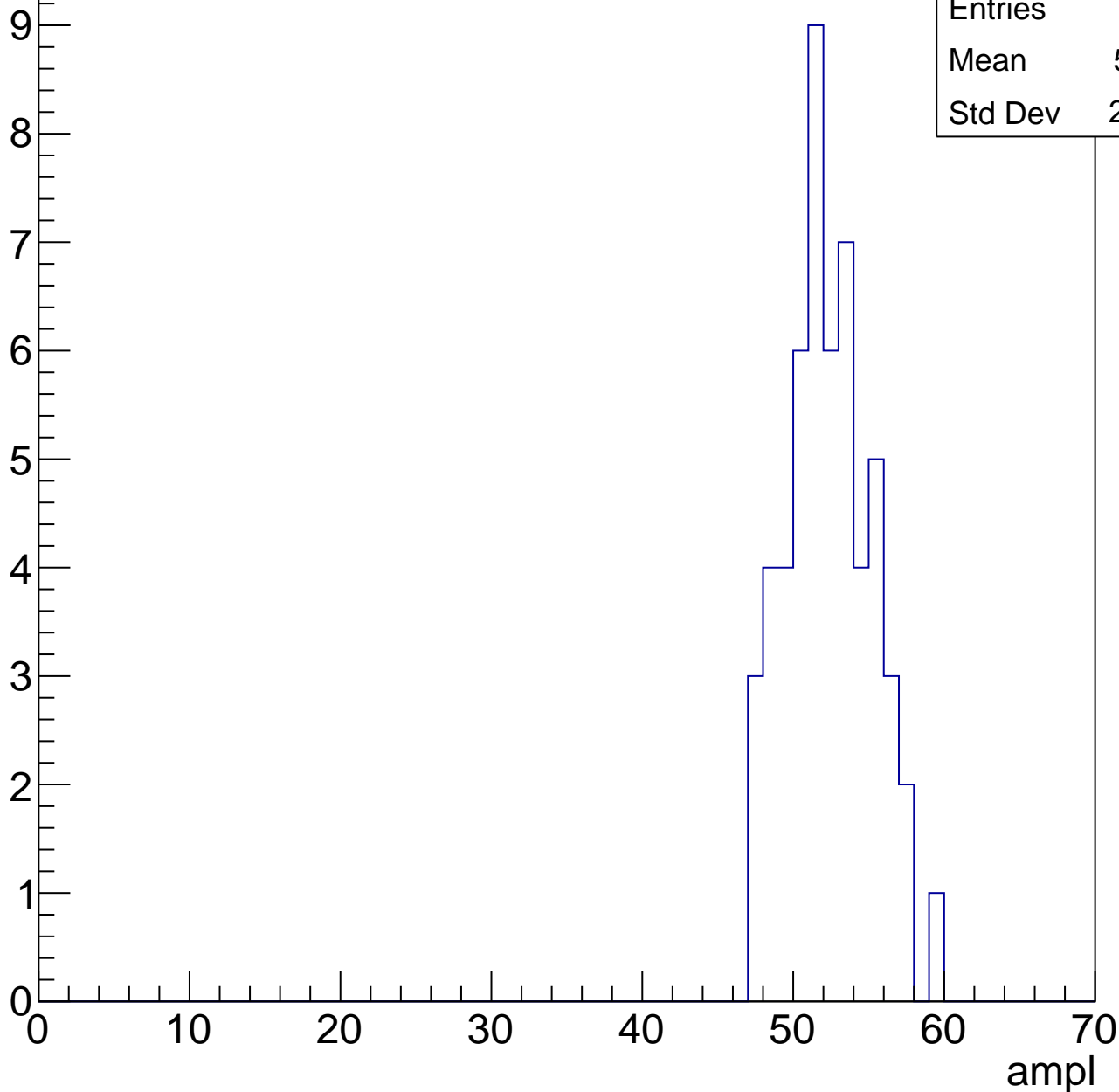
Entries	50
Mean	46.12
Std Dev	3.147



# B1L002S, U26-ch34, adc4

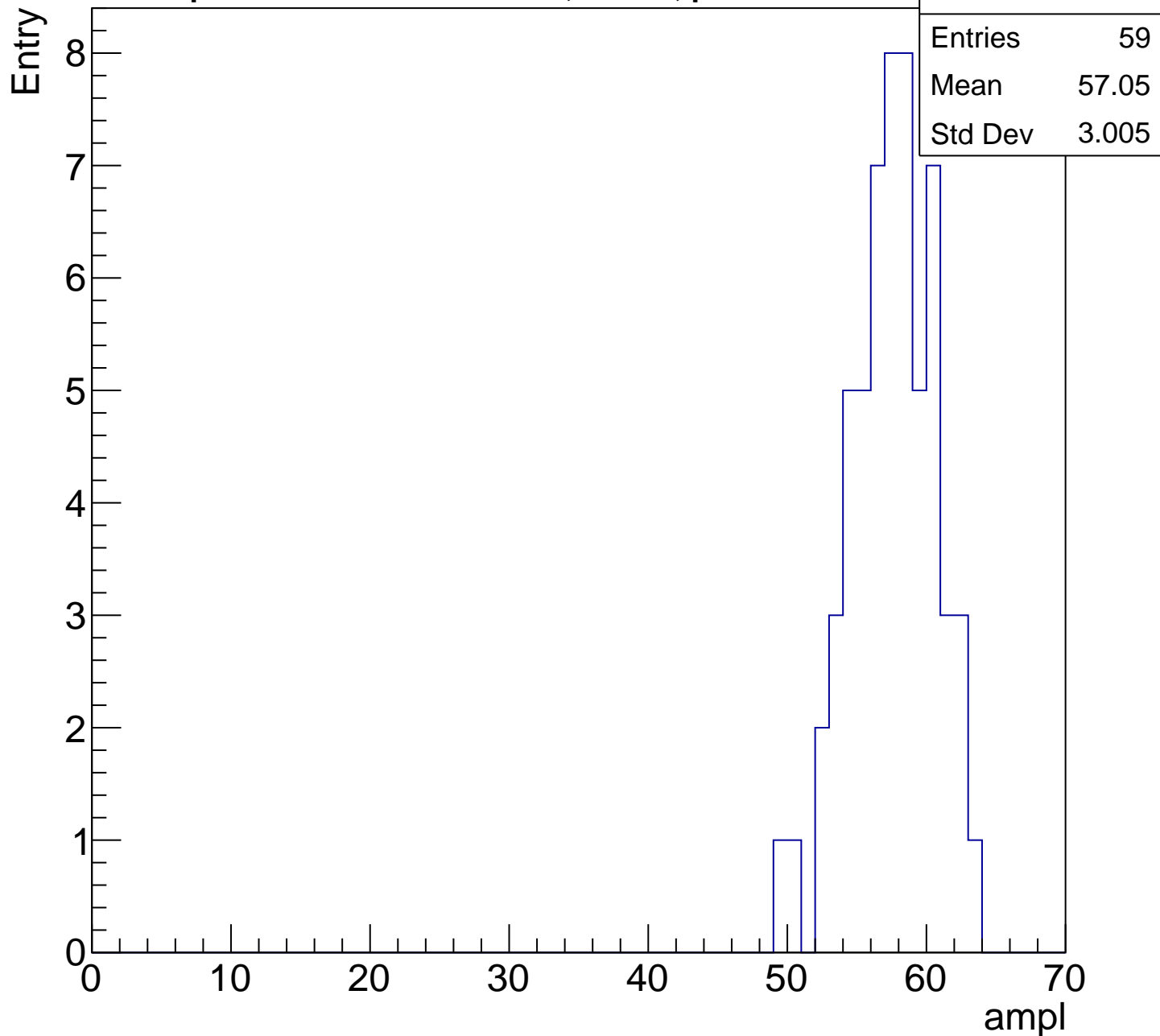
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

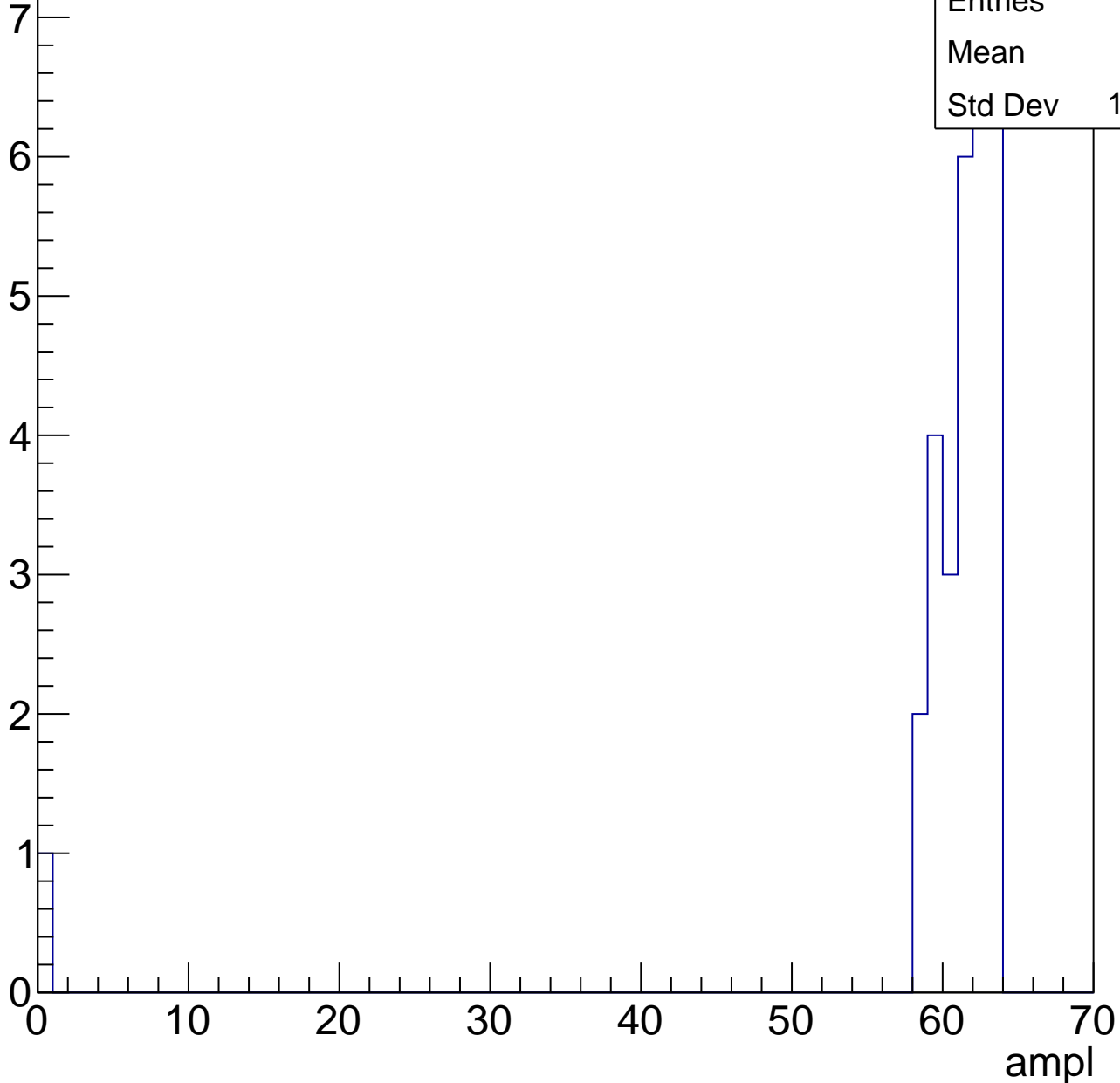


# B1L002S, U26-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59.1
Std Dev	11.08





# B1L002S, U26-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch35, adc0

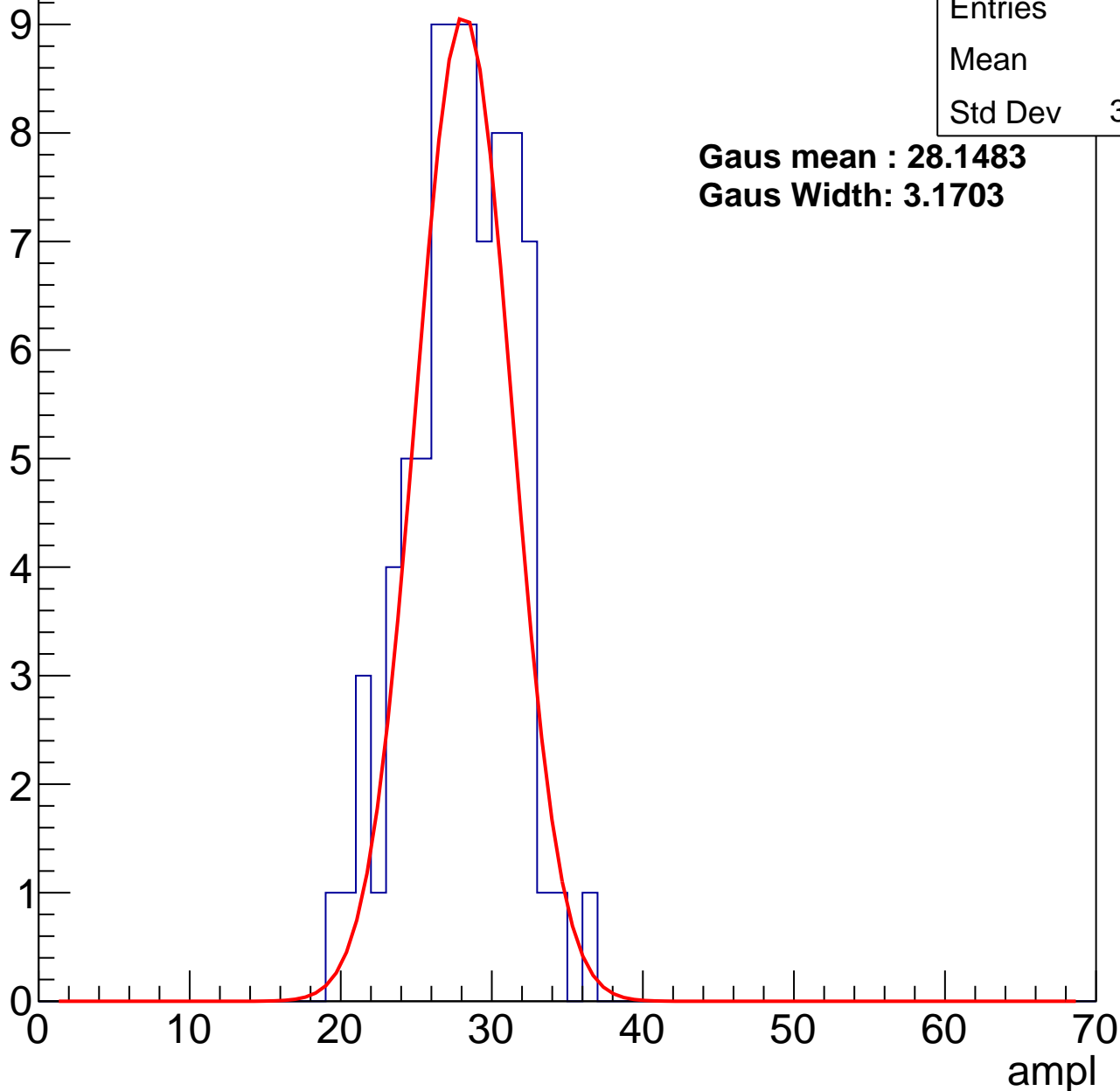
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	27.6
Std Dev	3.415

**Gaus mean : 28.1483**

**Gaus Width: 3.1703**



# B1L002S, U26-ch35, adc1

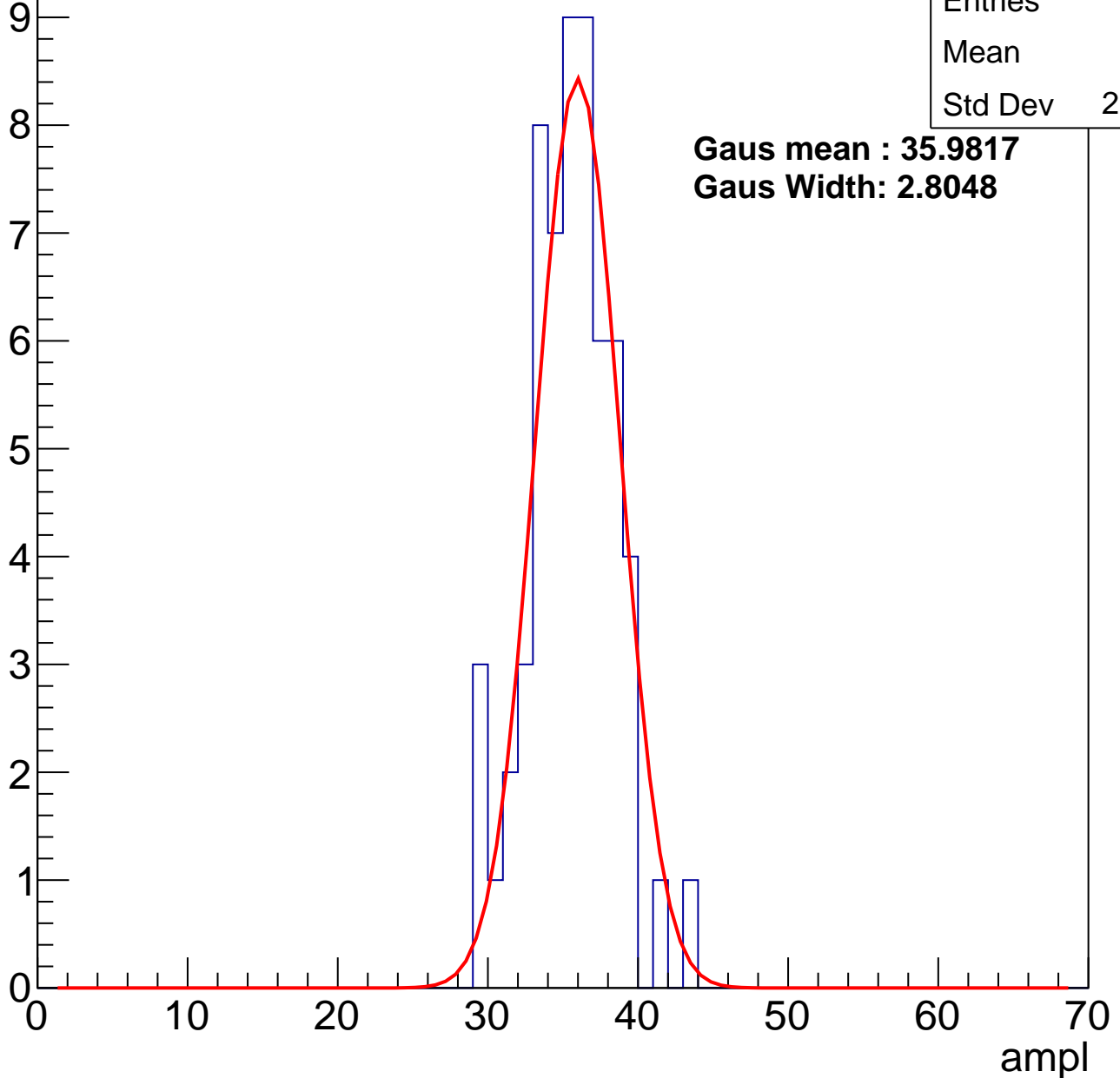
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.1
Std Dev	2.833

**Gaus mean : 35.9817**

**Gaus Width: 2.8048**



# B1L002S, U26-ch35, adc2

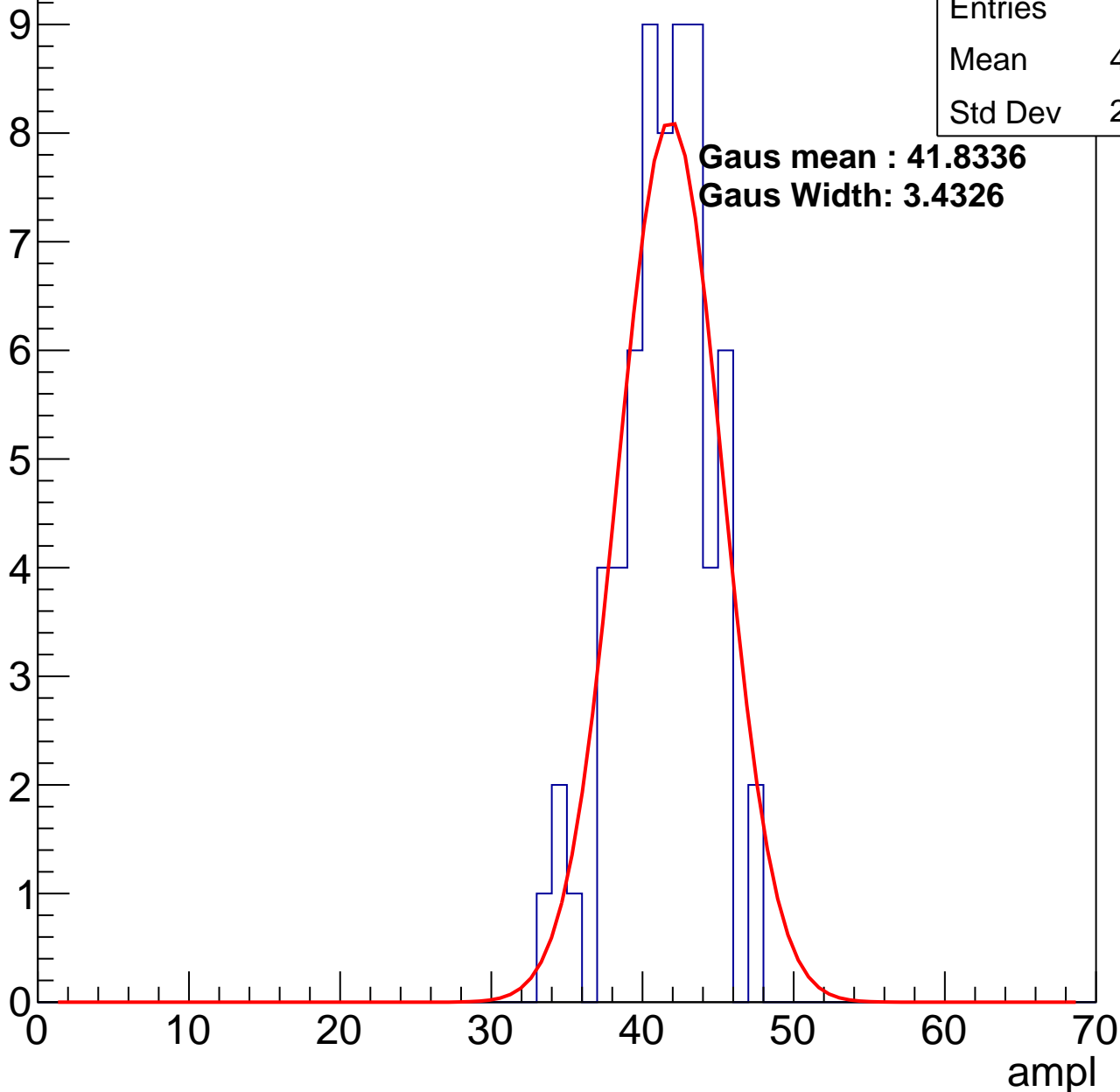
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	40.97
Std Dev	2.987

**Gaus mean : 41.8336**

**Gaus Width: 3.4326**

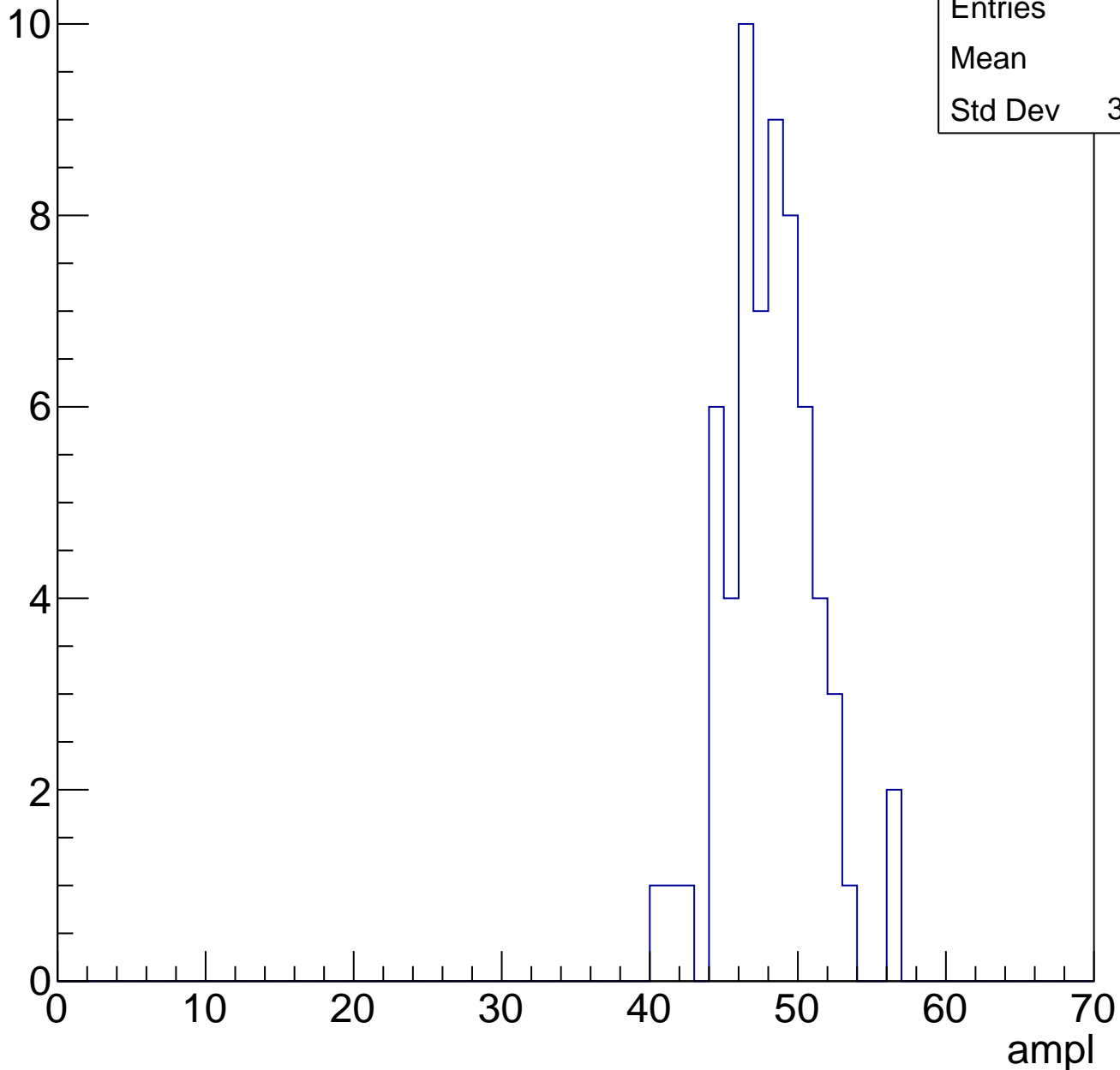


# B1L002S, U26-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	47.7
Std Dev	3.064

Entry

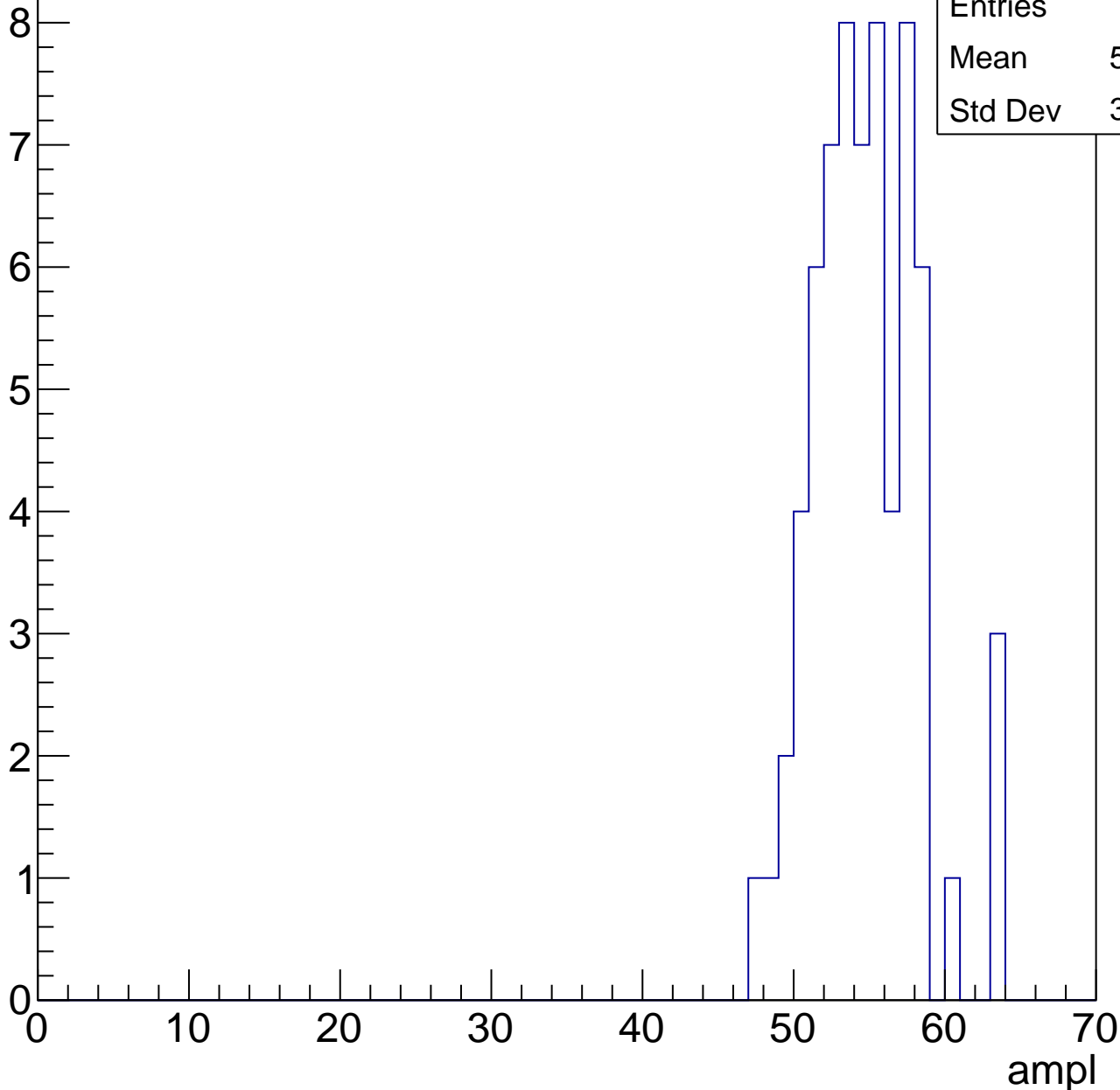


# B1L002S, U26-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	54.27
Std Dev	3.382

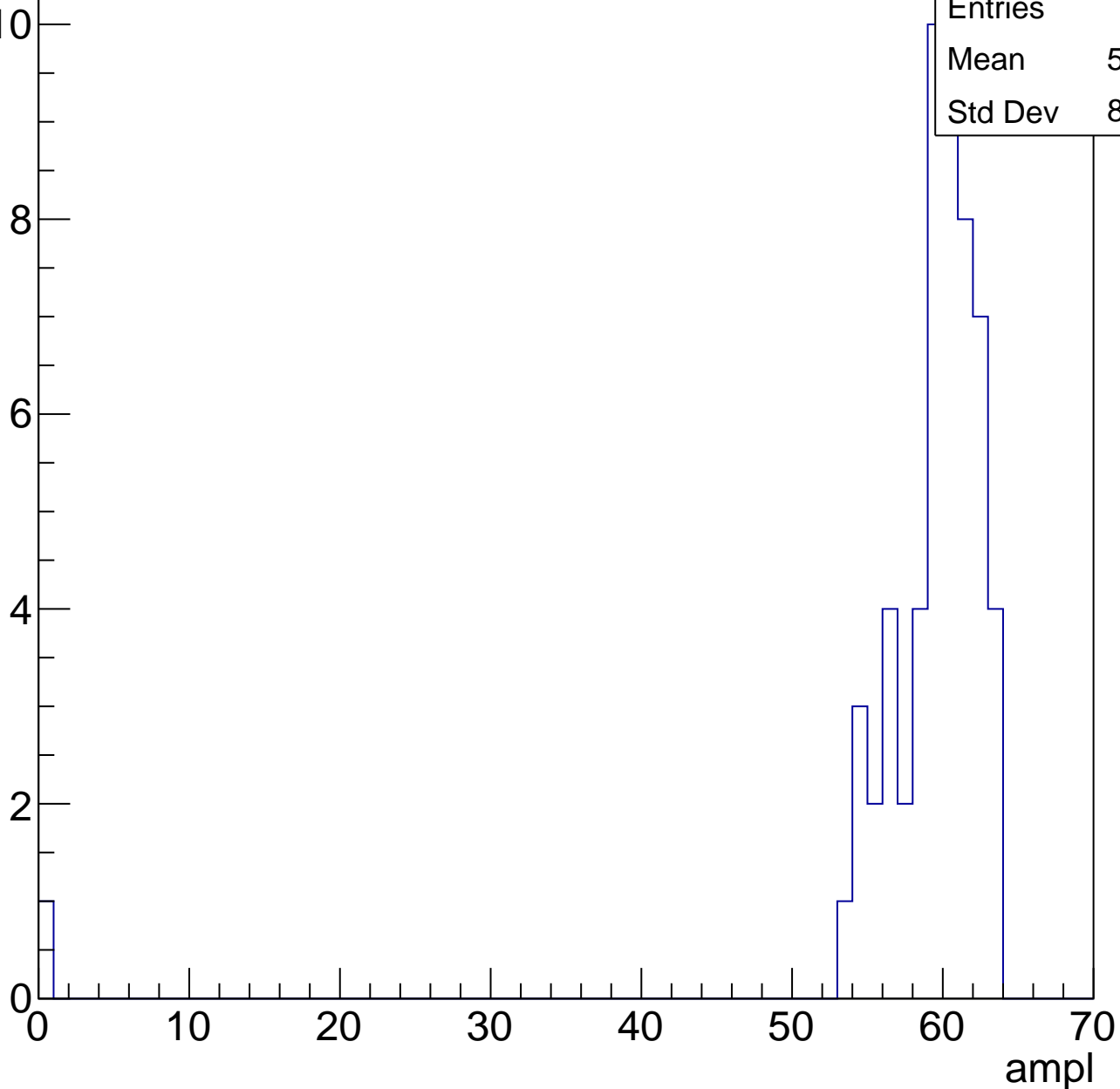


# B1L002S, U26-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

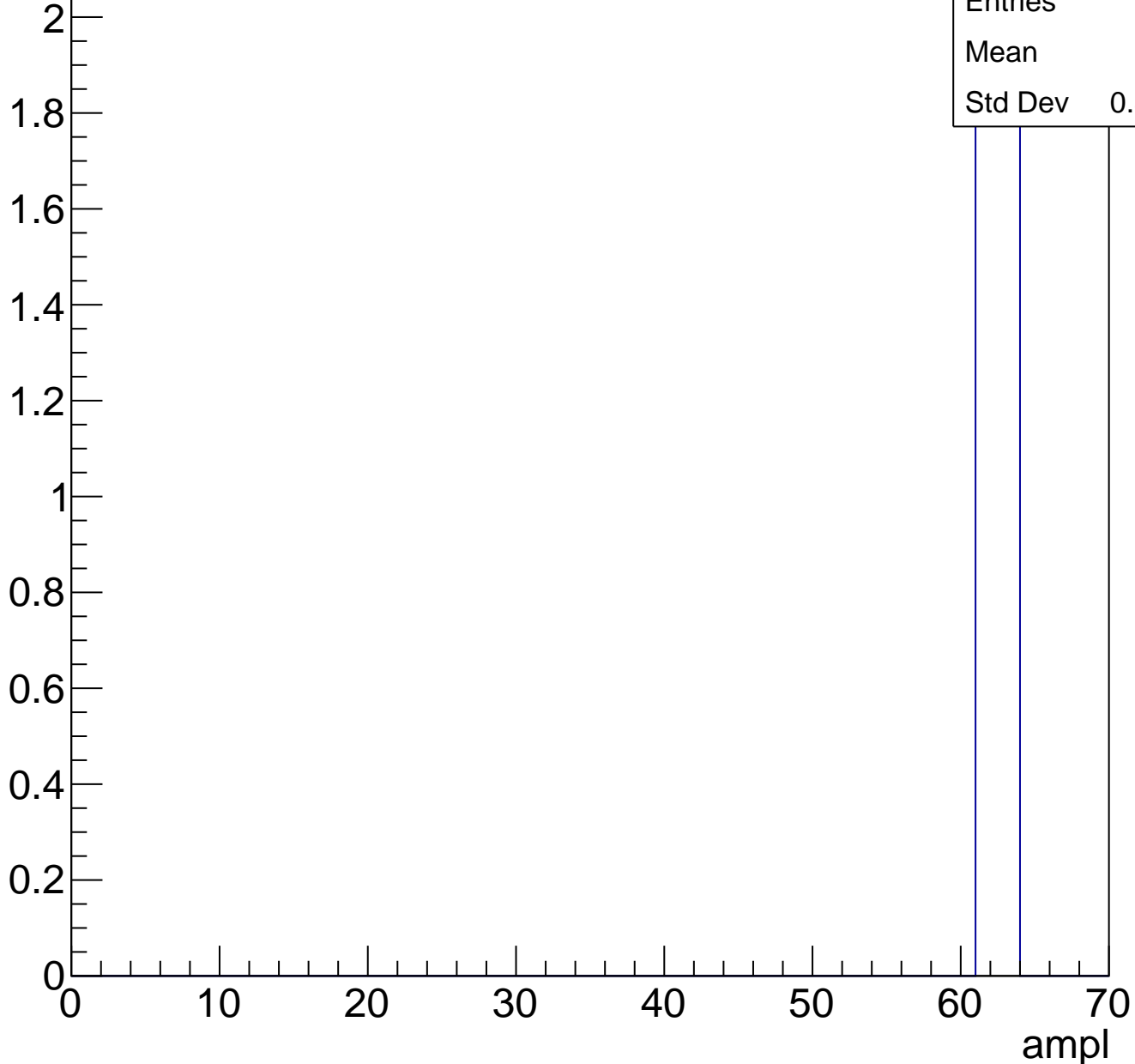
Entries	55
Mean	58.16
Std Dev	8.314



# B1L002S, U26-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch36, adc0

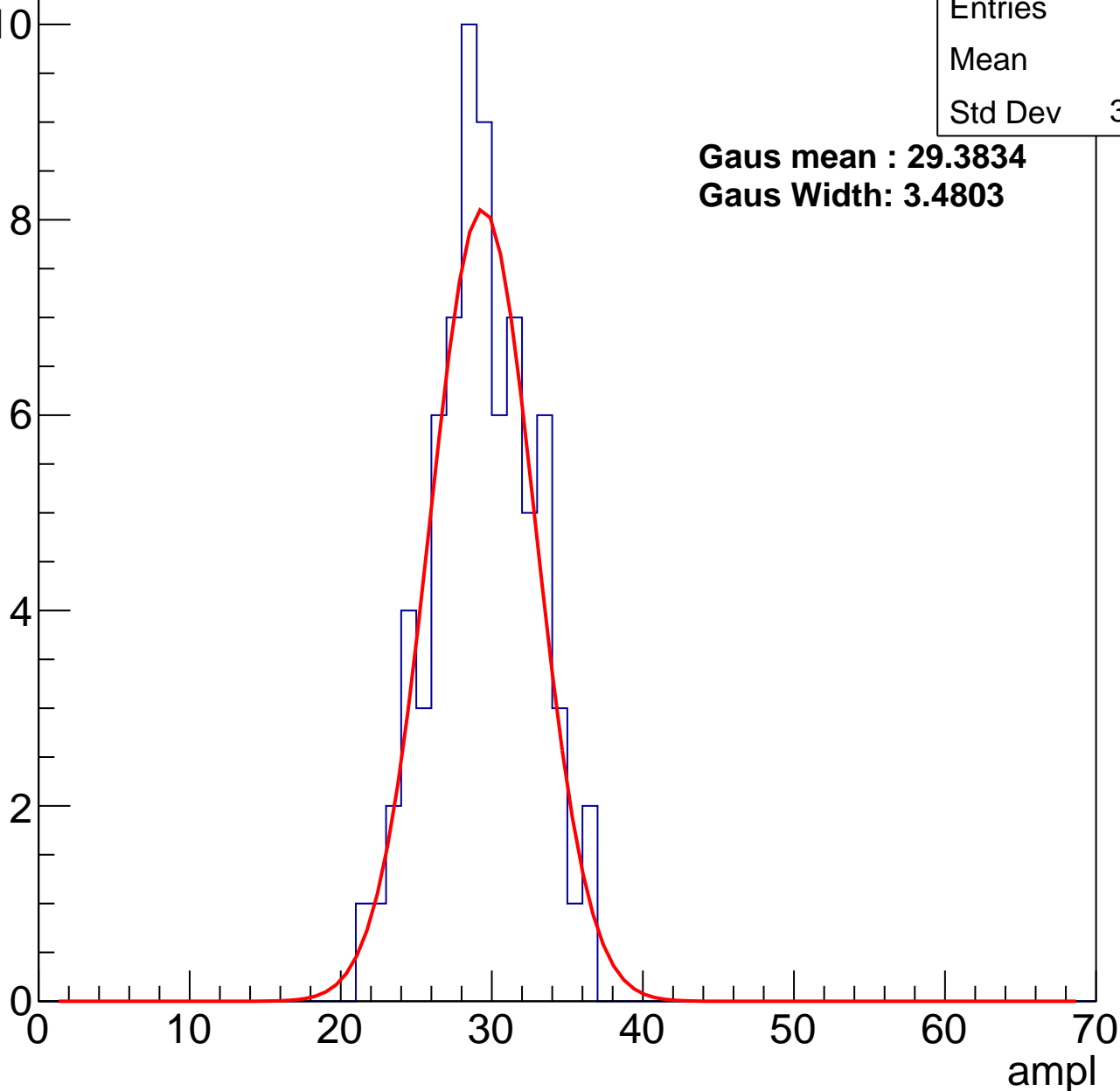
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	28.9
Std Dev	3.328

**Gaus mean : 29.3834**

**Gaus Width: 3.4803**



# B1L002S, U26-ch36, adc1

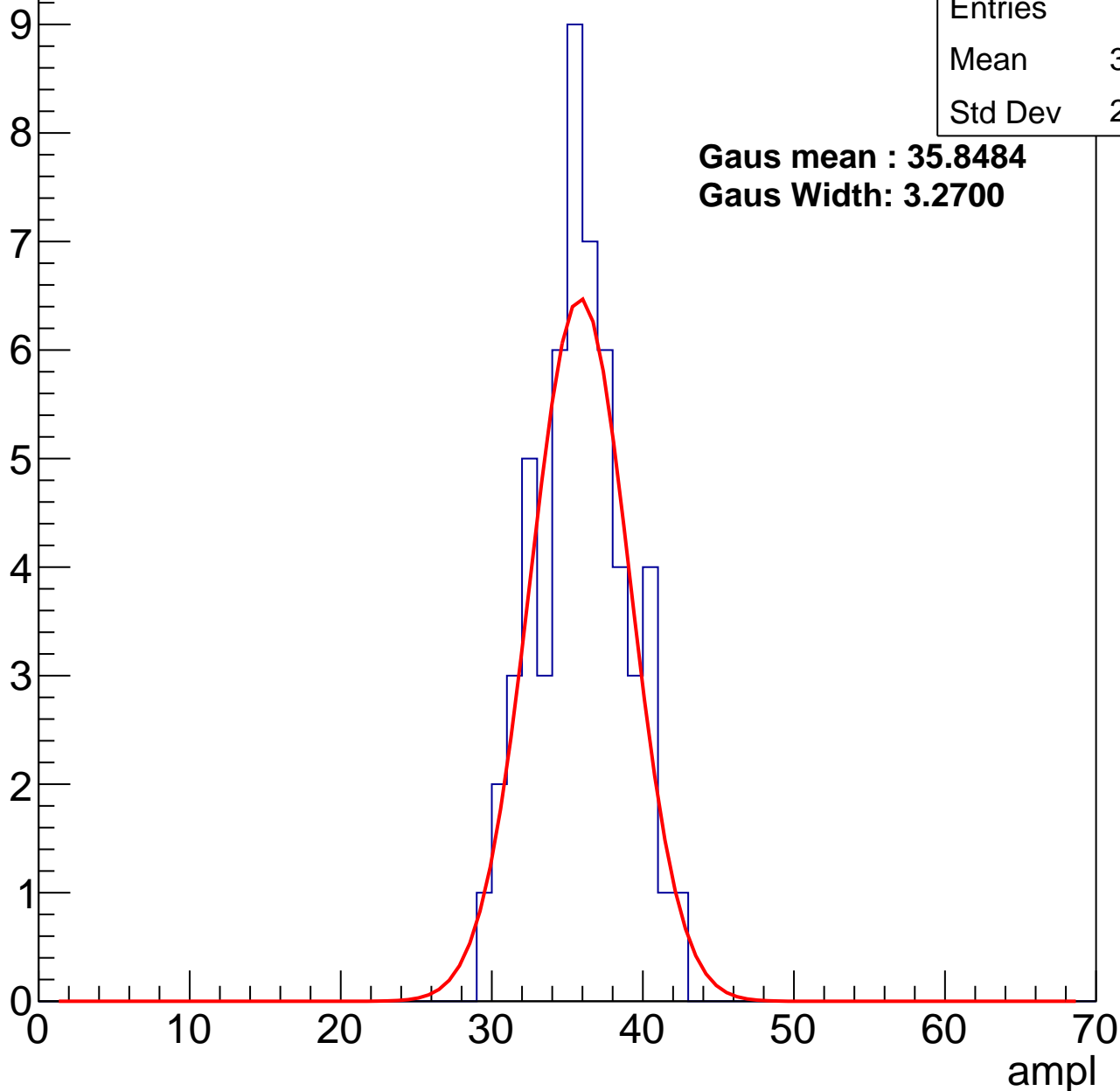
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	35.38
Std Dev	2.982

**Gaus mean : 35.8484**

**Gaus Width: 3.2700**



# B1L002S, U26-ch36, adc2

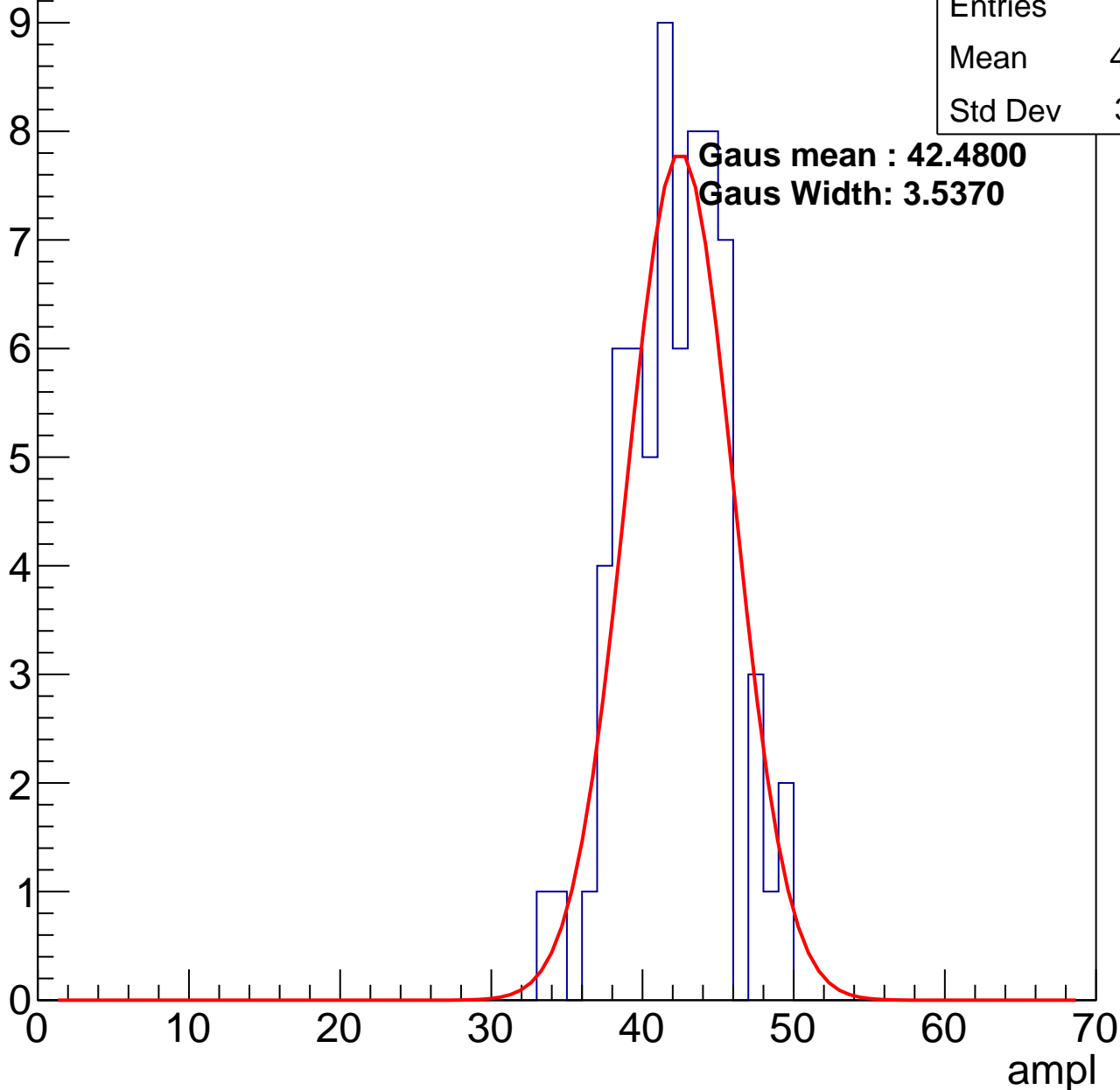
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.65
Std Dev	3.351

**Gaus mean : 42.4800**

**Gaus Width: 3.5370**

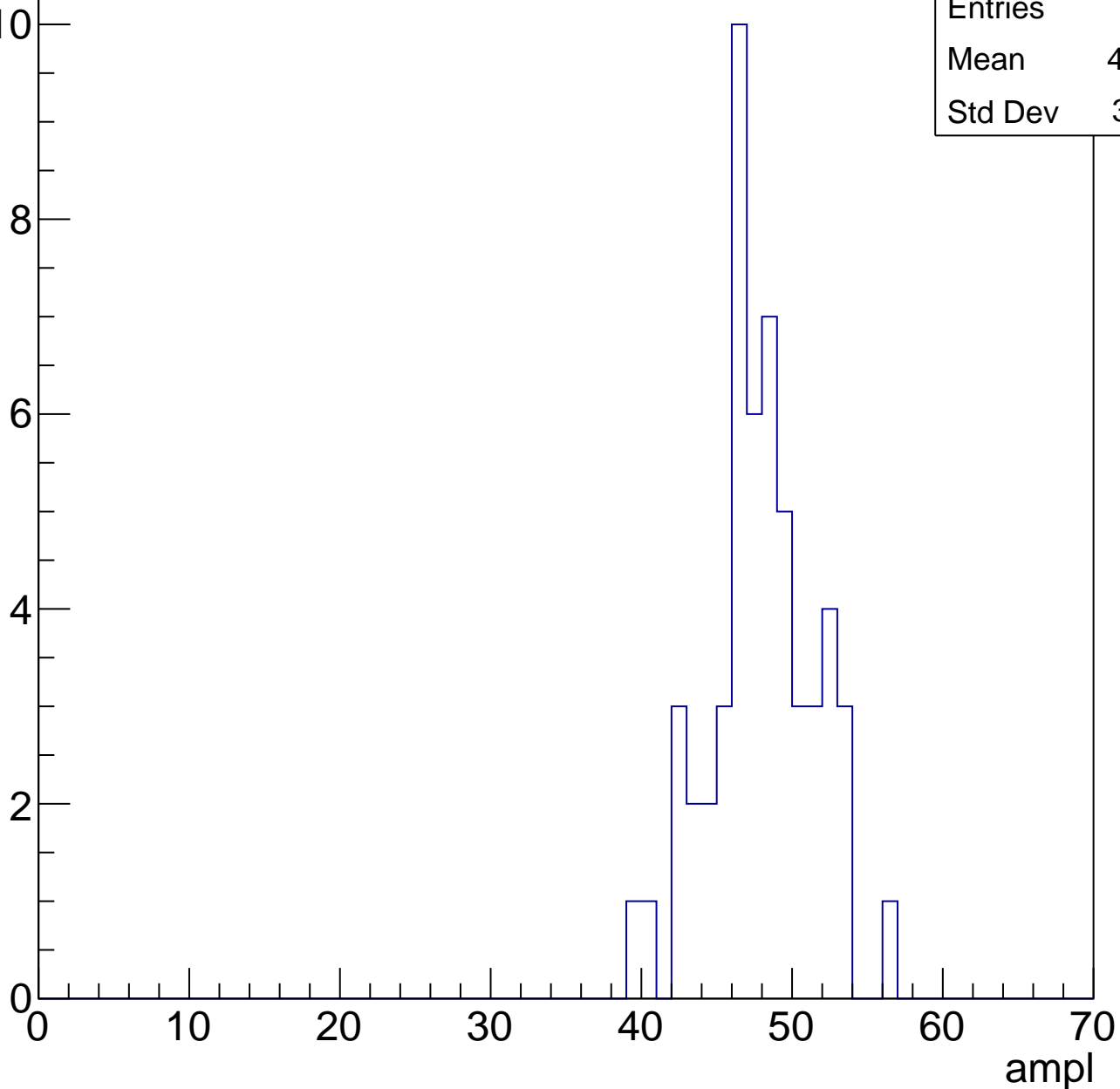


# B1L002S, U26-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	47.46
Std Dev	3.441

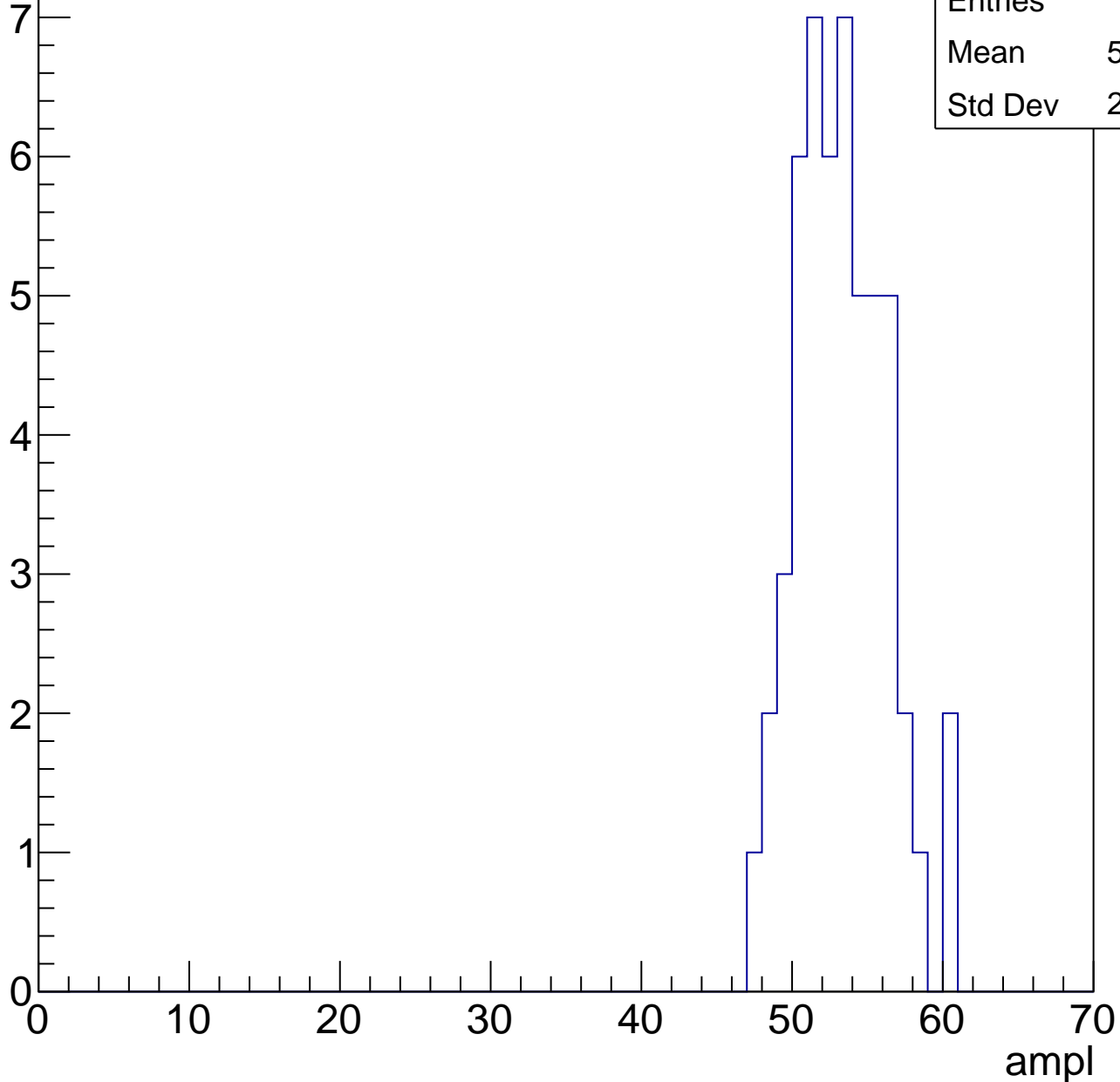


# B1L002S, U26-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.83
Std Dev	2.927

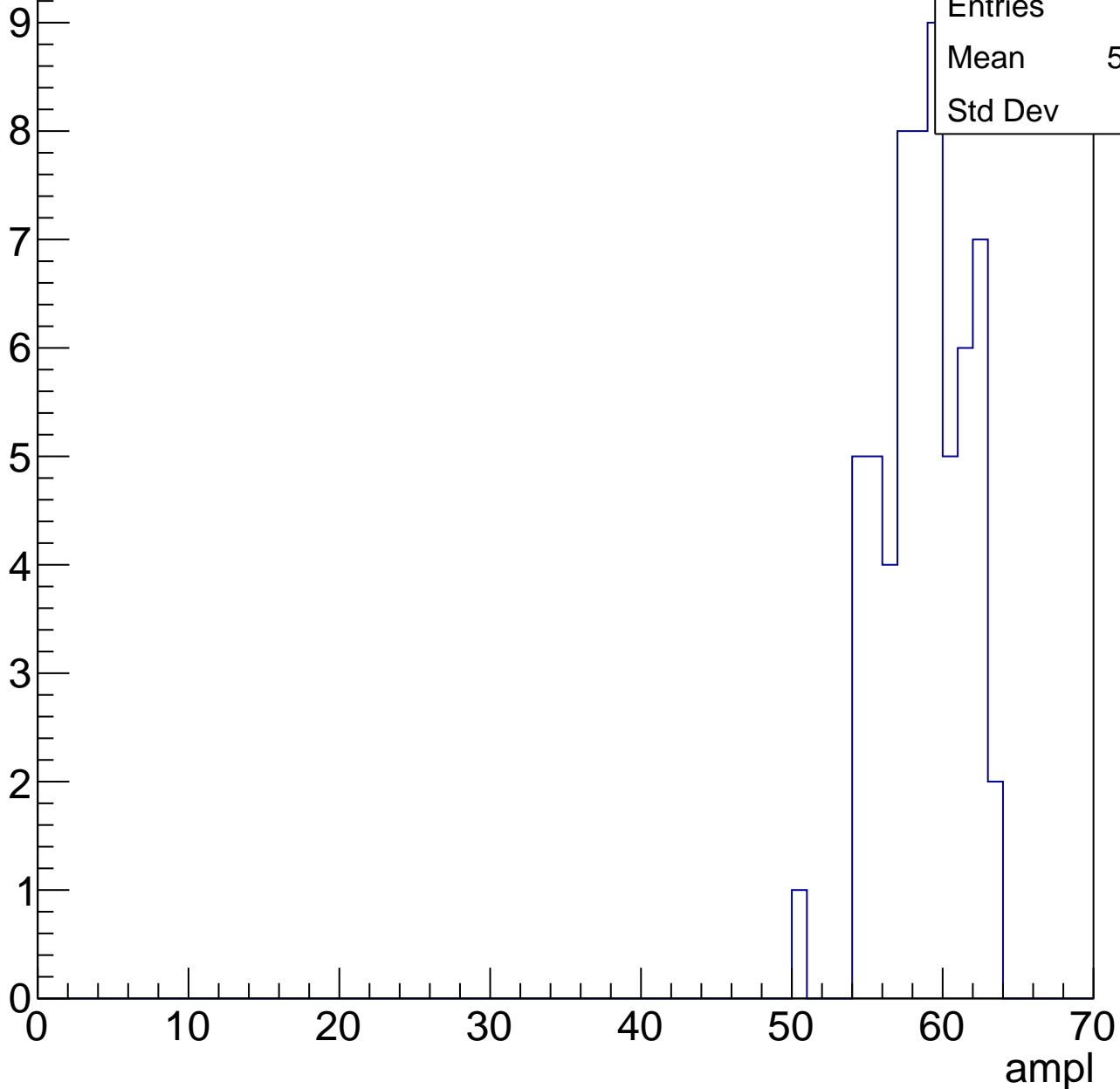


# B1L002S, U26-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.27
Std Dev	2.75

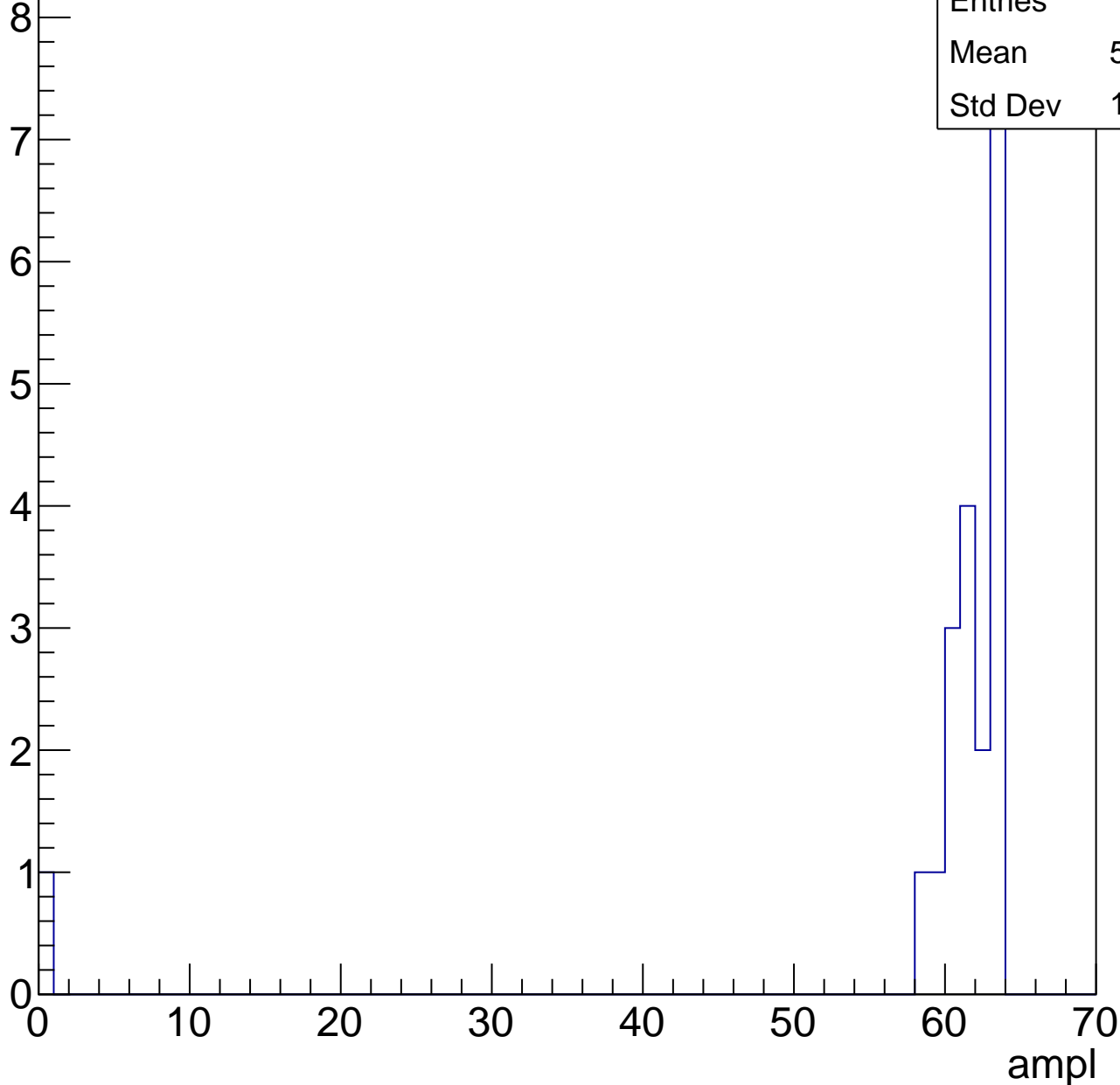


# B1L002S, U26-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.49

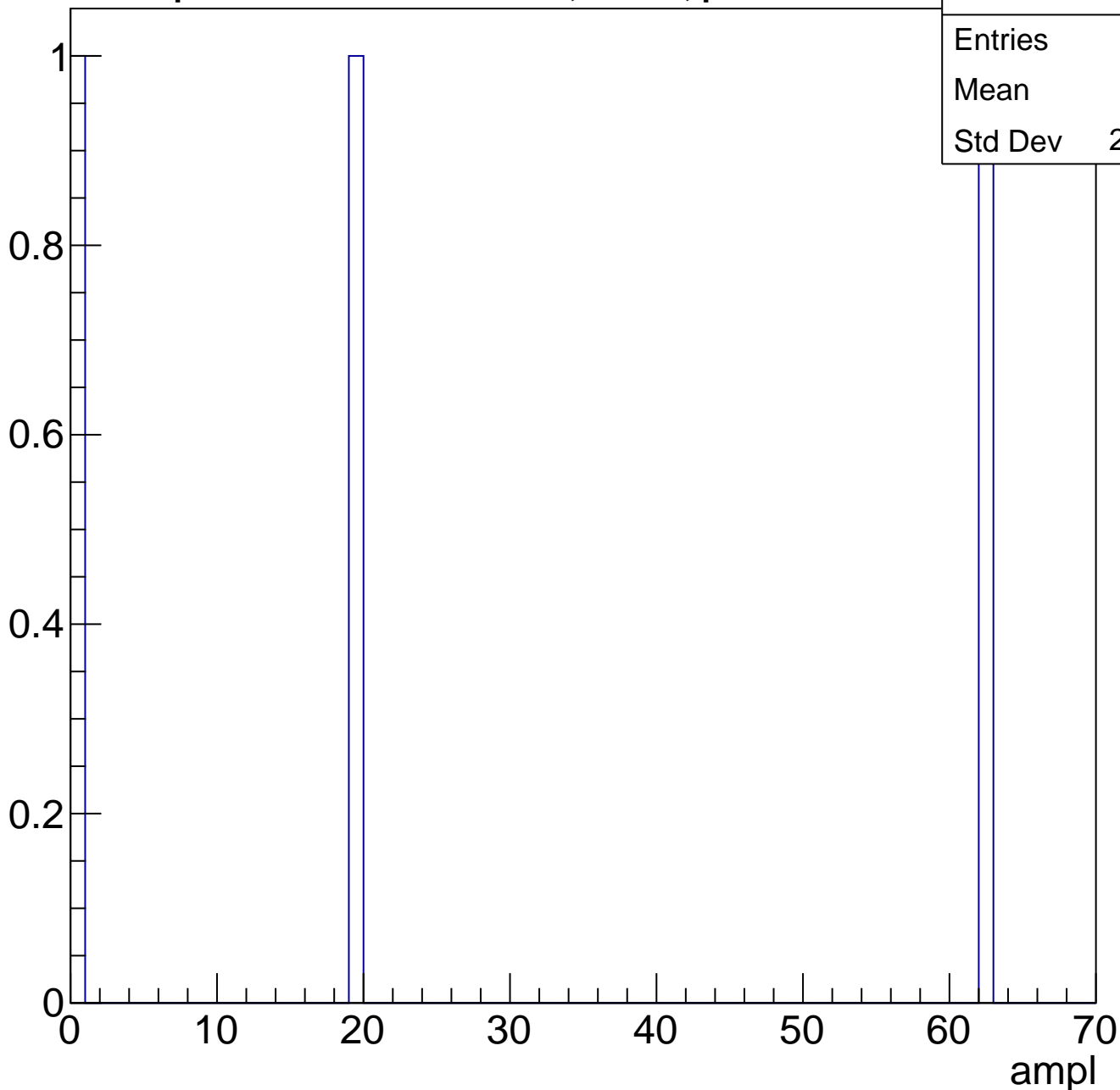




# B1L002S, U26-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27
Std Dev	25.94

# B1L002S, U26-ch37, adc0

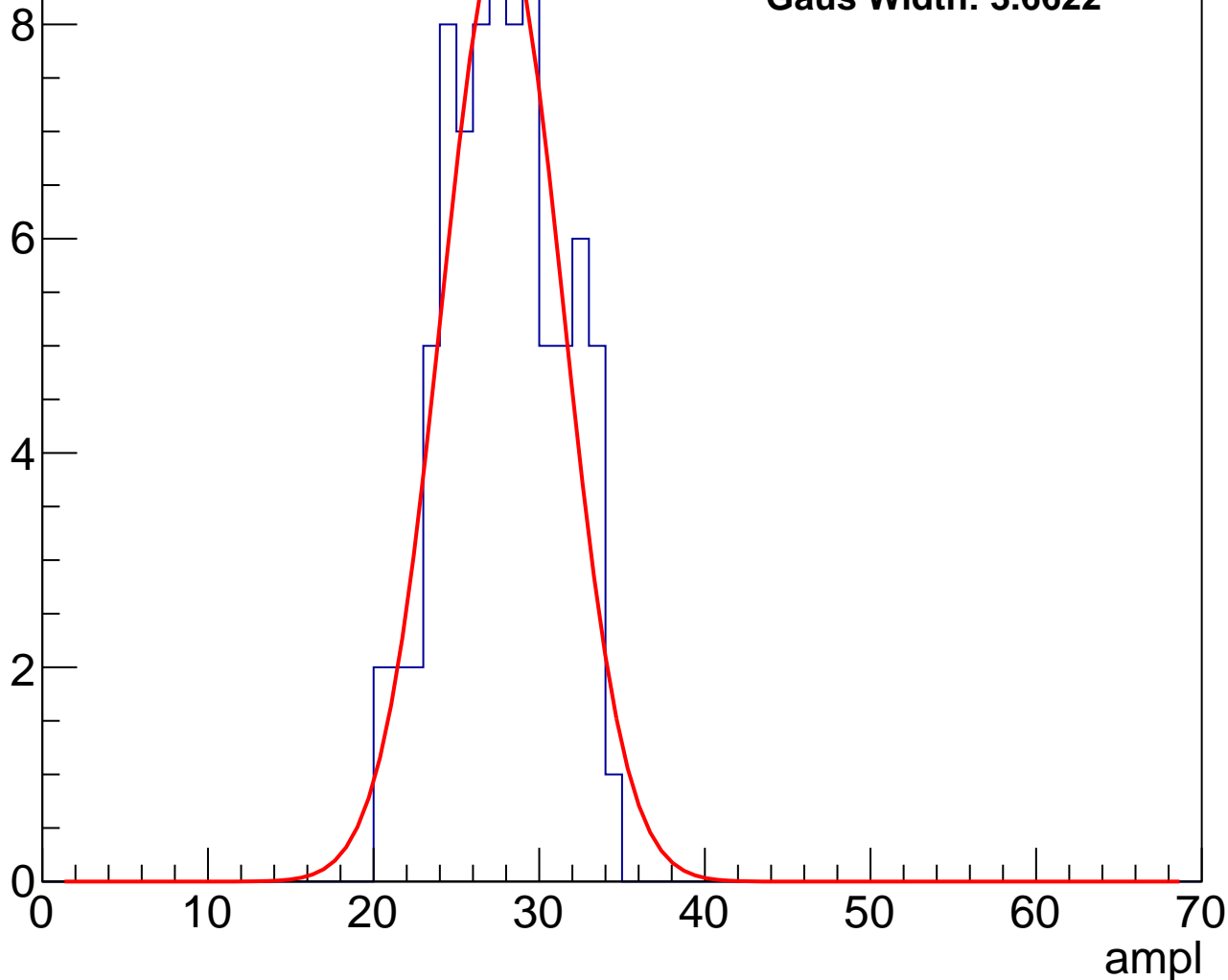
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	27.31
Std Dev	3.379

**Gaus mean : 27.7821**

**Gaus Width: 3.6622**



# B1L002S, U26-ch37, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	34.64
Std Dev	3.052

**Gaus mean : 35.6334**

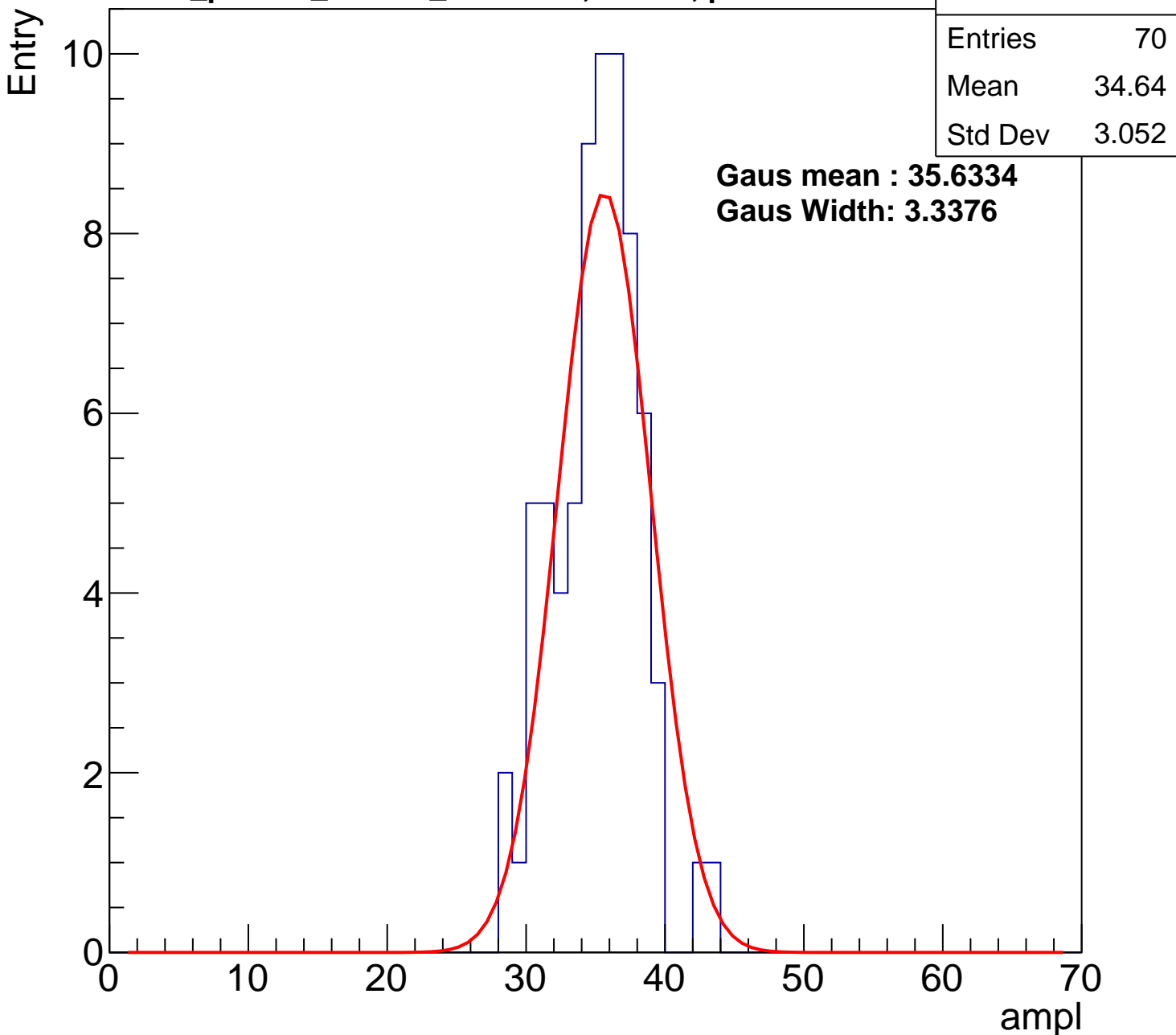
**Gaus Width: 3.3376**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch37, adc2

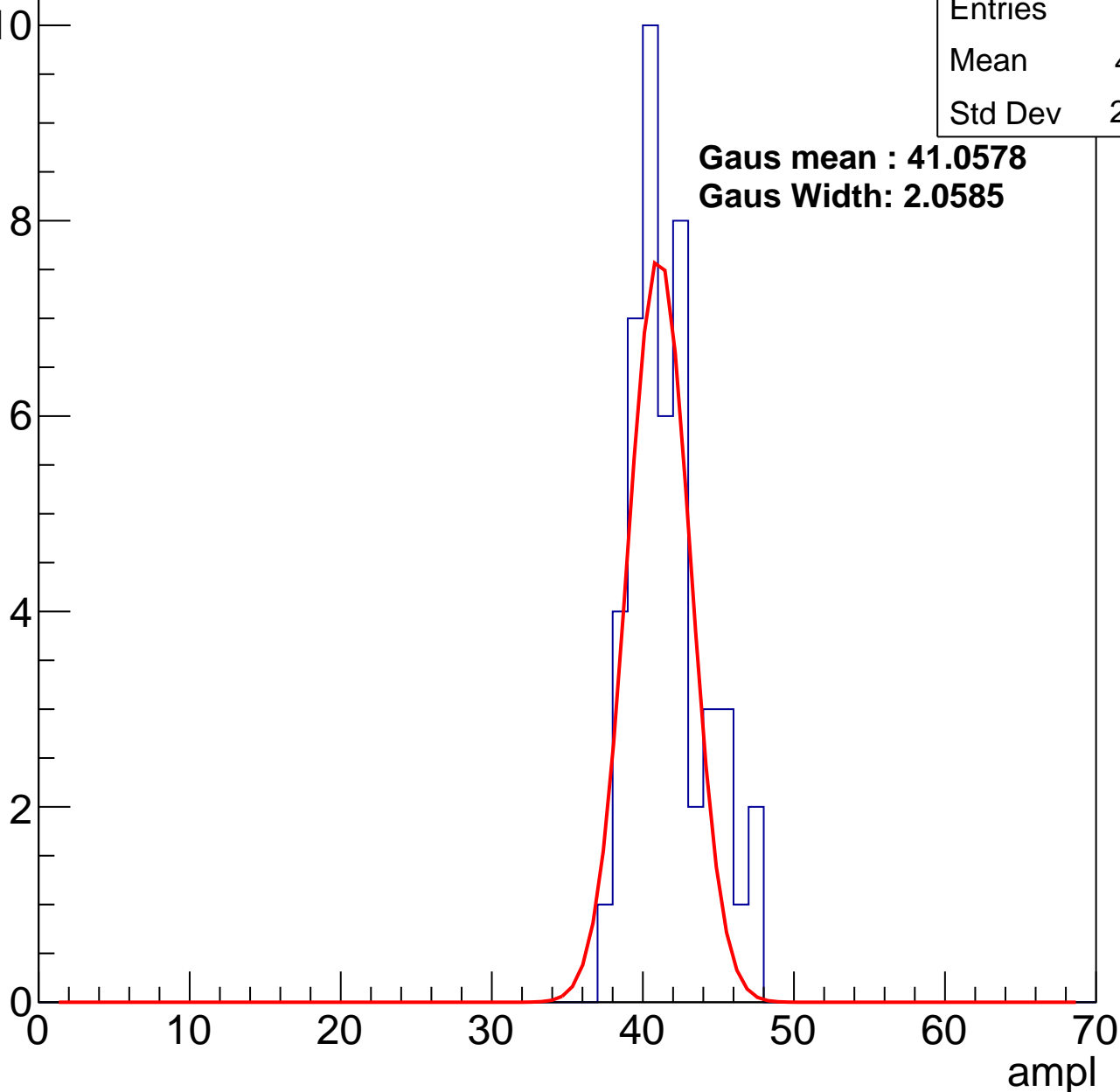
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	41.21
Std Dev	2.423

**Gaus mean : 41.0578**

**Gaus Width: 2.0585**

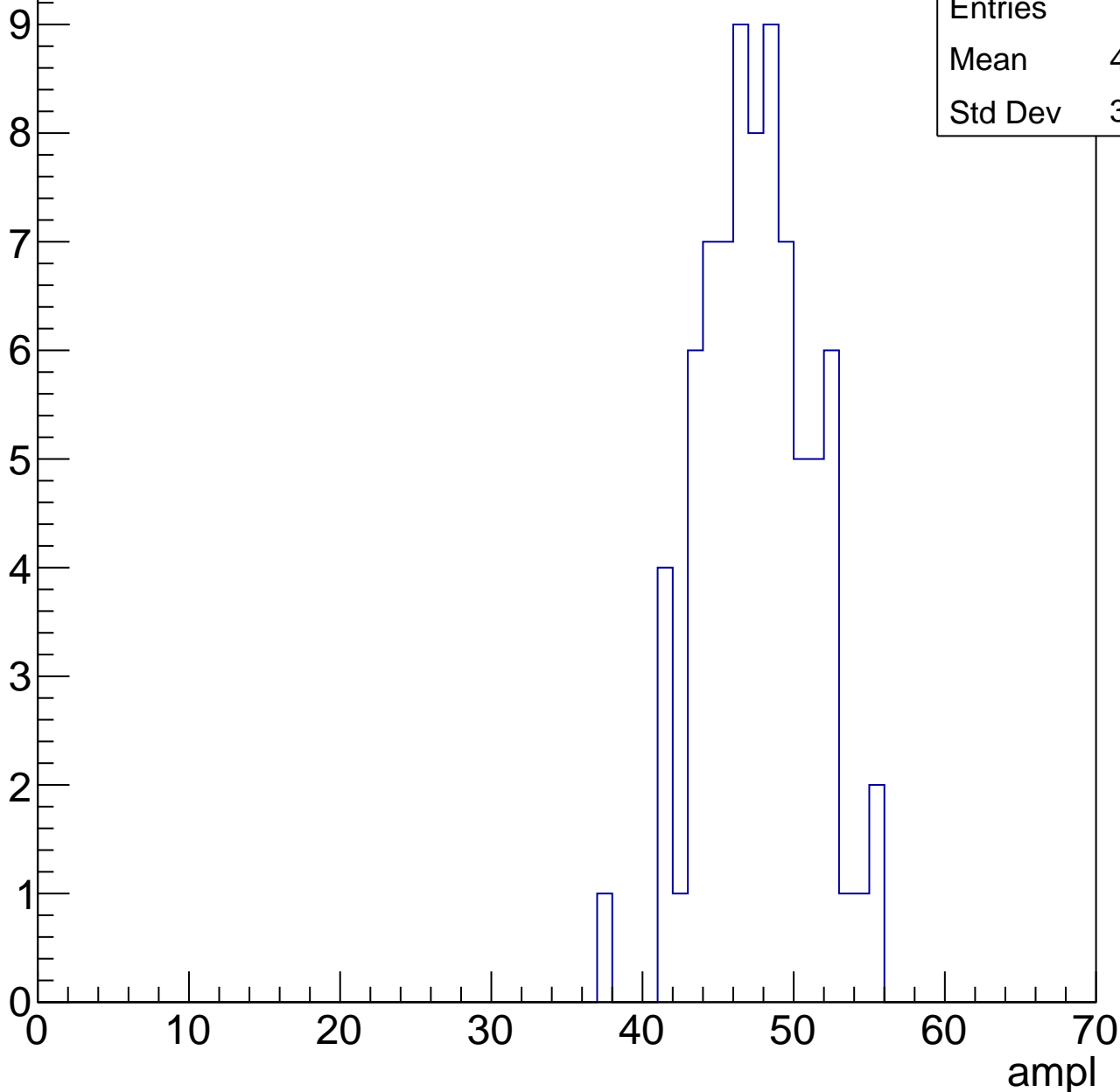


# B1L002S, U26-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	47.13
Std Dev	3.538

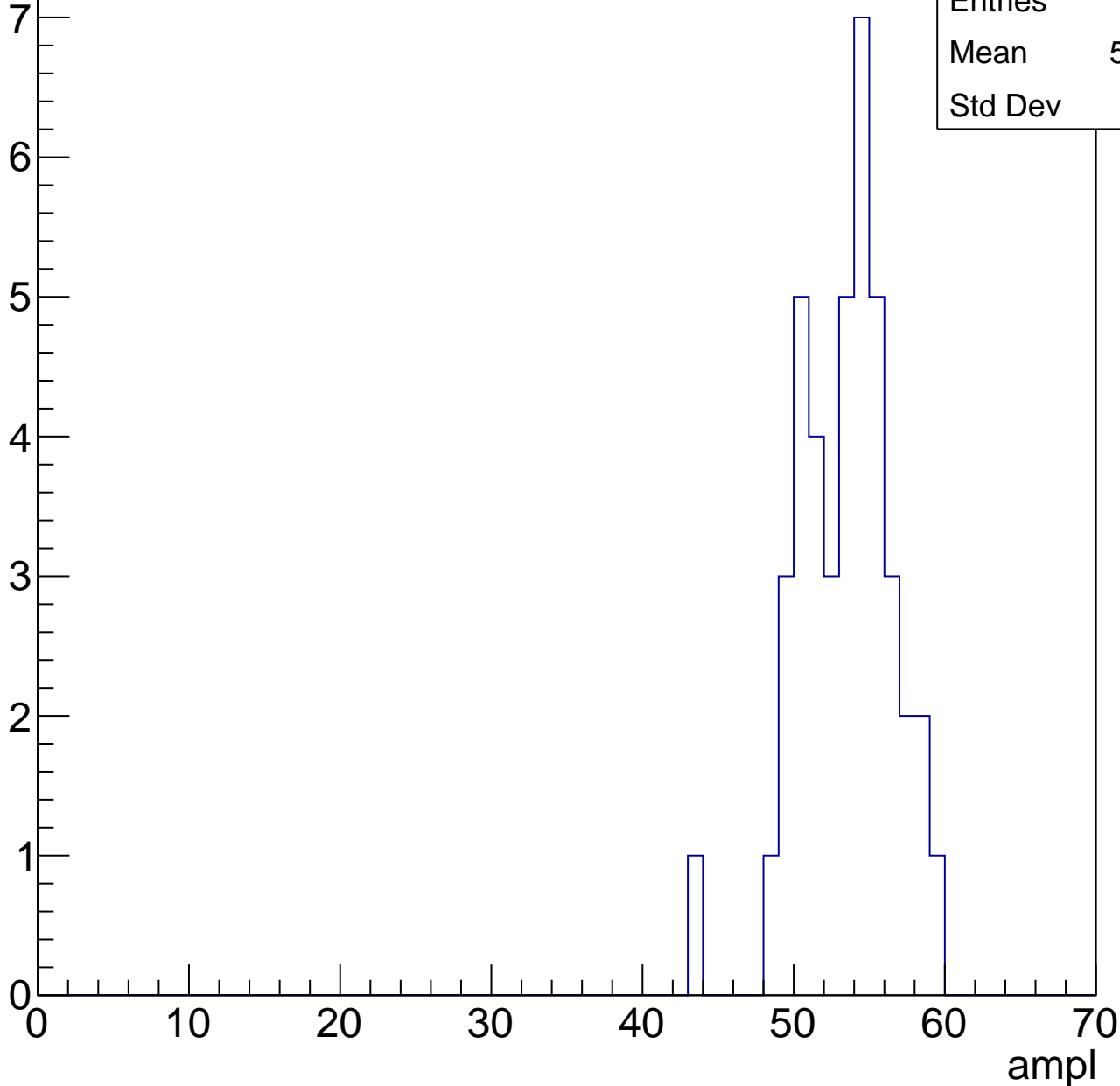


# B1L002S, U26-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	52.93
Std Dev	3.12

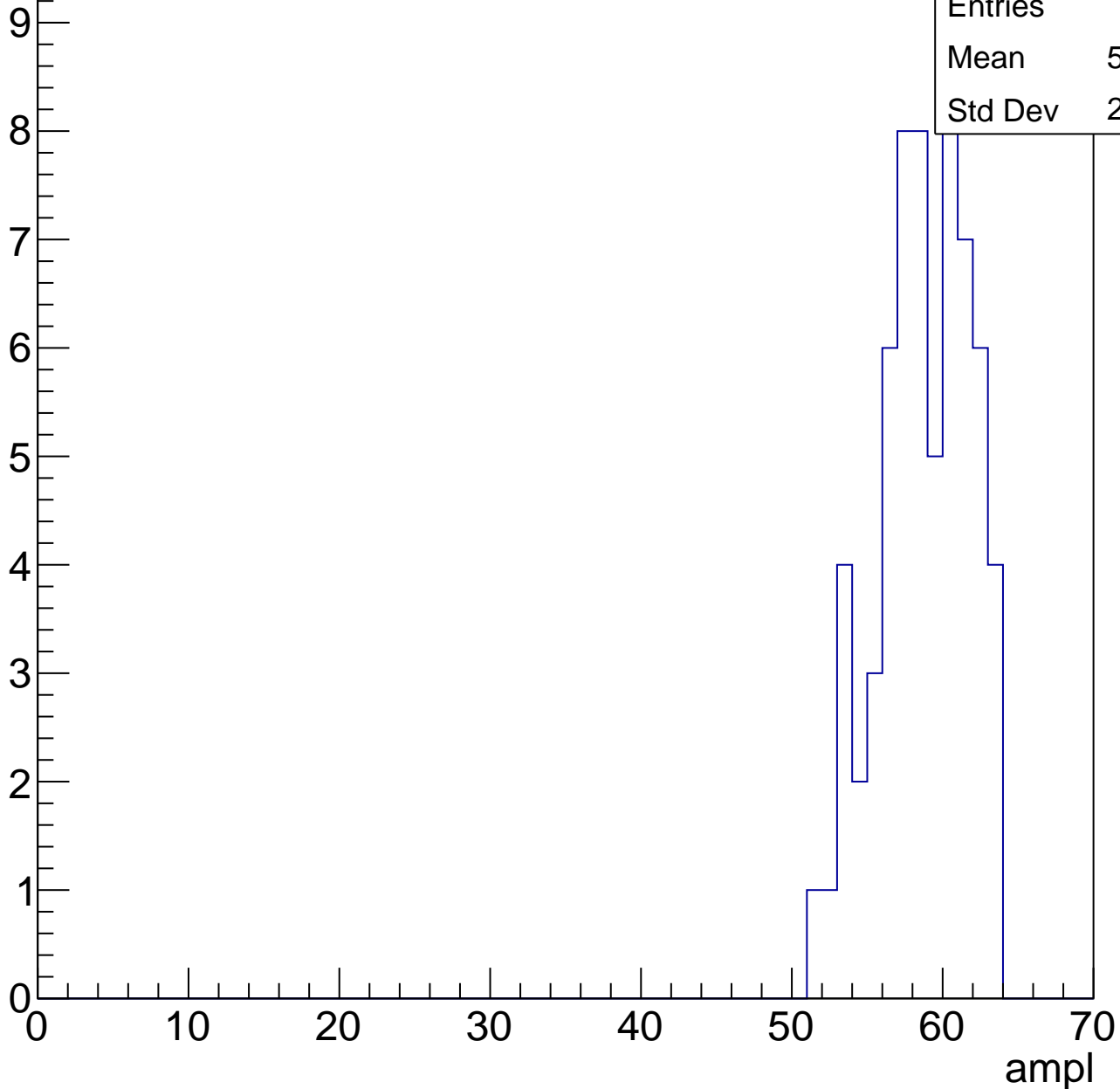


# B1L002S, U26-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	58.28
Std Dev	2.987

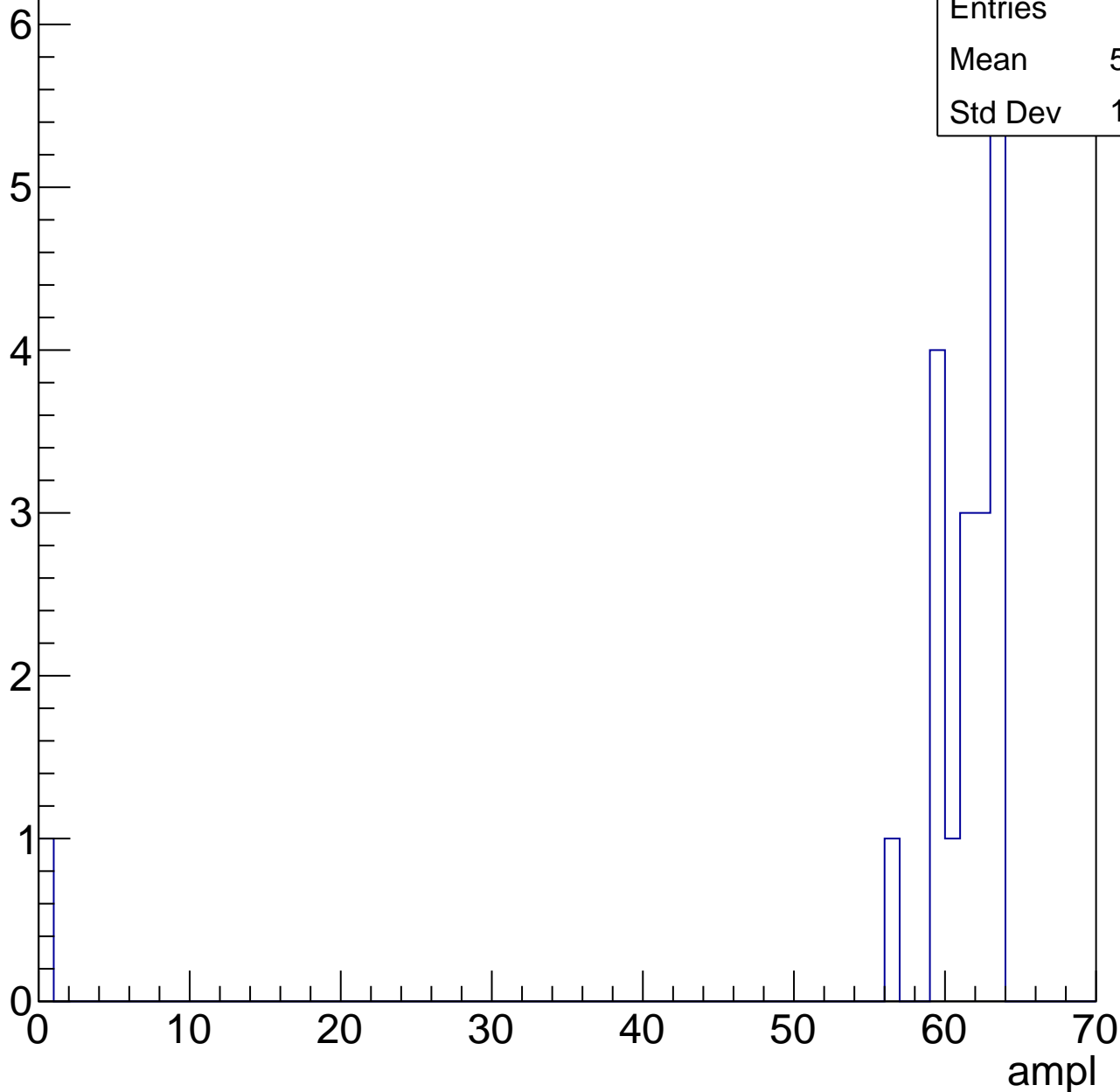


# B1L002S, U26-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	57.84
Std Dev	13.77





# B1L002S, U26-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch38, adc0

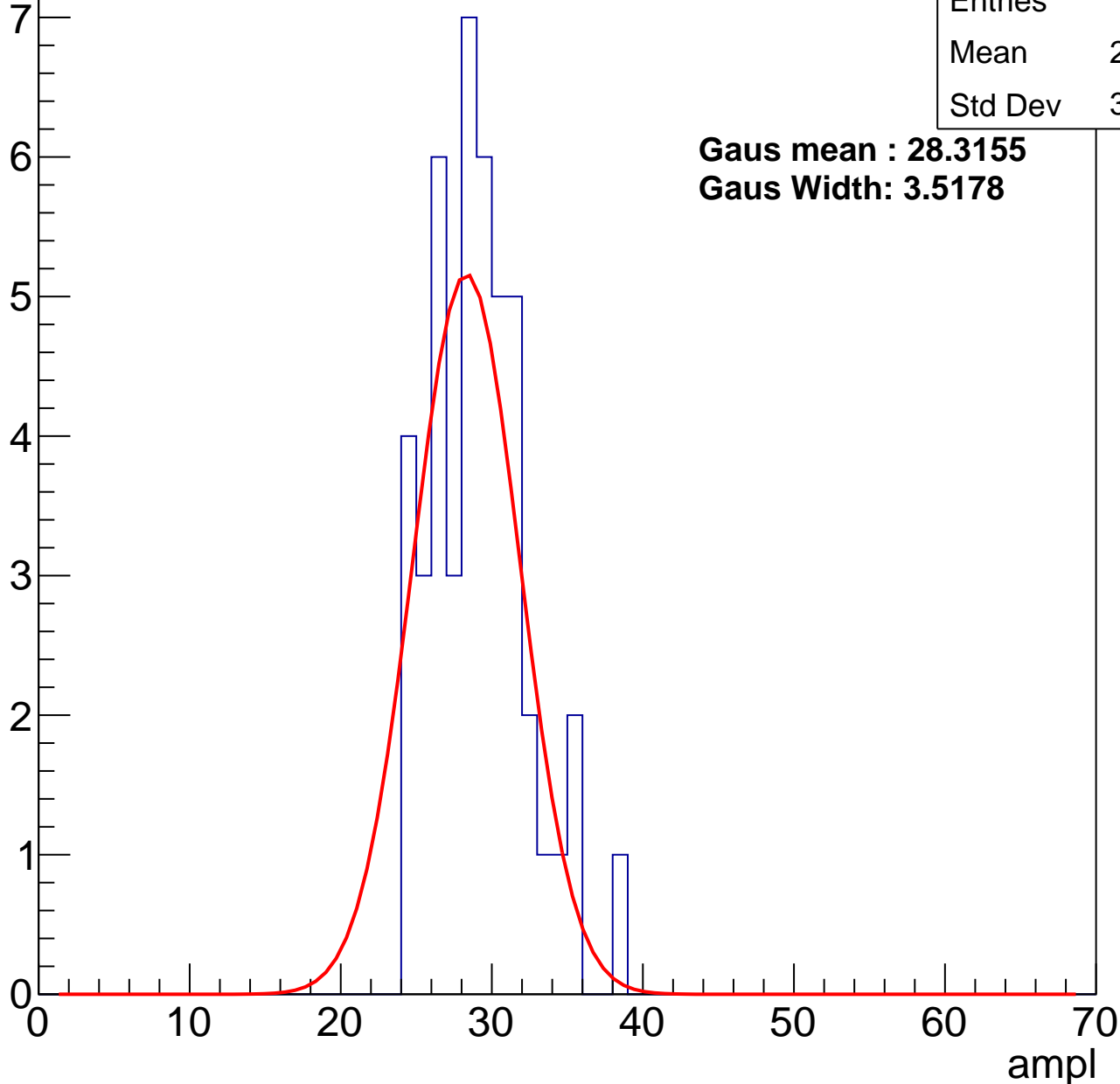
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	28.74
Std Dev	3.145

**Gaus mean : 28.3155**

**Gaus Width: 3.5178**



# B1L002S, U26-ch38, adc1

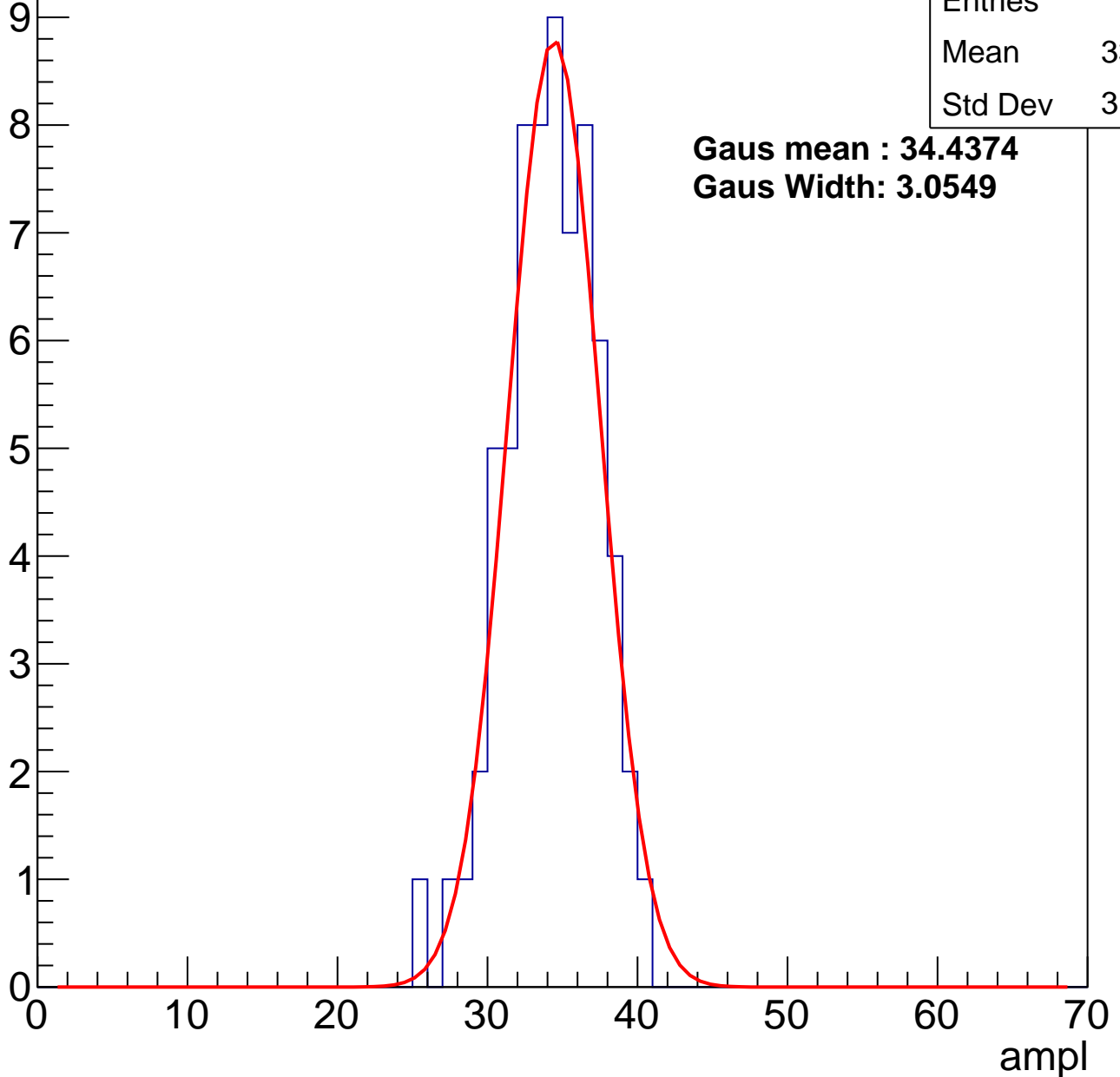
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.74
Std Dev	3.018

**Gaus mean : 34.4374**

**Gaus Width: 3.0549**



# B1L002S, U26-ch38, adc2

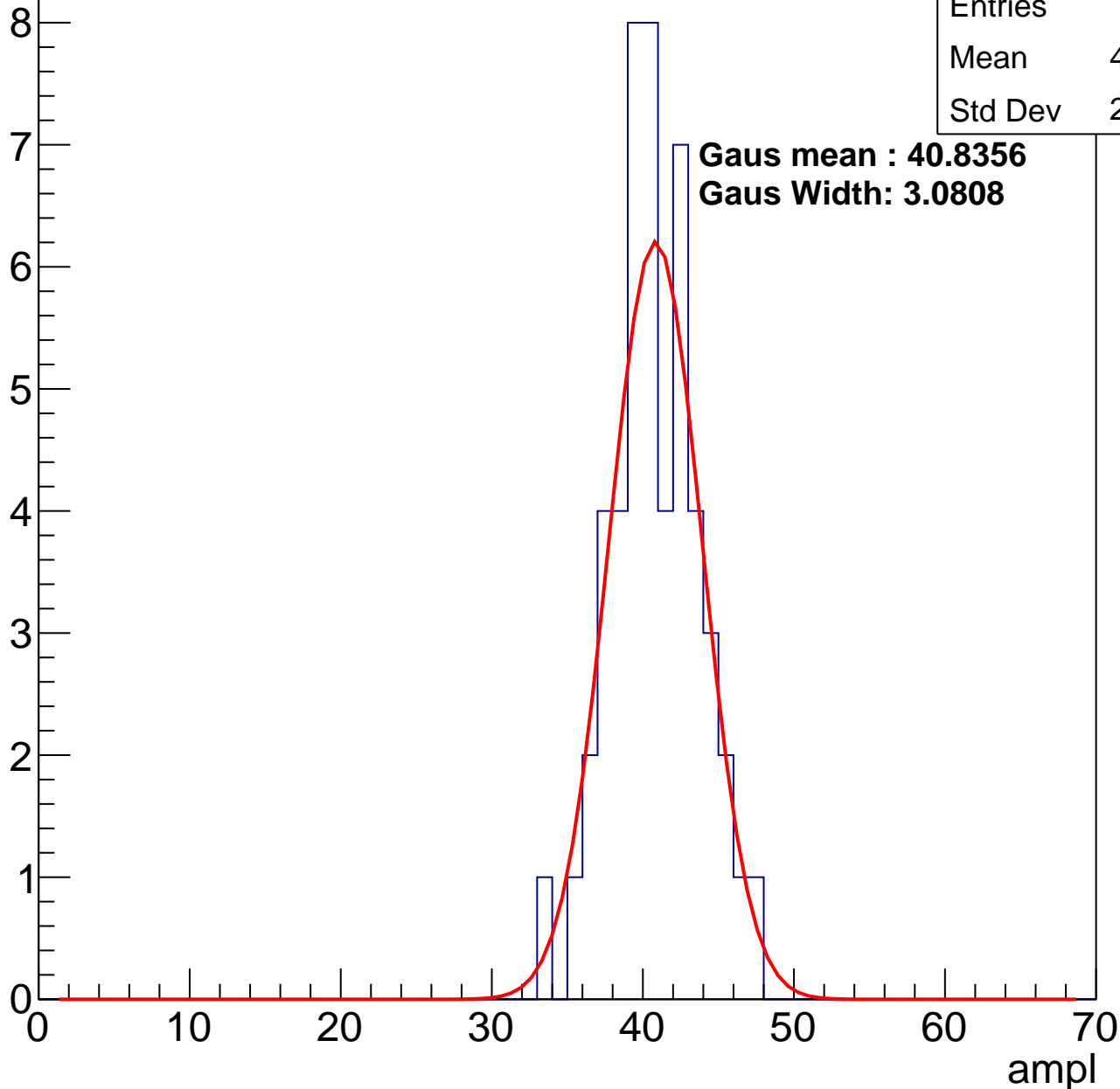
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.34
Std Dev	2.868

**Gaus mean : 40.8356**

**Gaus Width: 3.0808**

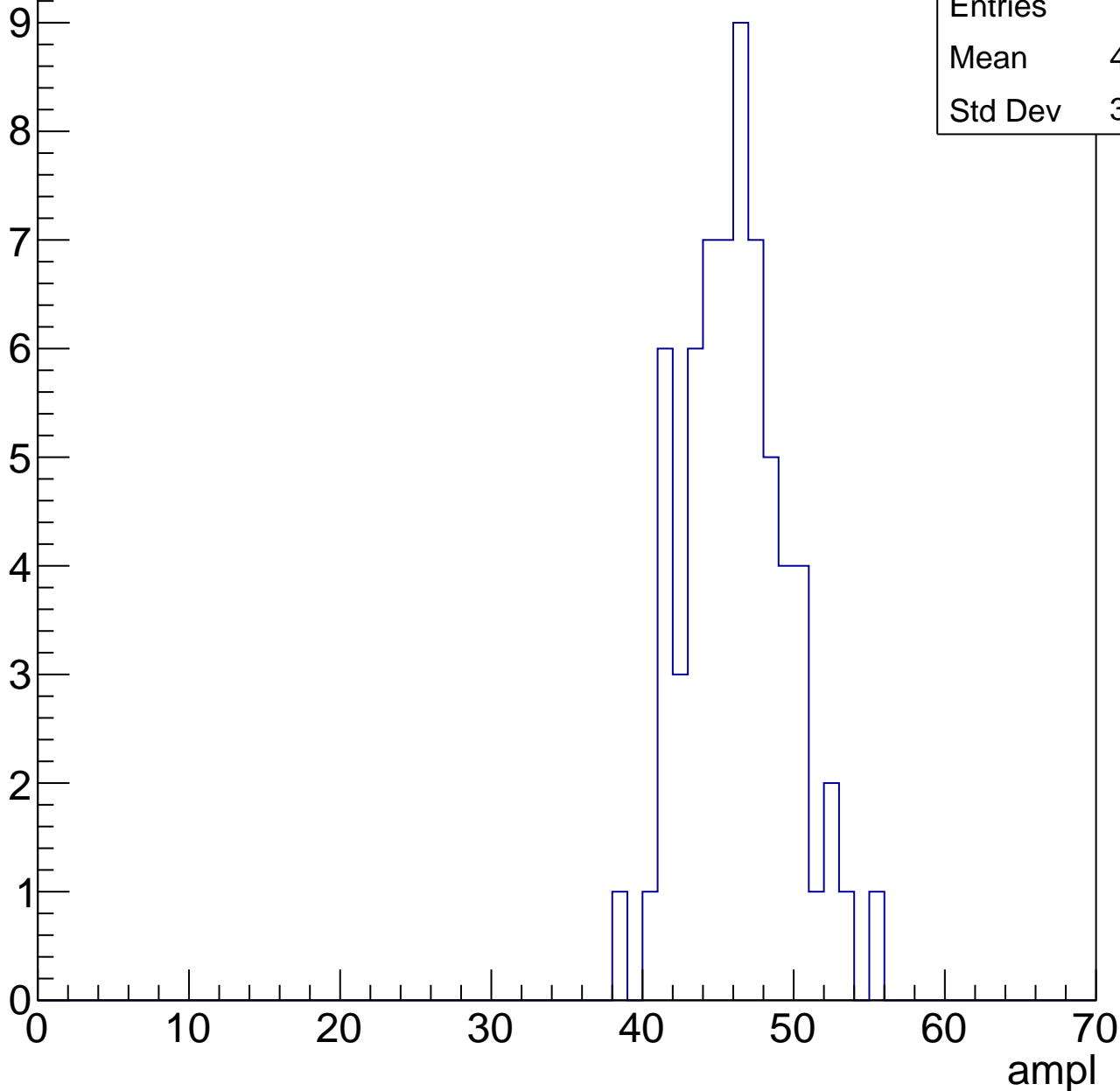


# B1L002S, U26-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	45.74
Std Dev	3.366

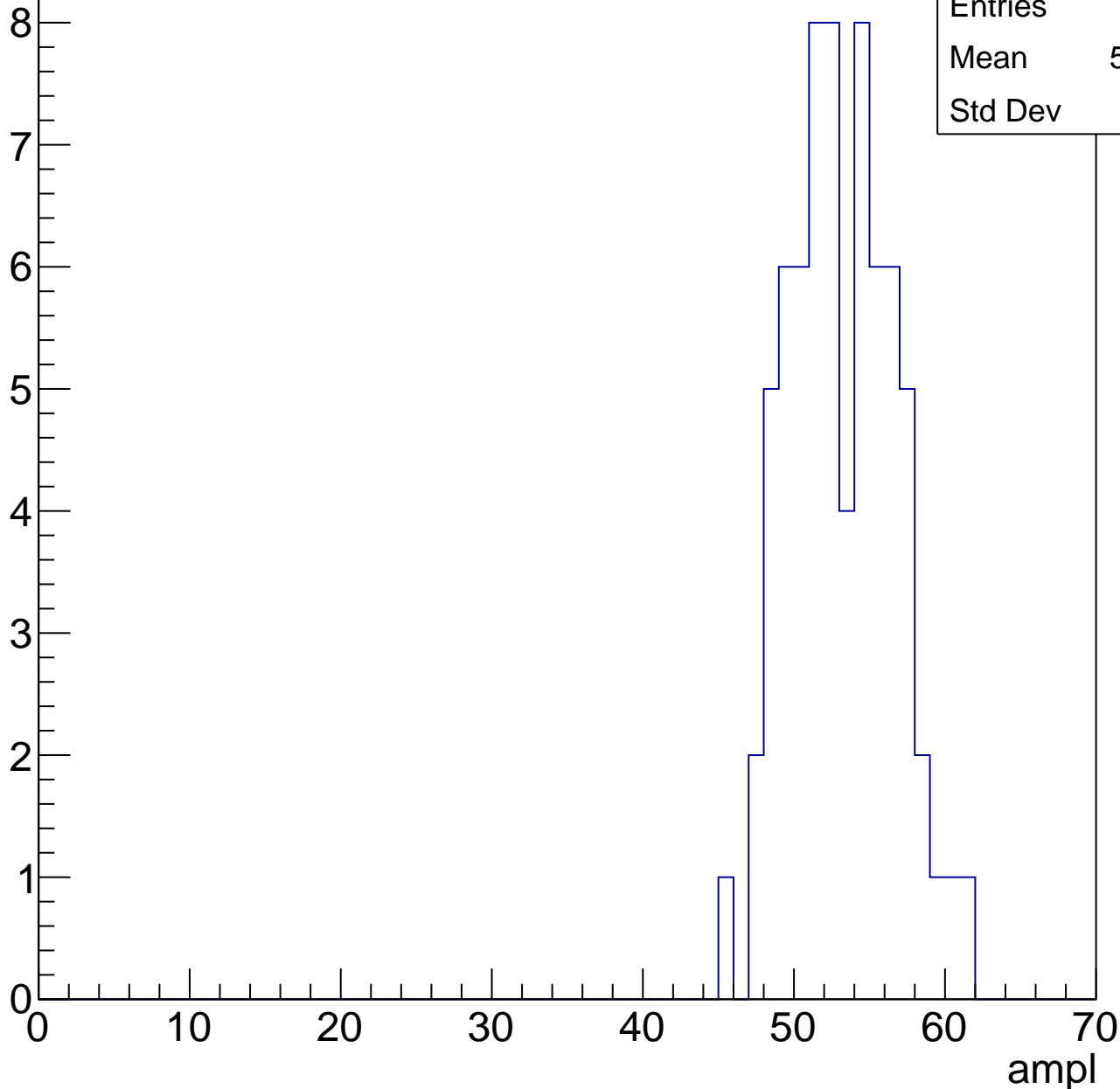


# B1L002S, U26-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	52.69
Std Dev	3.4



# B1L002S, U26-ch38, adc5

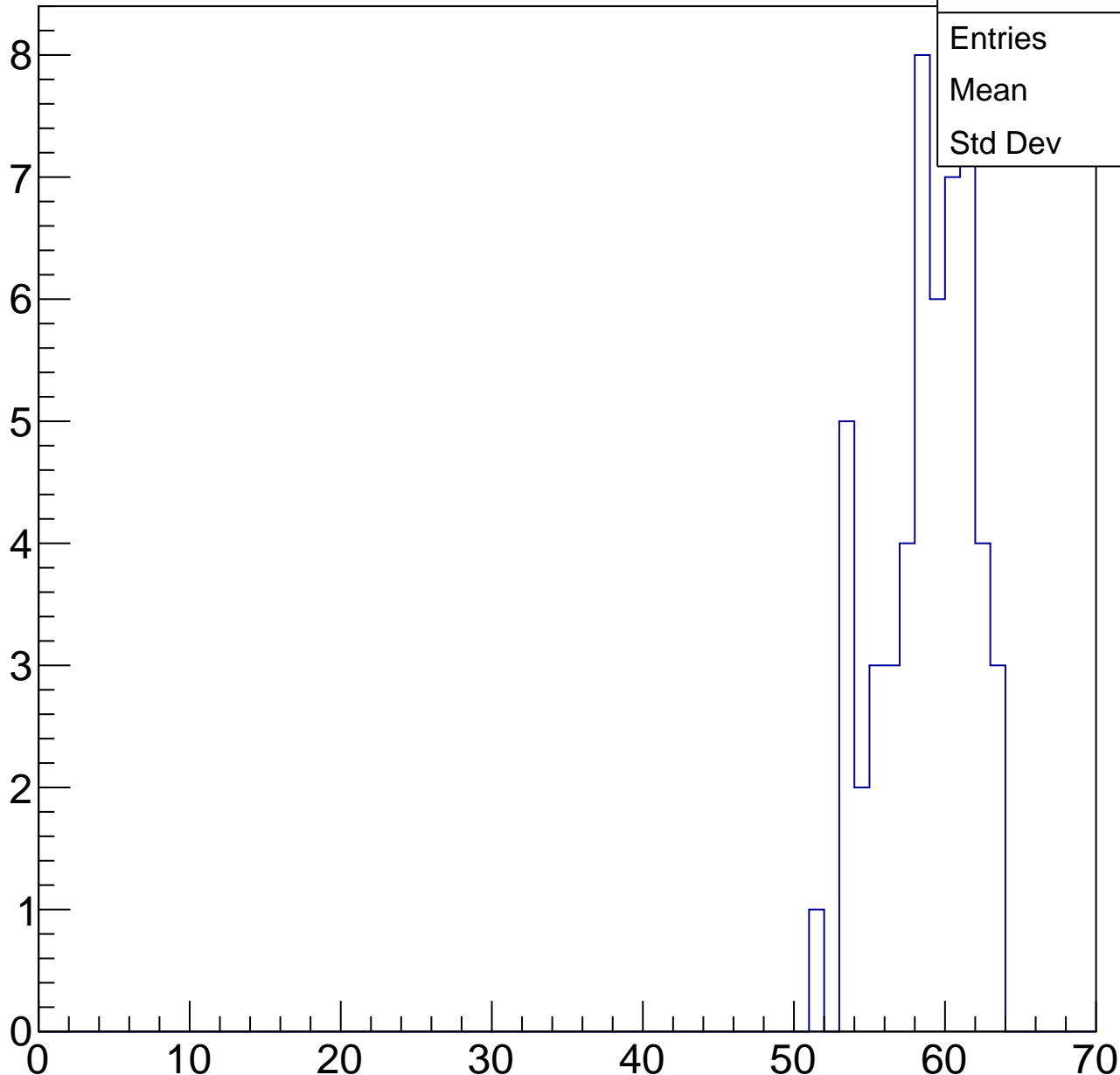
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.3
Std Dev	3.01

ampl

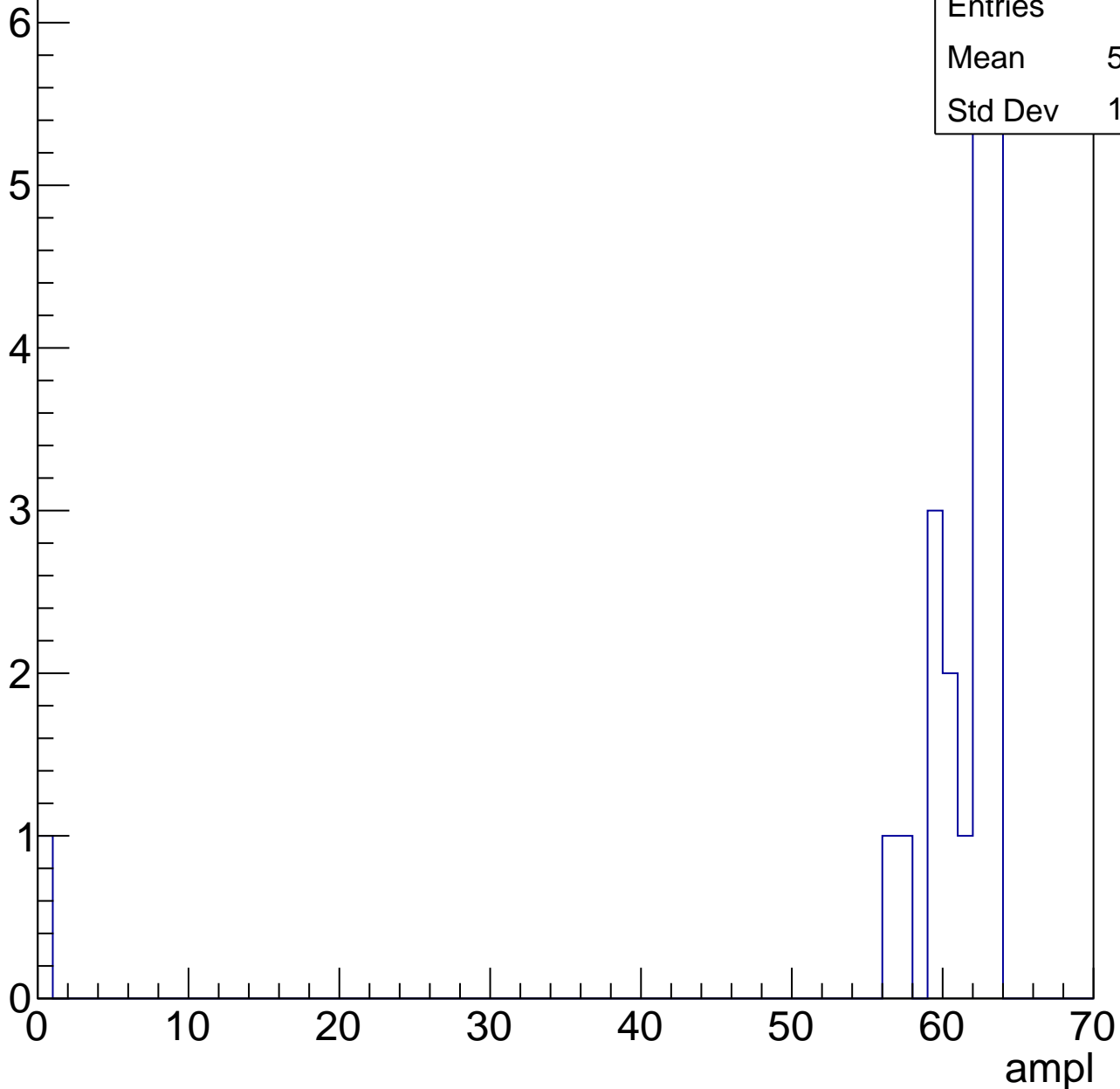


# B1L002S, U26-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.14
Std Dev	13.16

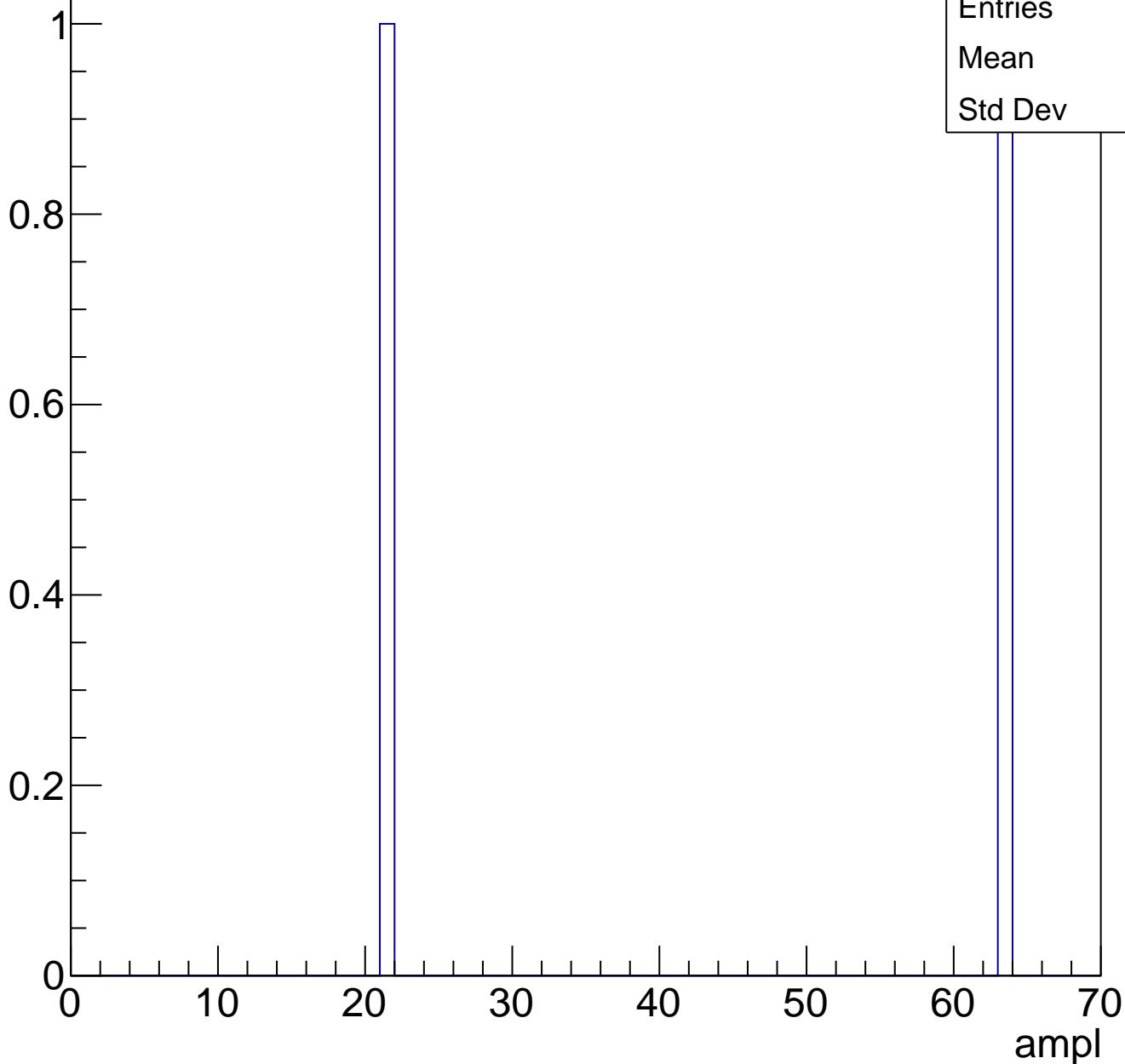




# B1L002S, U26-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch39, adc0

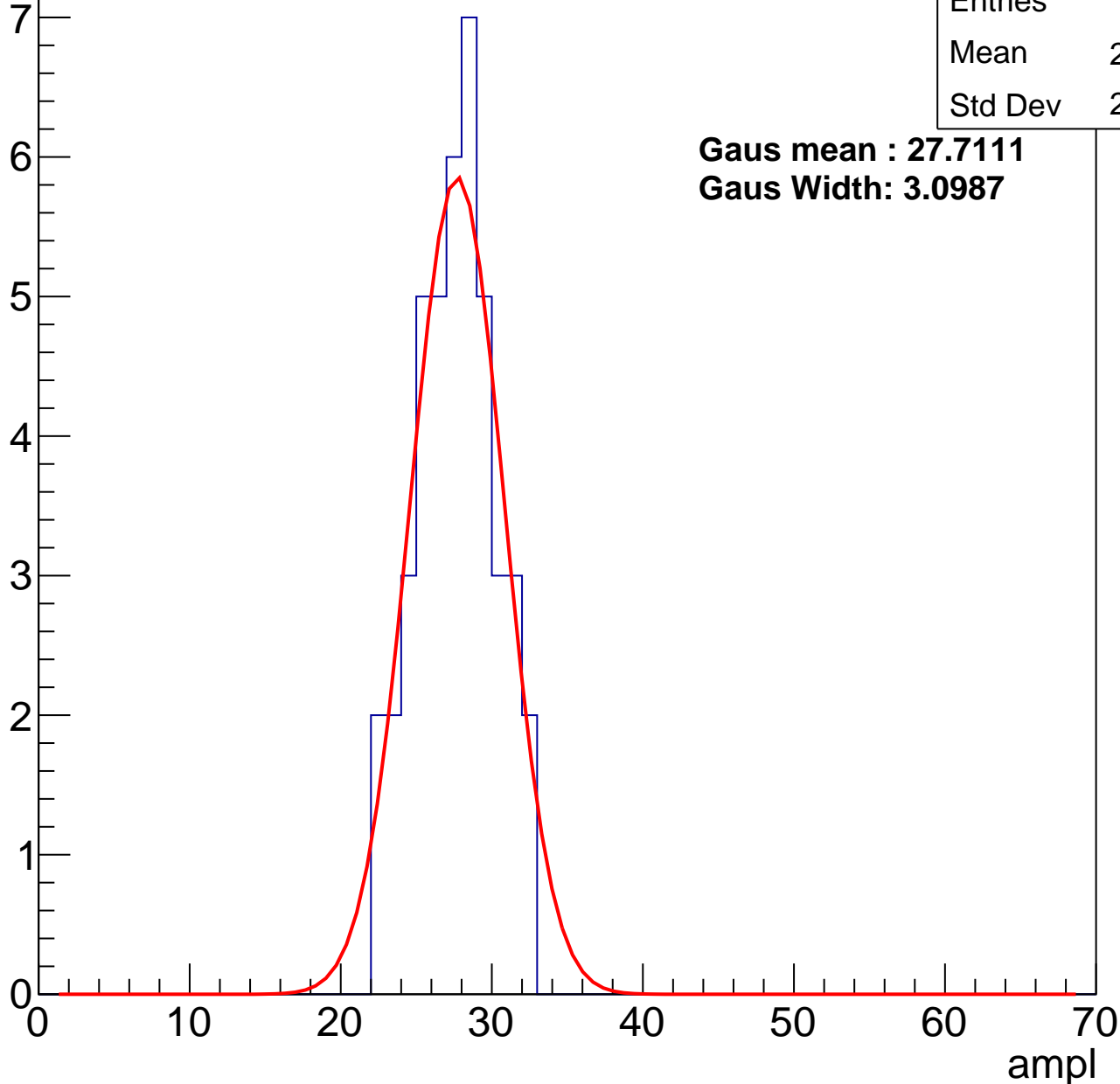
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	27.14
Std Dev	2.575

**Gaus mean : 27.7111**

**Gaus Width: 3.0987**



# B1L002S, U26-ch39, adc1

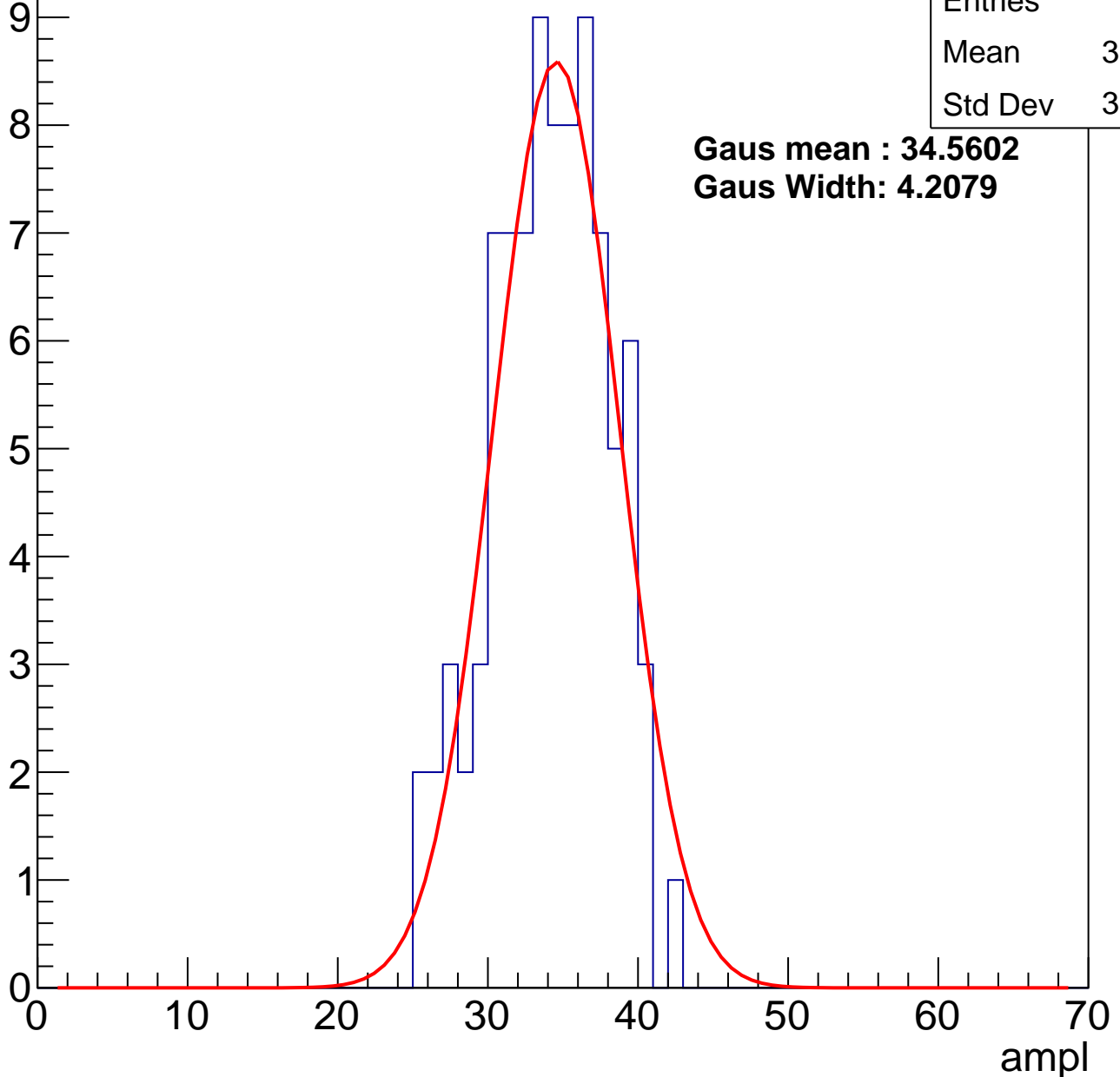
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	89
Mean	33.65
Std Dev	3.813

**Gaus mean : 34.5602**

**Gaus Width: 4.2079**



# B1L002S, U26-ch39, adc2

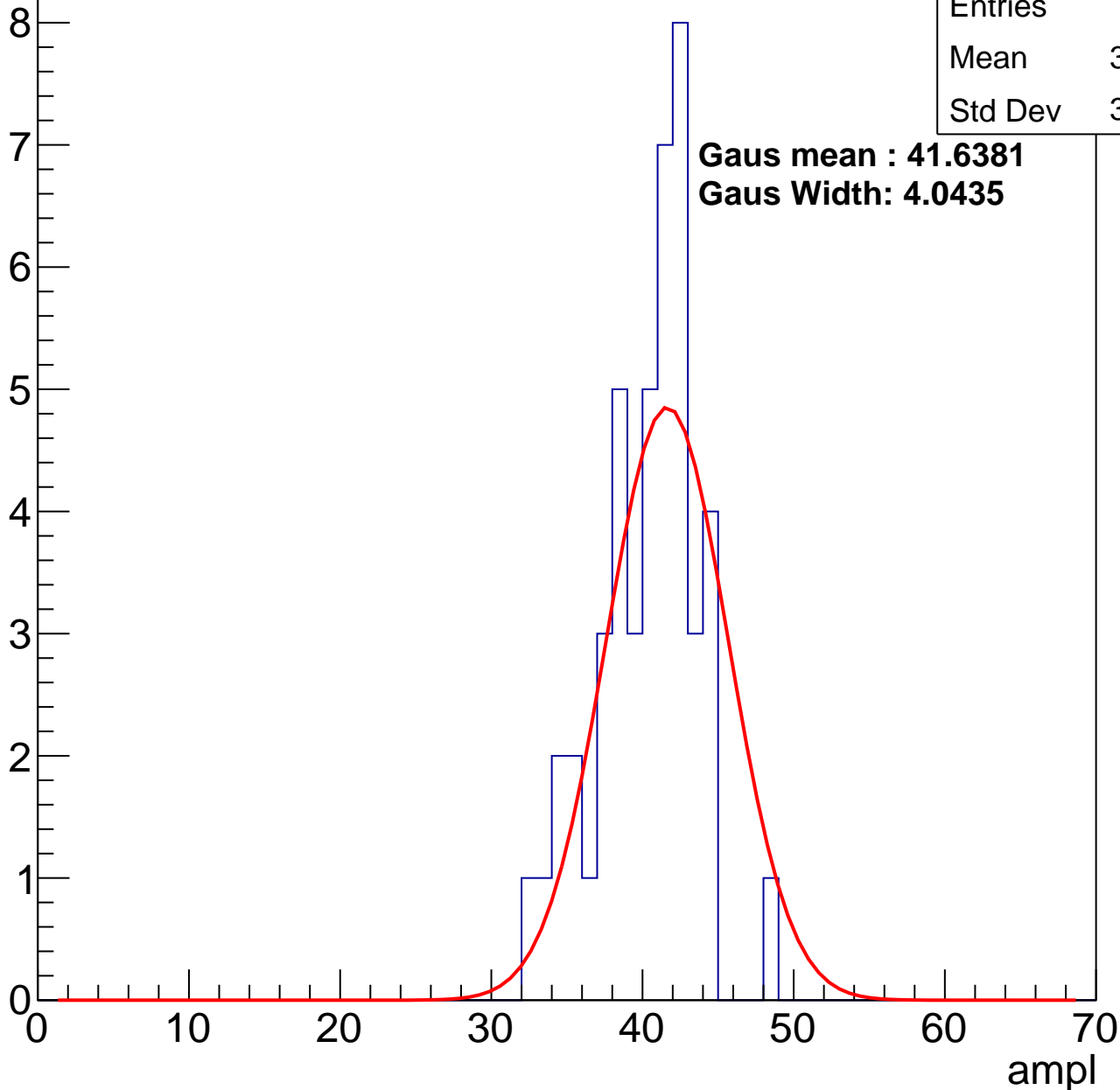
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	39.85
Std Dev	3.277

**Gaus mean : 41.6381**

**Gaus Width: 4.0435**



# B1L002S, U26-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	46.1
Std Dev	3.101

Entry

10

8

6

4

2

0

0

10

20

30

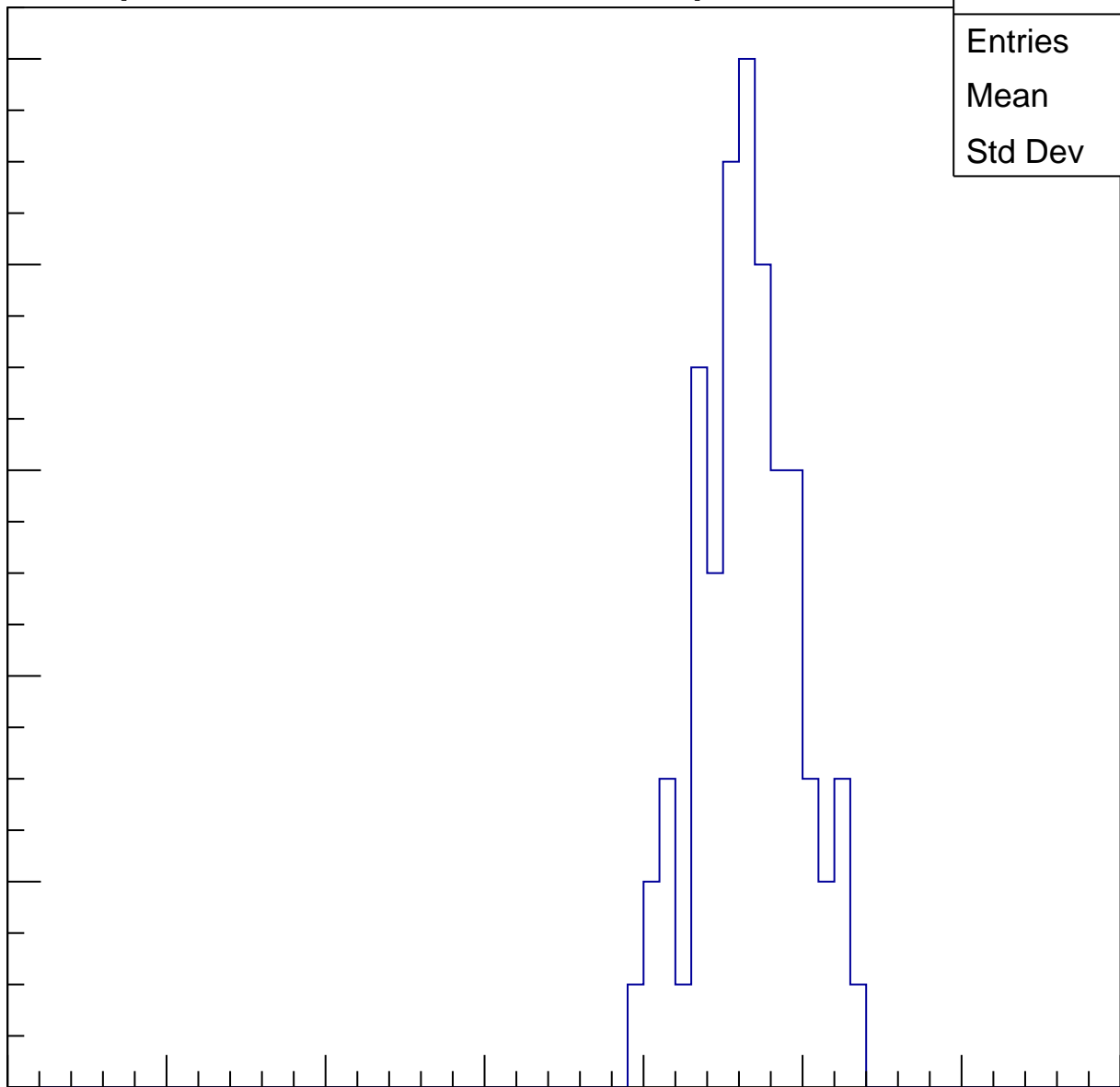
40

50

60

70

ampl

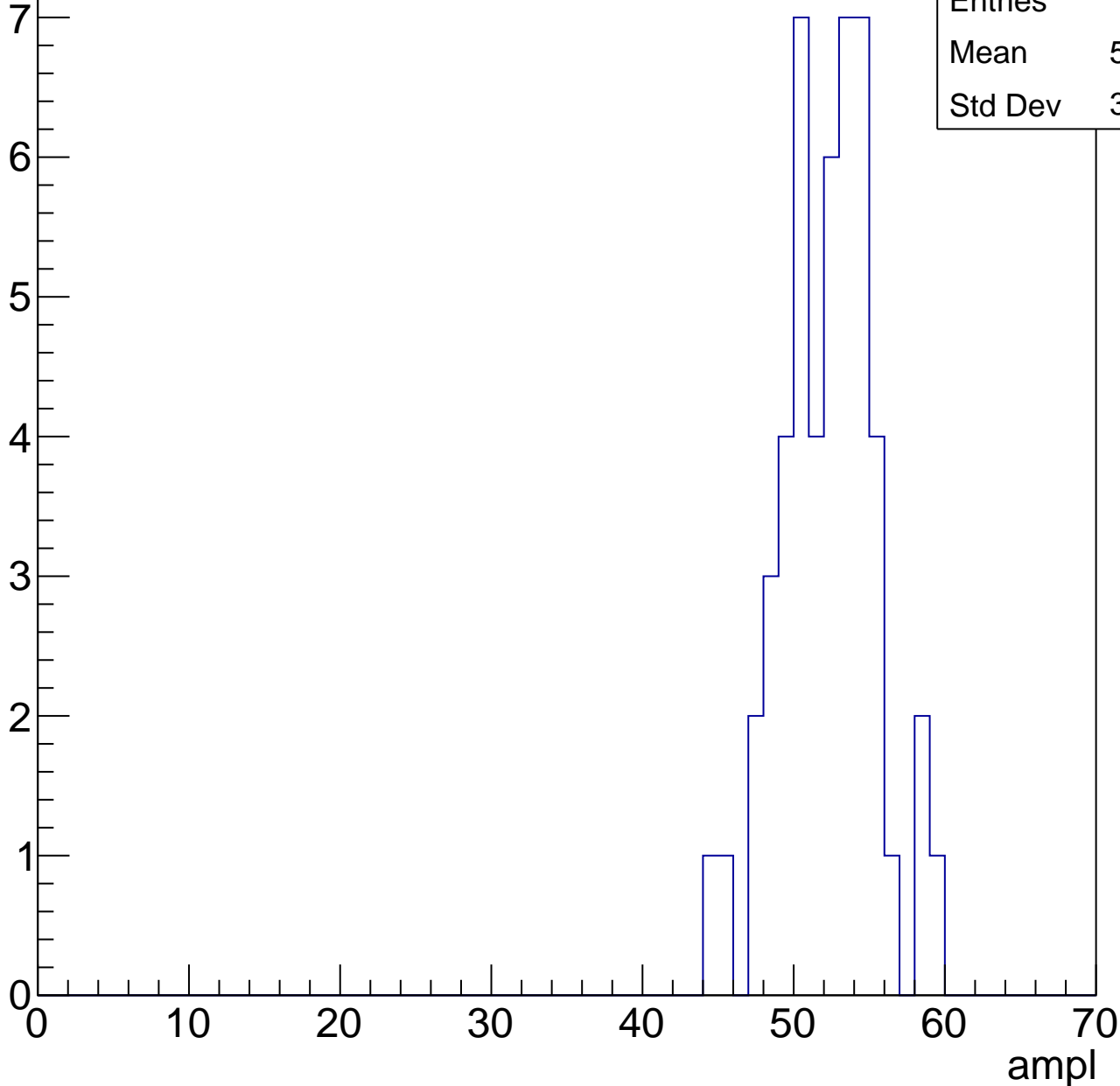


# B1L002S, U26-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	51.78
Std Dev	3.113



# B1L002S, U26-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	57.48
Std Dev	3.031

Entry

10

8

6

4

2

0

0

10

20

30

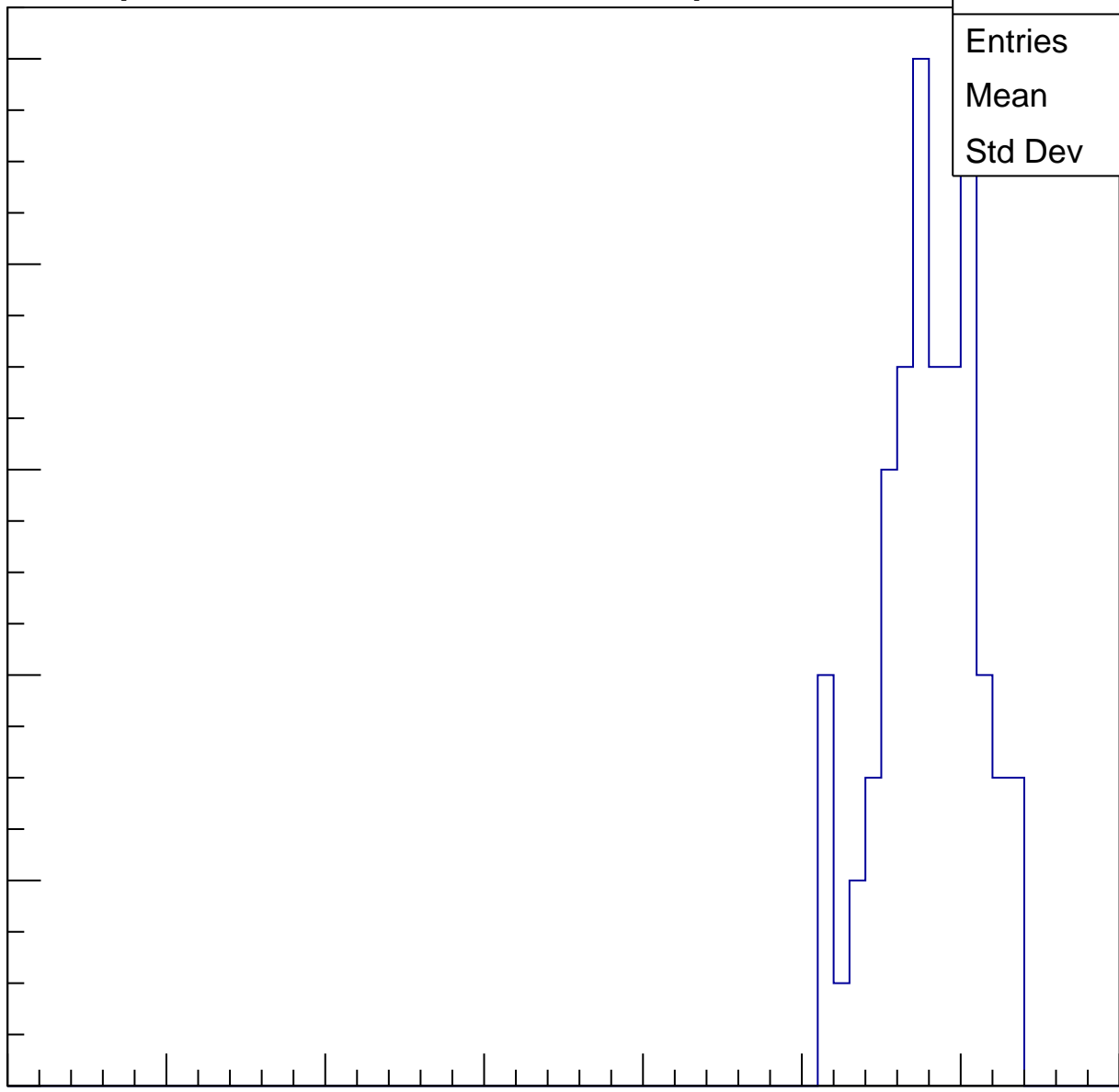
40

50

60

70

ampl

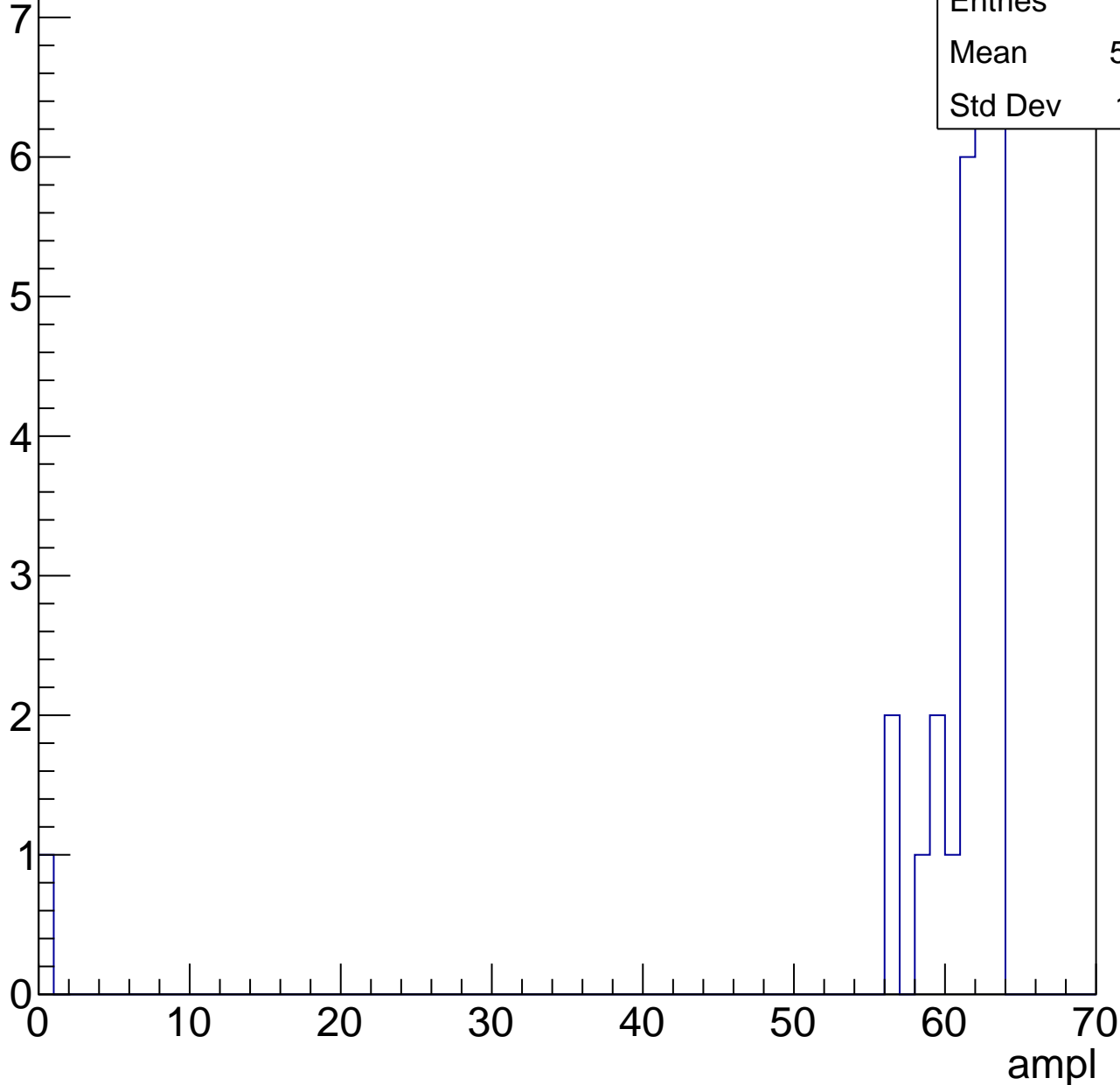


# B1L002S, U26-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.85
Std Dev	11.71





# B1L002S, U26-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L002S, U26-ch40, adc0

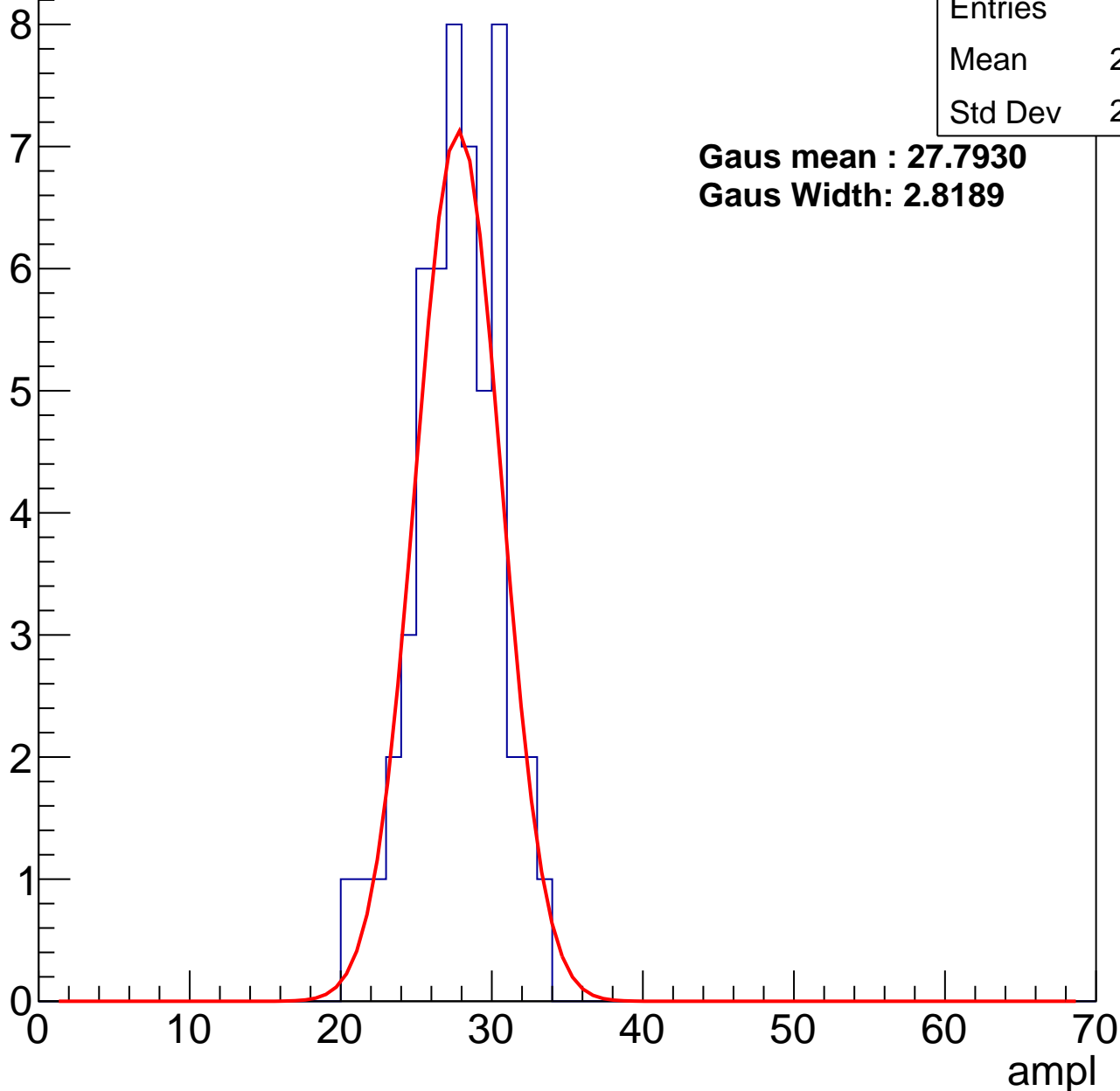
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	27.23
Std Dev	2.792

**Gaus mean : 27.7930**

**Gaus Width: 2.8189**



# B1L002S, U26-ch40, adc1

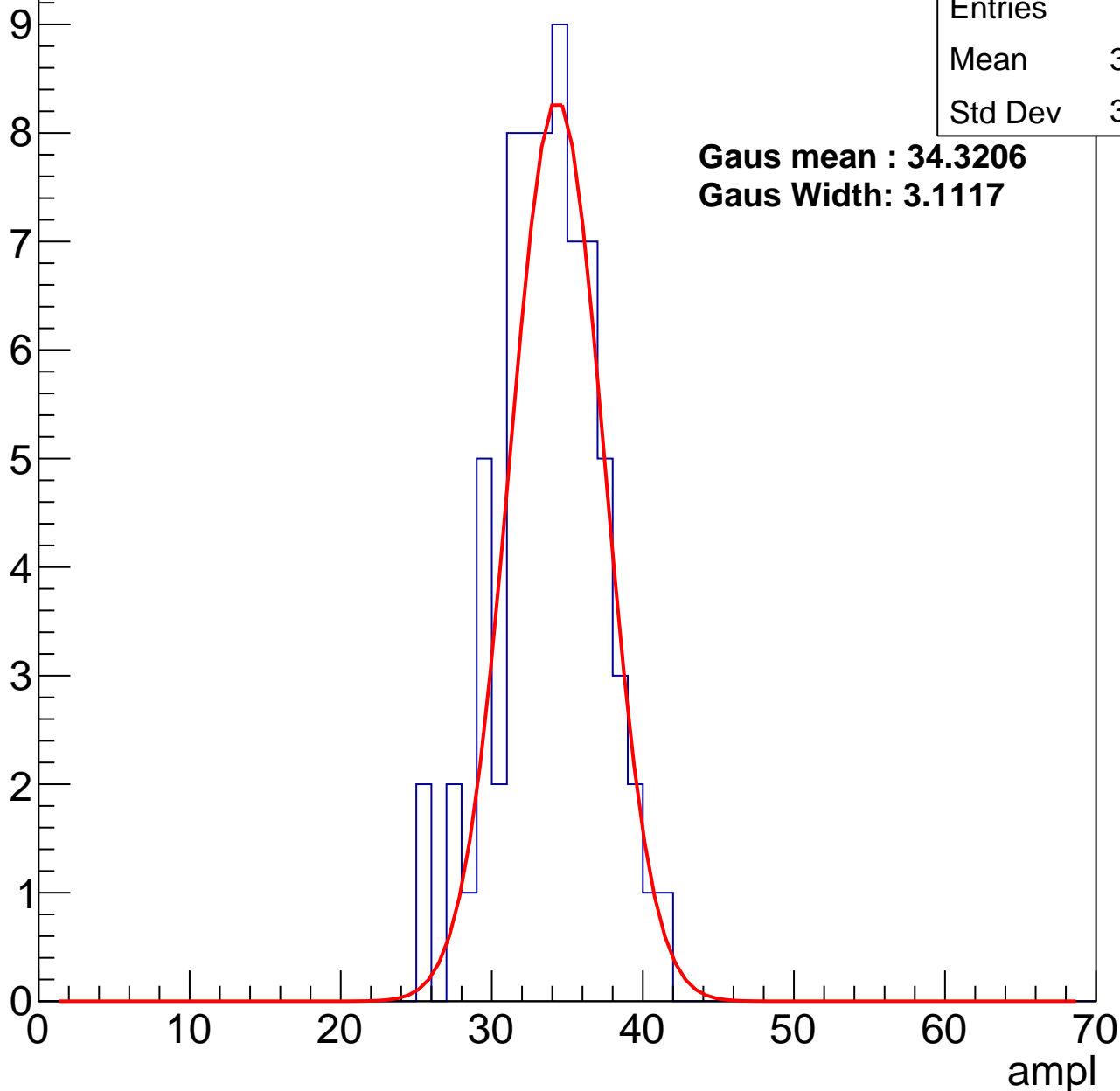
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	33.32
Std Dev	3.347

**Gaus mean : 34.3206**

**Gaus Width: 3.1117**



# B1L002S, U26-ch40, adc2

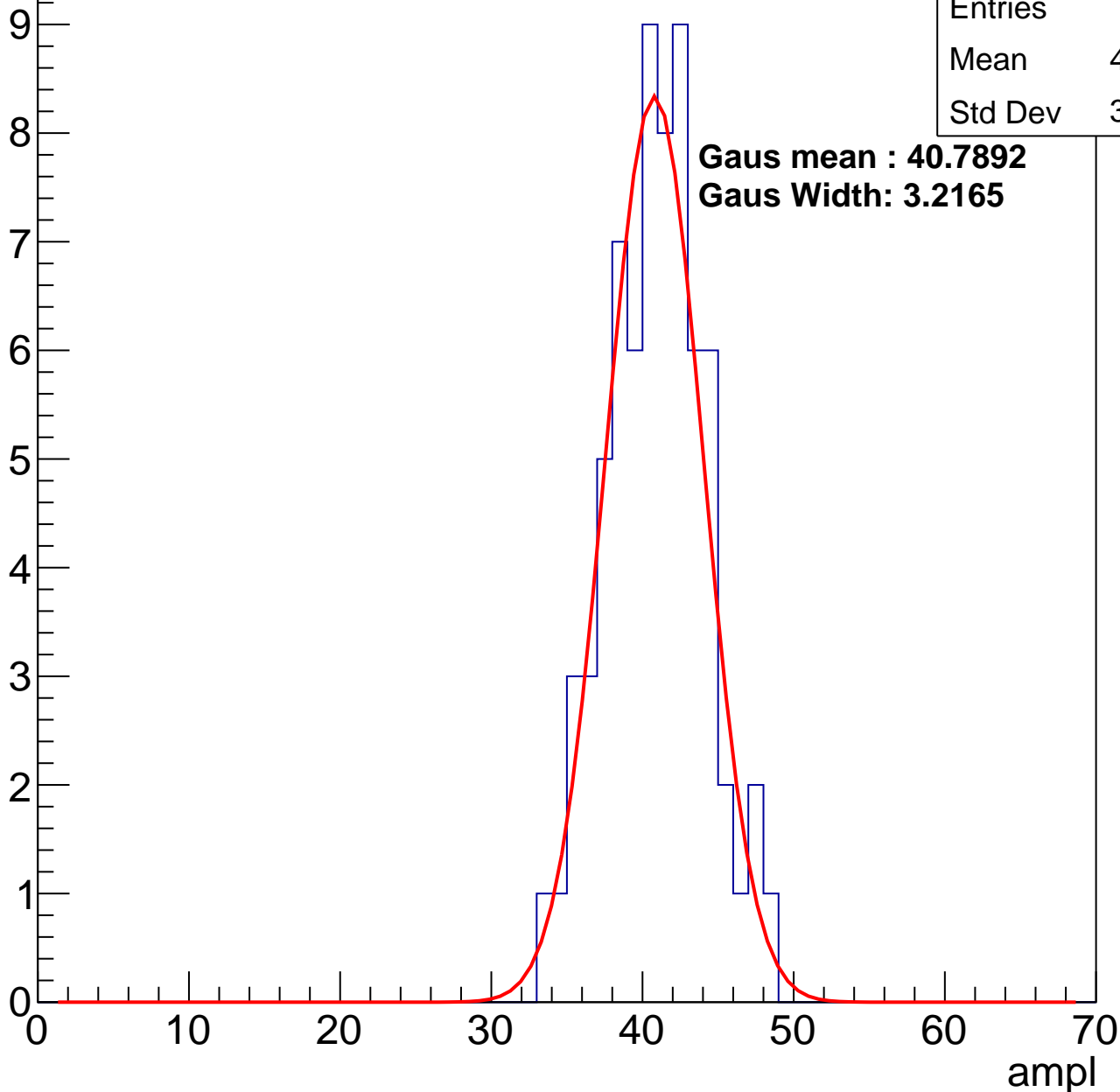
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.44
Std Dev	3.197

**Gaus mean : 40.7892**

**Gaus Width: 3.2165**

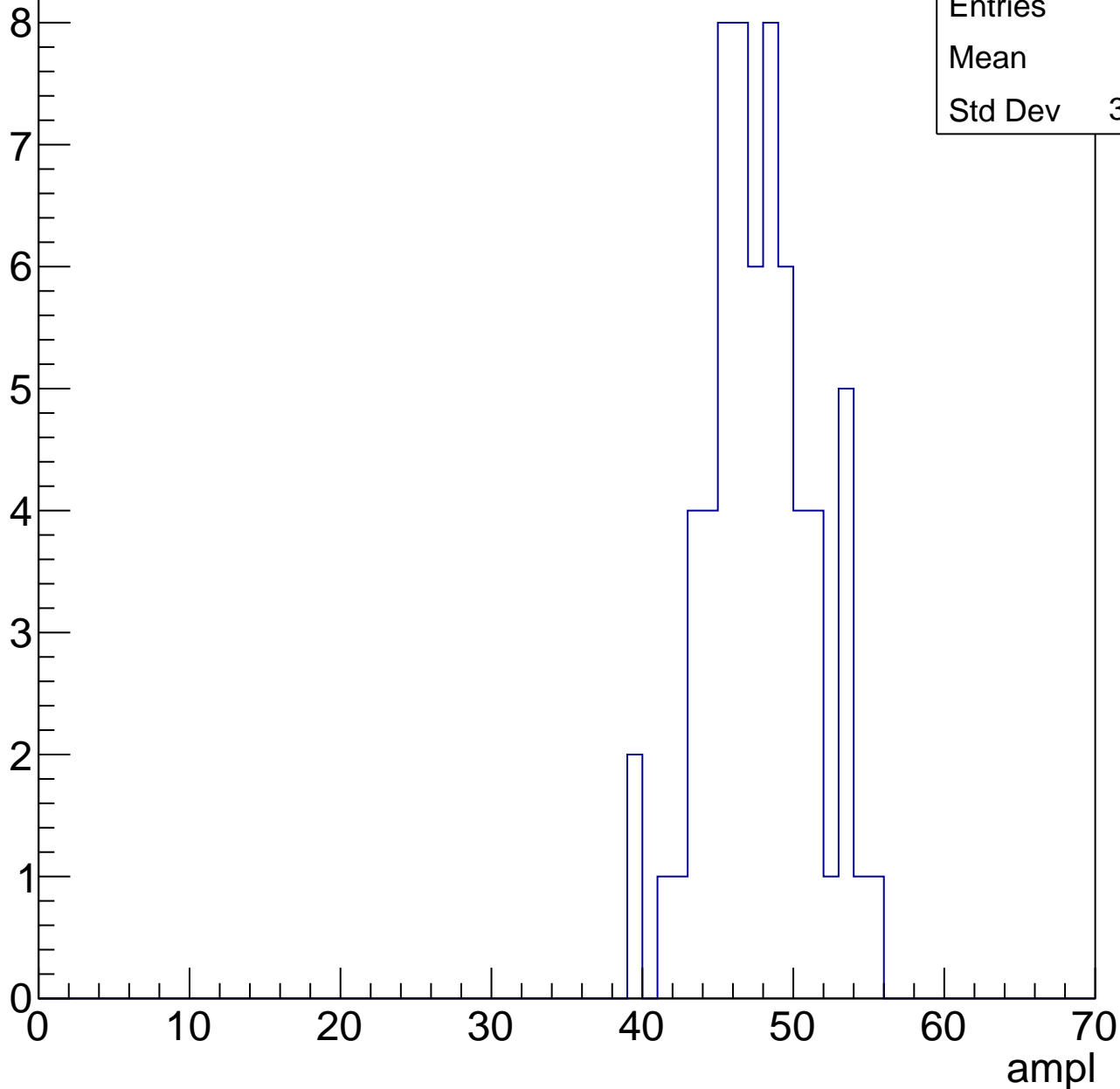


# B1L002S, U26-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	47.3
Std Dev	3.476

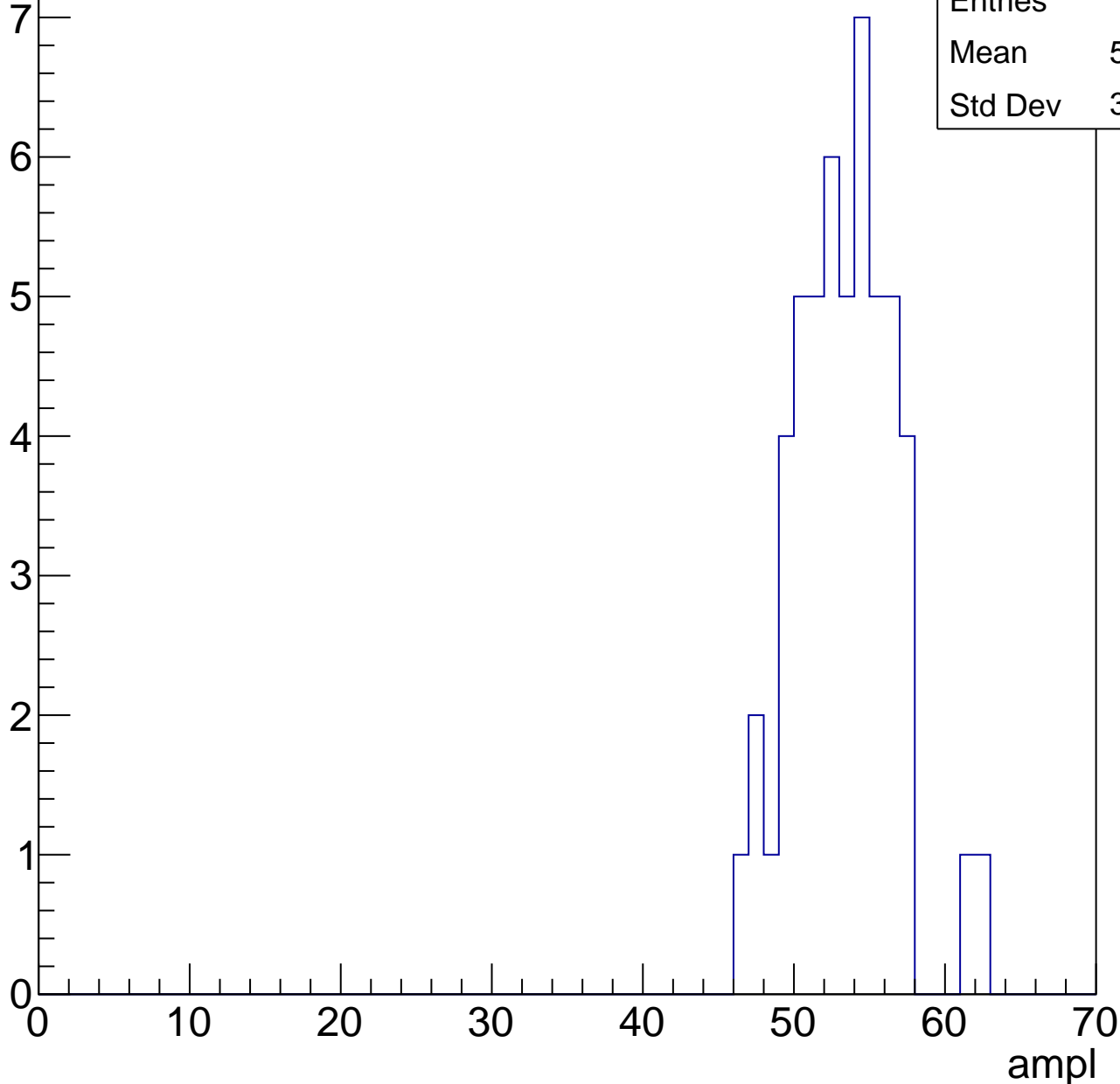


# B1L002S, U26-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	52.88
Std Dev	3.285

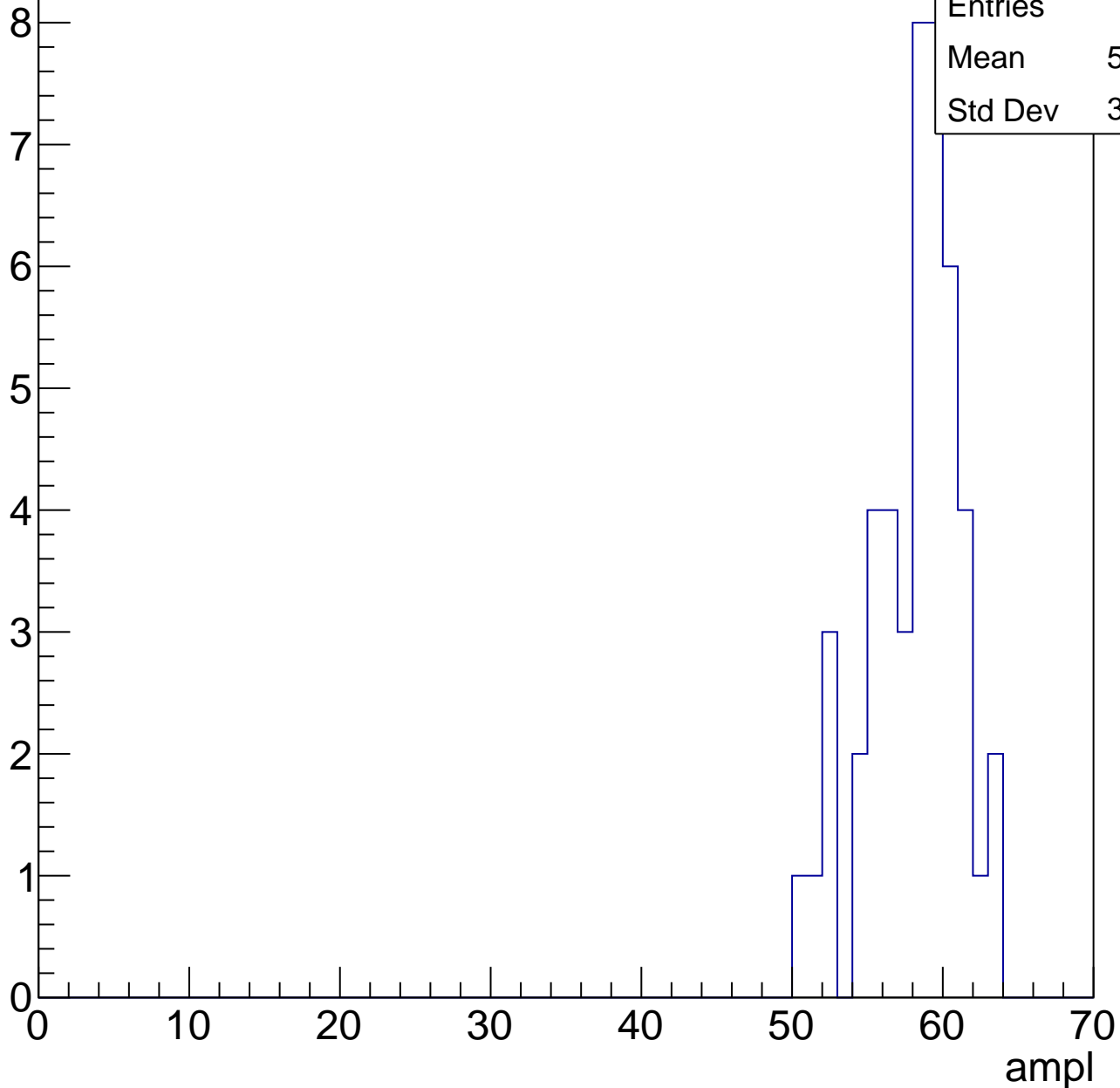


# B1L002S, U26-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.62
Std Dev	3.043

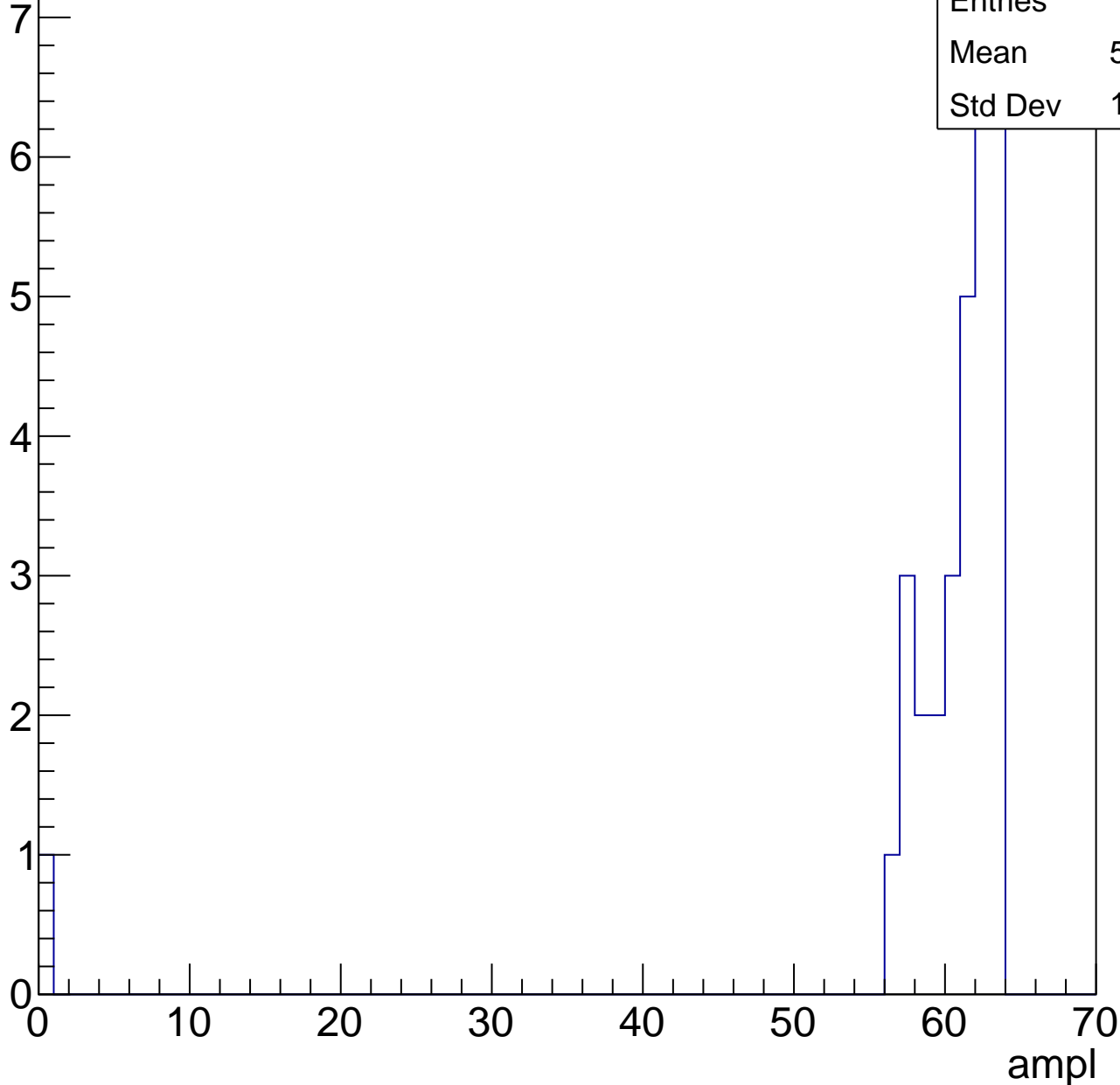


# B1L002S, U26-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	58.74
Std Dev	10.92

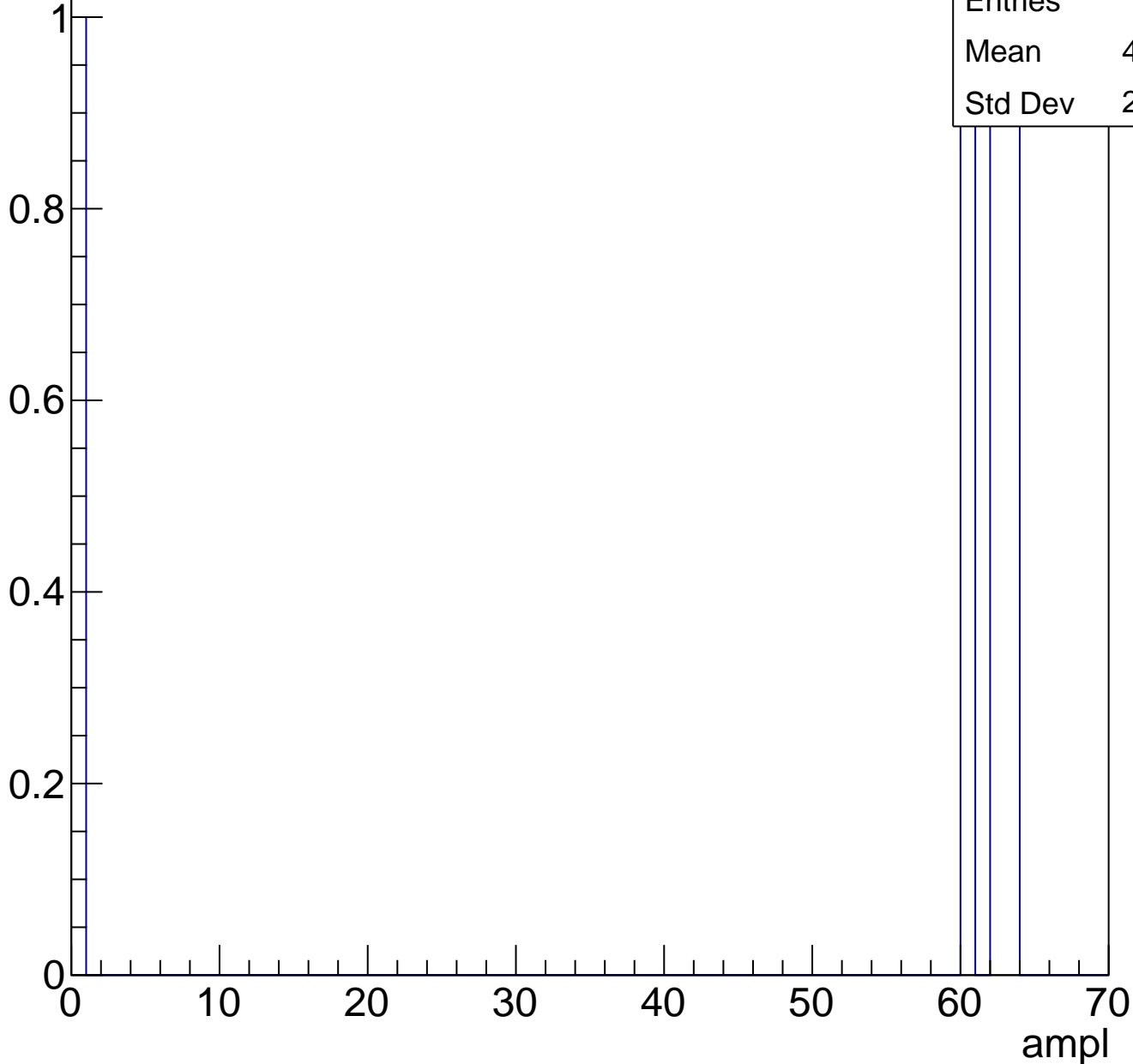




# B1L002S, U26-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch41, adc0

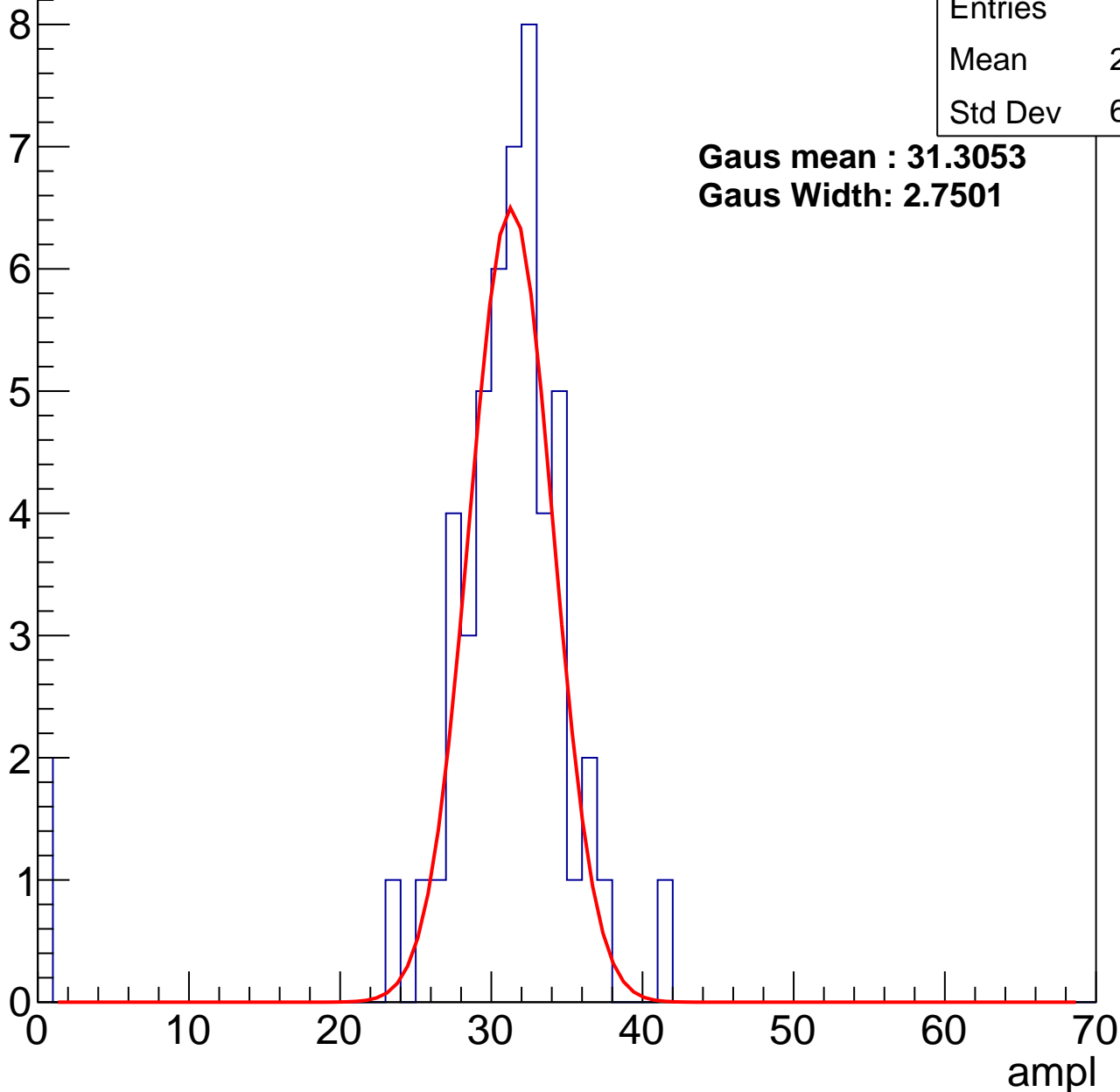
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	29.83
Std Dev	6.742

**Gaus mean : 31.3053**

**Gaus Width: 2.7501**



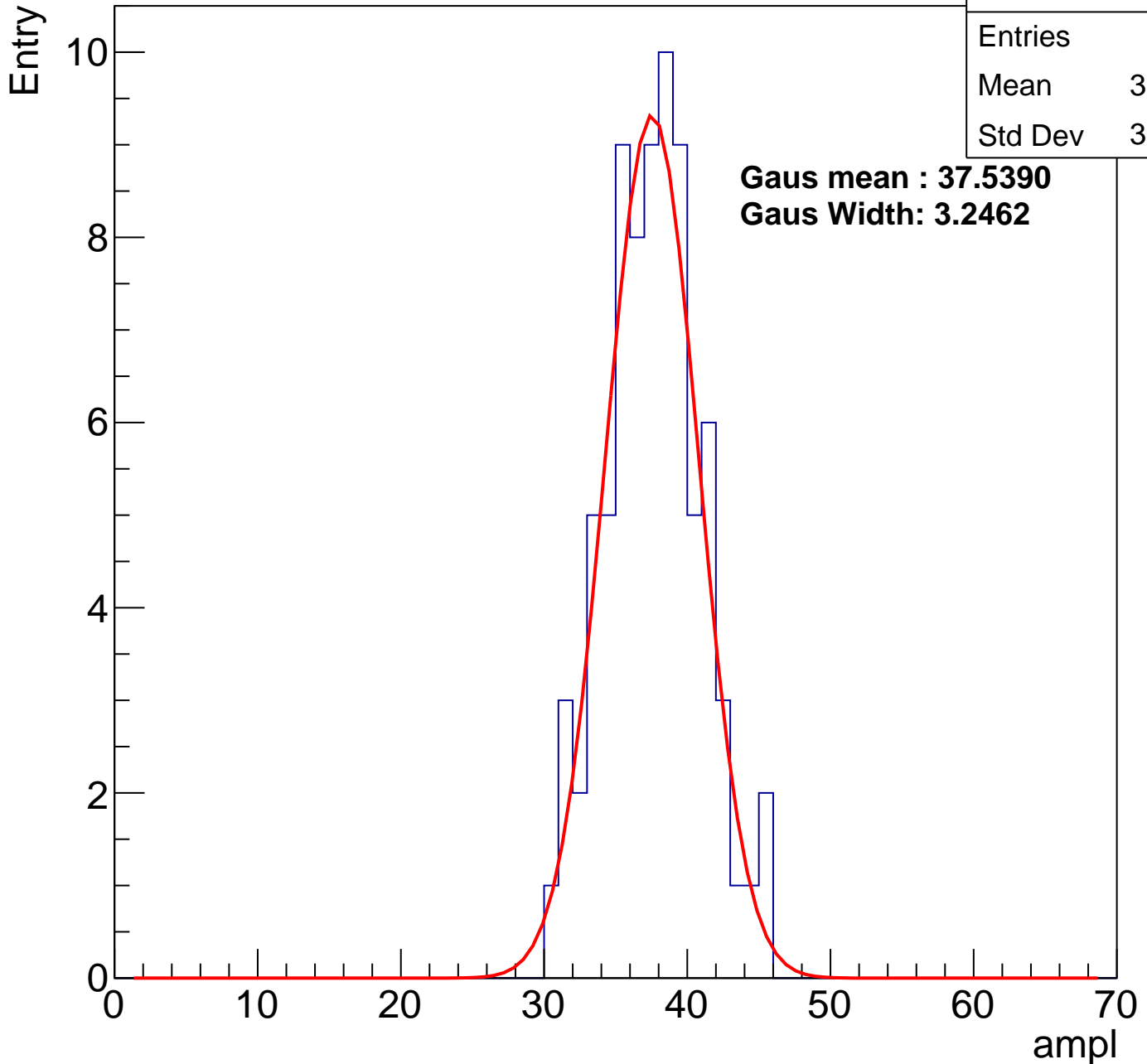
# B1L002S, U26-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	37.19
Std Dev	3.277

**Gaus mean : 37.5390**

**Gaus Width: 3.2462**



# B1L002S, U26-ch41, adc2

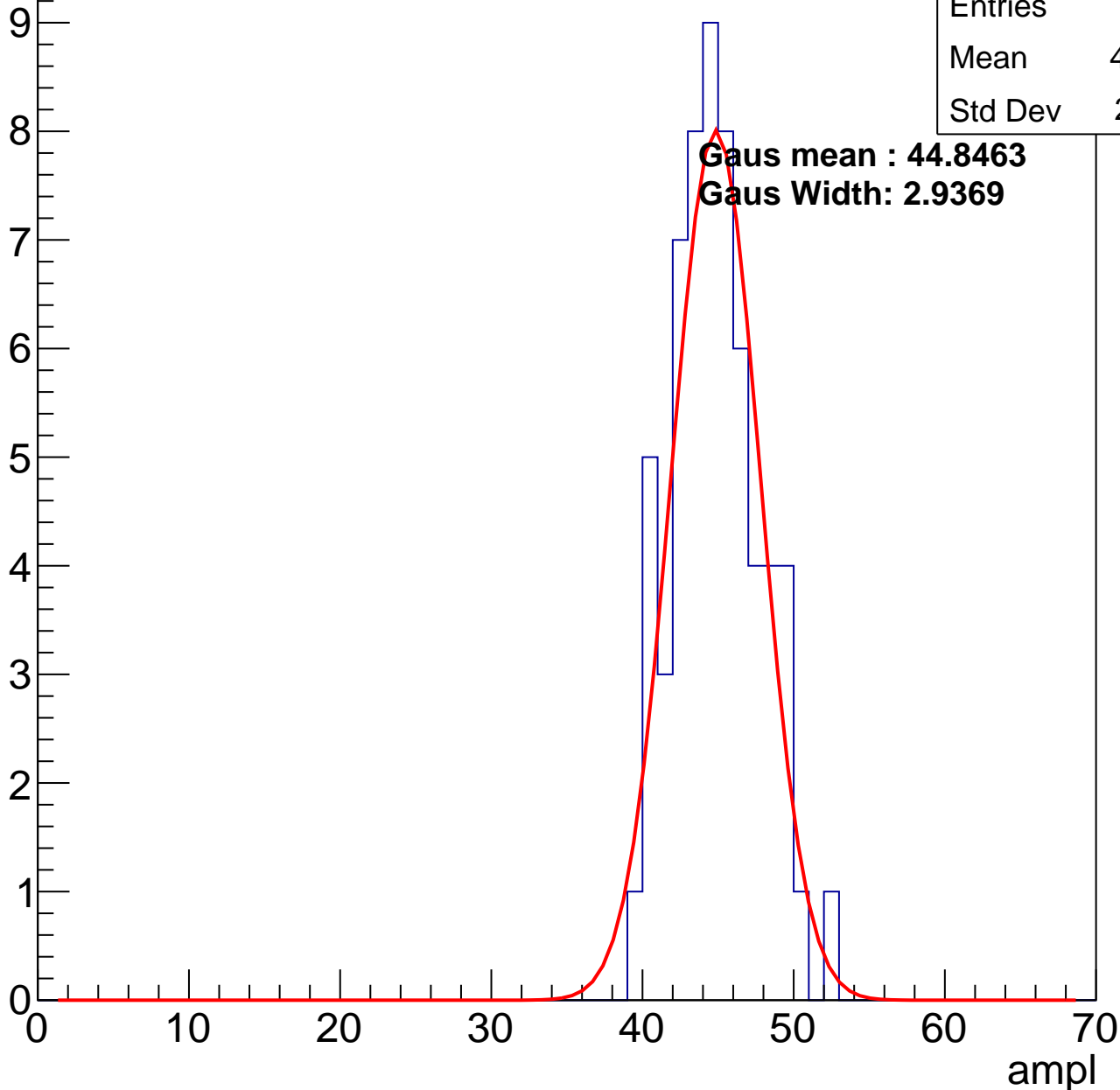
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	44.43
Std Dev	2.831

**Gaus mean : 44.8463**

**Gaus Width: 2.9369**

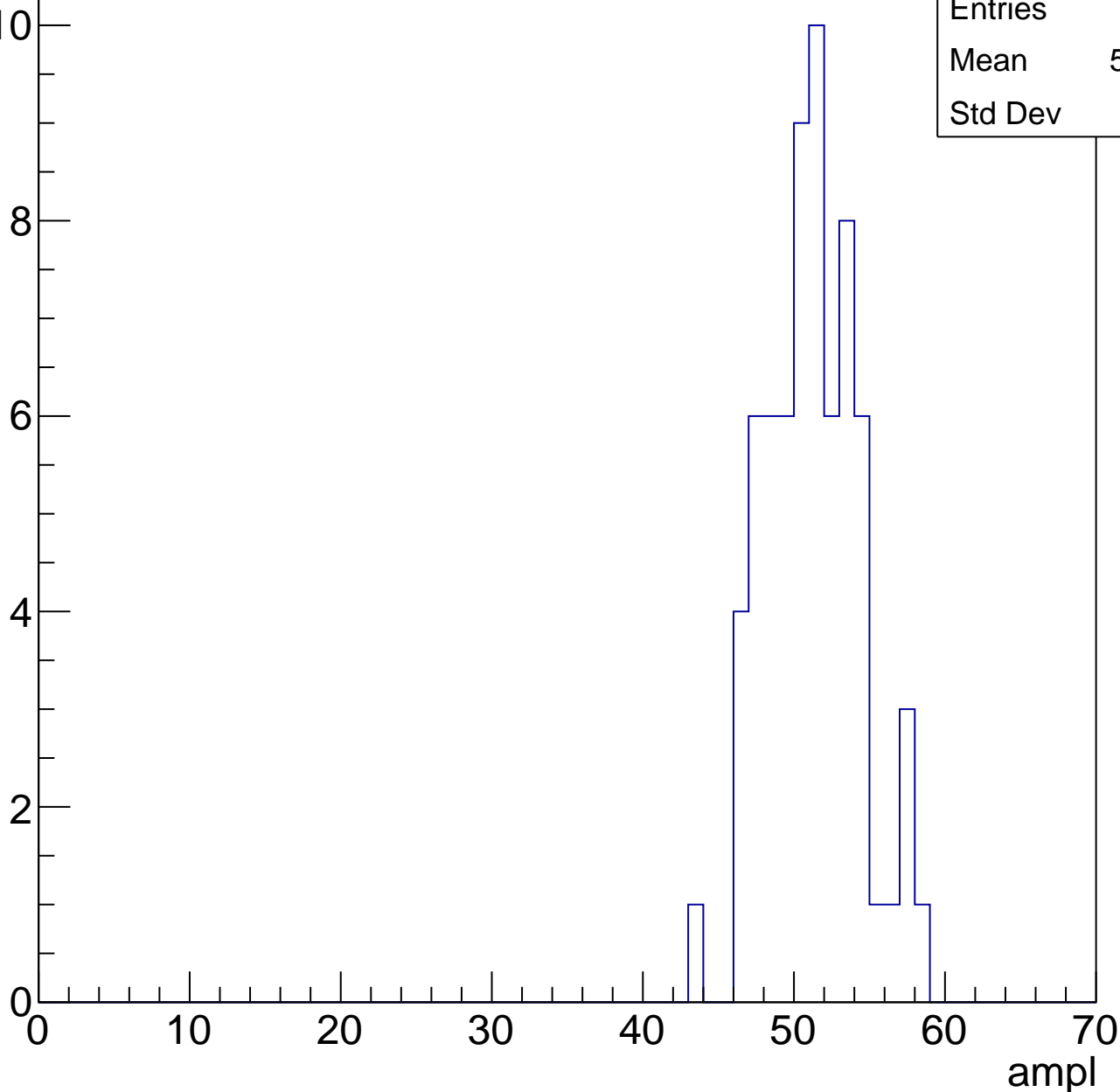


# B1L002S, U26-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

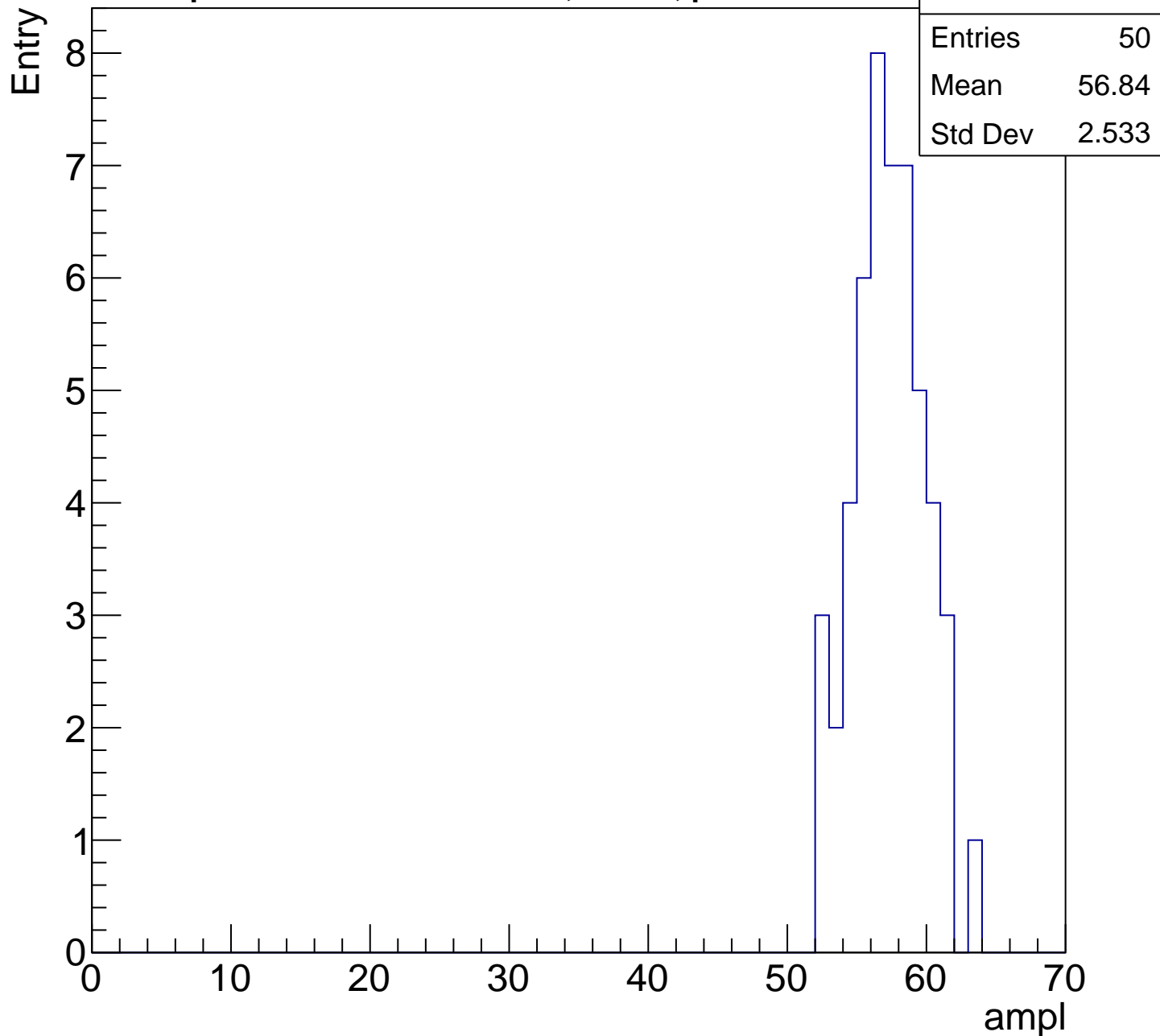
Entry

Entries	68
Mean	50.75
Std Dev	3.05



# B1L002S, U26-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	42
Mean	59.05
Std Dev	9.482

Entry

10

8

6

4

2

0

0

10

20

30

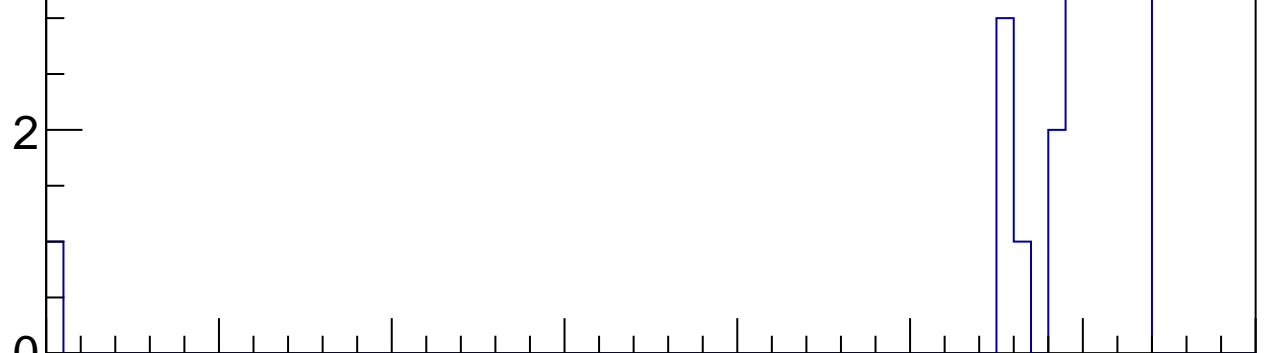
40

50

60

70

ampl



# B1L002S, U26-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	63
Std Dev	0



# B1L002S, U26-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch42, adc0

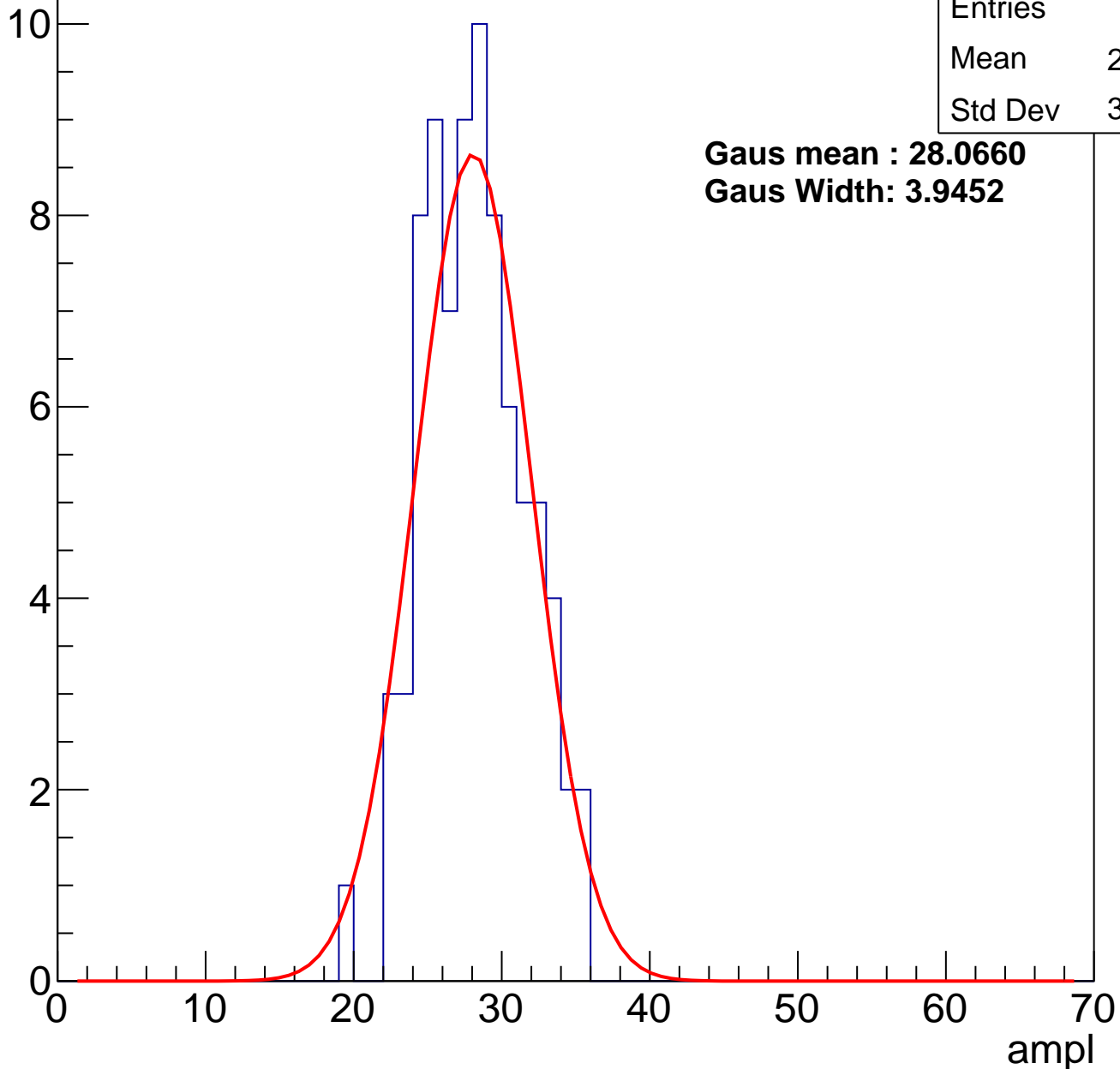
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	27.72
Std Dev	3.369

**Gaus mean : 28.0660**

**Gaus Width: 3.9452**

Entry



# B1L002S, U26-ch42, adc1

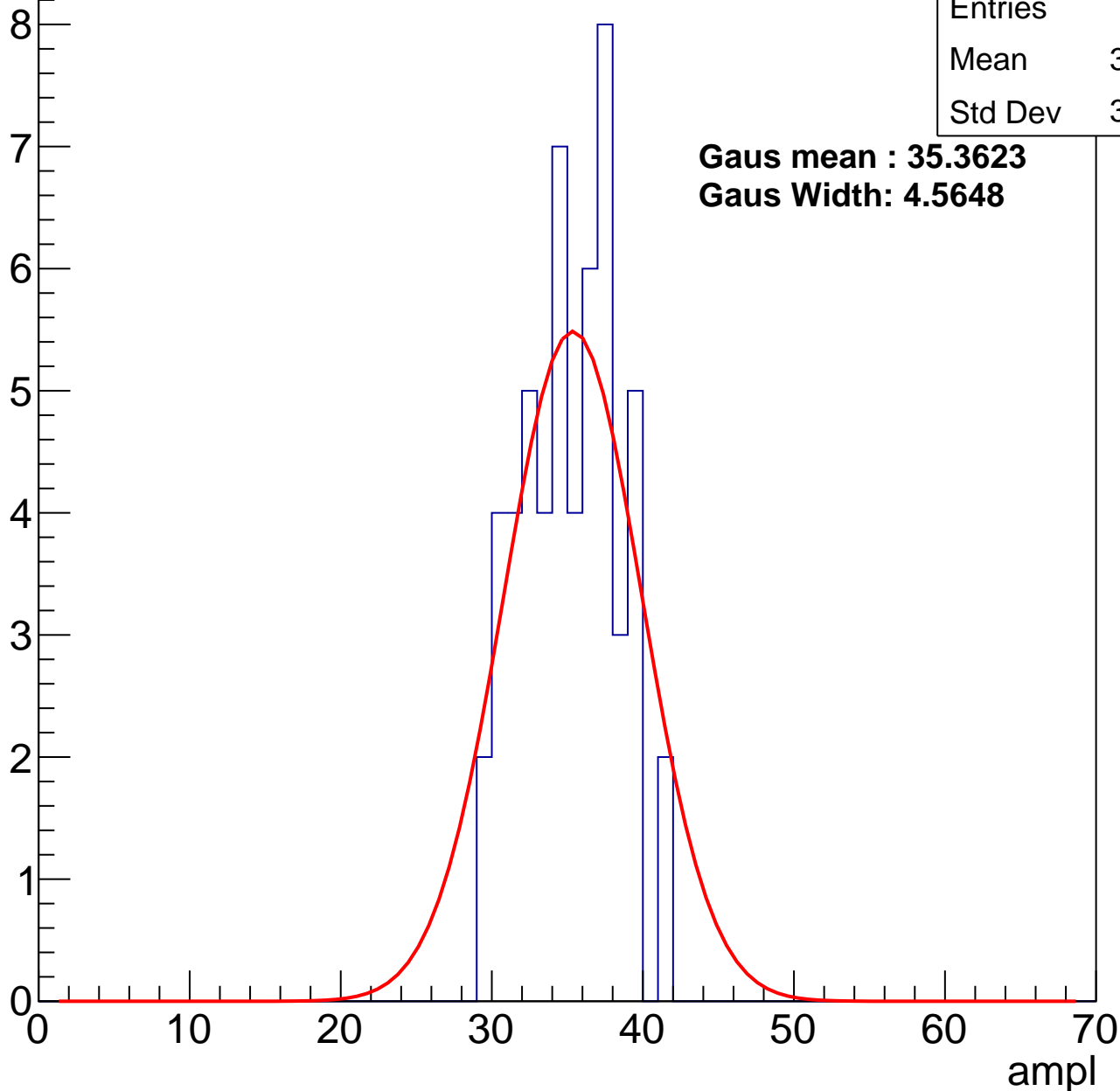
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	34.72
Std Dev	3.094

**Gaus mean : 35.3623**

**Gaus Width: 4.5648**



# B1L002S, U26-ch42, adc2

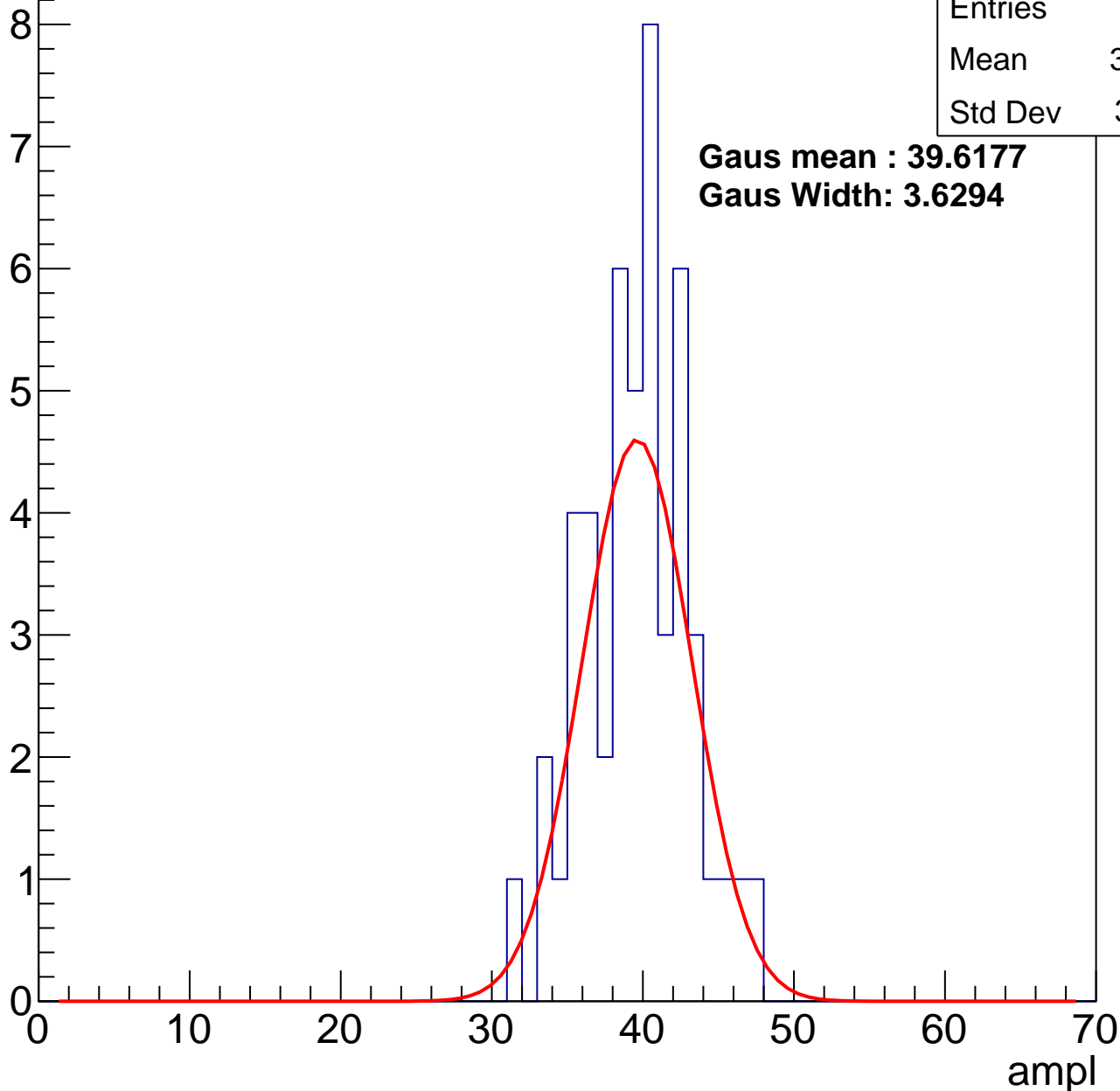
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	39.14
Std Dev	3.411

**Gaus mean : 39.6177**

**Gaus Width: 3.6294**

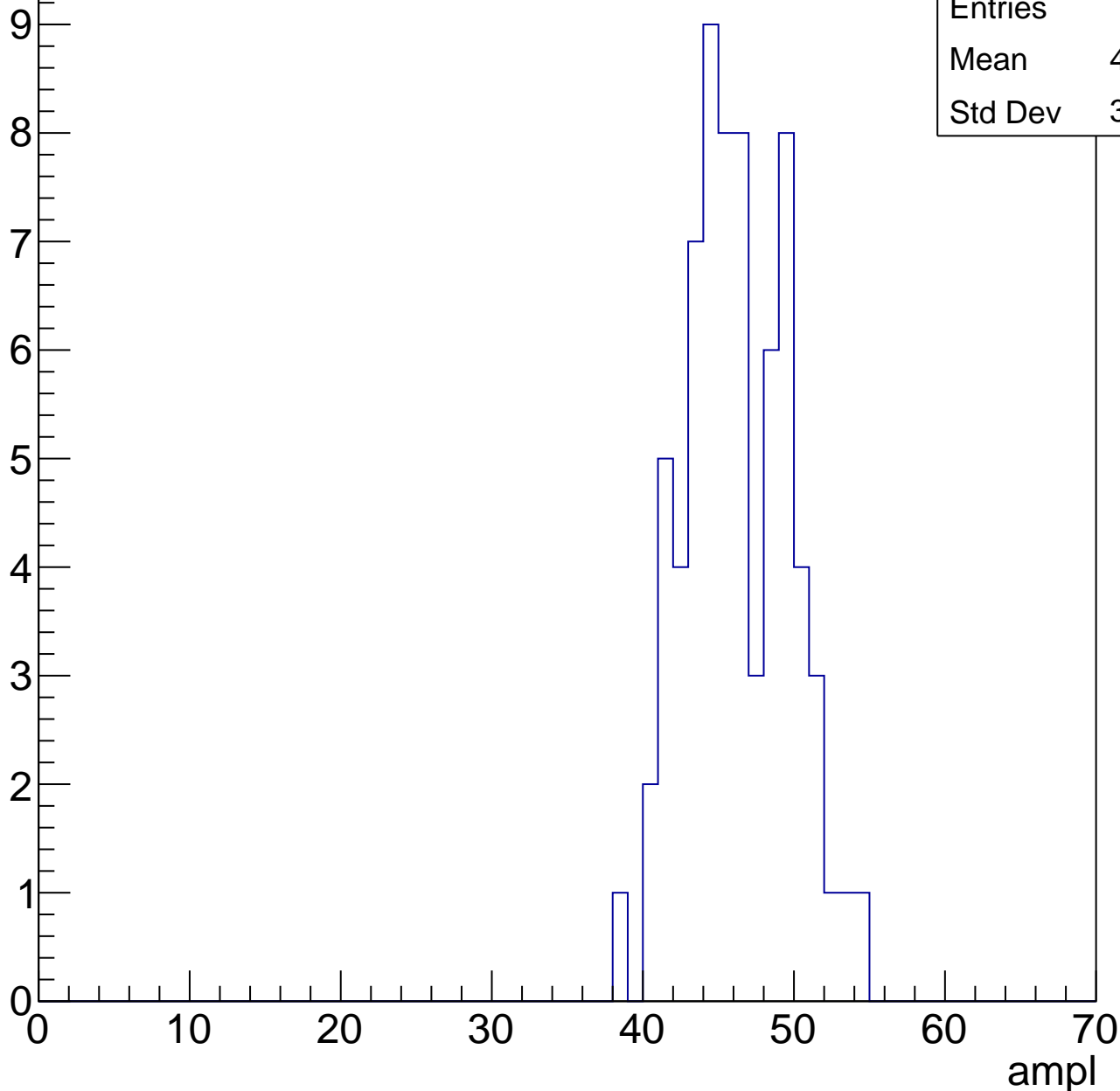


# B1L002S, U26-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	45.76
Std Dev	3.388

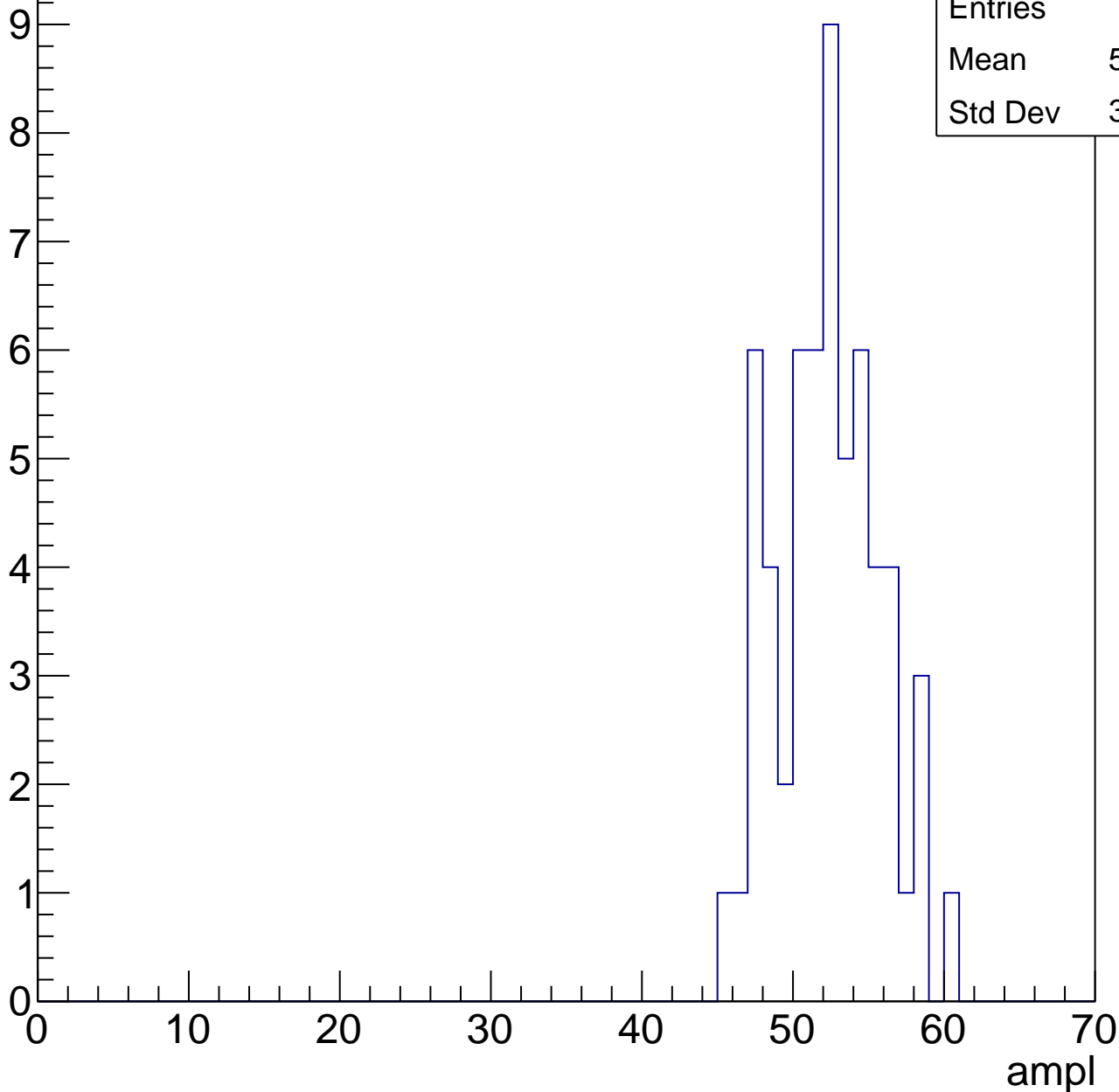


# B1L002S, U26-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	51.88
Std Dev	3.375

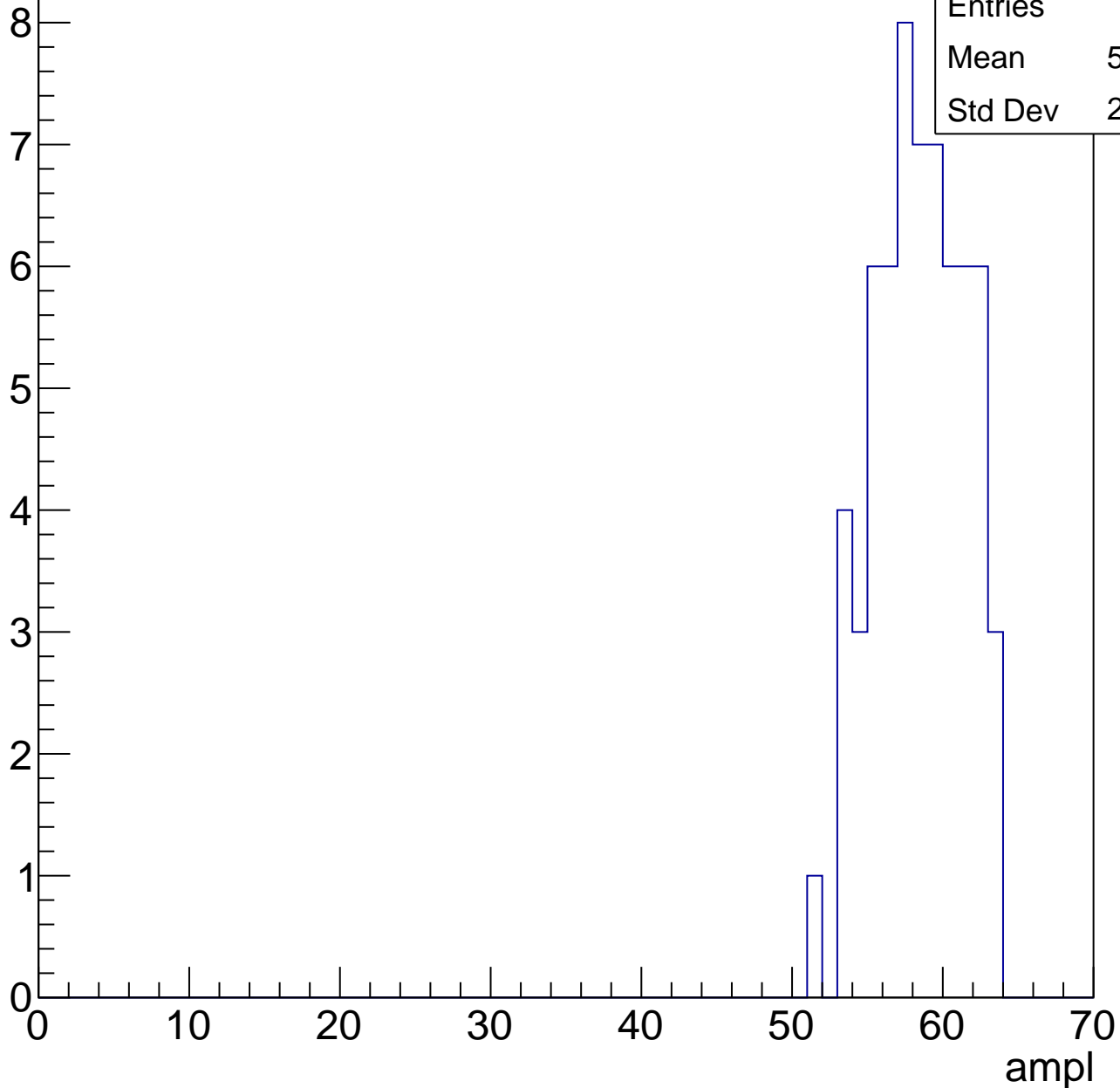


# B1L002S, U26-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	57.98
Std Dev	2.925

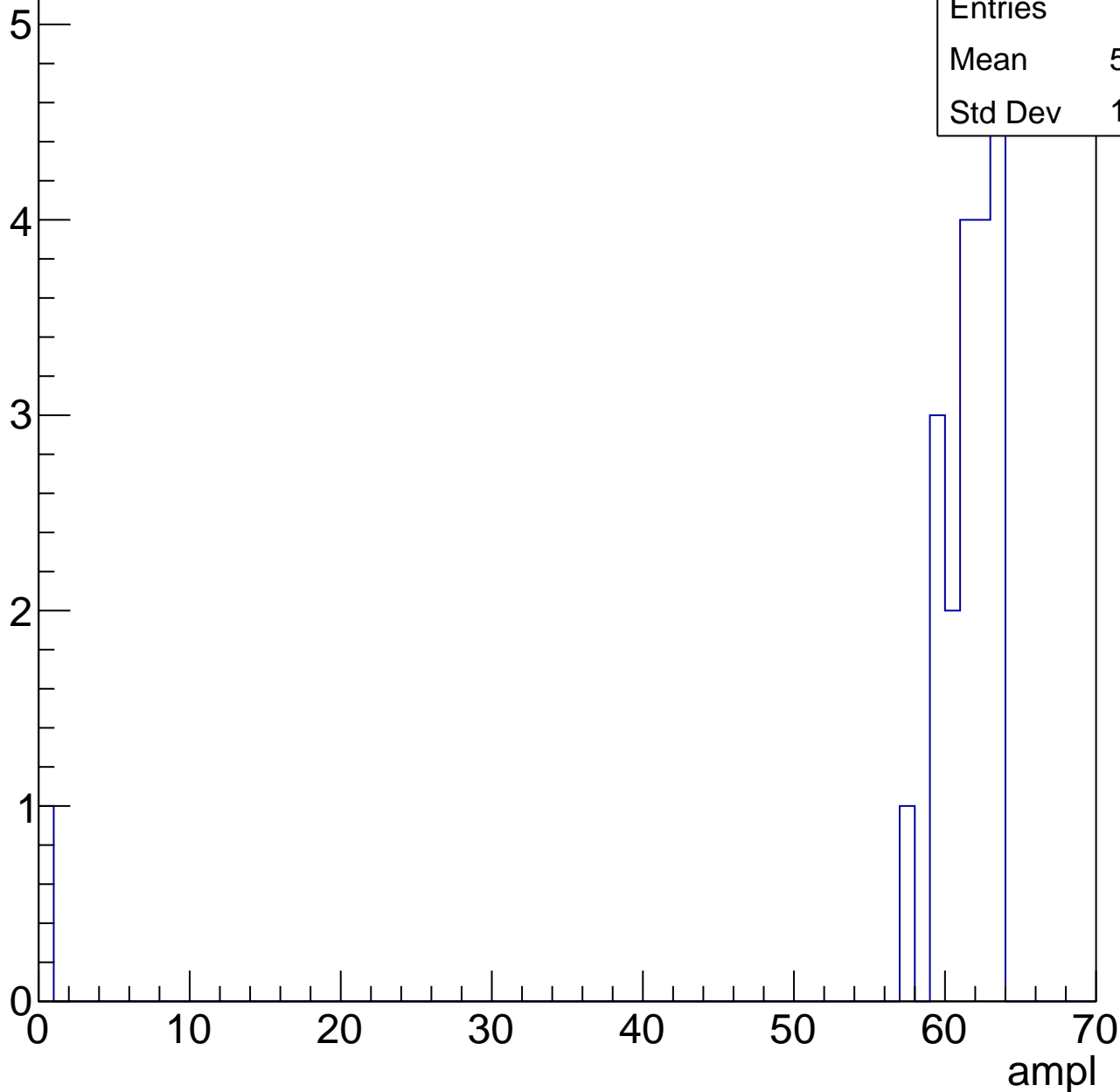


# B1L002S, U26-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.05
Std Dev	13.42

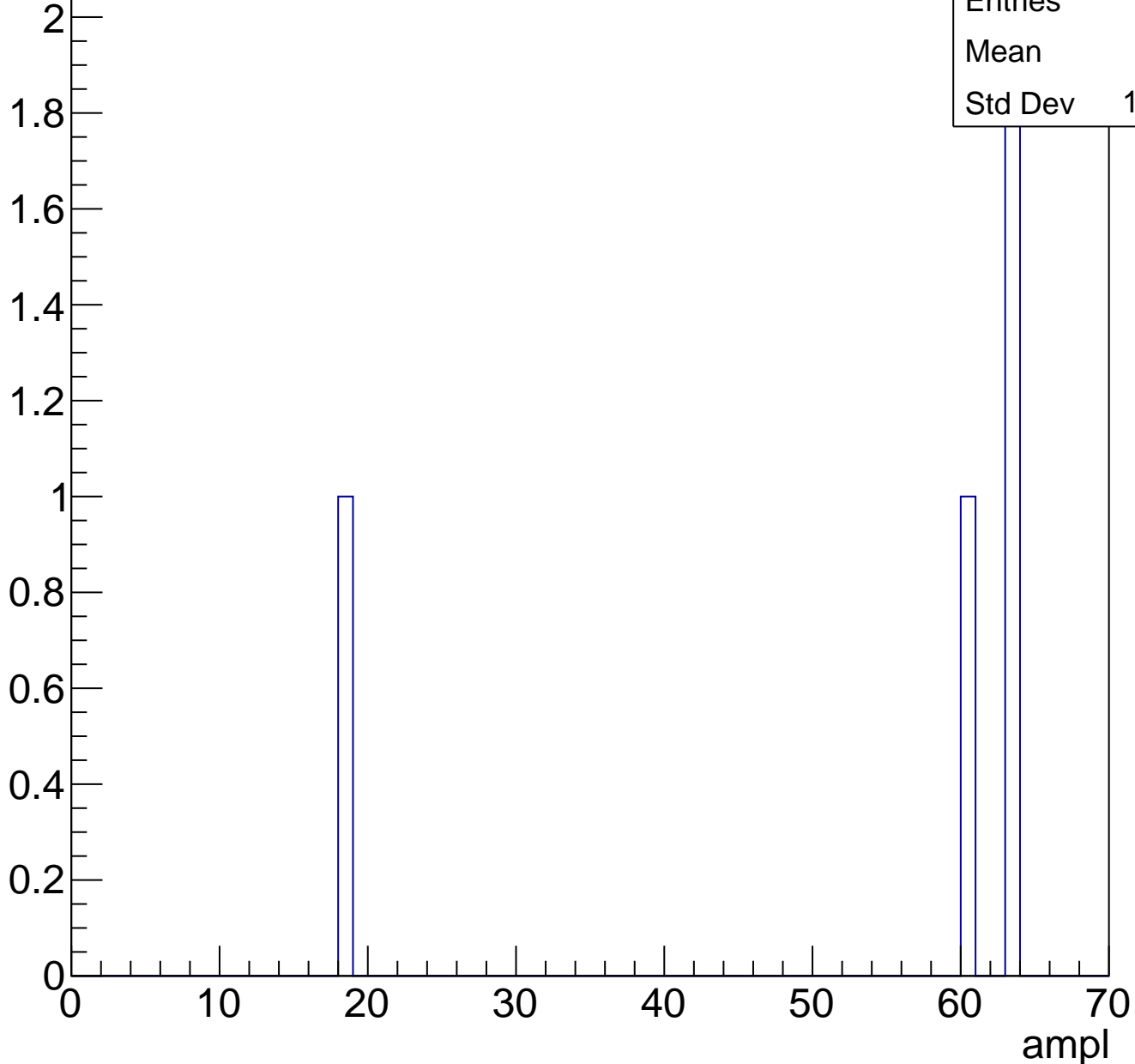




# B1L002S, U26-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	51
Std Dev	19.09

# B1L002S, U26-ch43, adc0

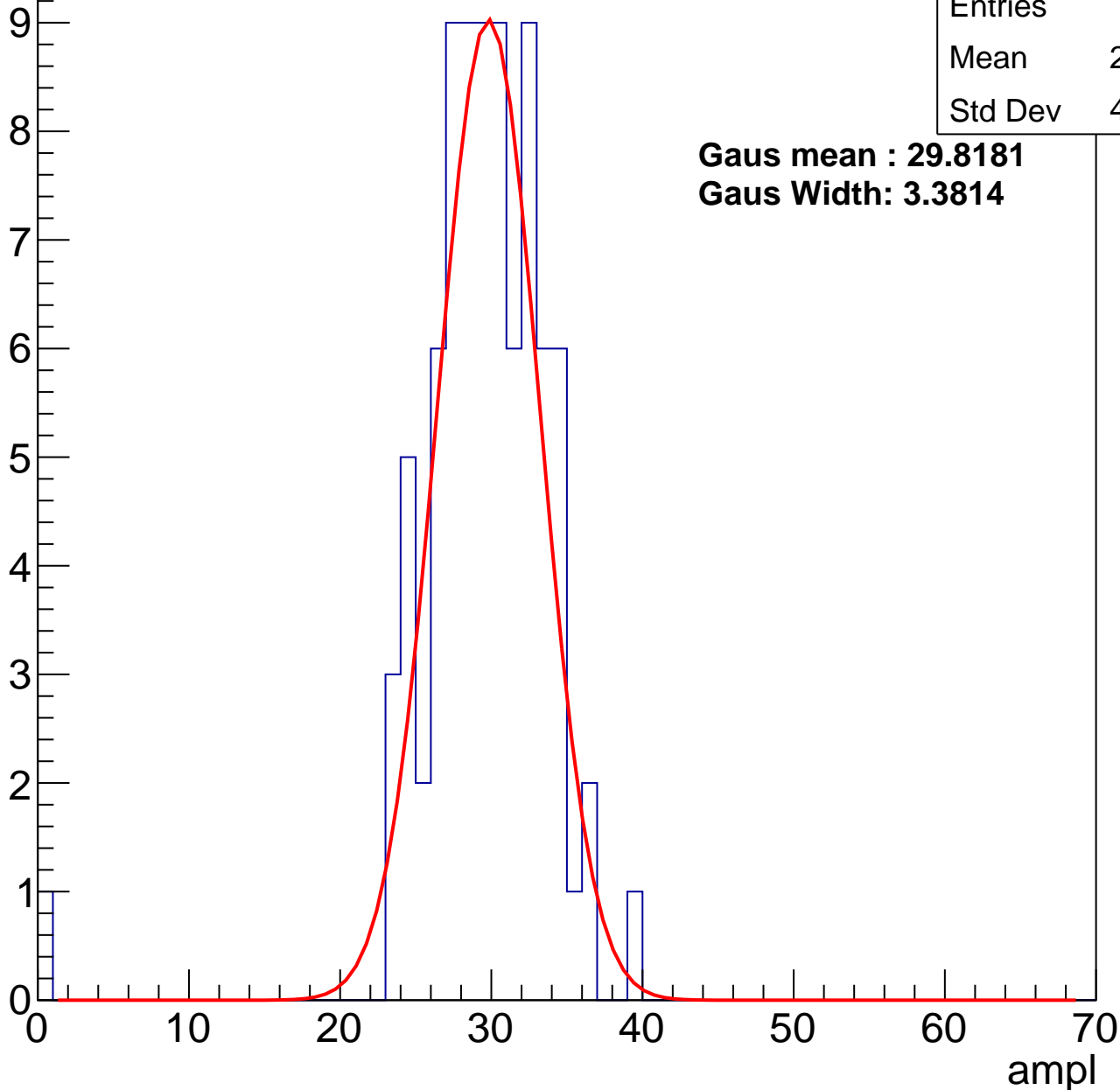
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	84
Mean	29.08
Std Dev	4.627

**Gaus mean : 29.8181**

**Gaus Width: 3.3814**



# B1L002S, U26-ch43, adc1

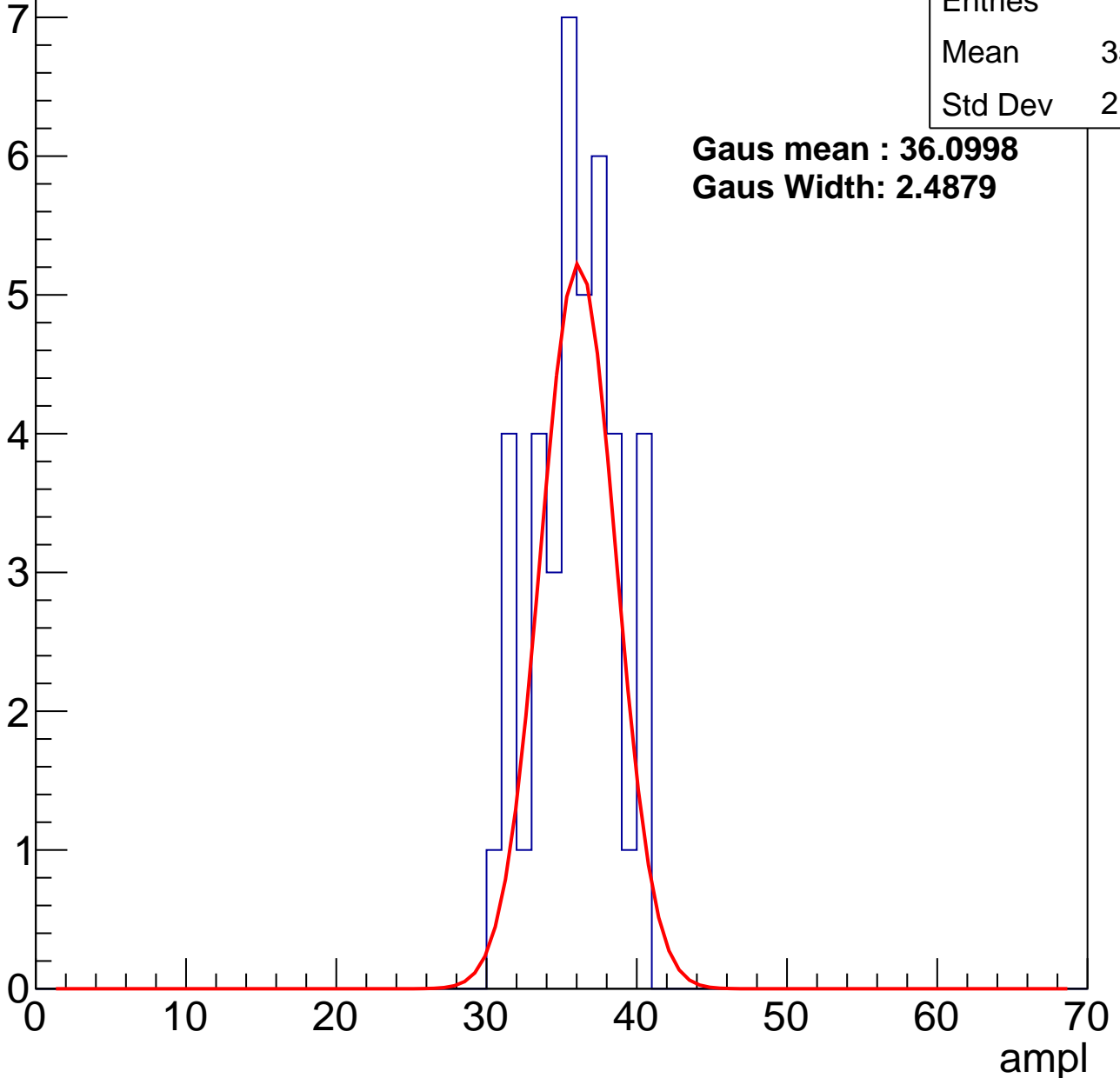
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	35.45
Std Dev	2.692

**Gaus mean : 36.0998**

**Gaus Width: 2.4879**



# B1L002S, U26-ch43, adc2

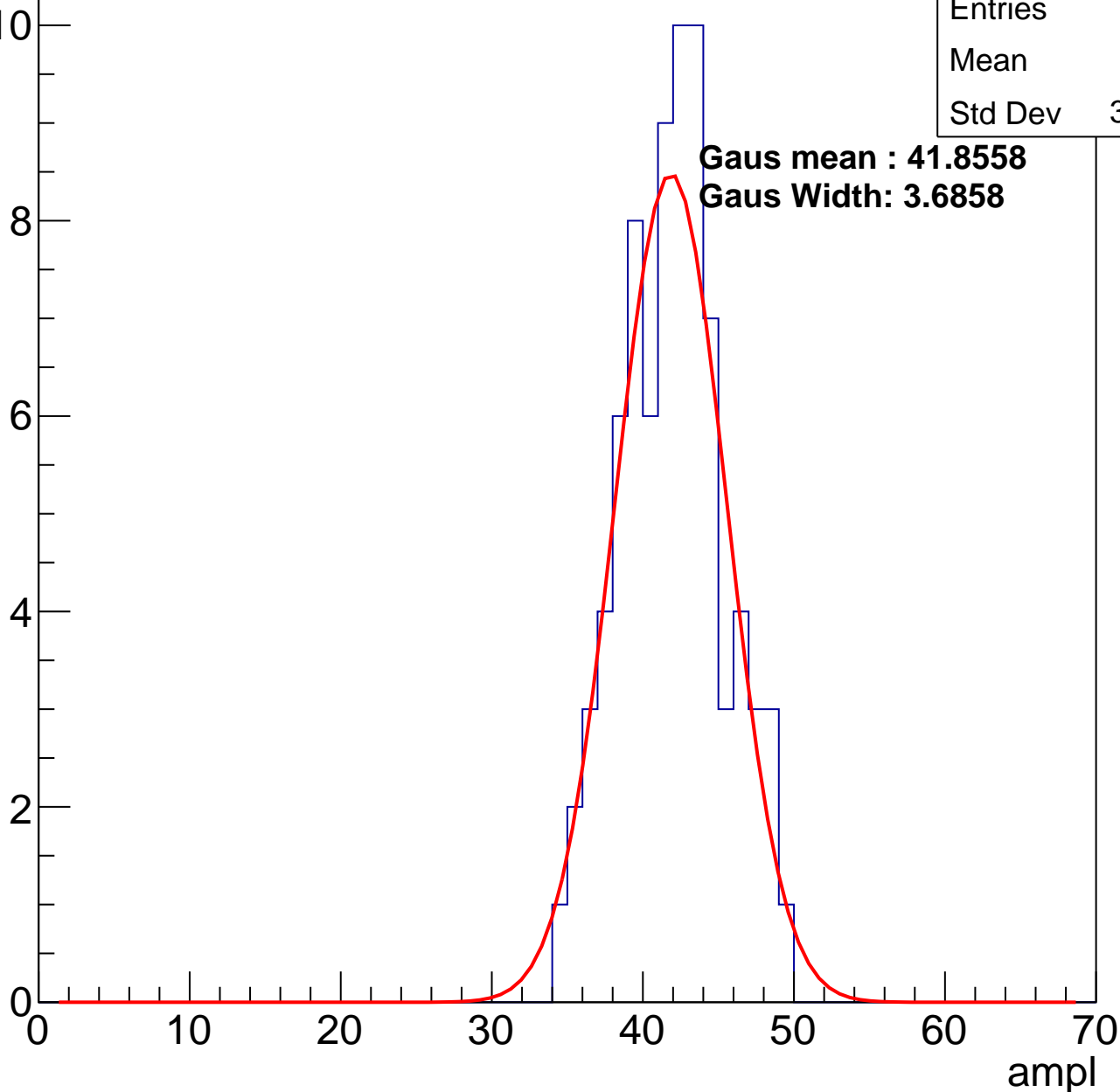
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	41.5
Std Dev	3.376

**Gaus mean : 41.8558**

**Gaus Width: 3.6858**

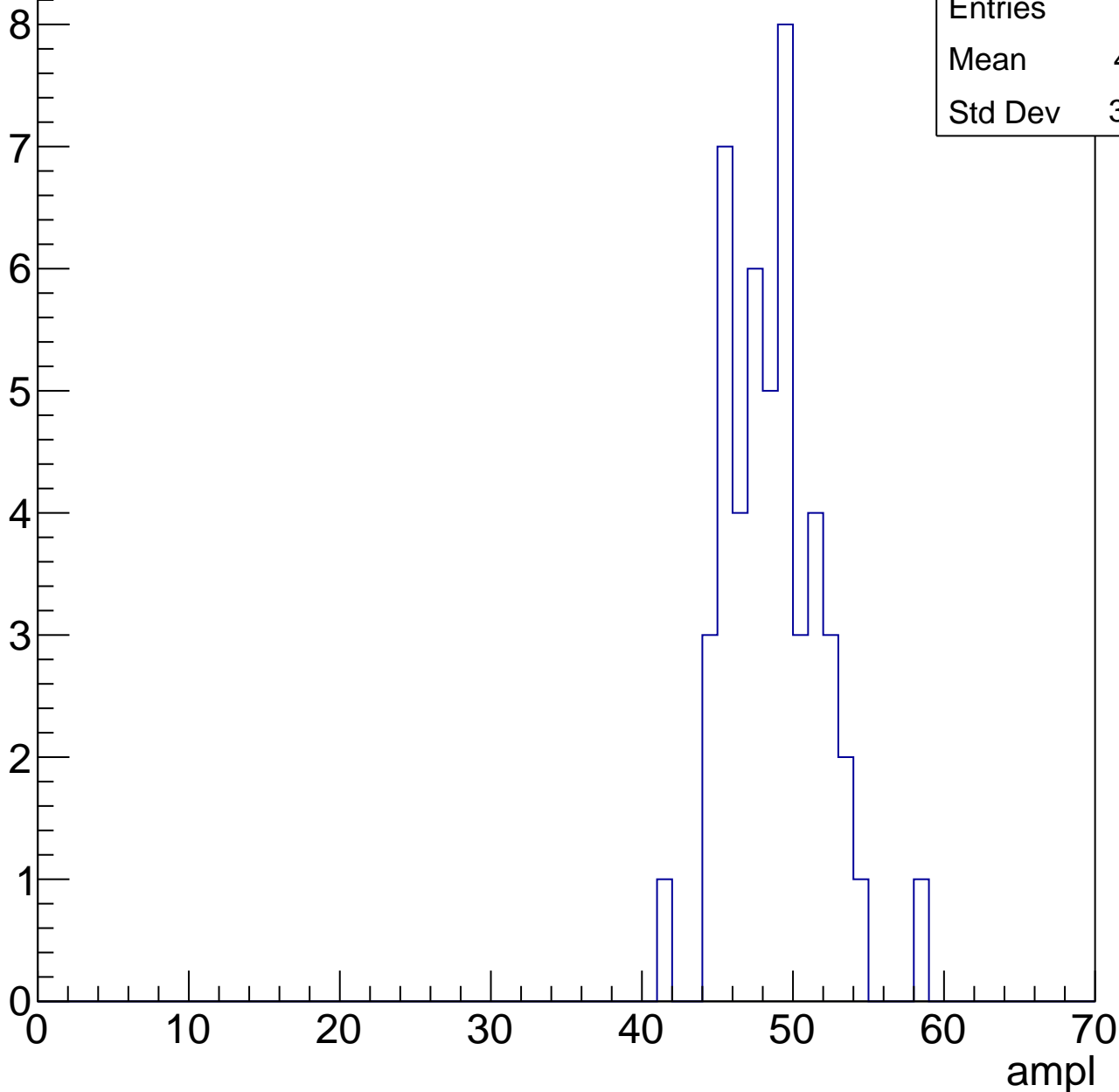


# B1L002S, U26-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

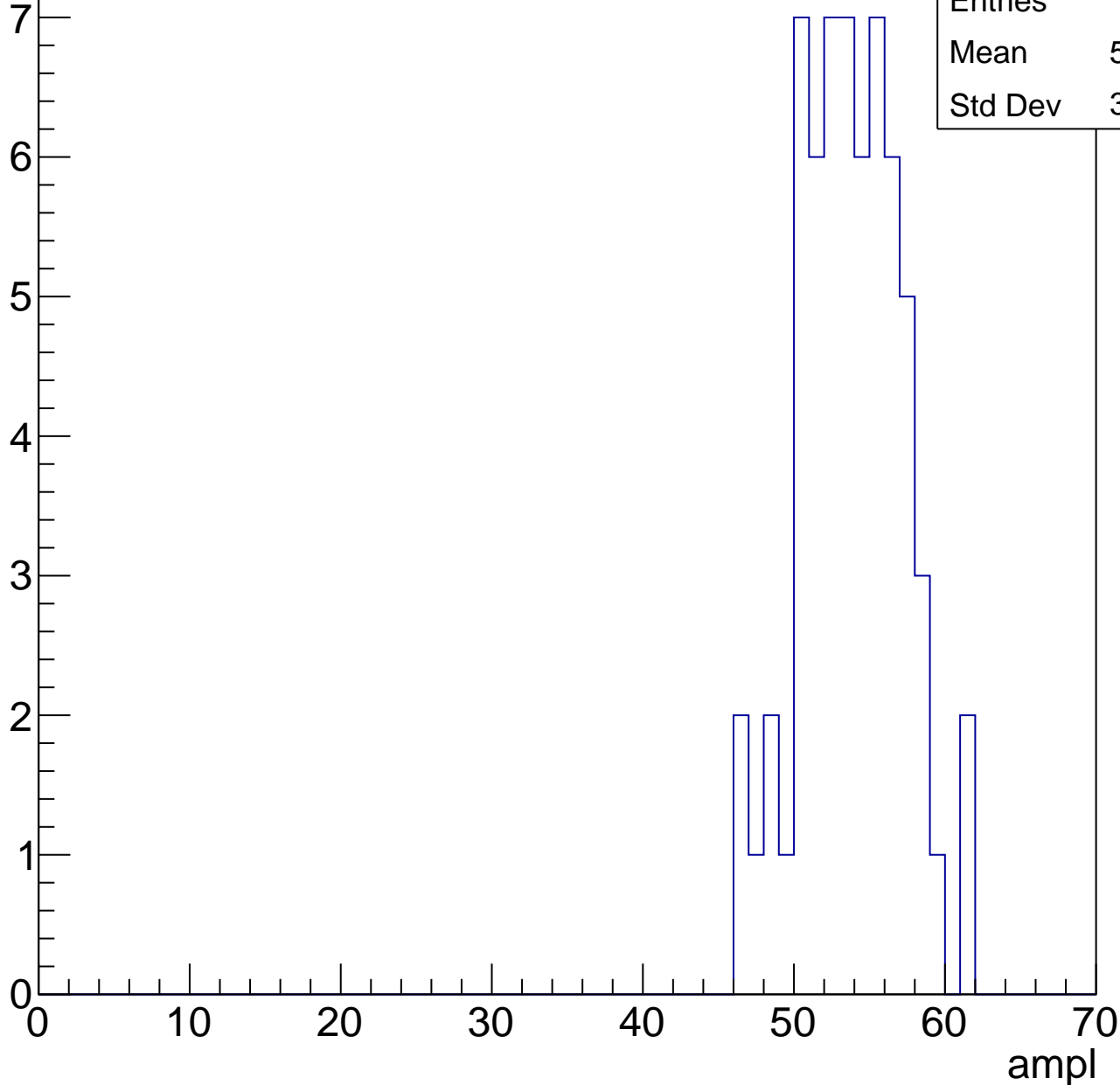
Entries	48
Mean	48.21
Std Dev	3.129



# B1L002S, U26-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch43, adc5

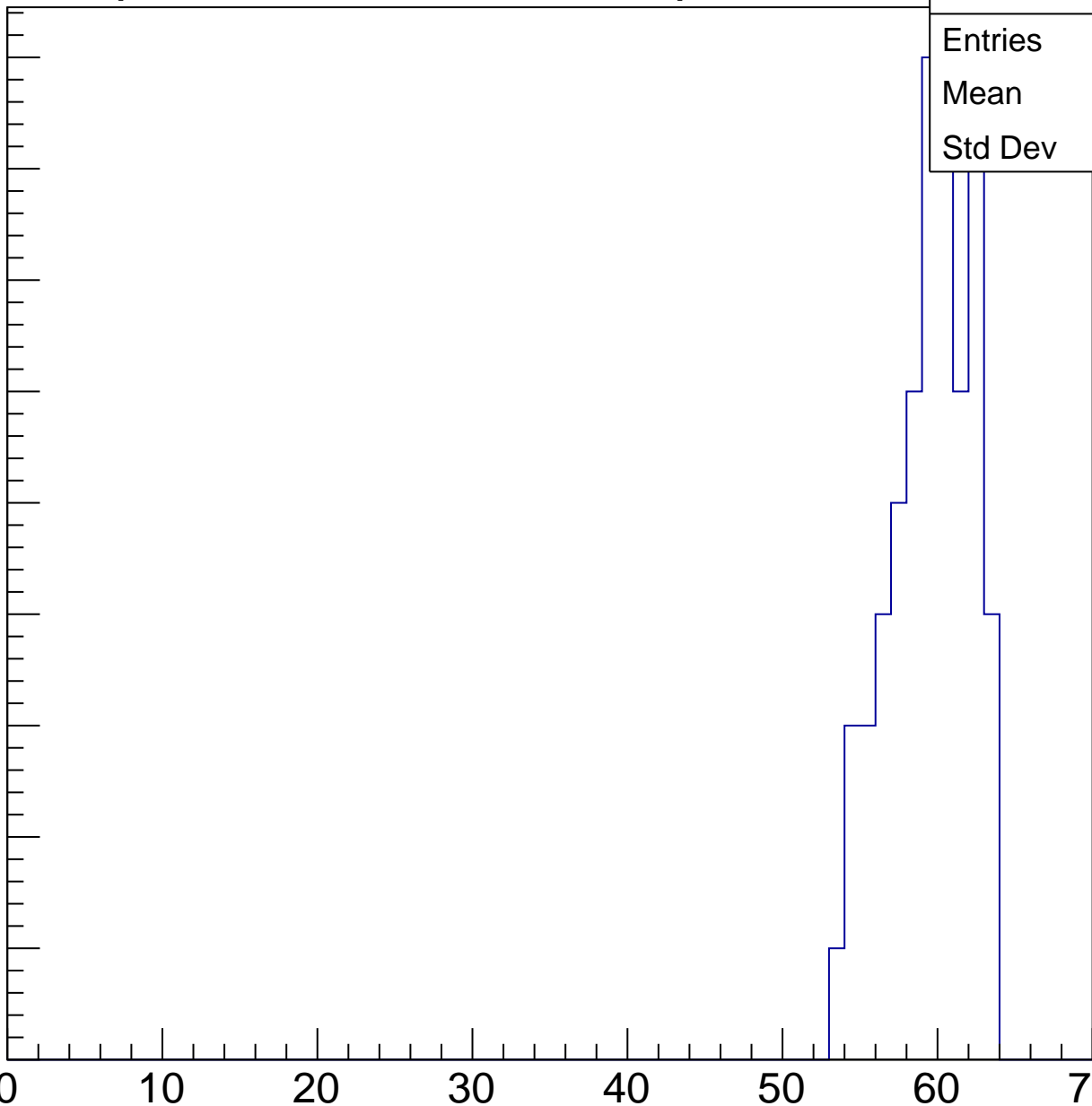
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	59
Mean	59.05
Std Dev	2.6

ampl

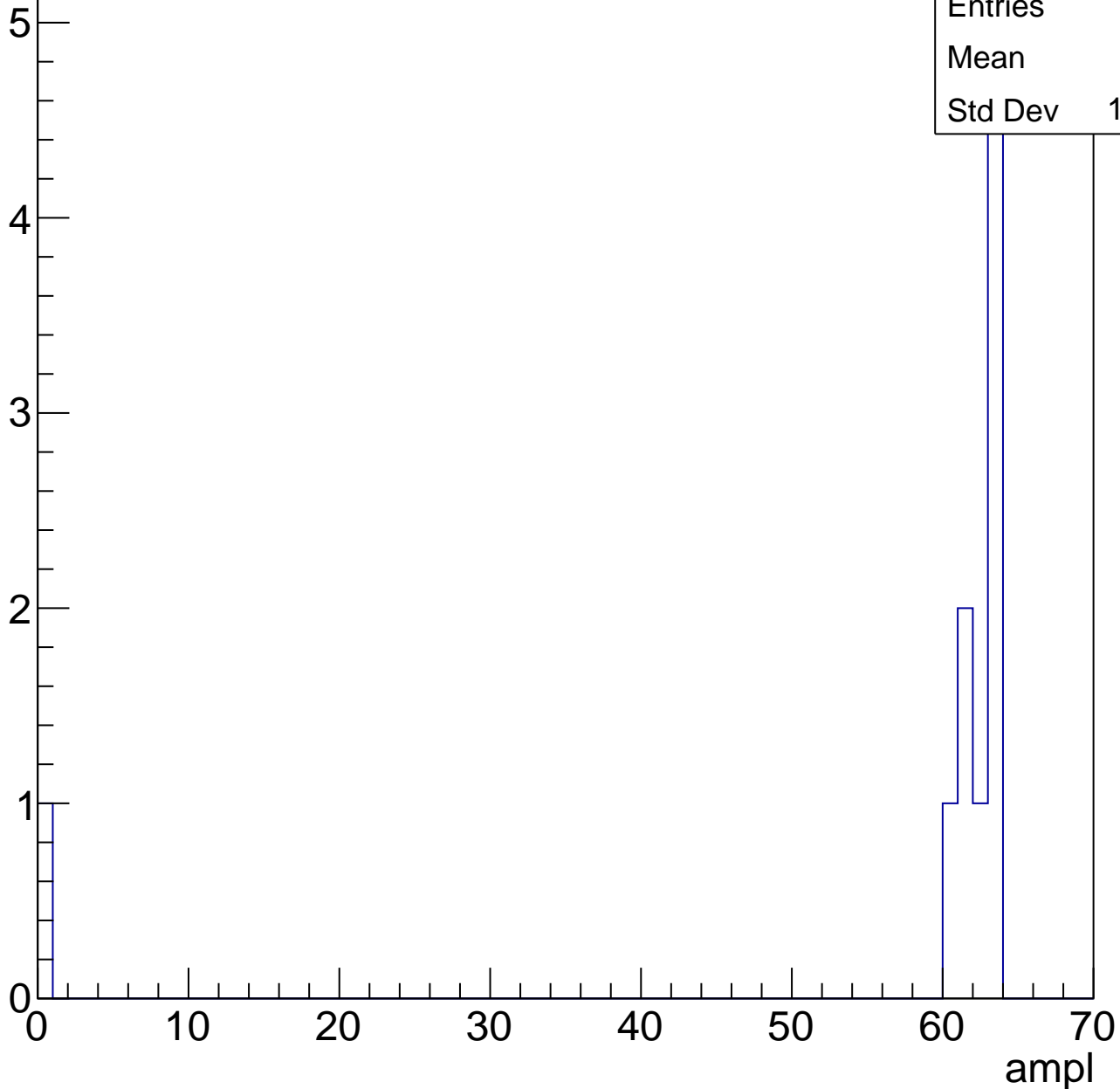


# B1L002S, U26-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	10
Mean	55.9
Std Dev	18.66

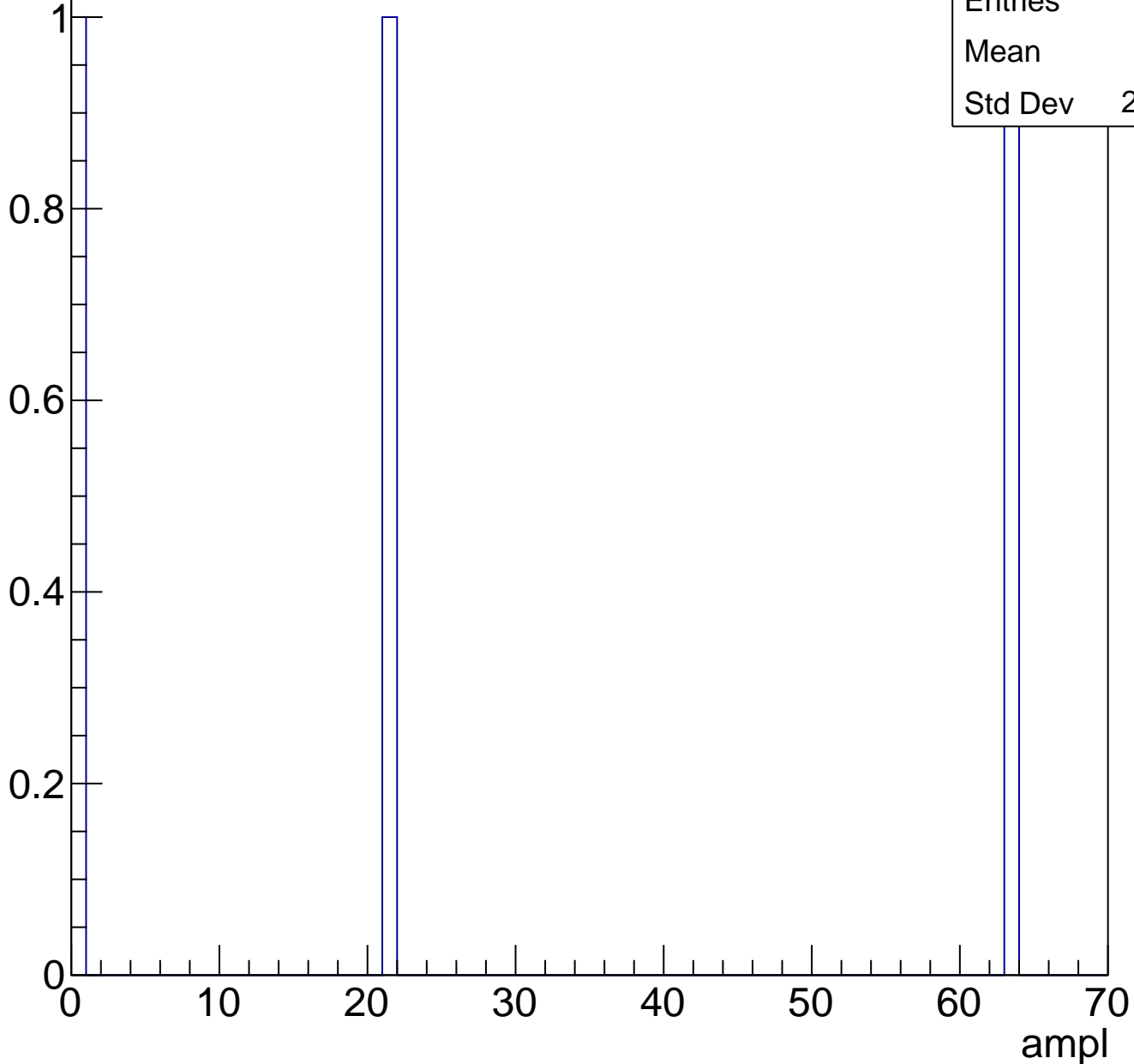




# B1L002S, U26-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28
Std Dev	26.19

# B1L002S, U26-ch44, adc0

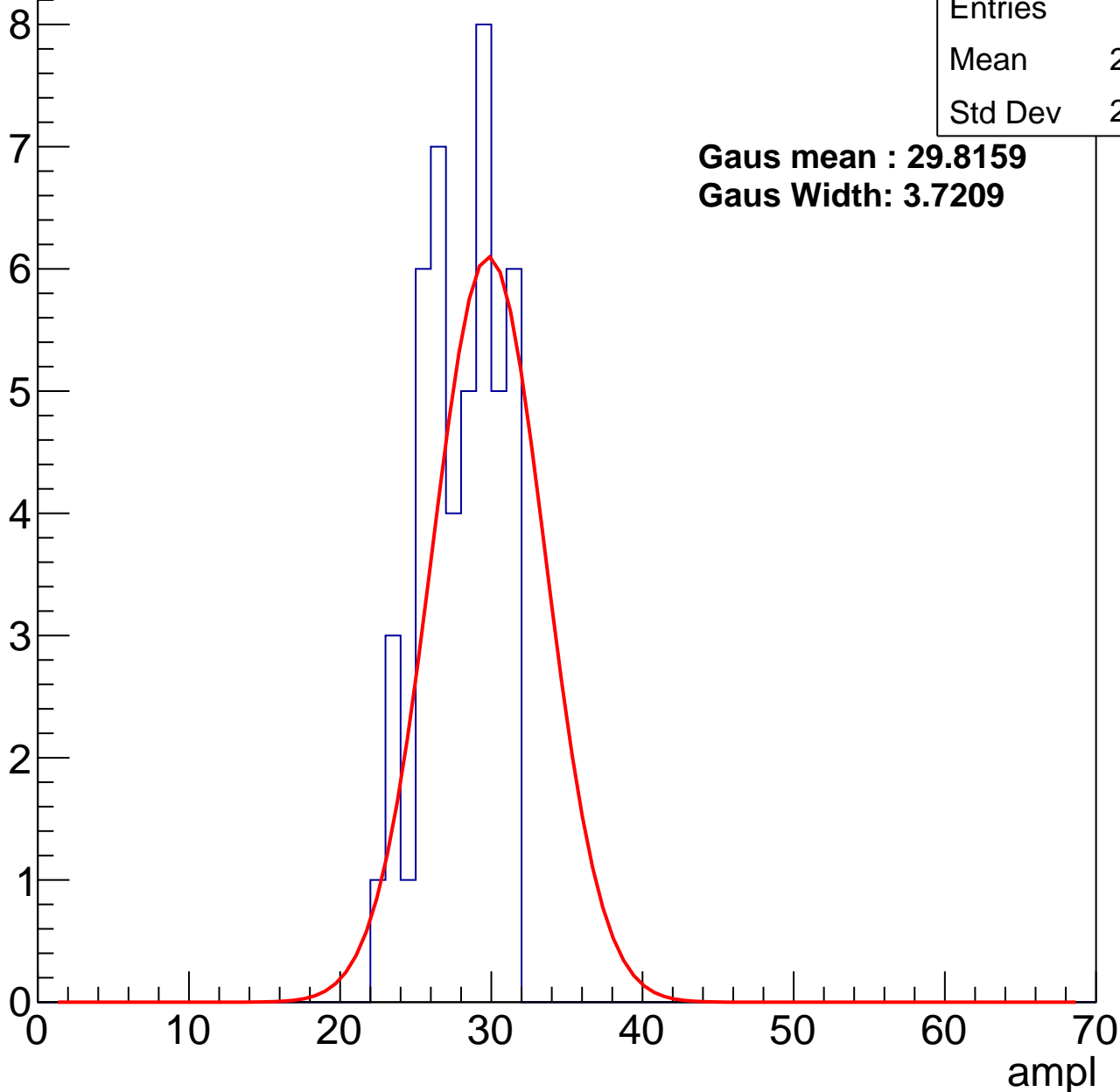
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	27.46
Std Dev	2.473

**Gaus mean : 29.8159**

**Gaus Width: 3.7209**



# B1L002S, U26-ch44, adc1

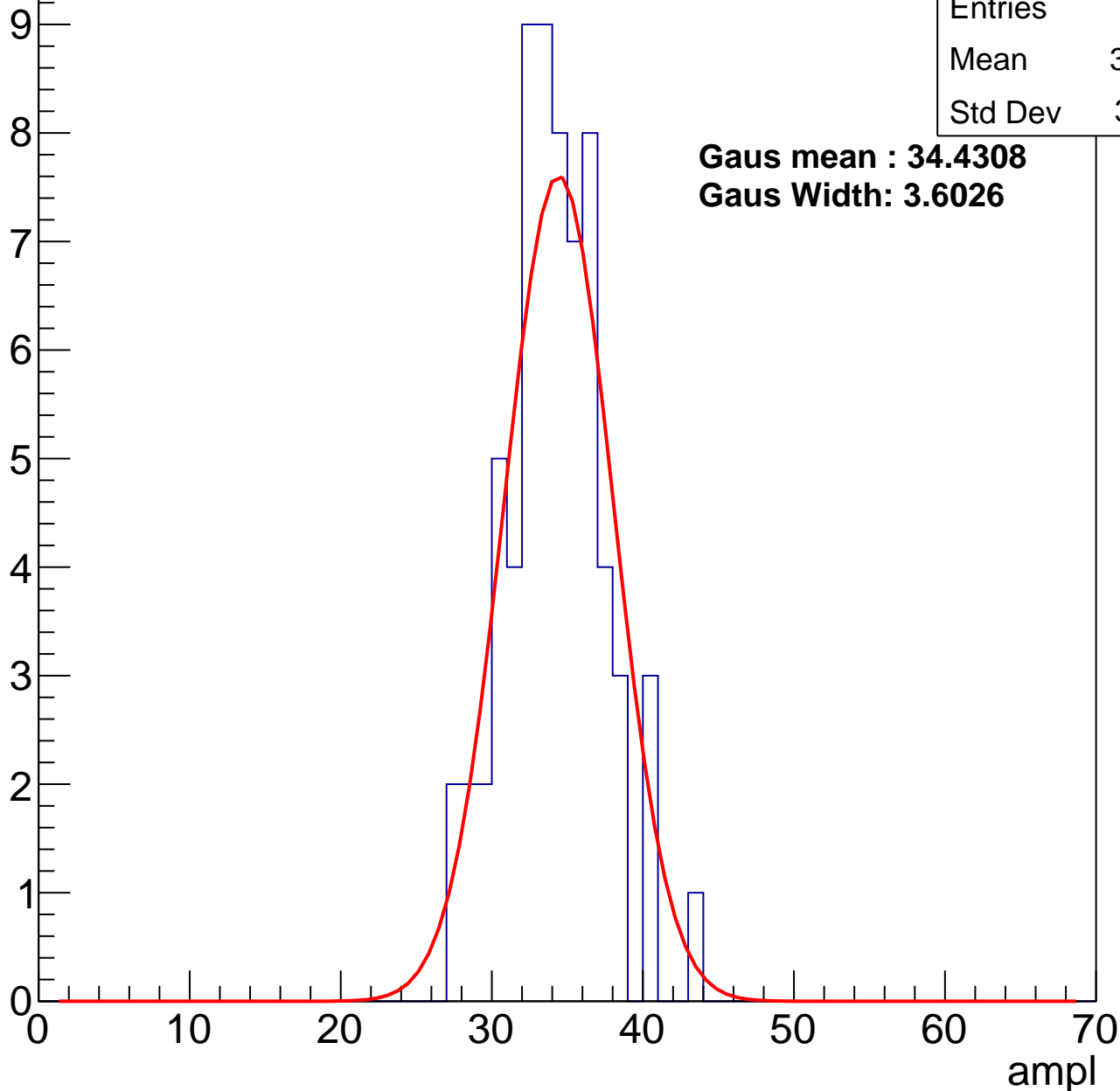
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.69
Std Dev	3.201

**Gaus mean : 34.4308**

**Gaus Width: 3.6026**



# B1L002S, U26-ch44, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	39.91
Std Dev	3.367

**Gaus mean : 40.2283**

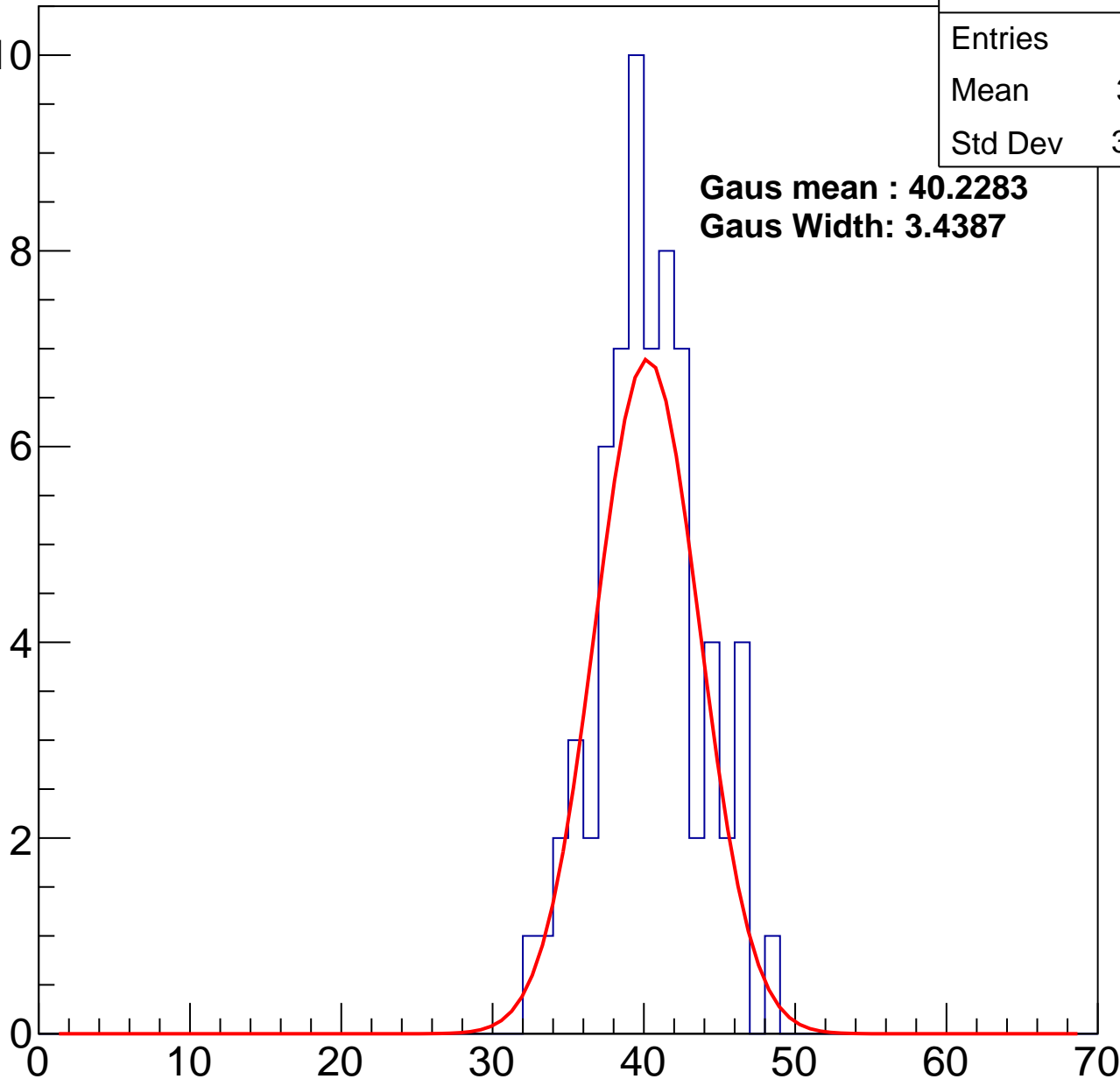
**Gaus Width: 3.4387**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

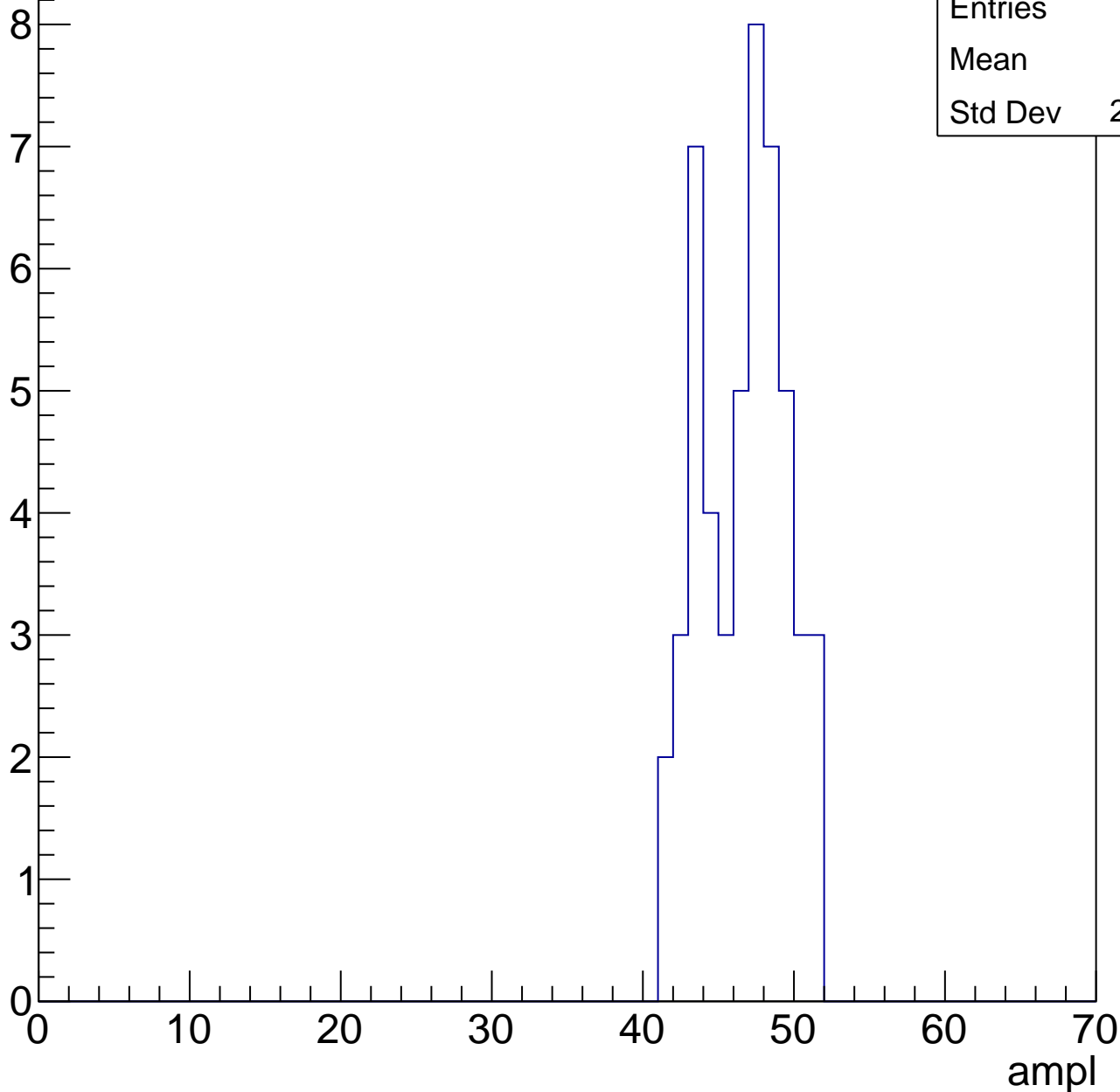


# B1L002S, U26-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	46.2
Std Dev	2.764



# B1L002S, U26-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

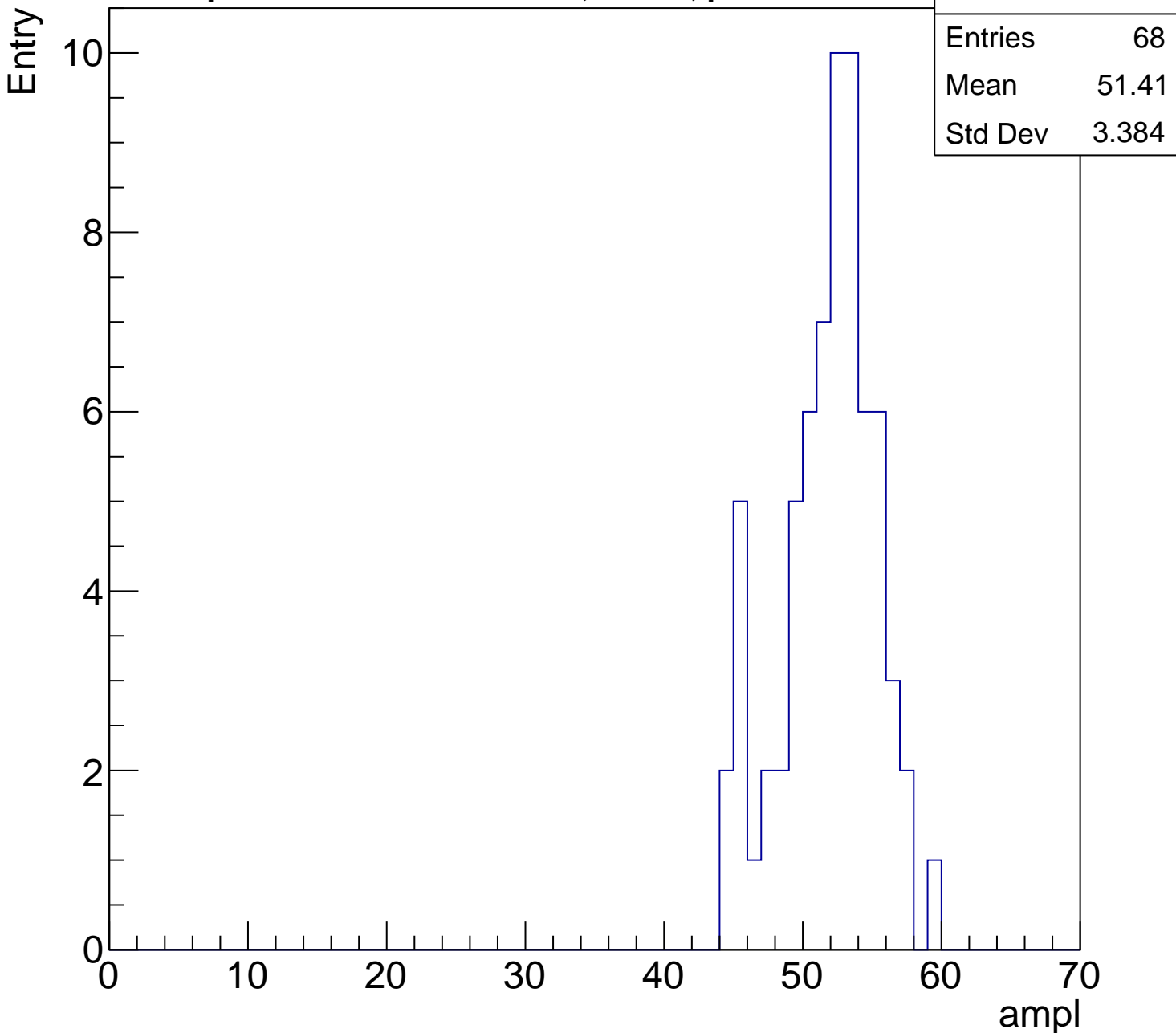
Entries	68
Mean	51.41
Std Dev	3.384

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

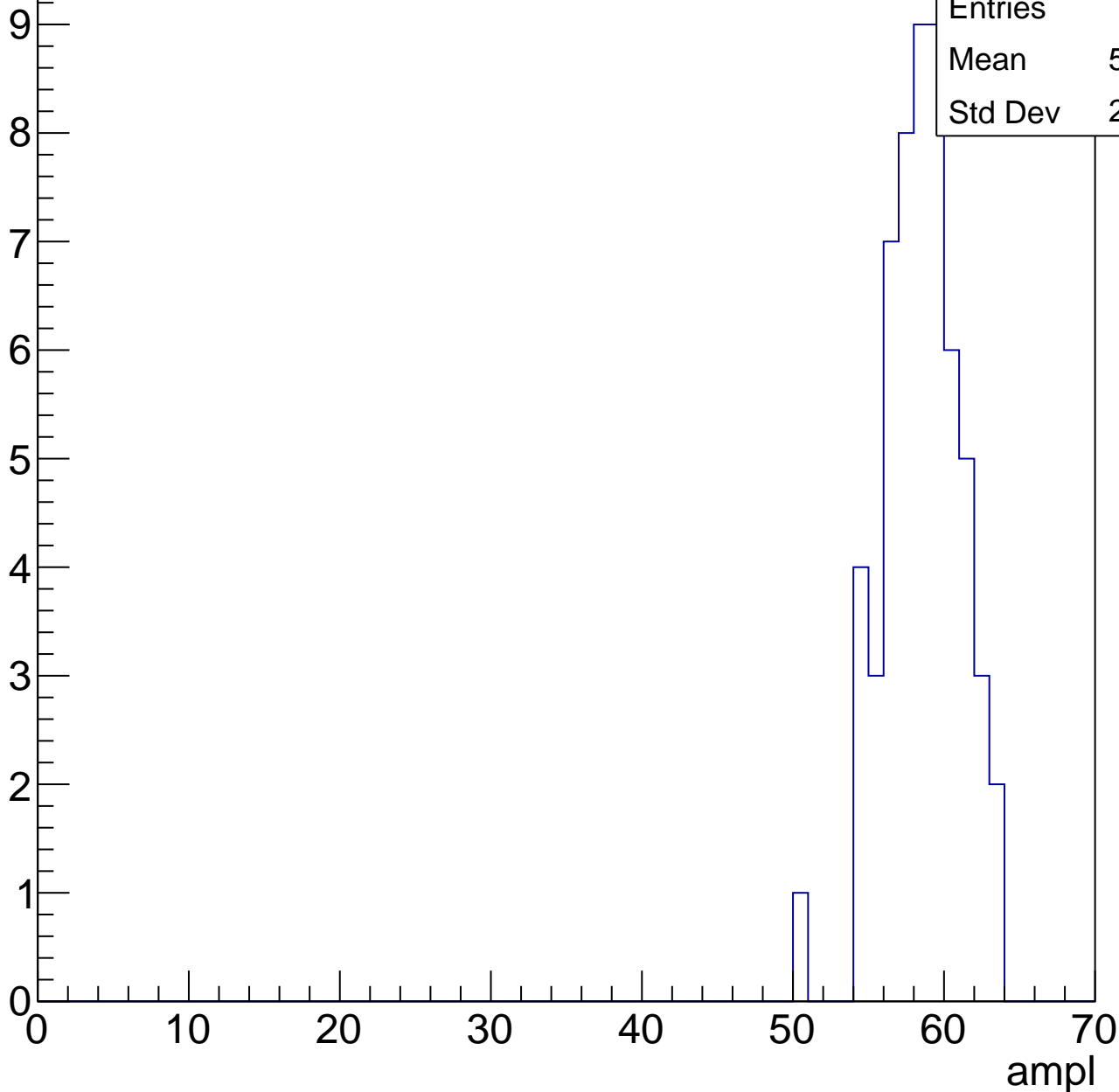


# B1L002S, U26-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	58.05
Std Dev	2.537

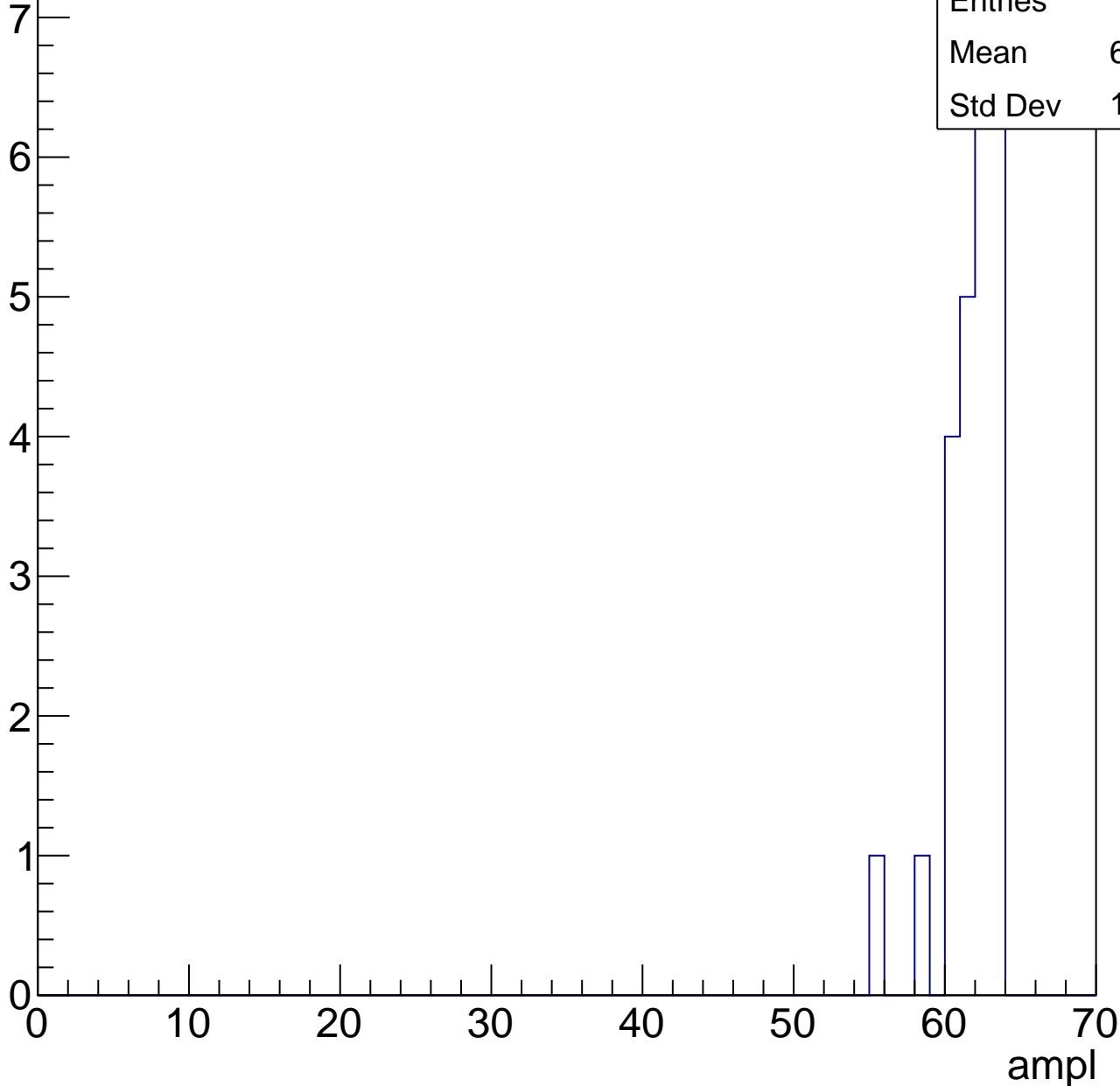


# B1L002S, U26-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.32
Std Dev	1.805

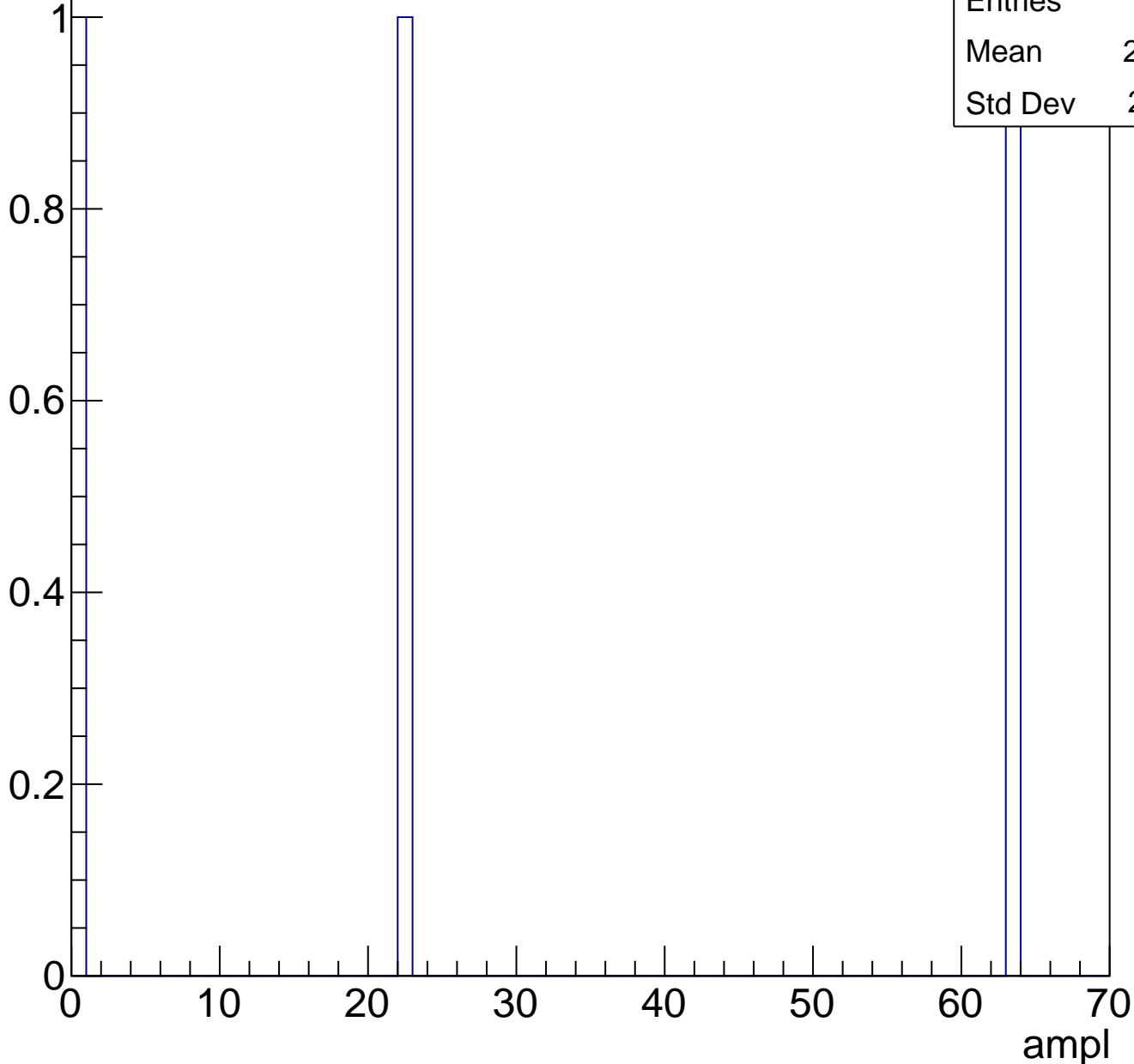




# B1L002S, U26-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L002S, U26-ch45, adc0

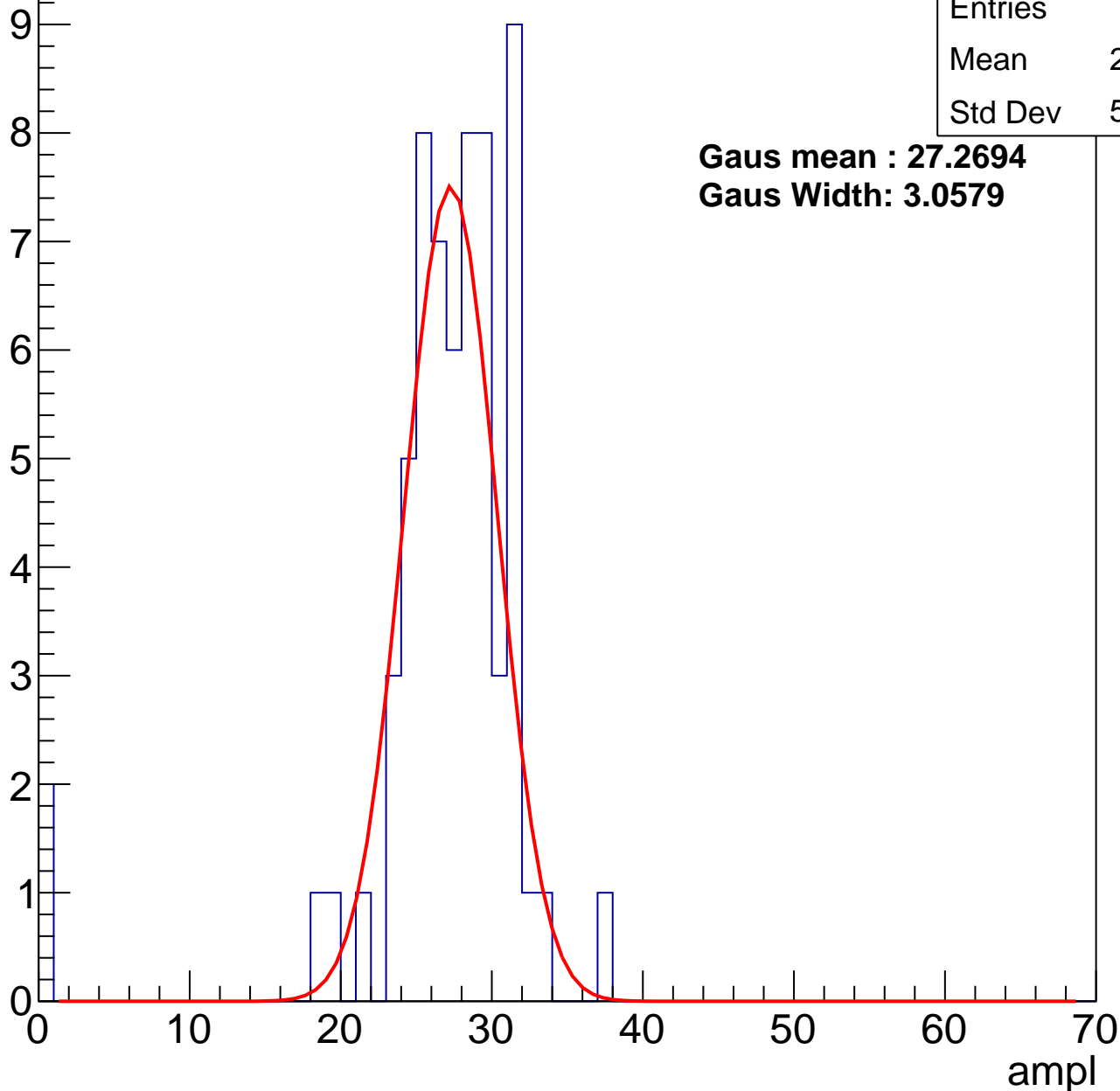
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	26.43
Std Dev	5.714

**Gaus mean : 27.2694**

**Gaus Width: 3.0579**



# B1L002S, U26-ch45, adc1

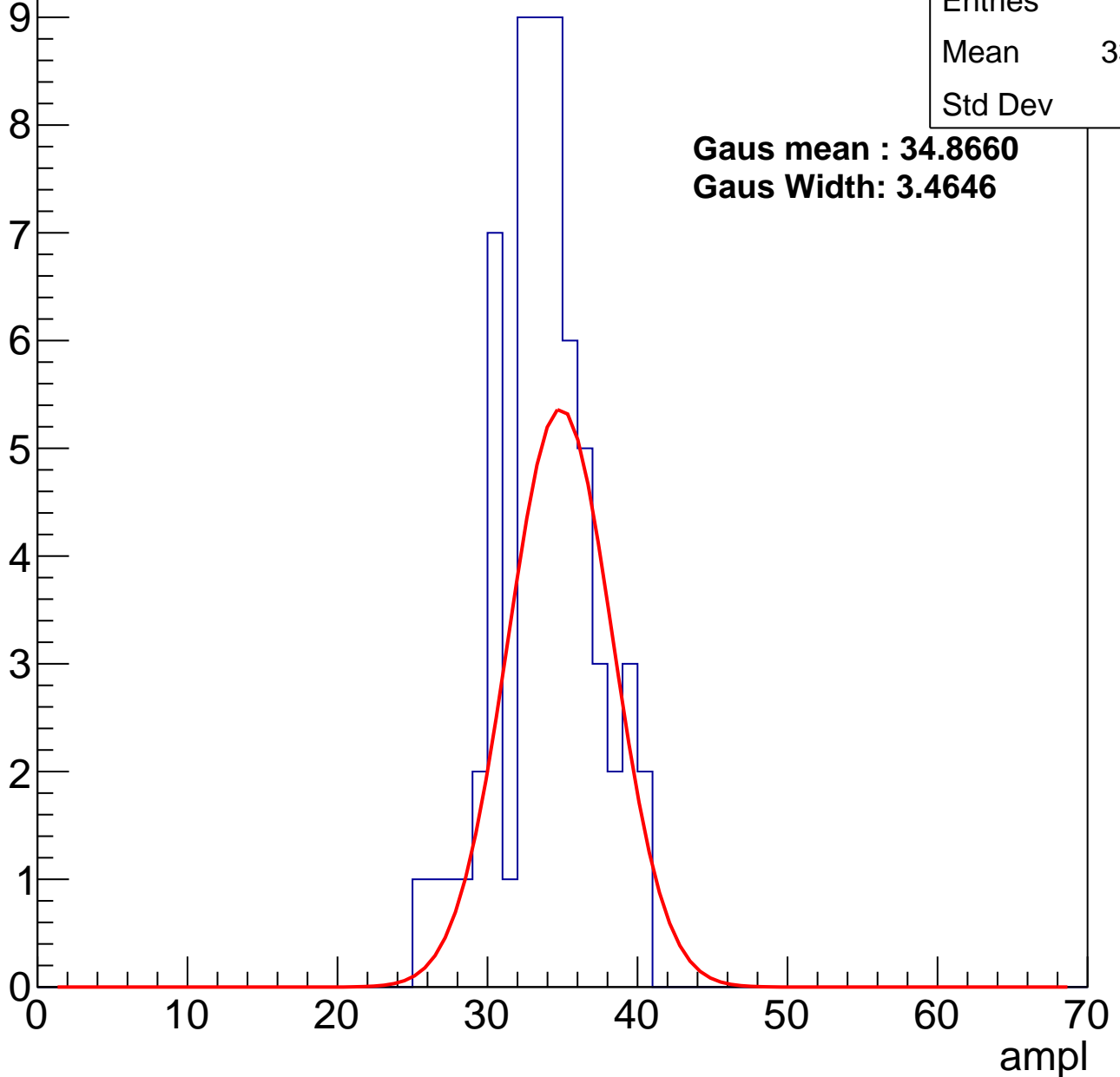
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	33.39
Std Dev	3.25

**Gaus mean : 34.8660**

**Gaus Width: 3.4646**



# B1L002S, U26-ch45, adc2

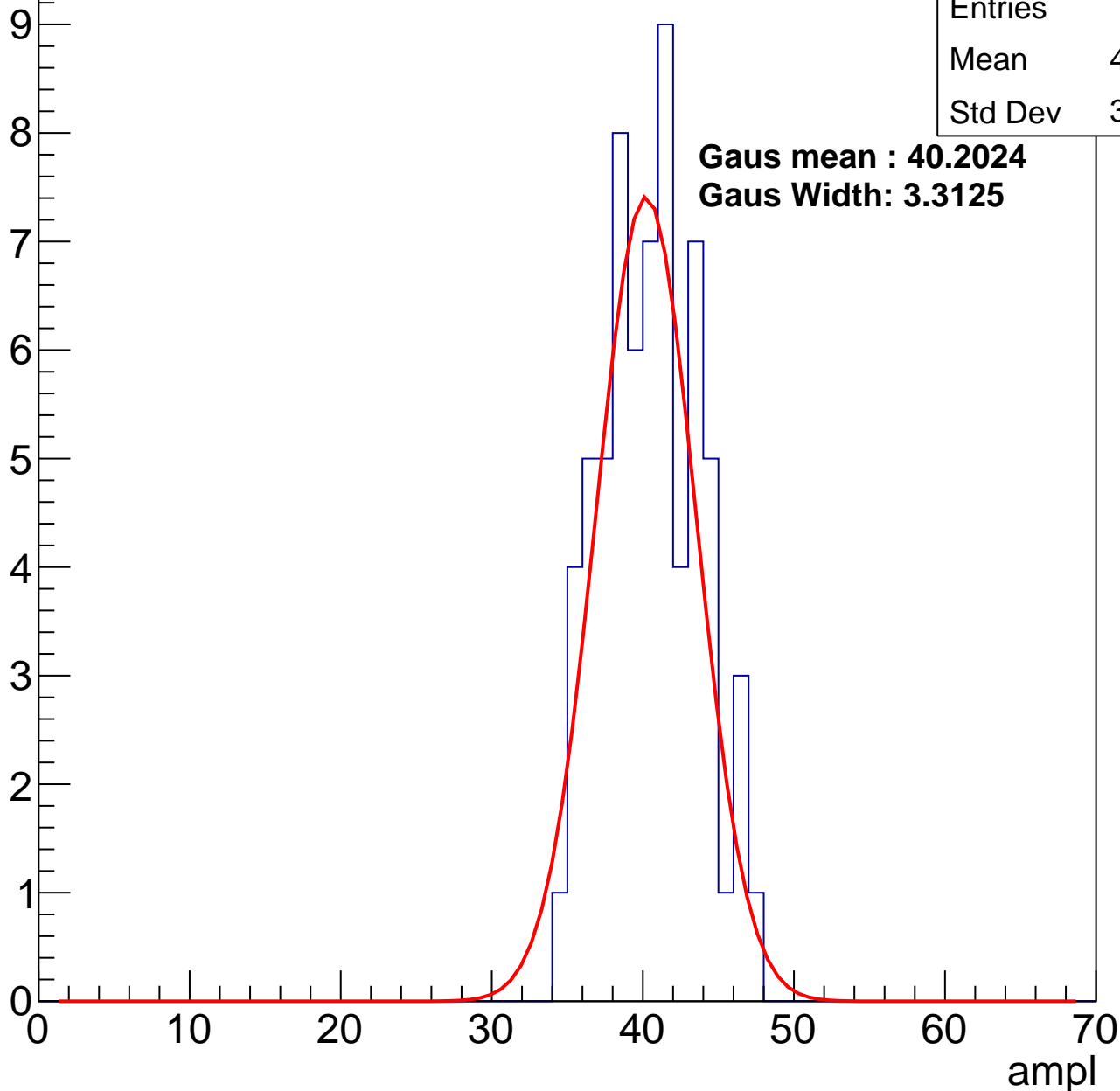
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.08
Std Dev	3.135

**Gaus mean : 40.2024**

**Gaus Width: 3.3125**

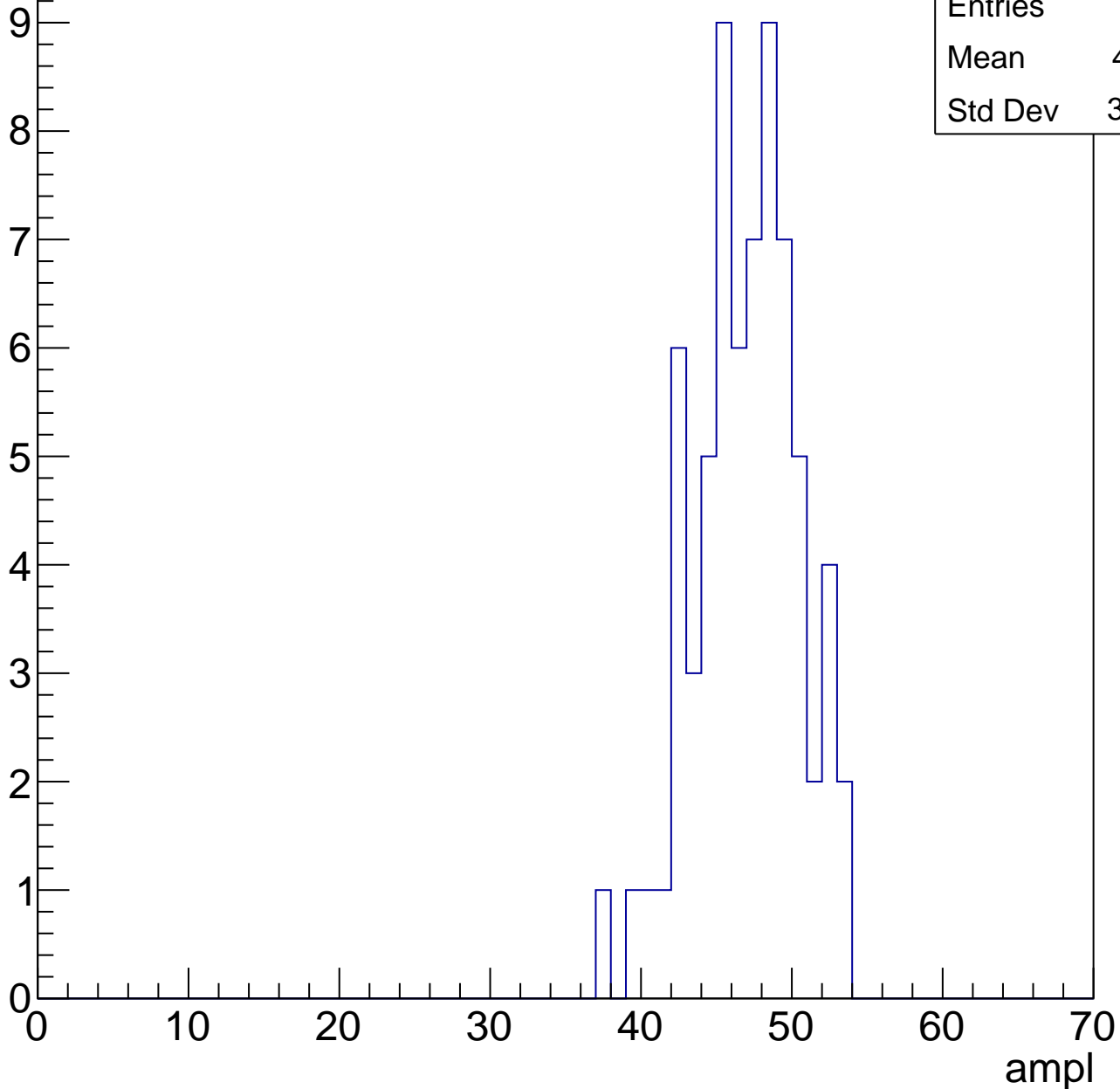


# B1L002S, U26-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	46.51
Std Dev	3.412

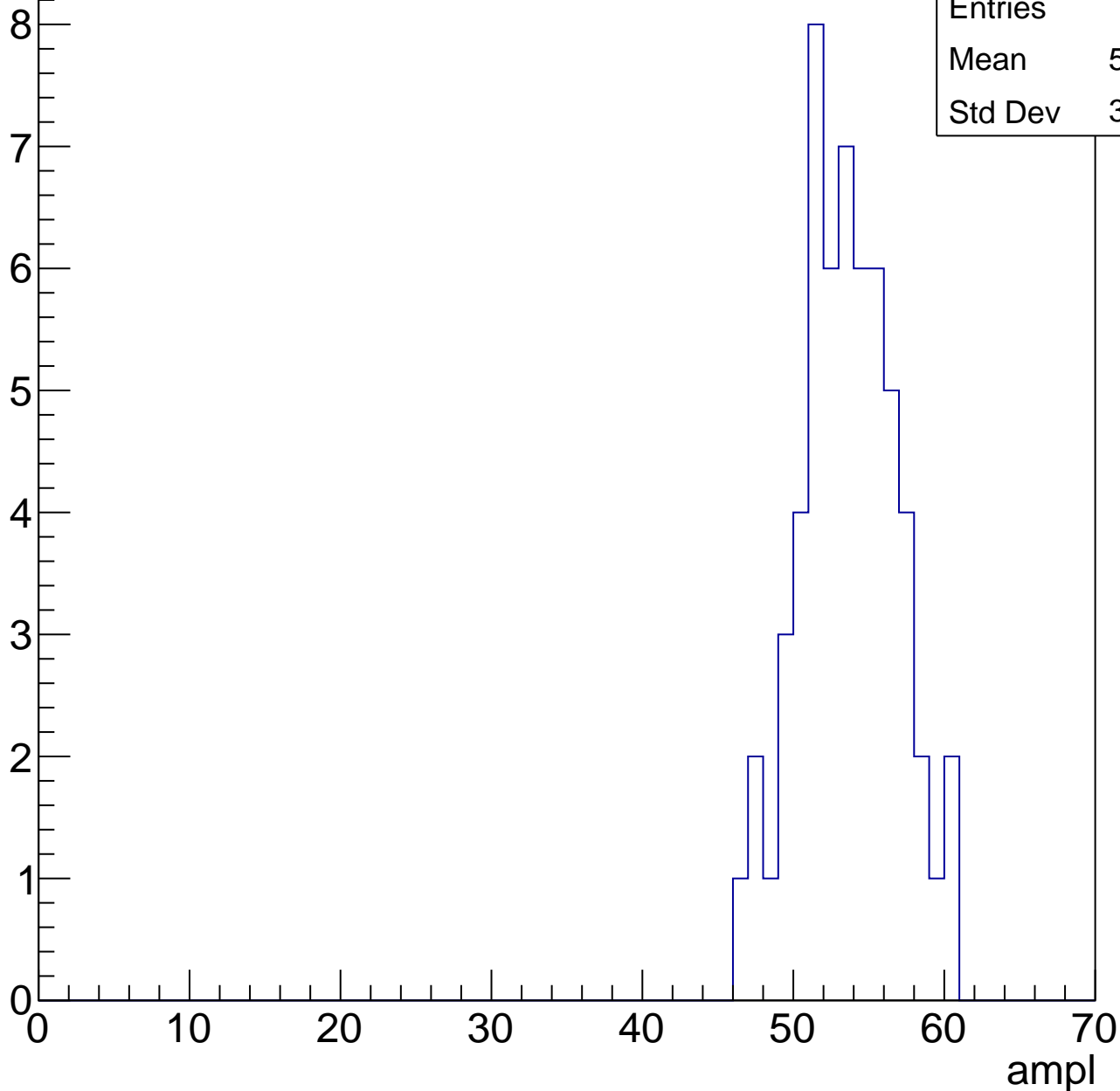


# B1L002S, U26-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.16
Std Dev	3.188

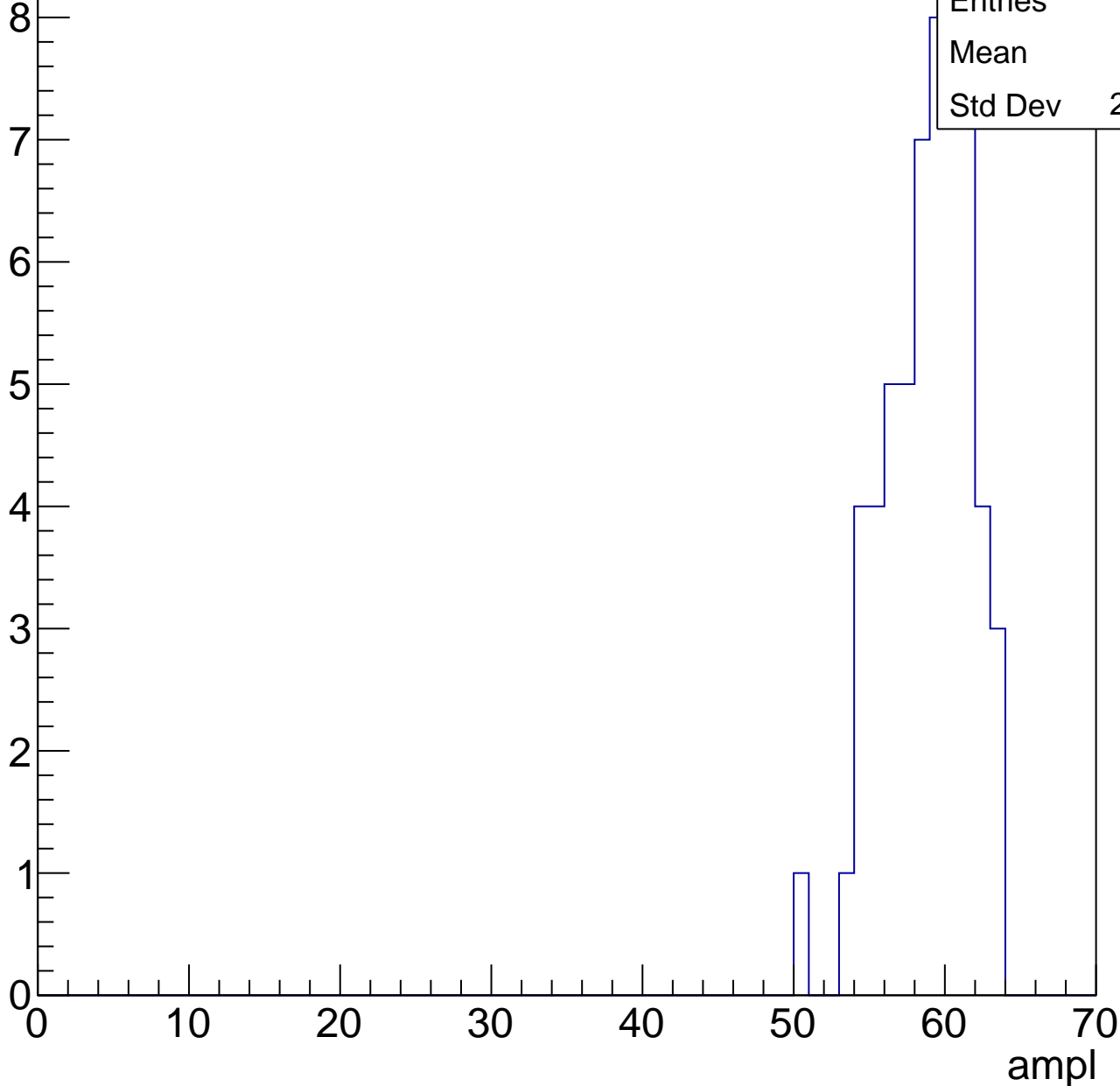


# B1L002S, U26-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.4
Std Dev	2.804

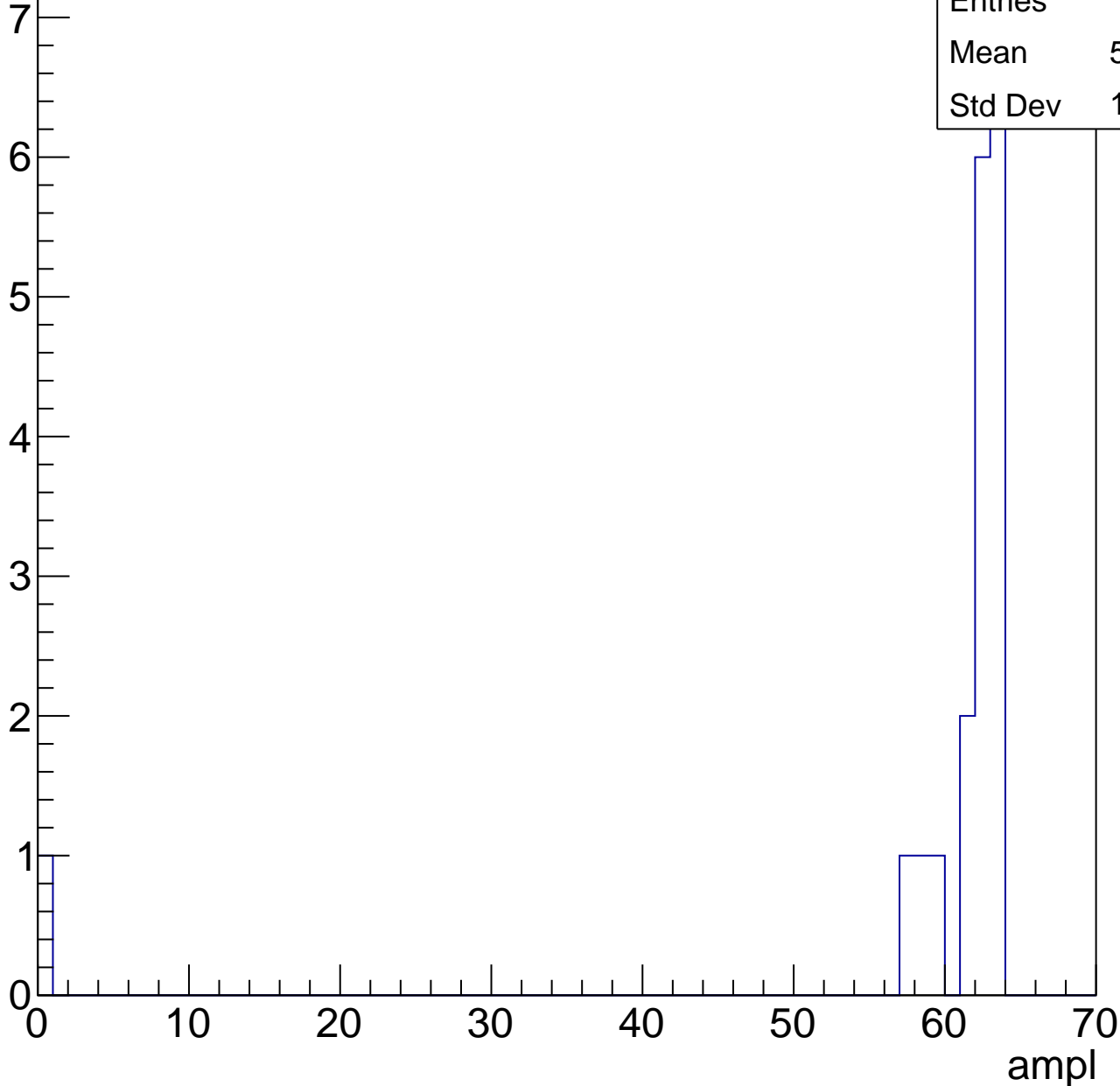


# B1L002S, U26-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.37
Std Dev	13.86





# B1L002S, U26-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch46, adc0

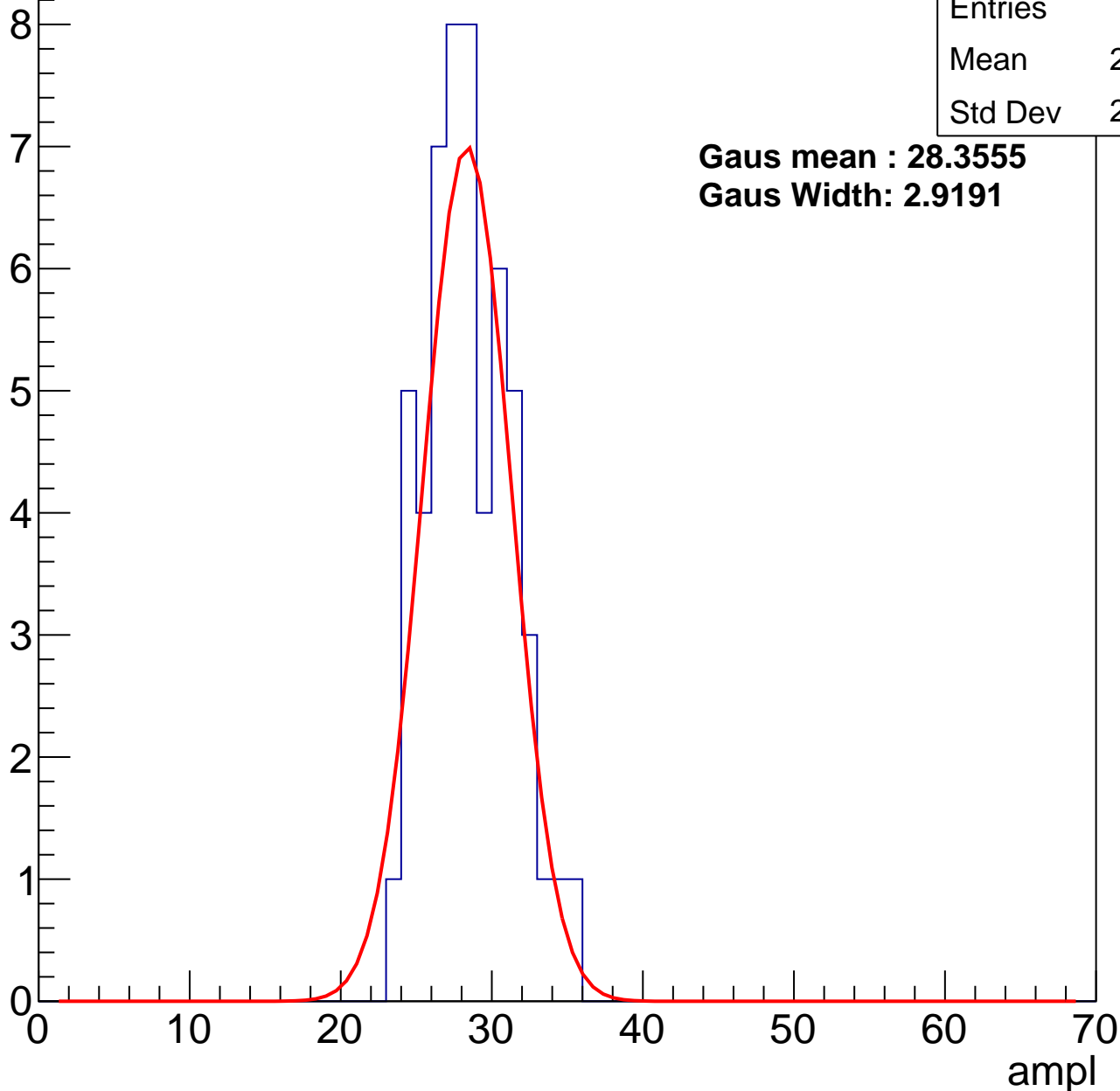
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.04
Std Dev	2.748

**Gaus mean : 28.3555**

**Gaus Width: 2.9191**



# B1L002S, U26-ch46, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	84
Mean	34.14
Std Dev	3.668

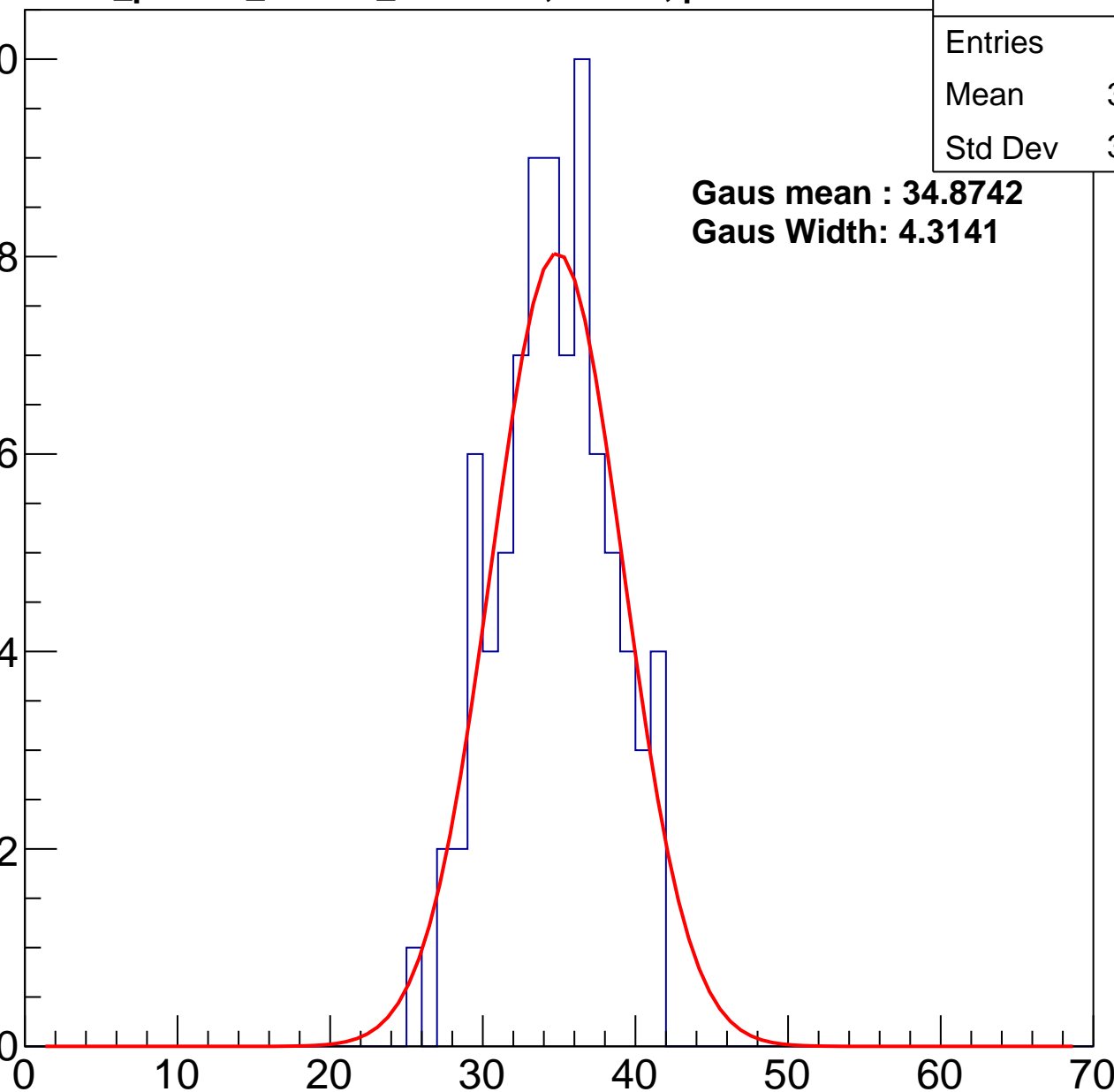
**Gaus mean : 34.8742**

**Gaus Width: 4.3141**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U26-ch46, adc2

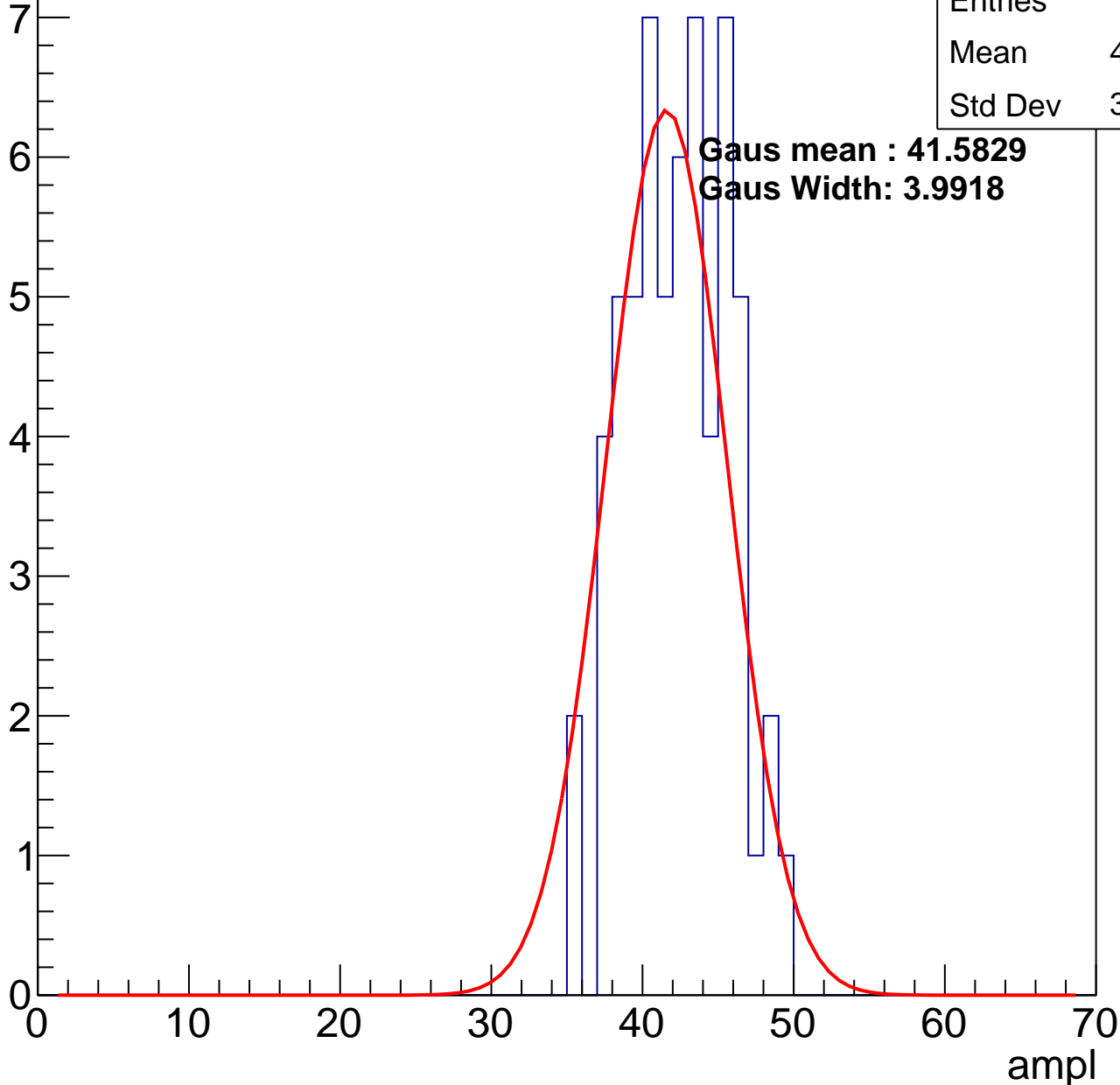
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.87
Std Dev	3.307

**Gaus mean : 41.5829**

**Gaus Width: 3.9918**

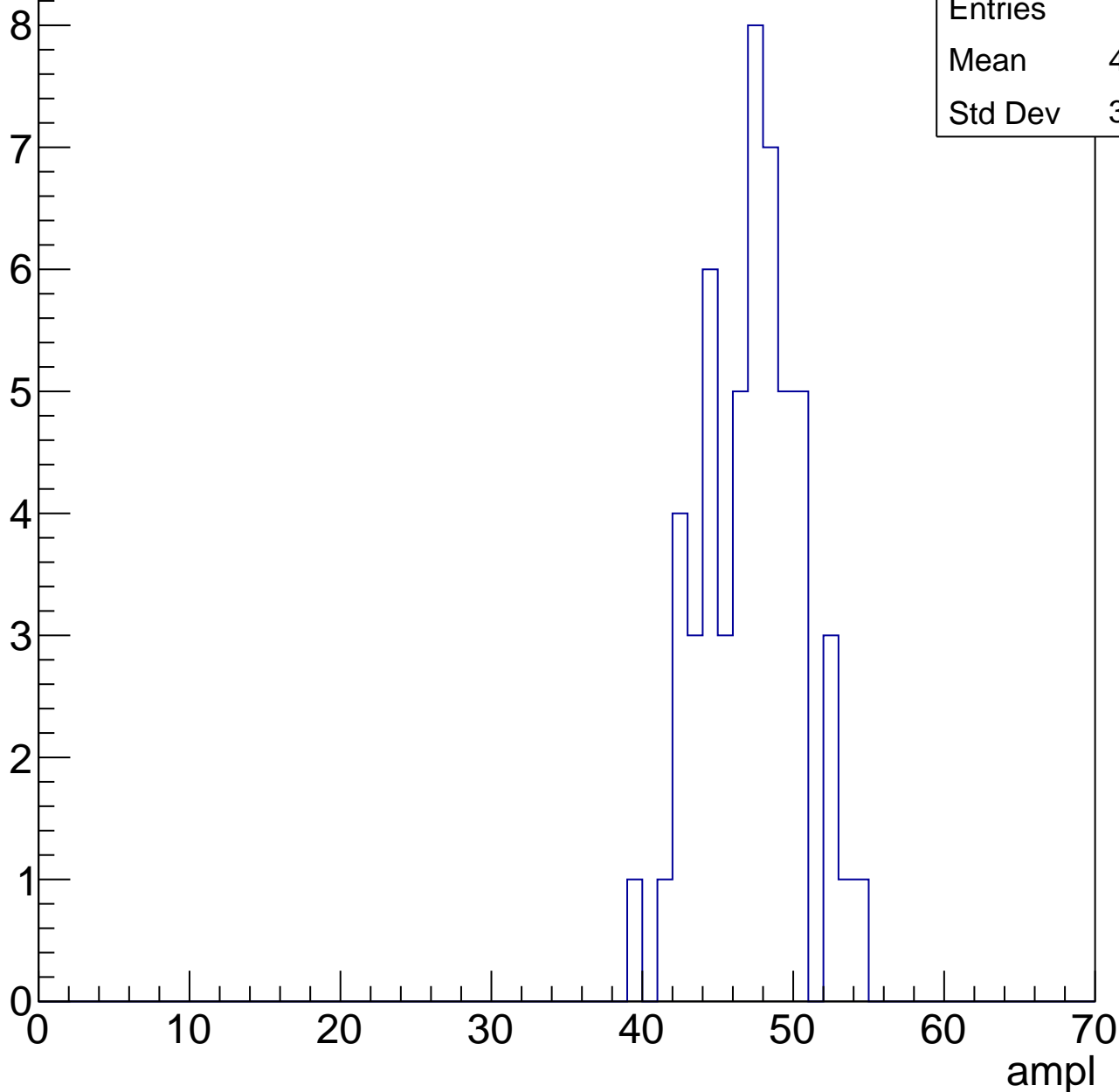


# B1L002S, U26-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.72
Std Dev	3.212

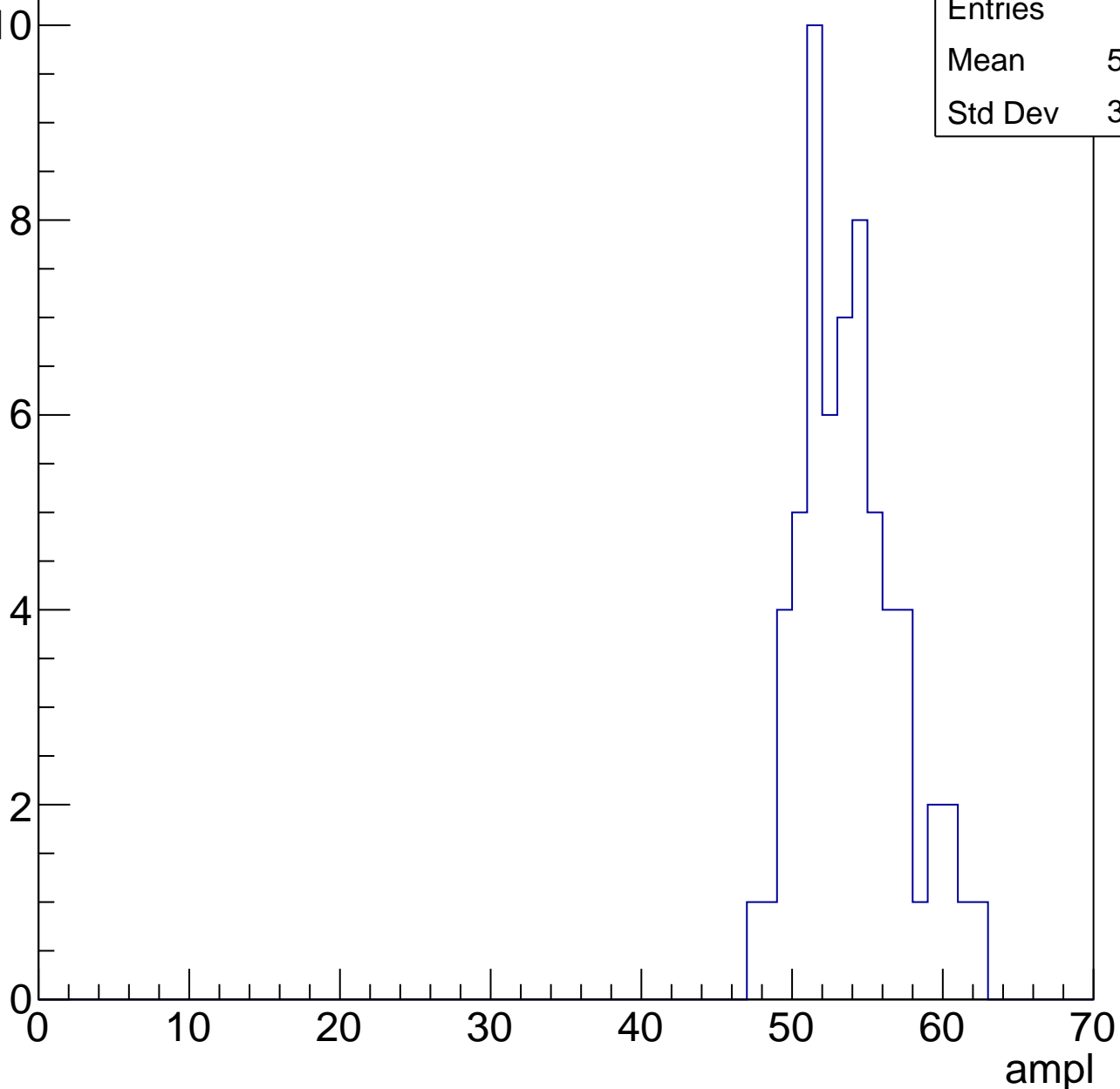


# B1L002S, U26-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

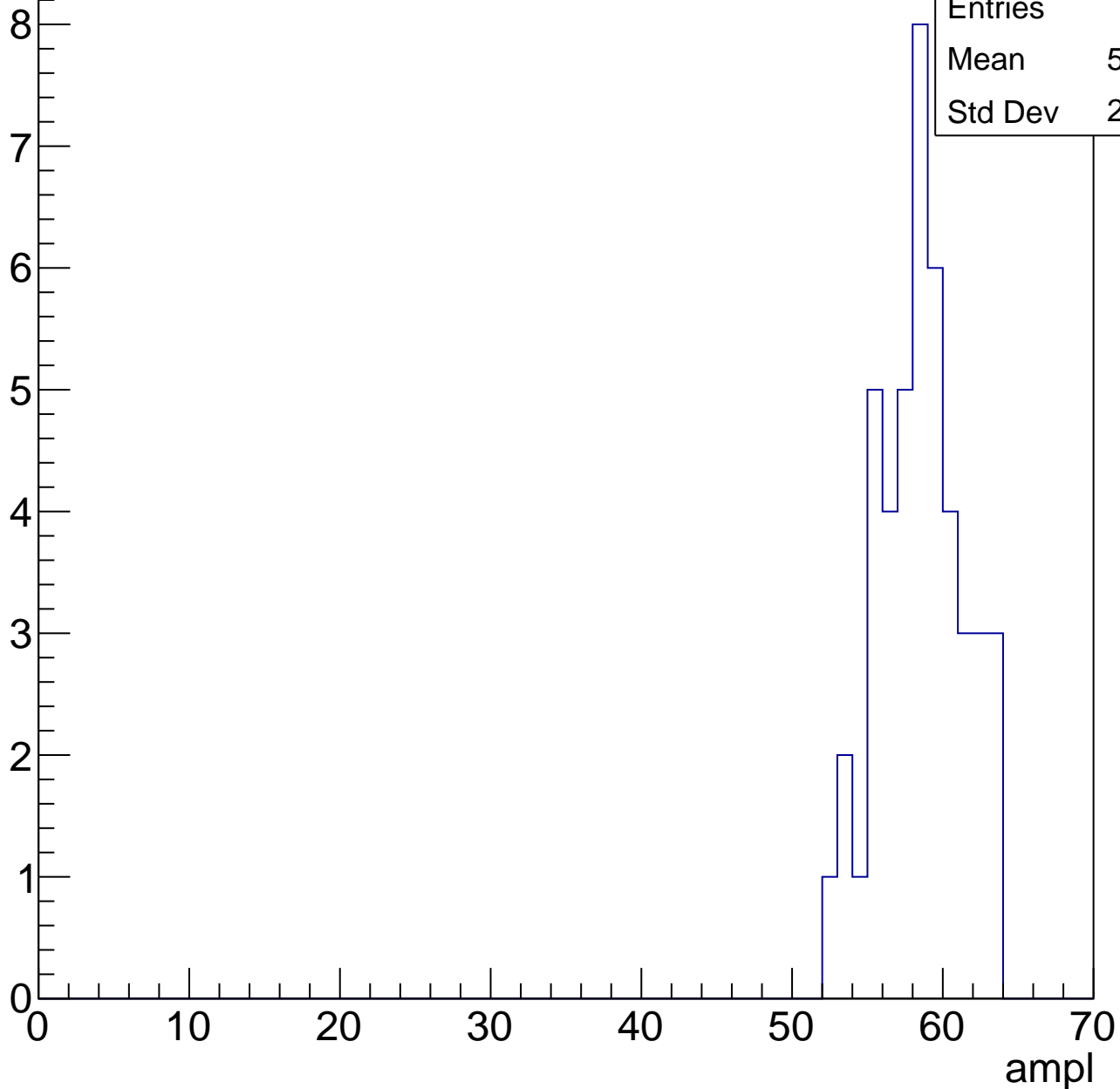
Entries	62
Mean	53.42
Std Dev	3.295



# B1L002S, U26-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



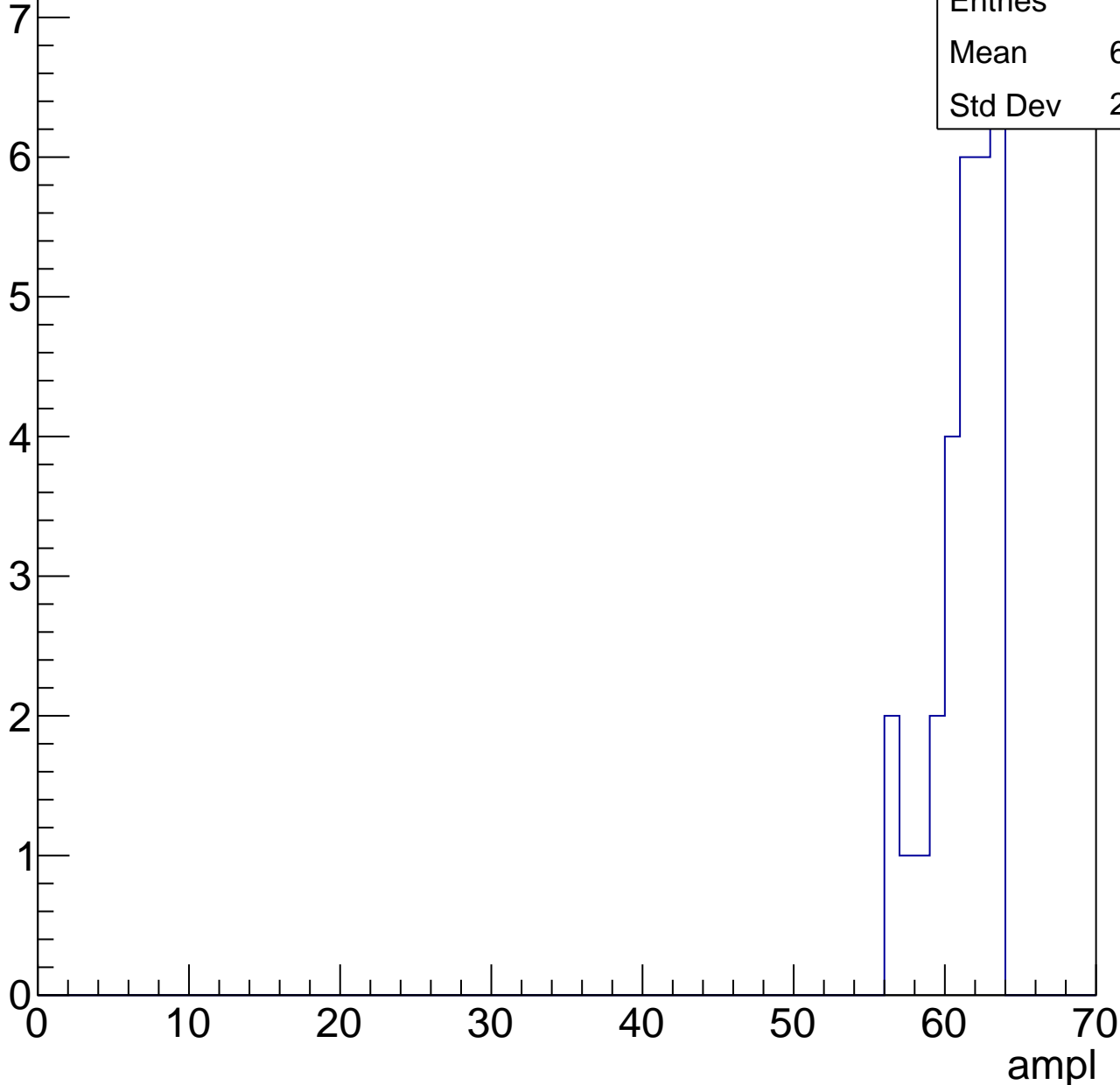
Entries	45
Mean	58.04
Std Dev	2.748

# B1L002S, U26-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	60.83
Std Dev	2.035

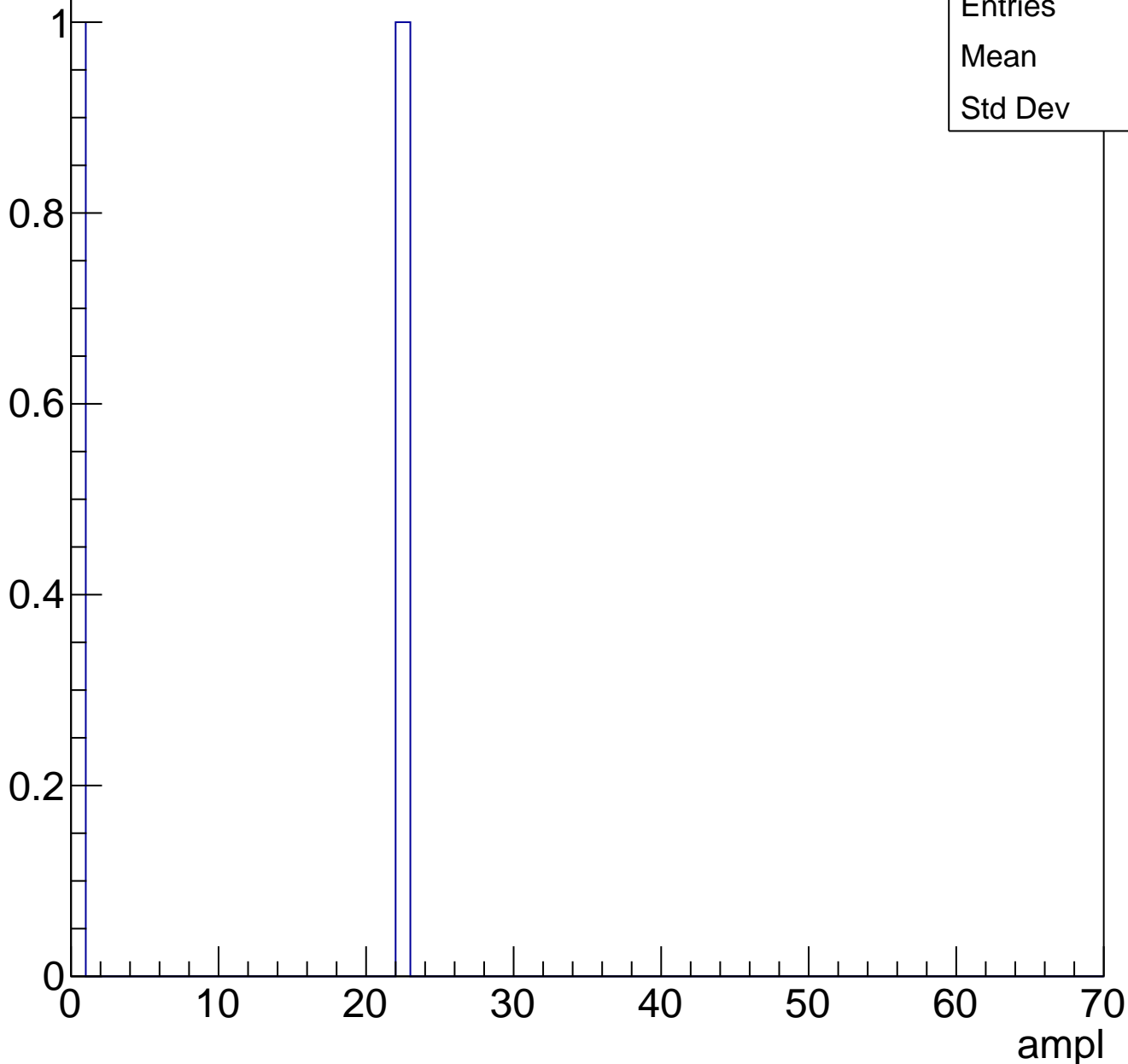




# B1L002S, U26-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U26-ch47, adc0

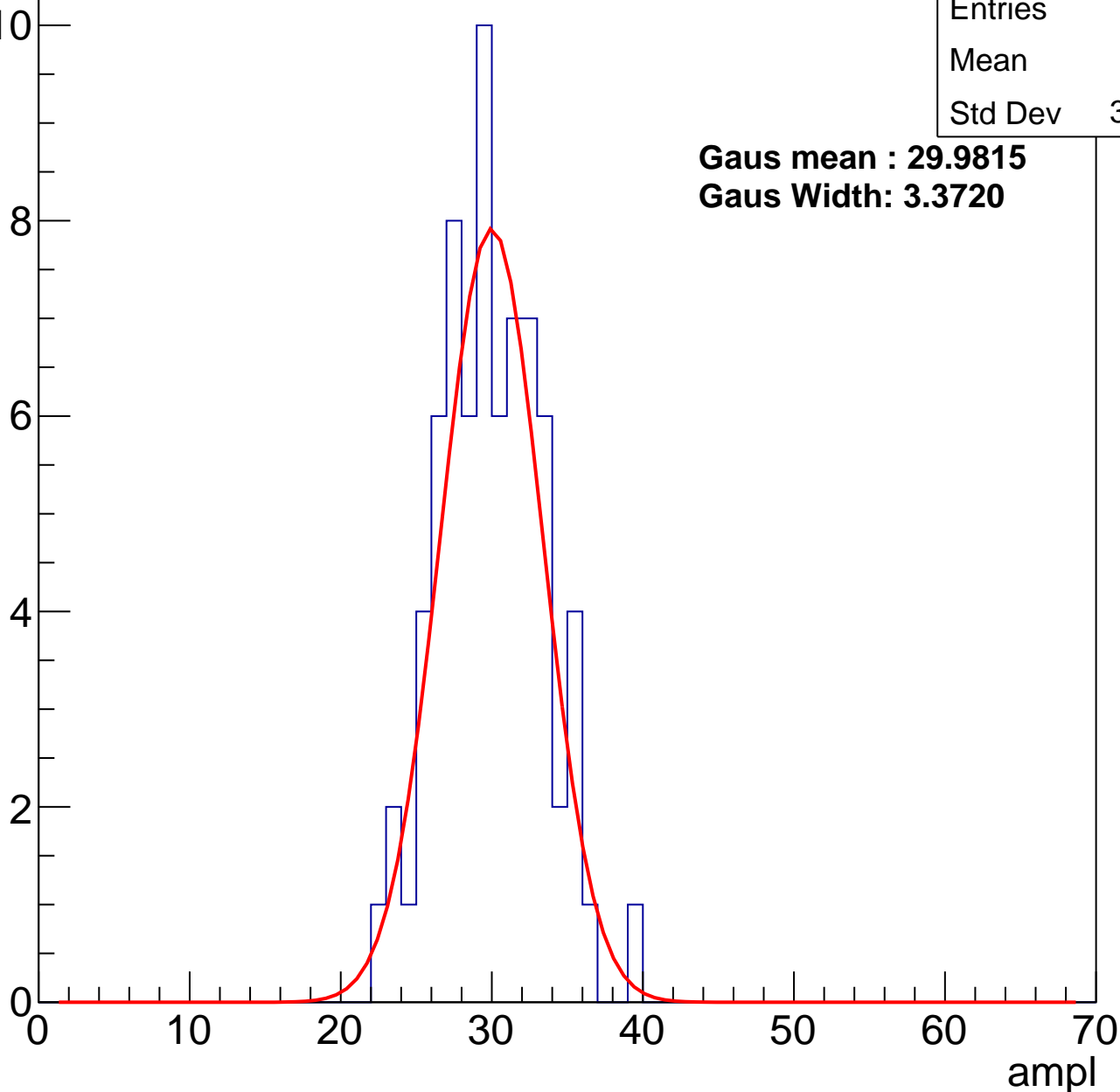
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	29.5
Std Dev	3.375

**Gaus mean : 29.9815**

**Gaus Width: 3.3720**



# B1L002S, U26-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	54
Mean	35.19
Std Dev	2.722

**Gaus mean : 35.4238**

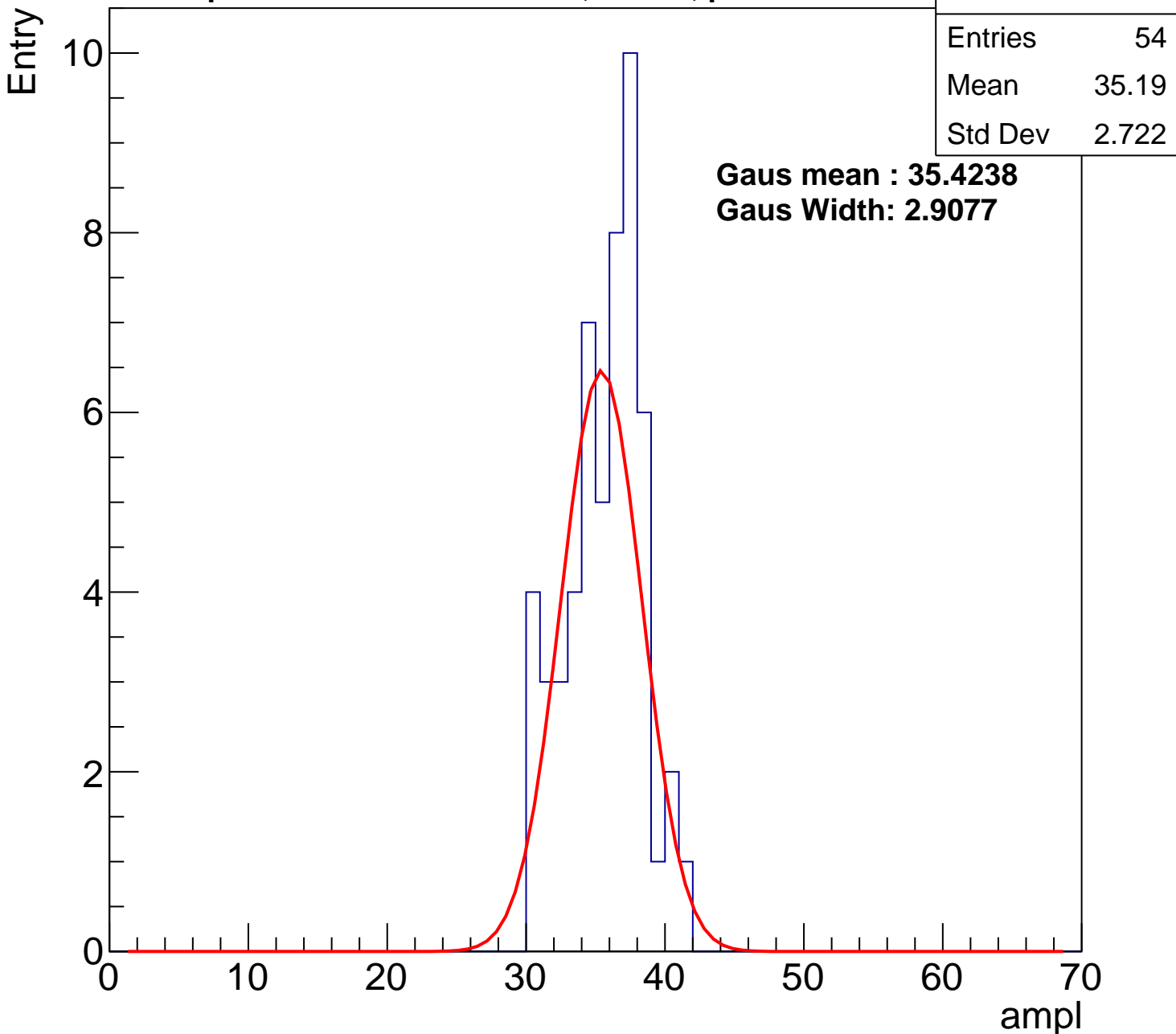
**Gaus Width: 2.9077**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	42.05
Std Dev	2.937

**Gaus mean : 42.6181**

**Gaus Width: 3.1958**

10

8

6

4

2

0

0

10

20

30

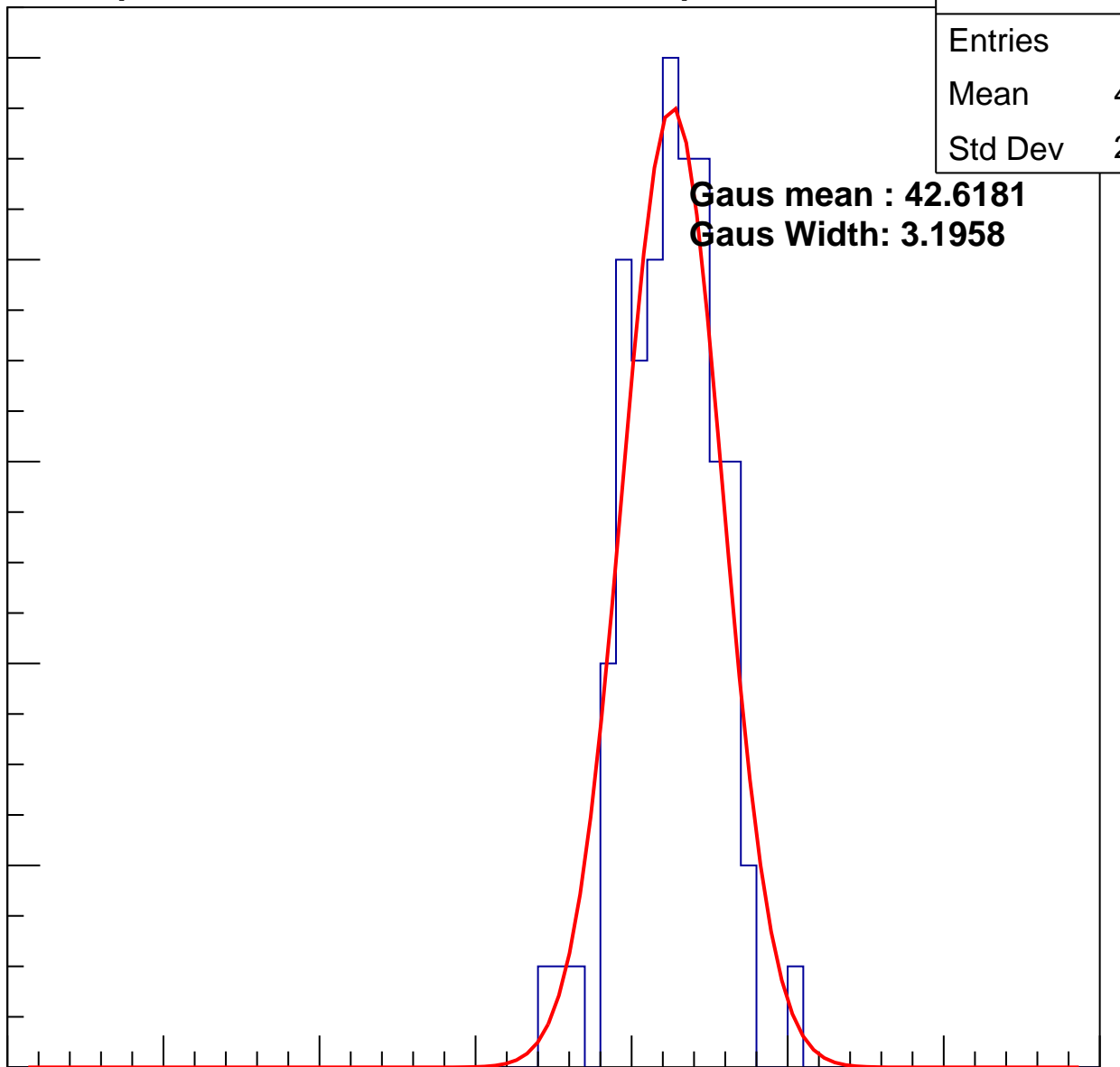
40

50

60

70

ampl

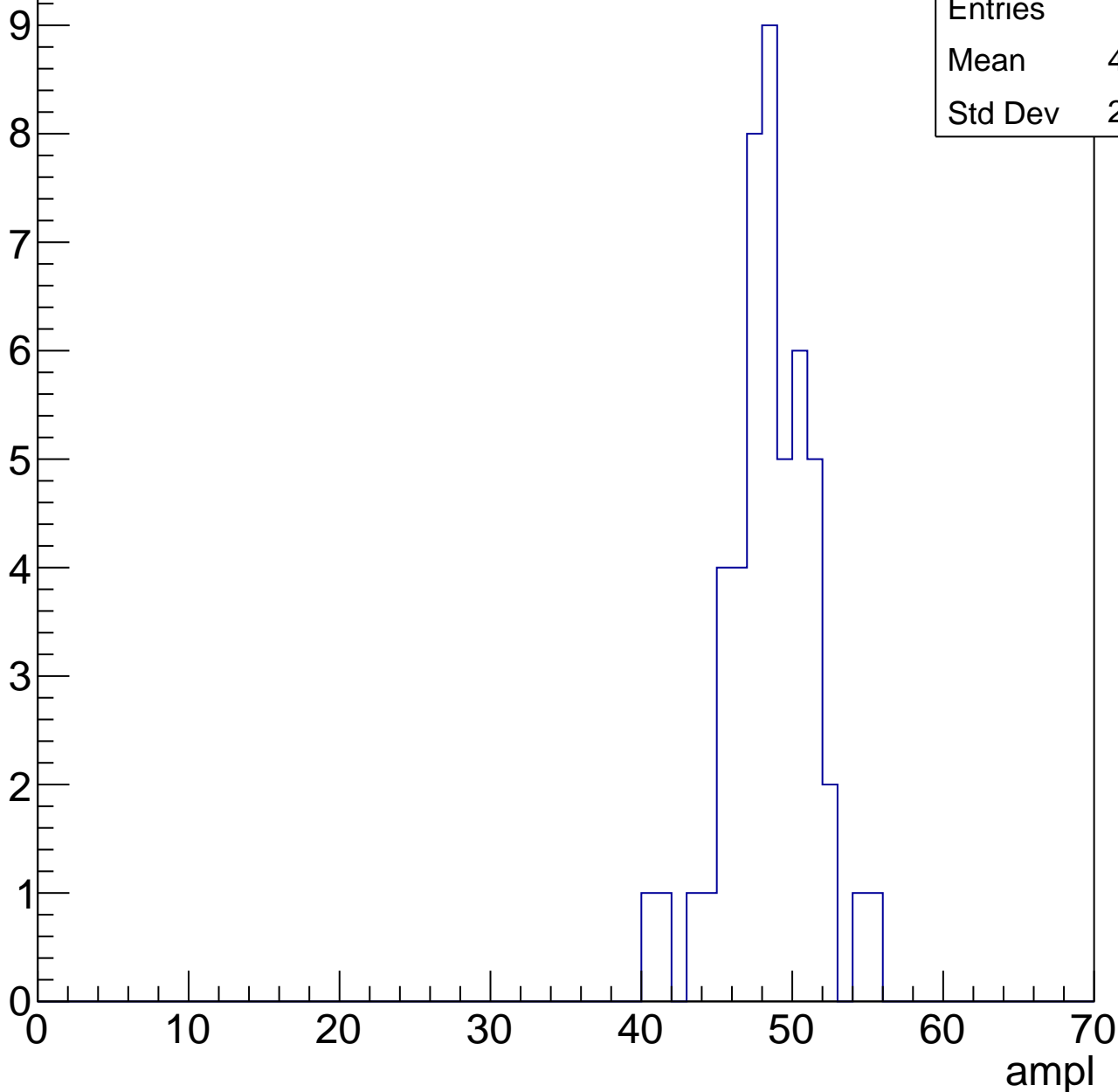


# B1L002S, U26-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

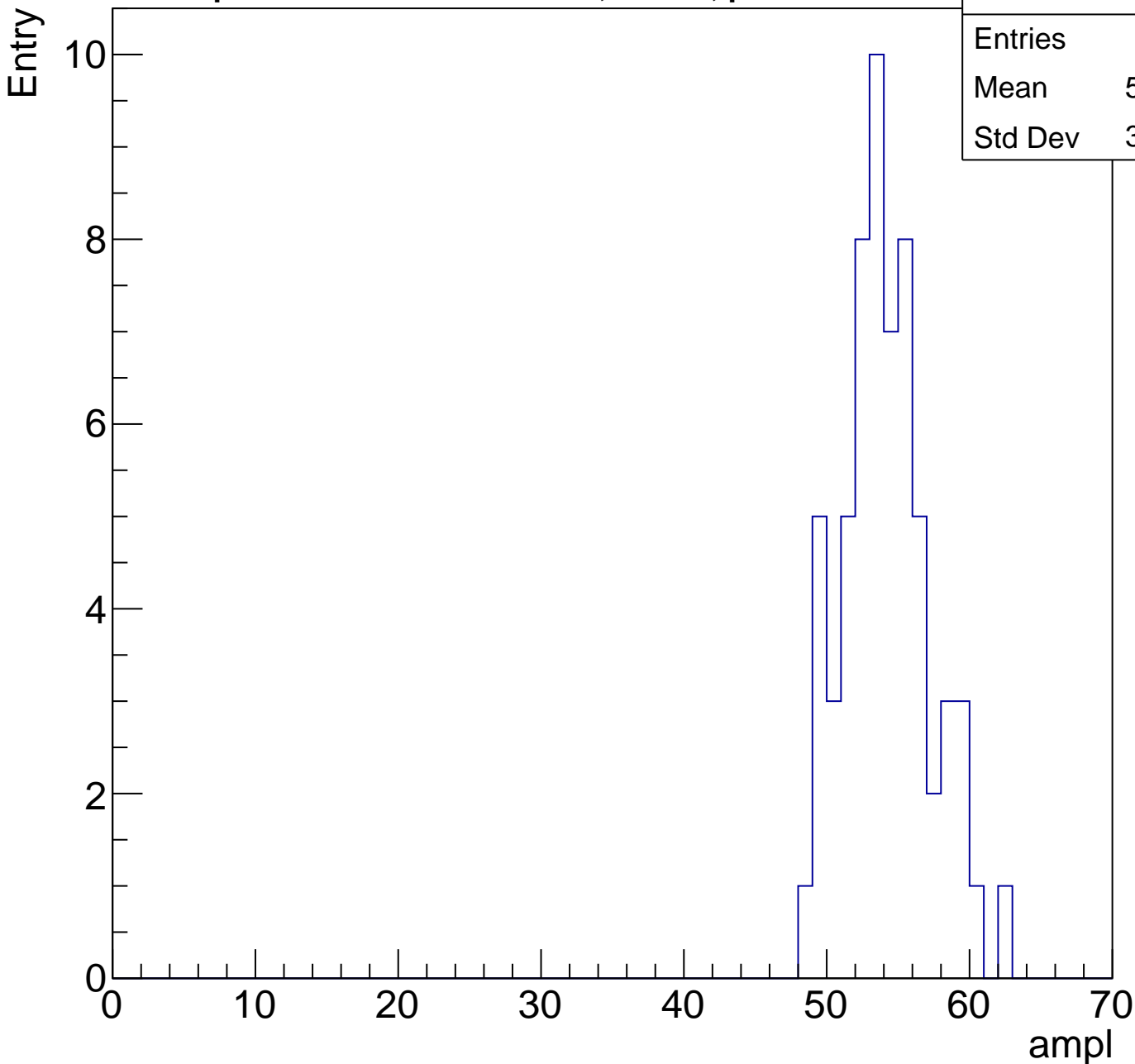
Entries	49
Mean	48.02
Std Dev	2.875



# B1L002S, U26-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

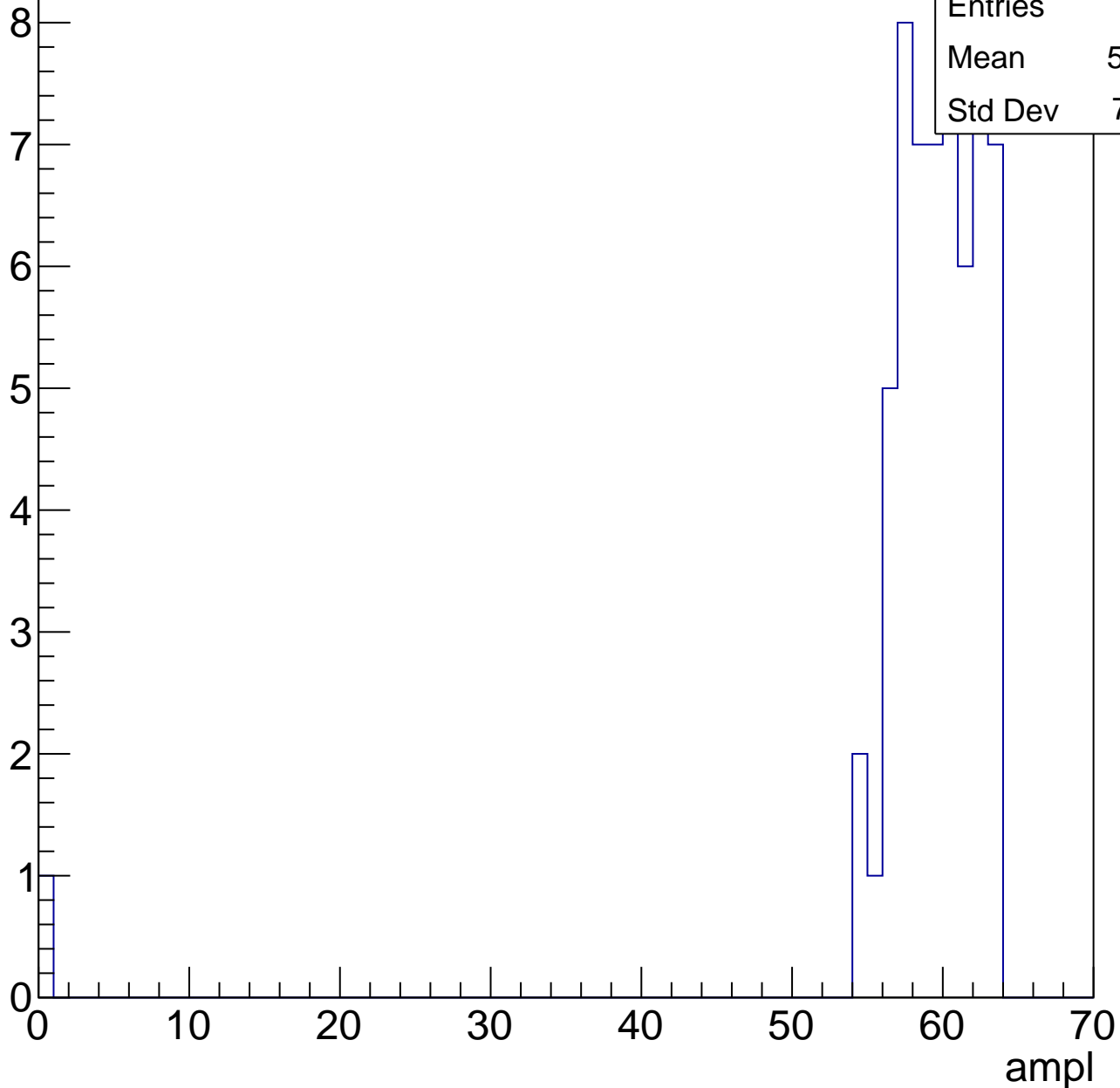
Entries	62
Mean	53.69
Std Dev	3.003



# B1L002S, U26-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

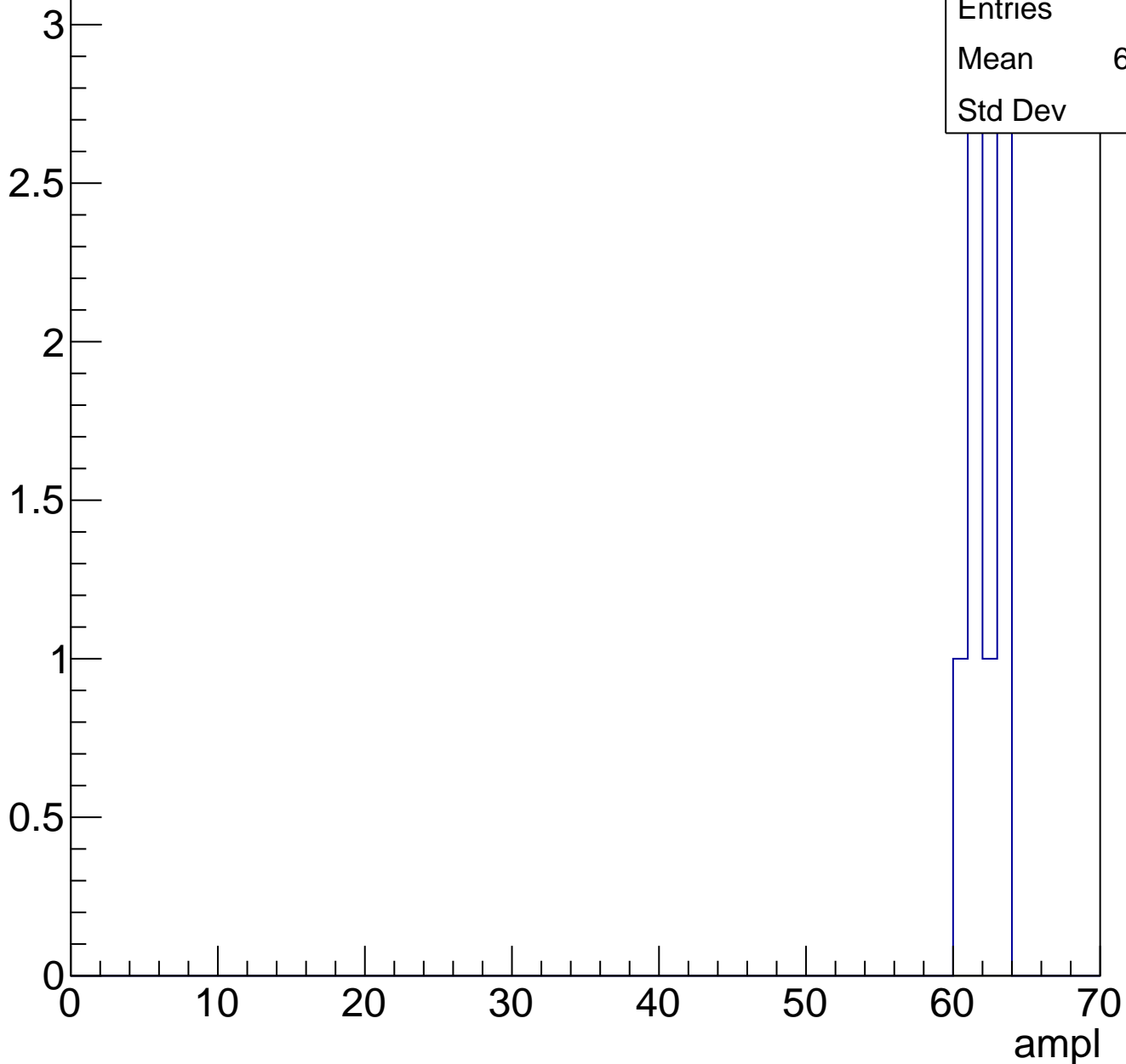
Entry



# B1L002S, U26-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

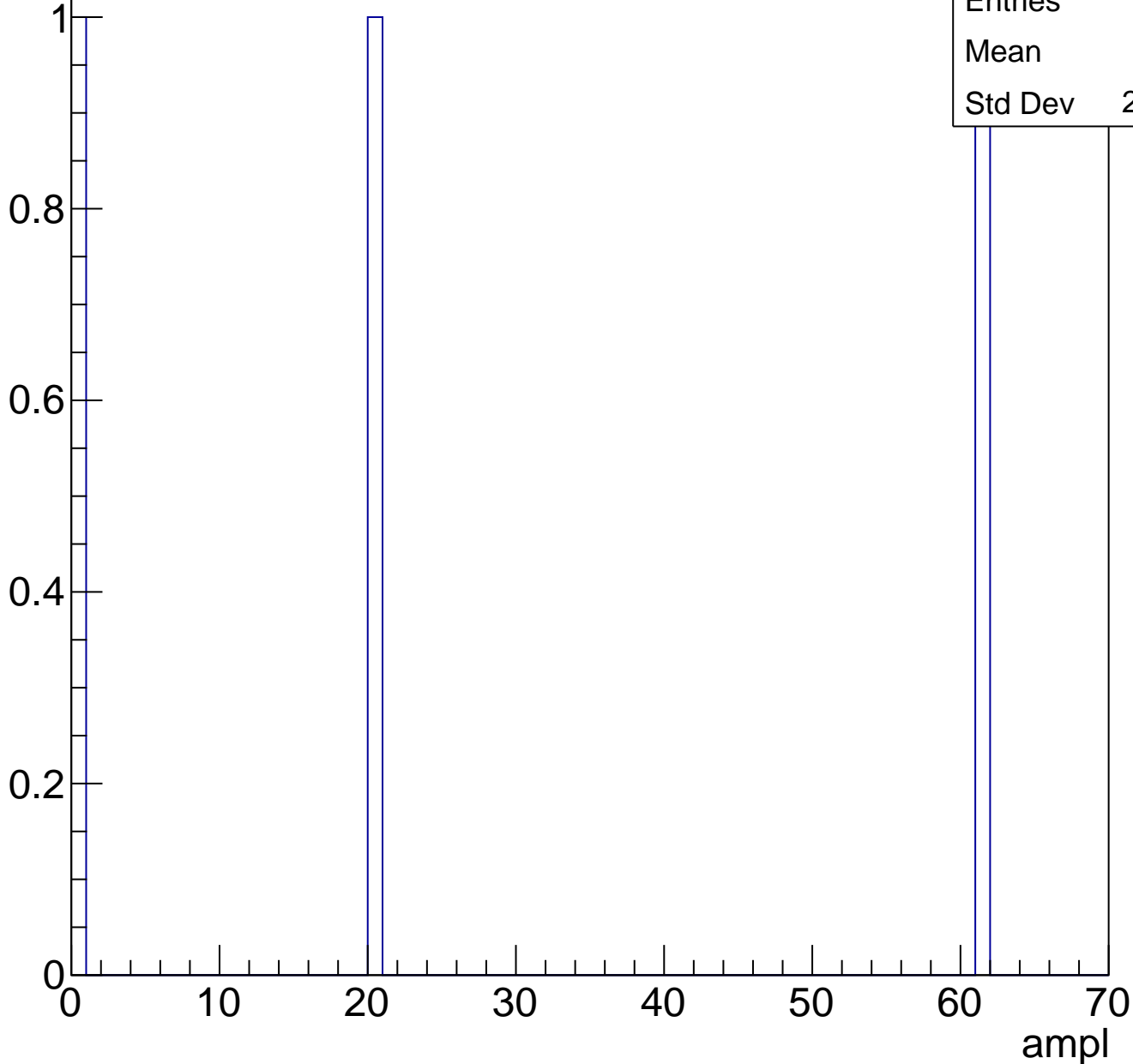




# B1L002S, U26-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27
Std Dev	25.39

# B1L002S, U26-ch48, adc0

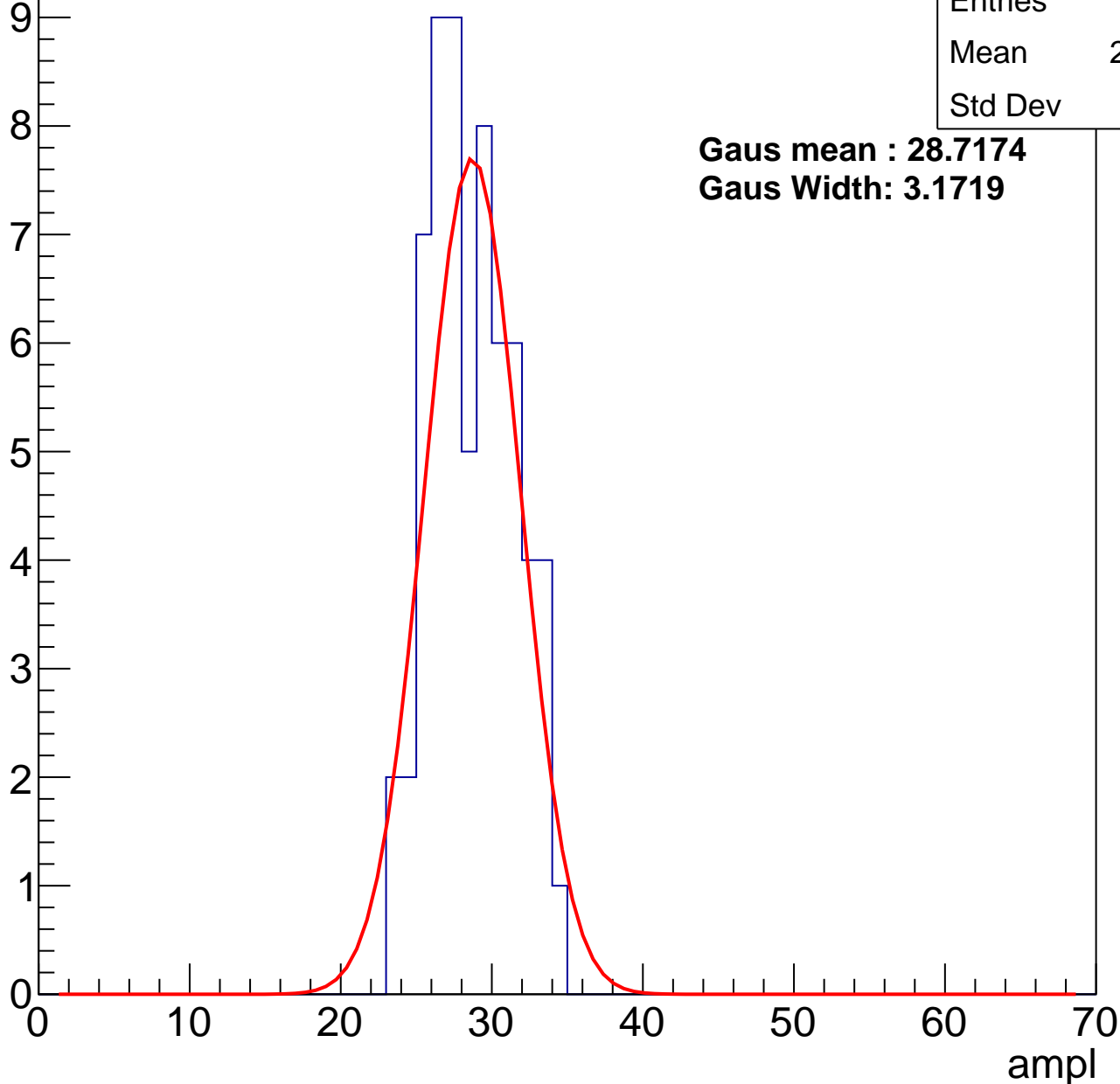
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.22
Std Dev	2.74

**Gaus mean : 28.7174**

**Gaus Width: 3.1719**



# B1L002S, U26-ch48, adc1

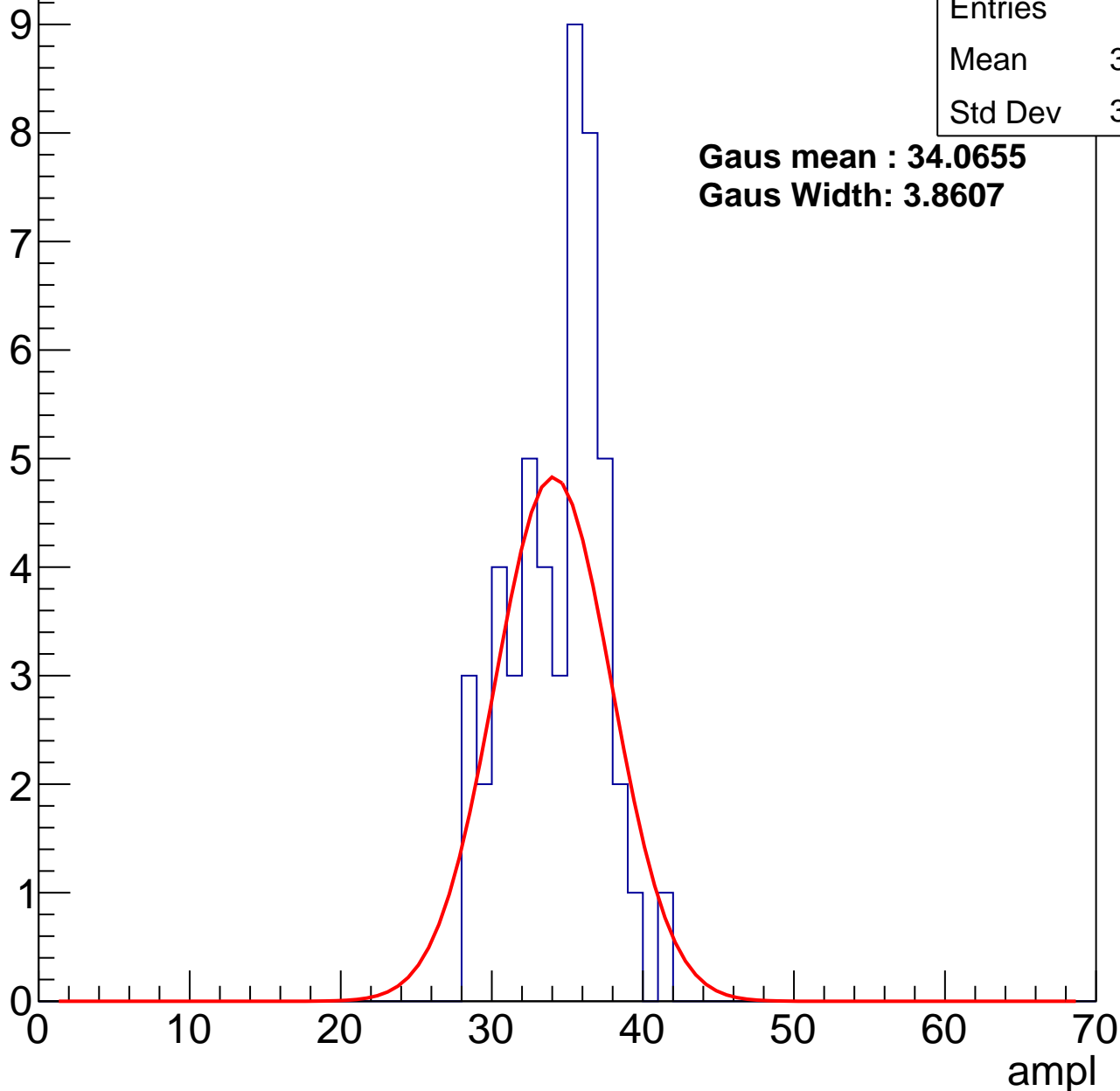
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	33.86
Std Dev	3.046

**Gaus mean : 34.0655**

**Gaus Width: 3.8607**



# B1L002S, U26-ch48, adc2

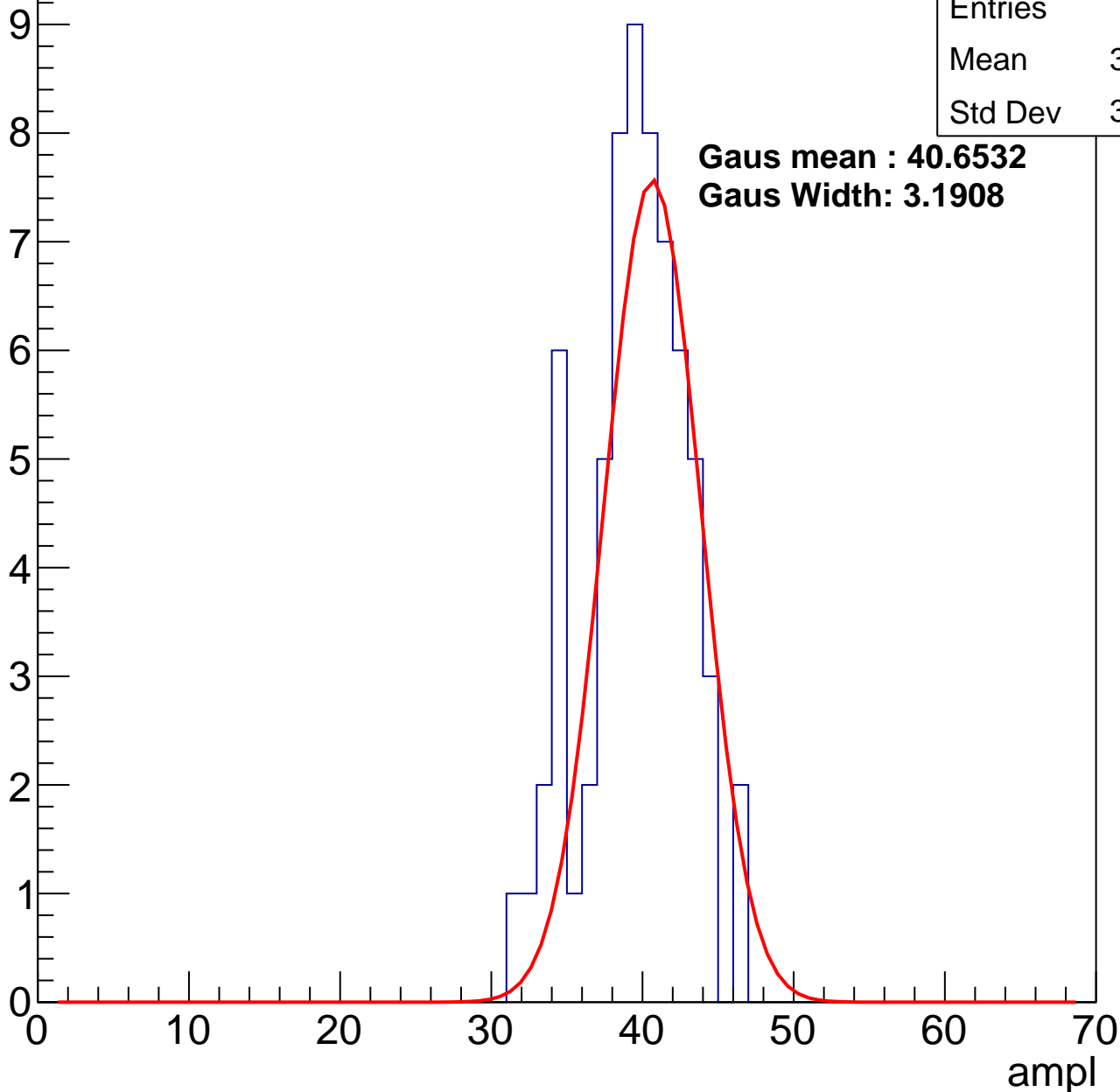
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	39.06
Std Dev	3.348

**Gaus mean : 40.6532**

**Gaus Width: 3.1908**



# B1L002S, U26-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	45.72
Std Dev	2.787

Entry

10

8

6

4

2

0

0

10

20

30

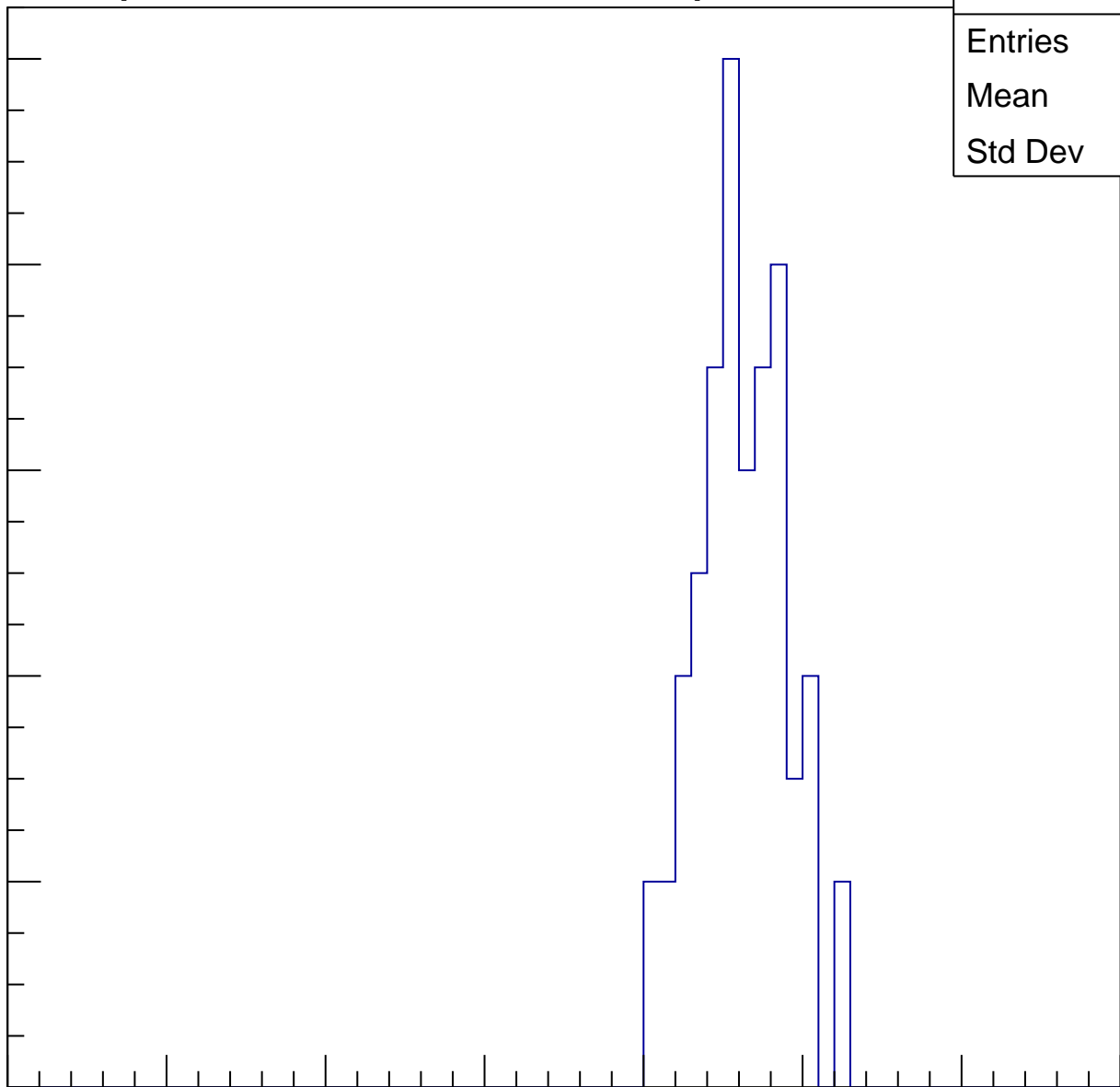
40

50

60

70

ampl



# B1L002S, U26-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	57
Mean	51.68
Std Dev	2.933

Entry

10

8

6

4

2

0

0

10

20

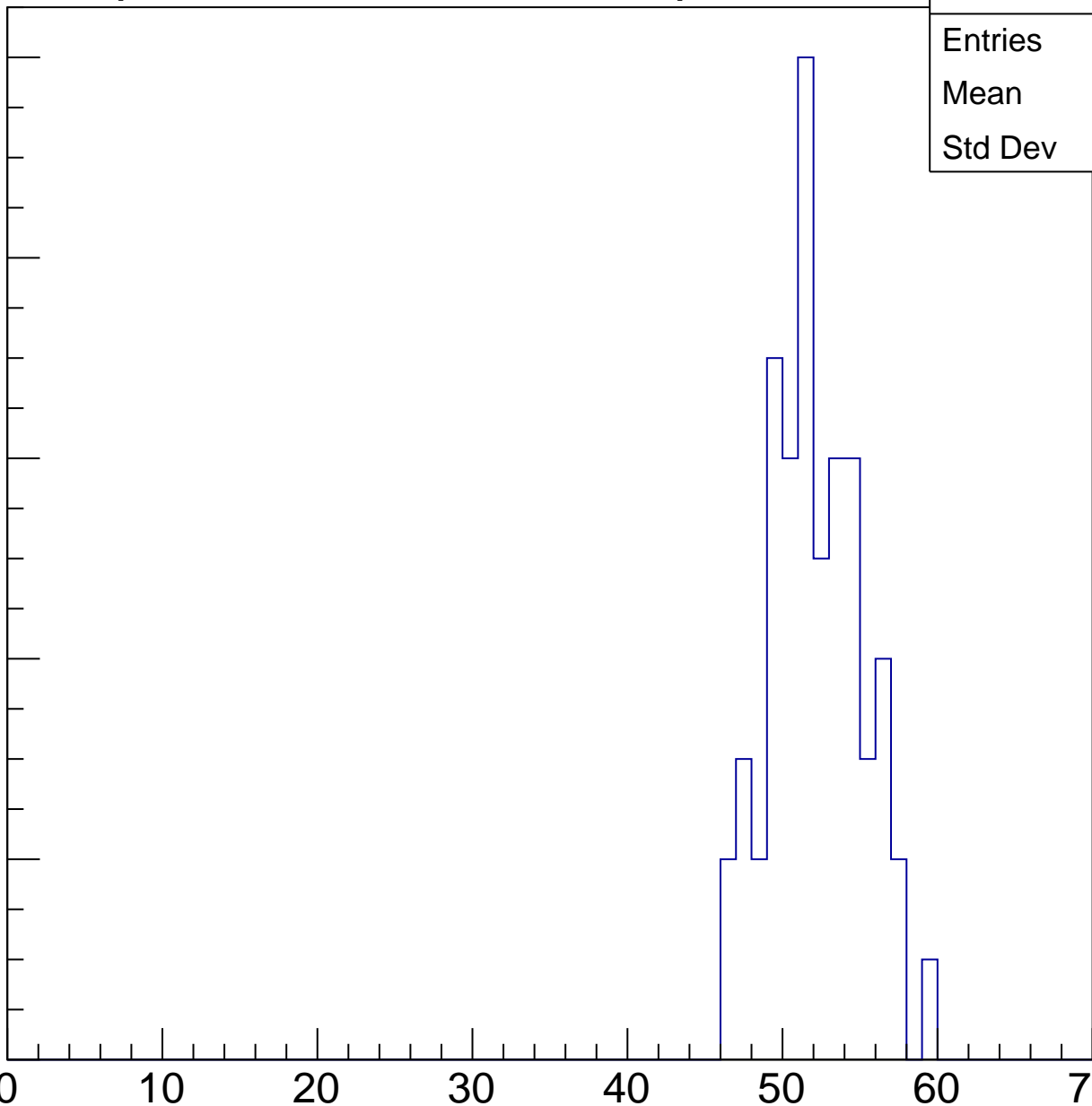
30

40

50

60

ampl

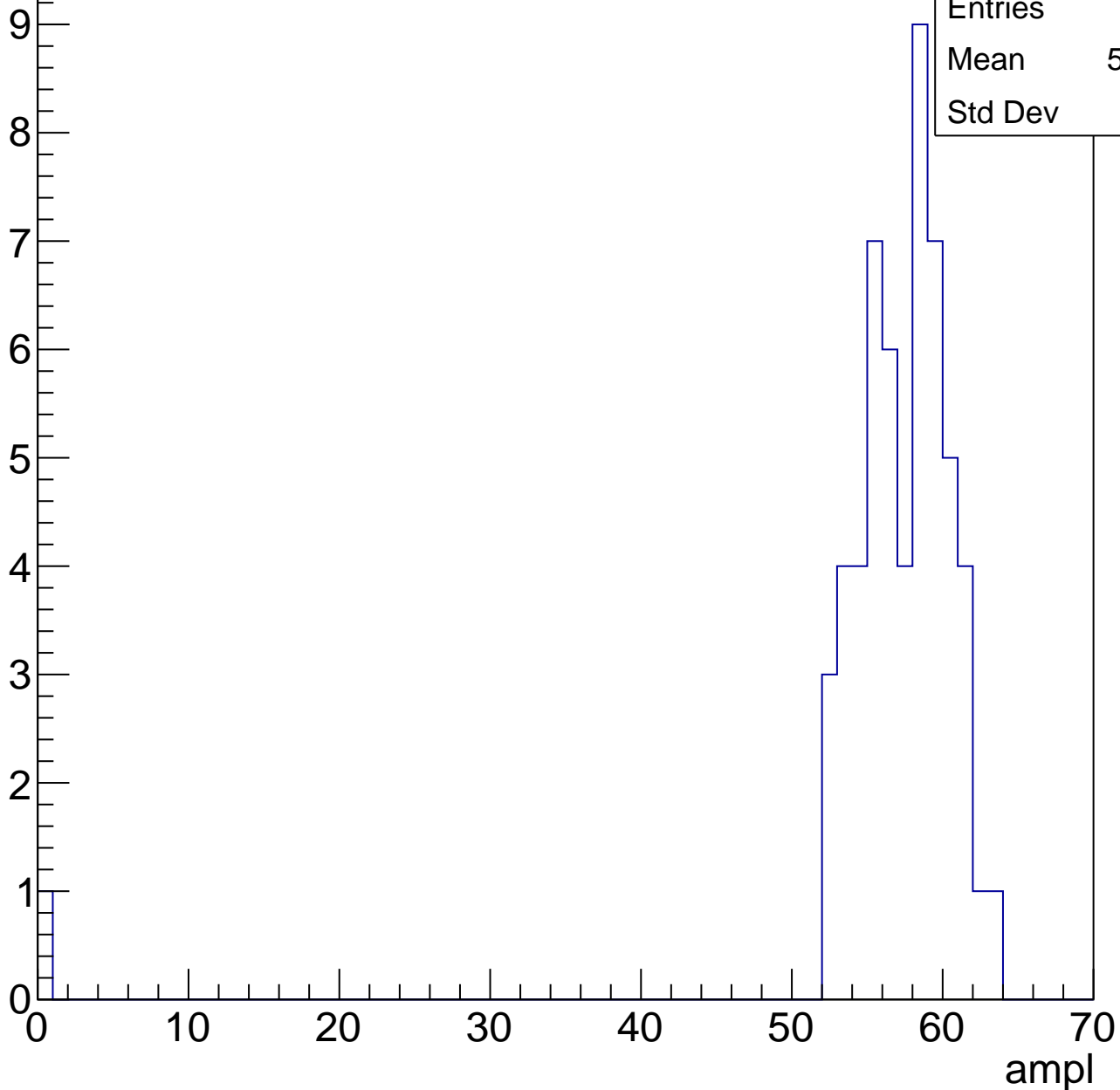


# B1L002S, U26-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.02
Std Dev	8.03

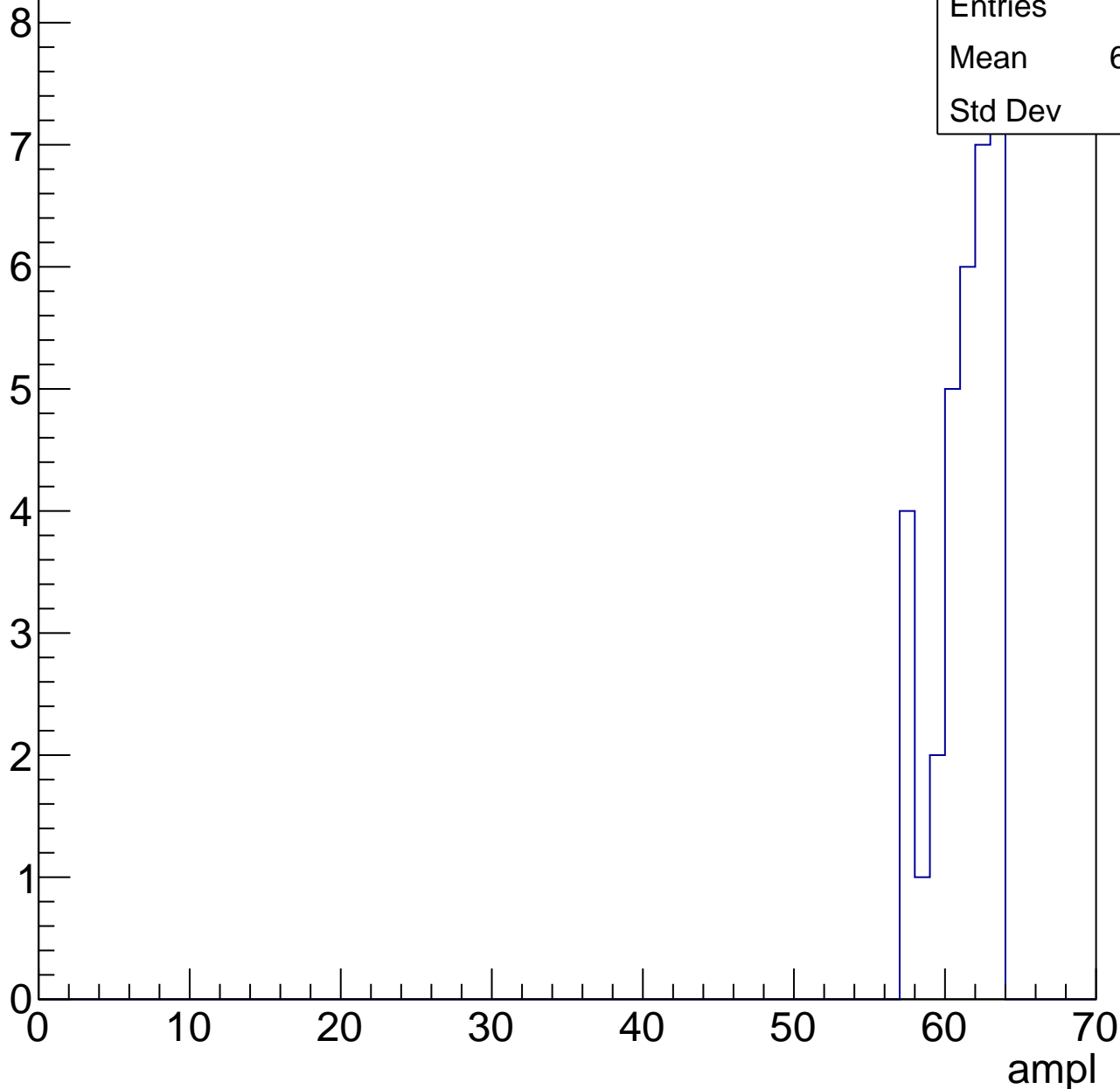


# B1L002S, U26-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	60.85
Std Dev	1.94

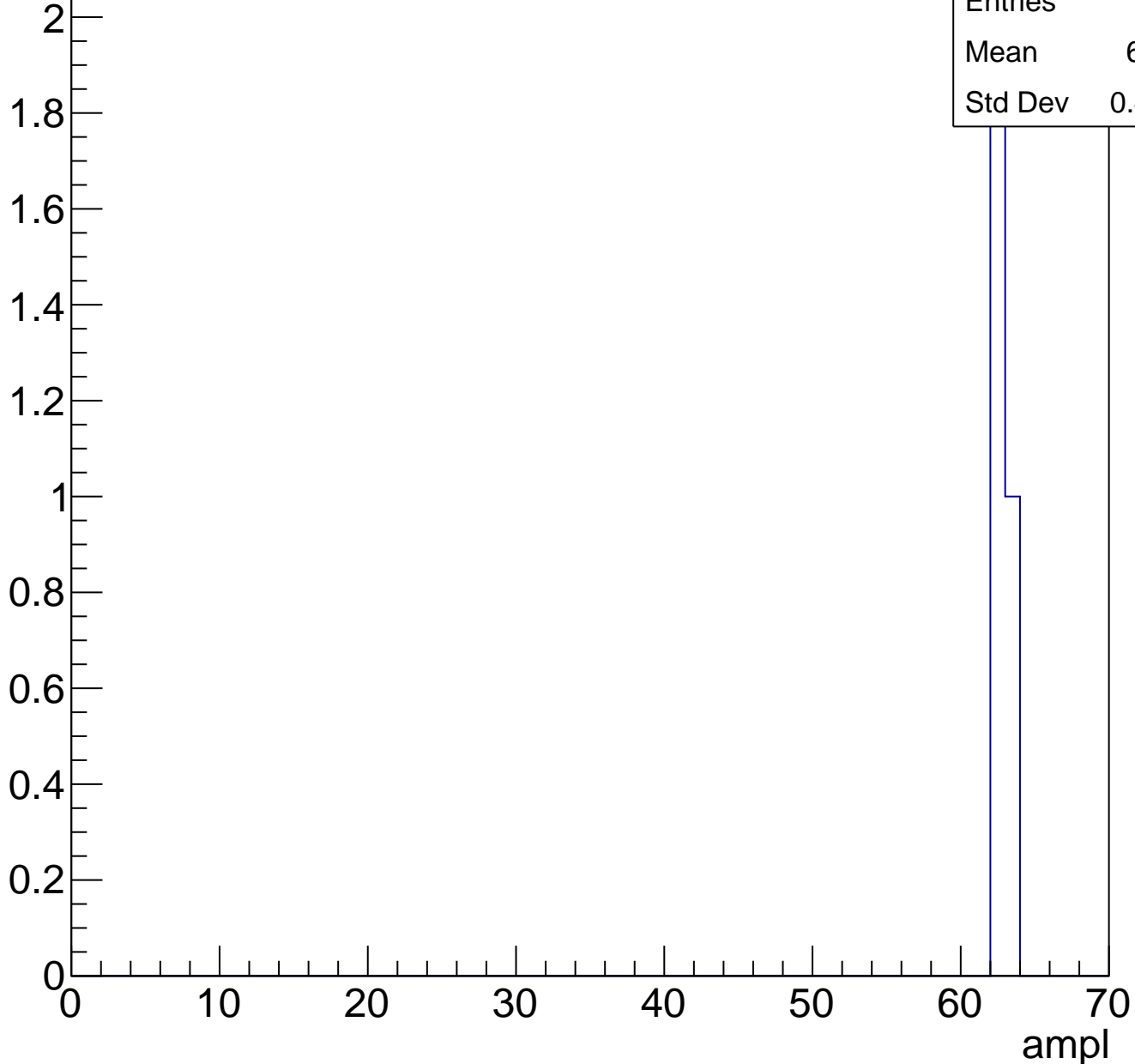




# B1L002S, U26-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch49, adc0

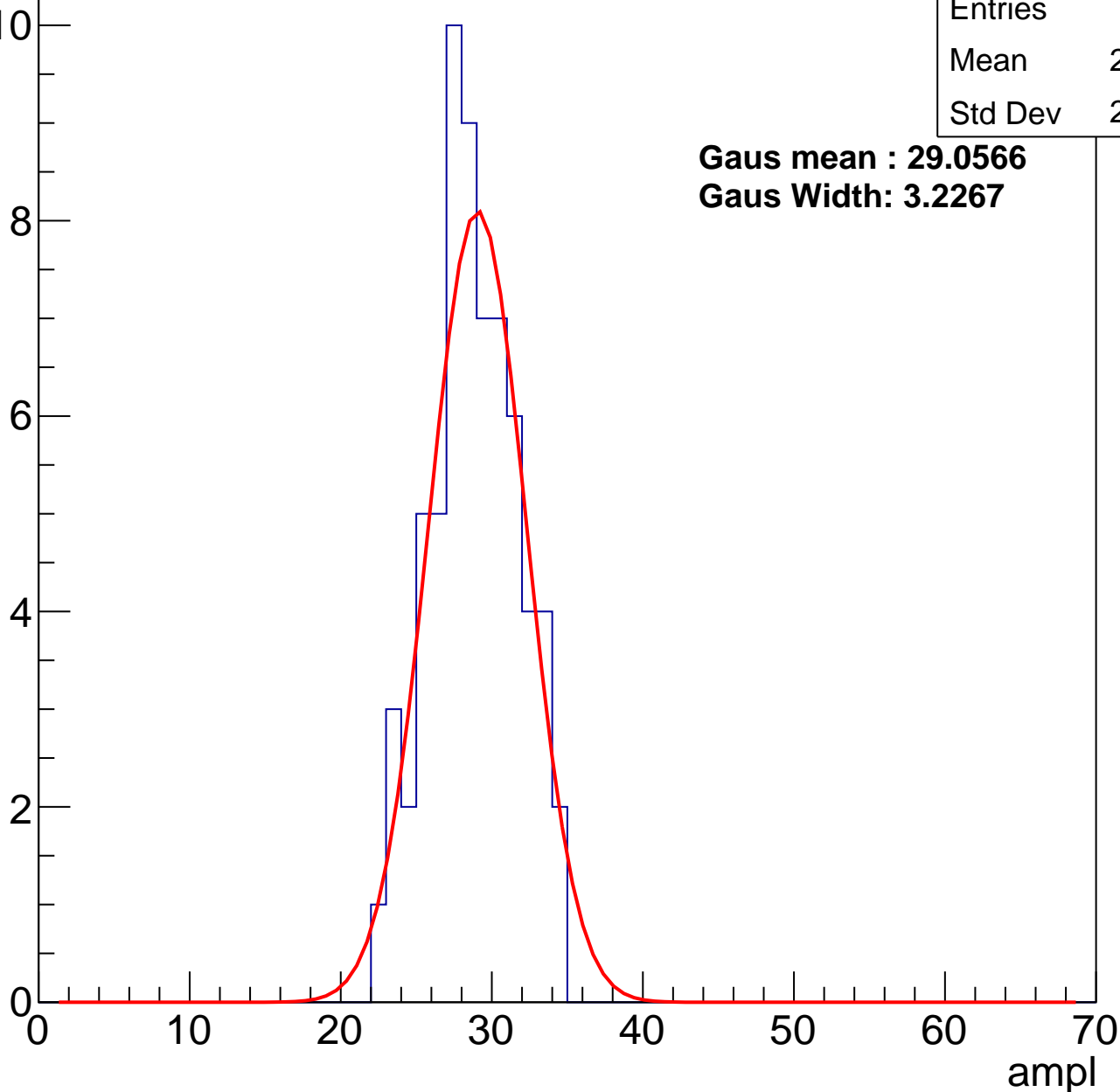
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.35
Std Dev	2.869

**Gaus mean : 29.0566**

**Gaus Width: 3.2267**



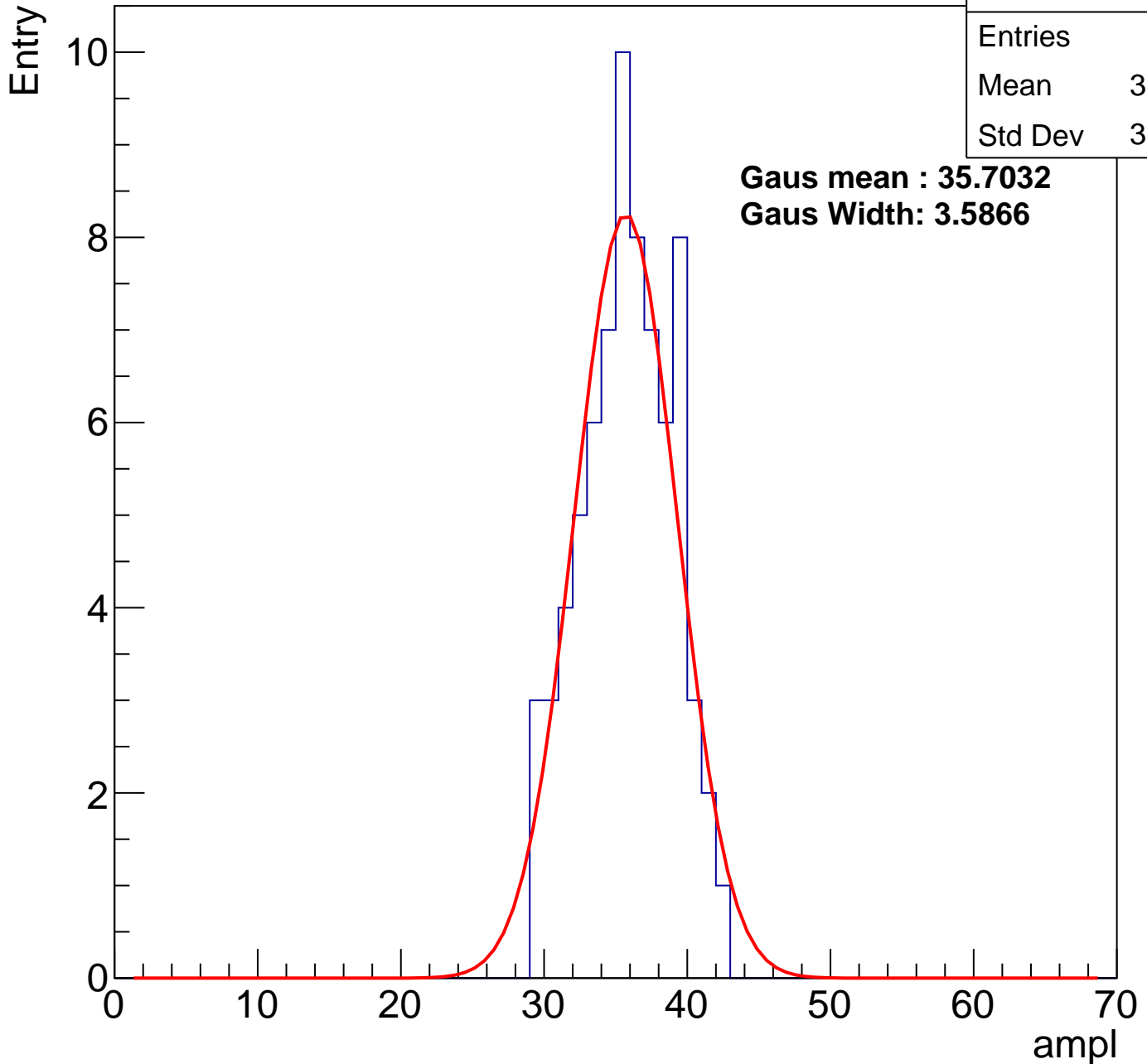
# B1L002S, U26-ch49, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	35.32
Std Dev	3.162

**Gaus mean : 35.7032**

**Gaus Width: 3.5866**



# B1L002S, U26-ch49, adc2

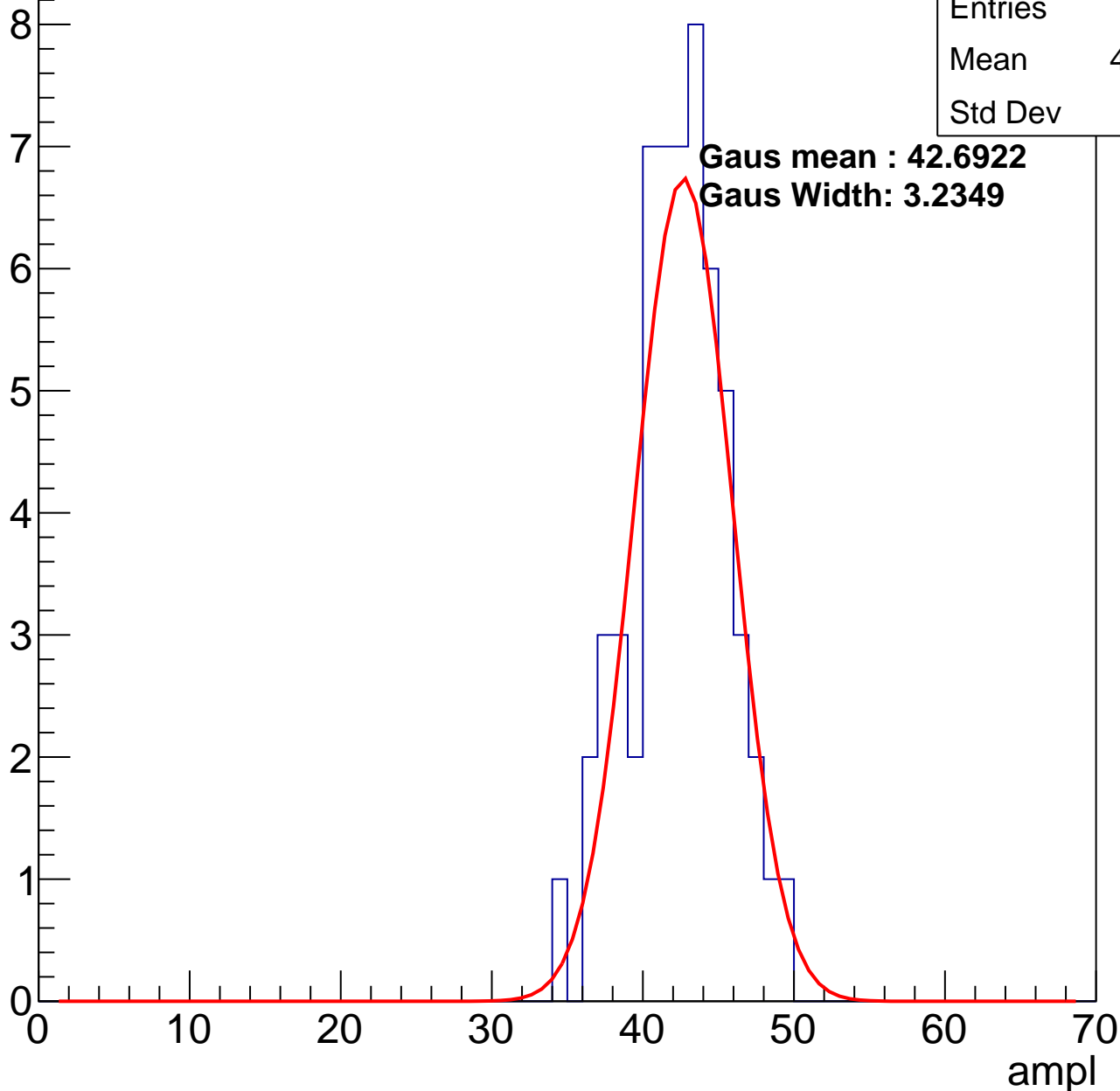
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.93
Std Dev	3.14

**Gaus mean : 42.6922**

**Gaus Width: 3.2349**

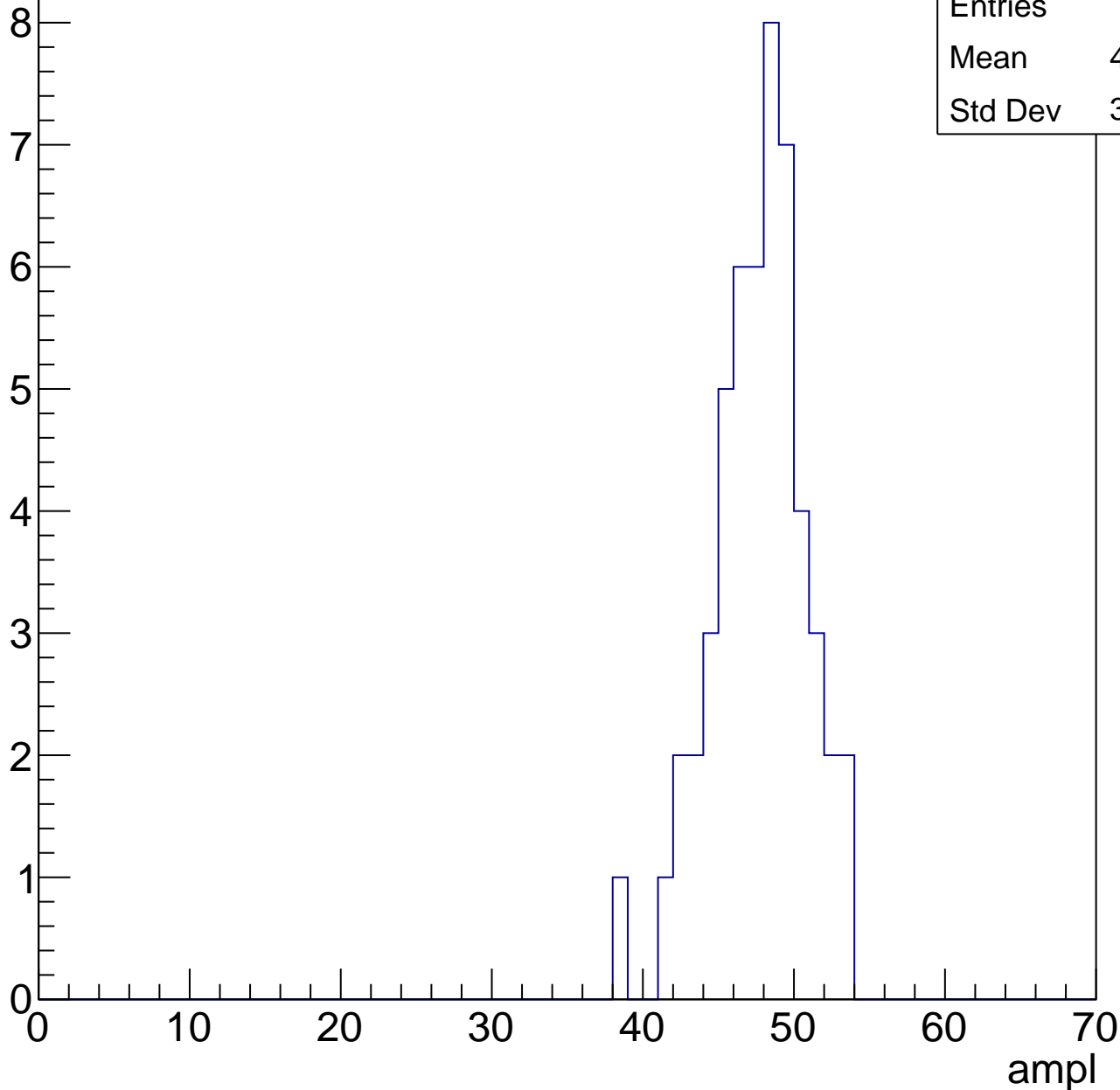


# B1L002S, U26-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	47.19
Std Dev	3.076

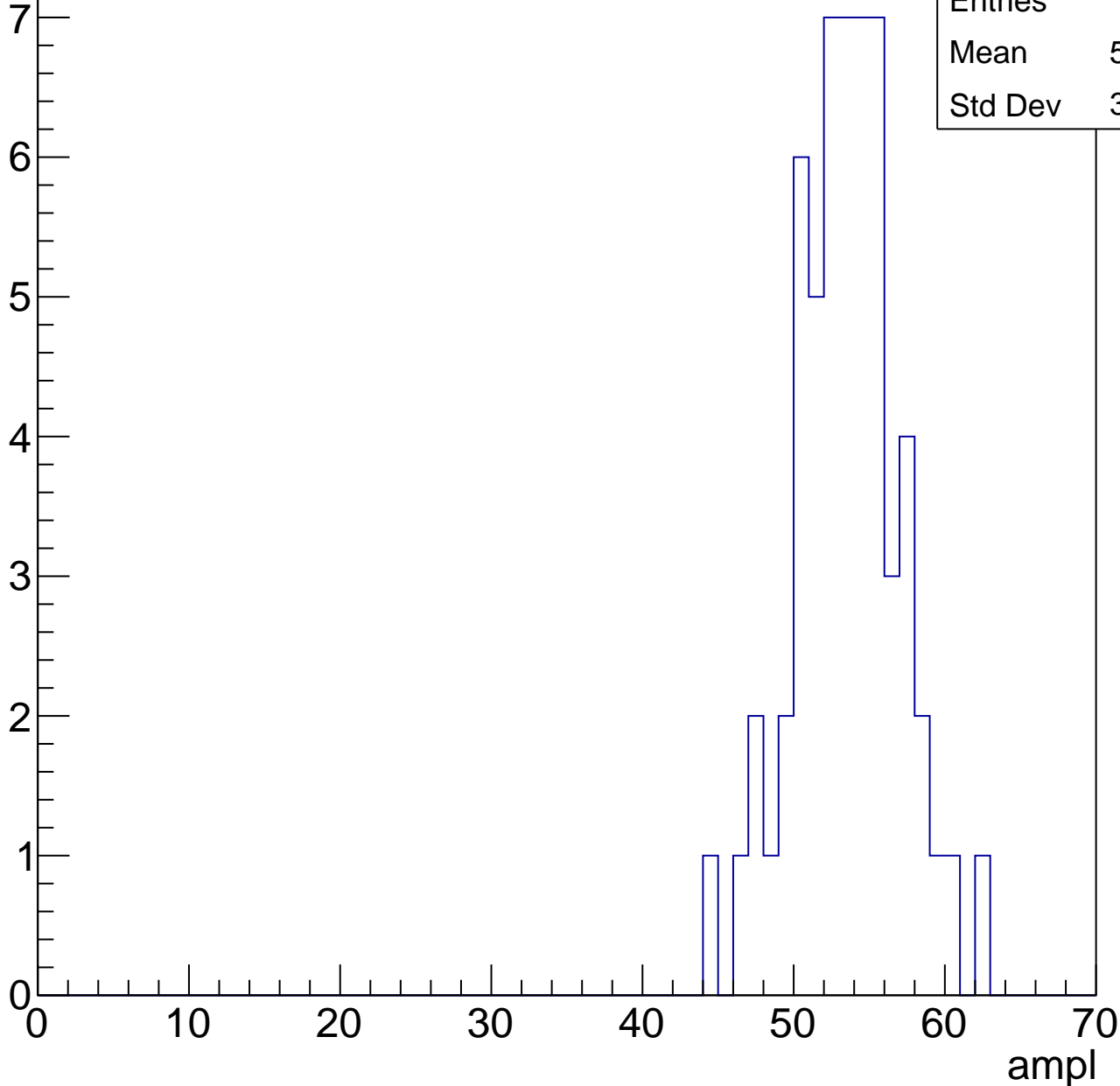


# B1L002S, U26-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.03
Std Dev	3.429

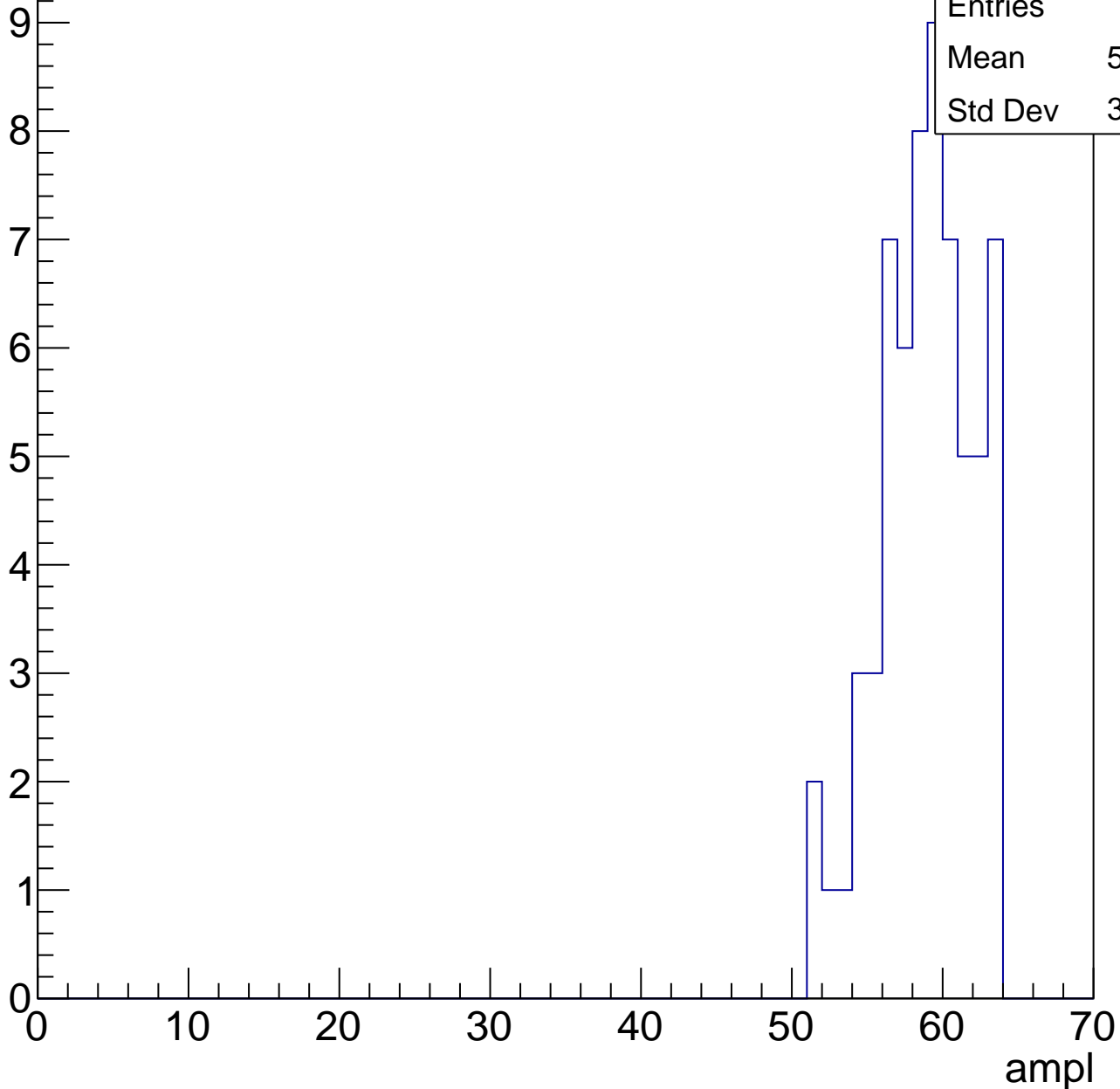


# B1L002S, U26-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	58.42
Std Dev	3.046

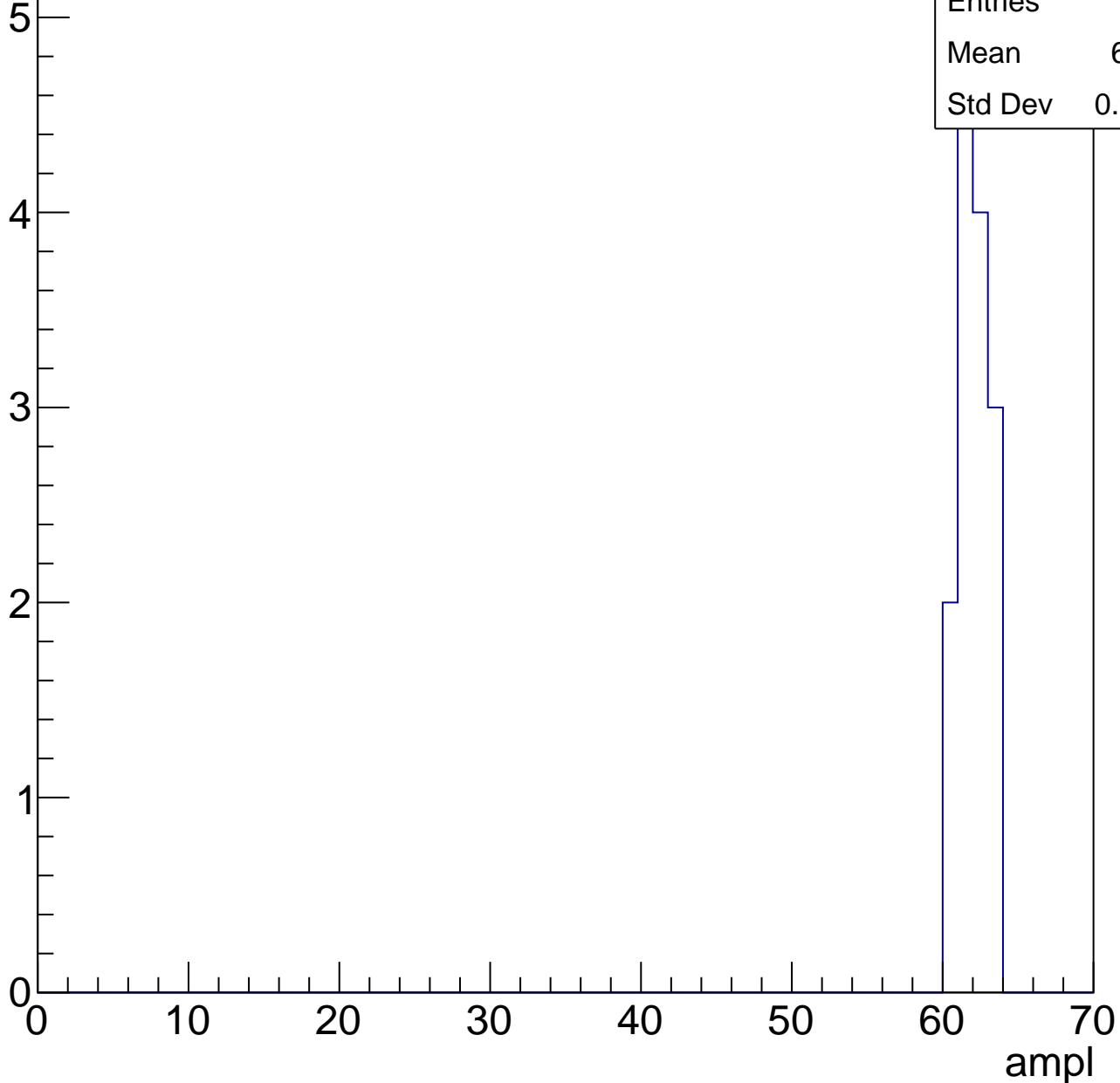


# B1L002S, U26-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.57
Std Dev	0.9794





# B1L002S, U26-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch50, adc0

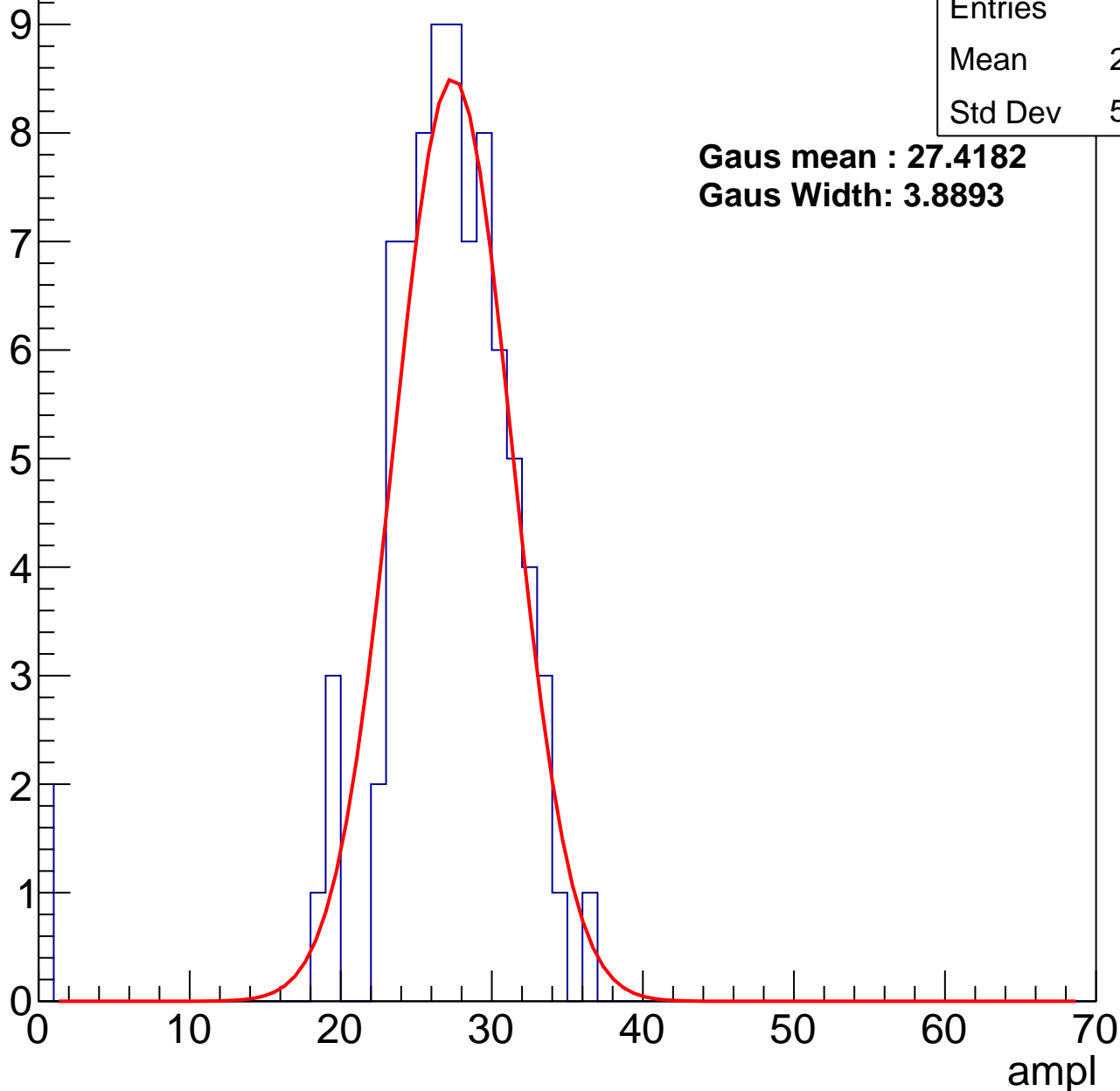
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	26.33
Std Dev	5.455

**Gaus mean : 27.4182**

**Gaus Width: 3.8893**



# B1L002S, U26-ch50, adc1

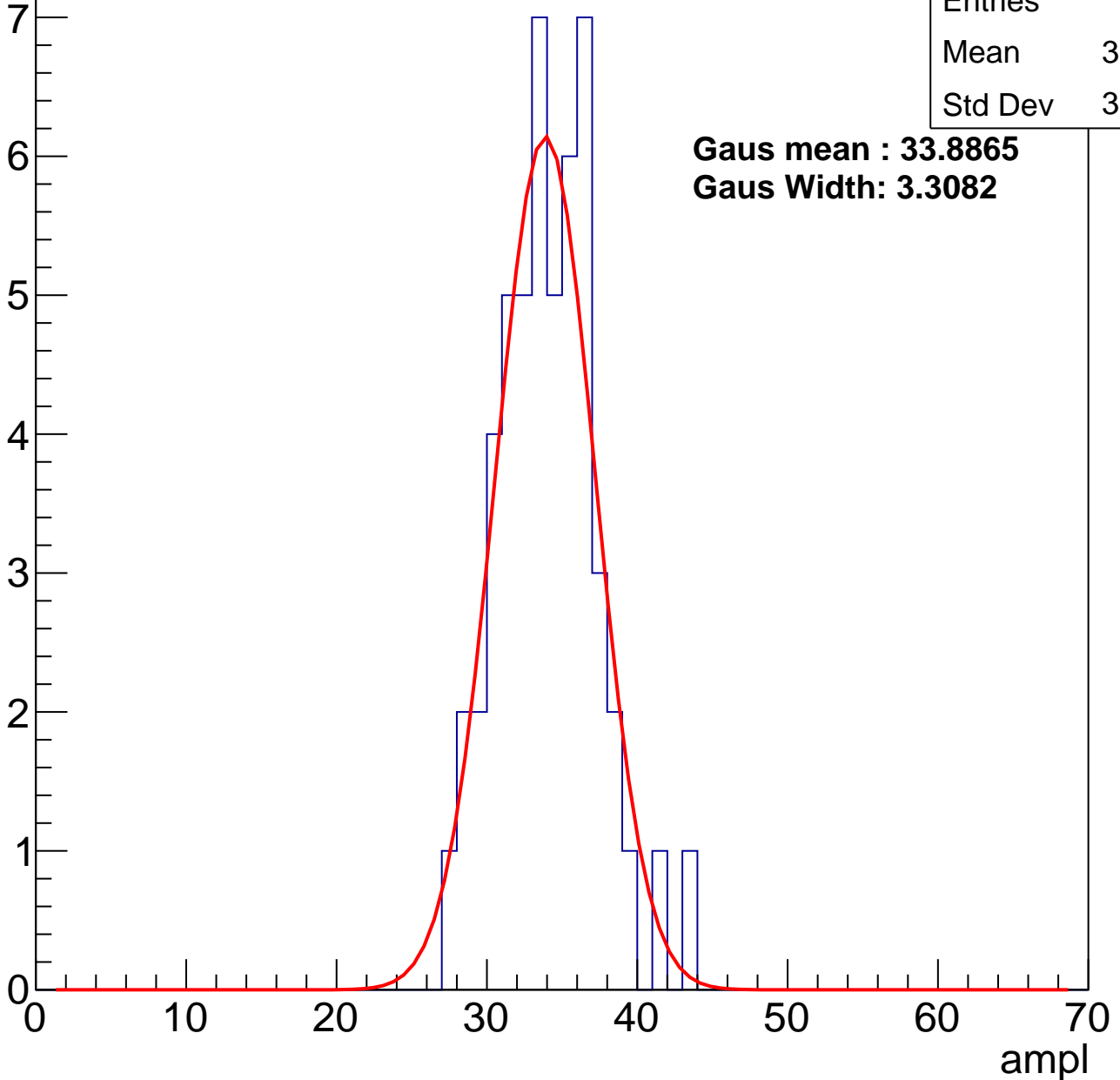
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.63
Std Dev	3.246

**Gaus mean : 33.8865**

**Gaus Width: 3.3082**



# B1L002S, U26-ch50, adc2

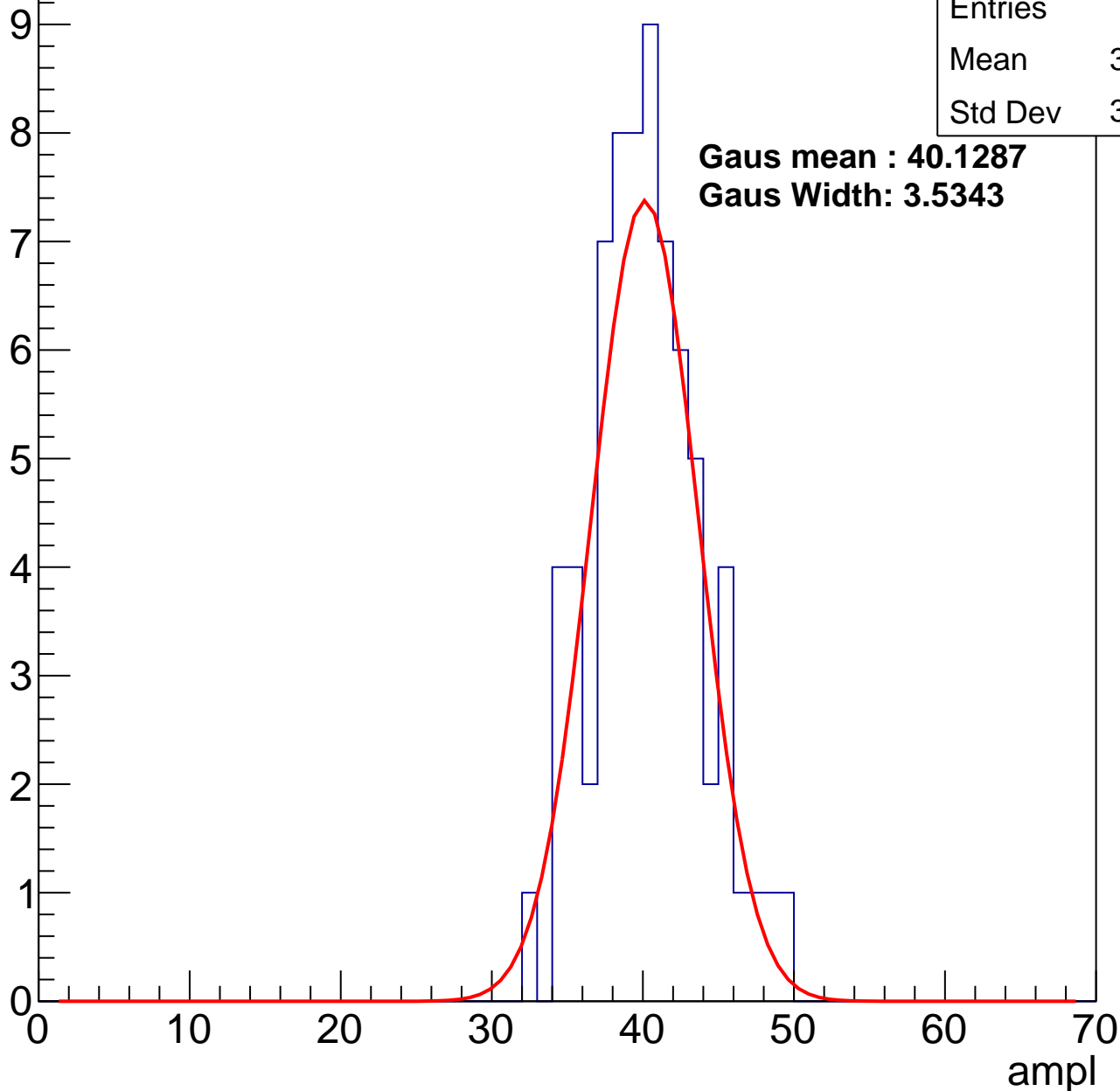
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	39.82
Std Dev	3.518

**Gaus mean : 40.1287**

**Gaus Width: 3.5343**

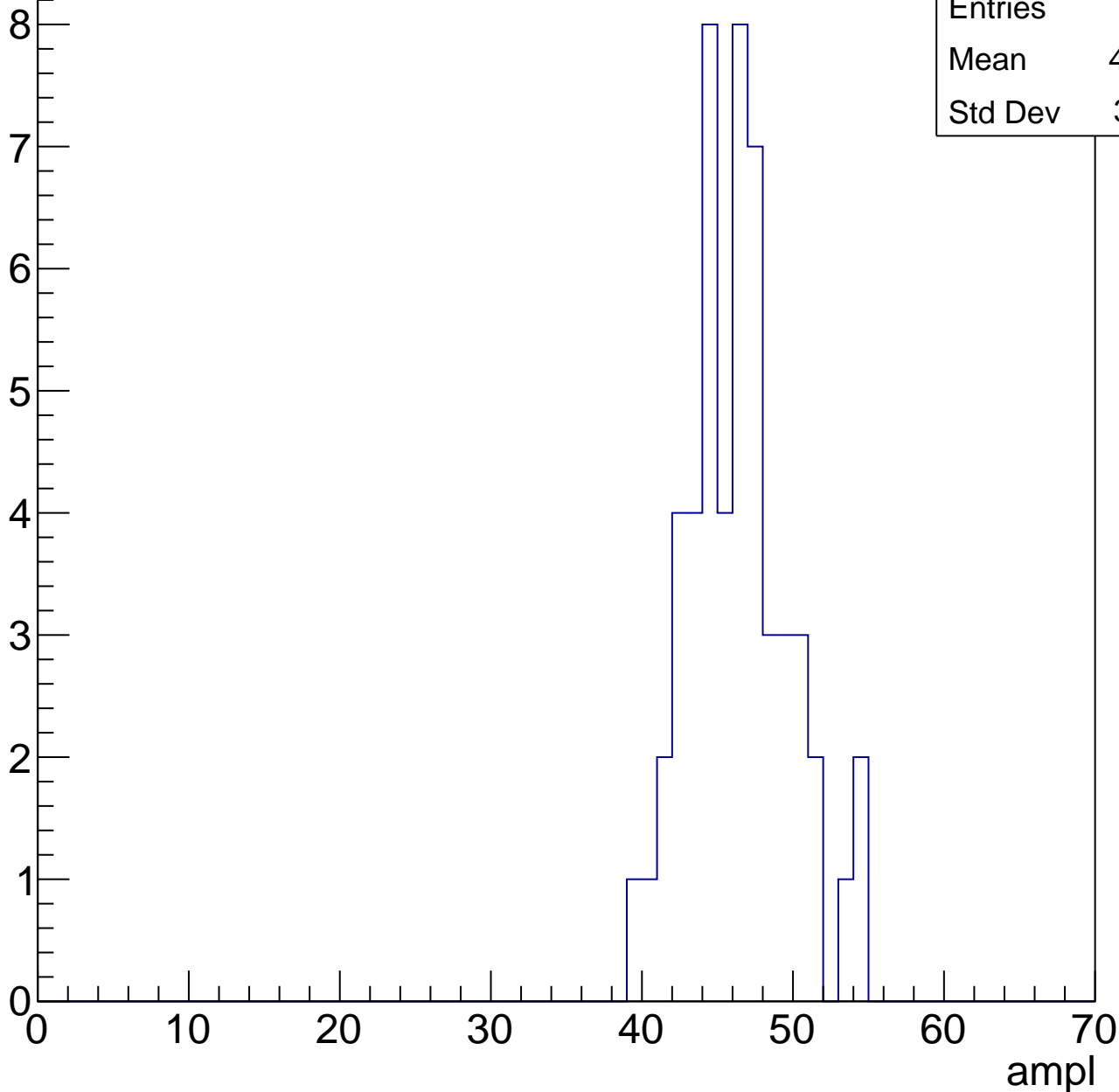


# B1L002S, U26-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	45.92
Std Dev	3.341

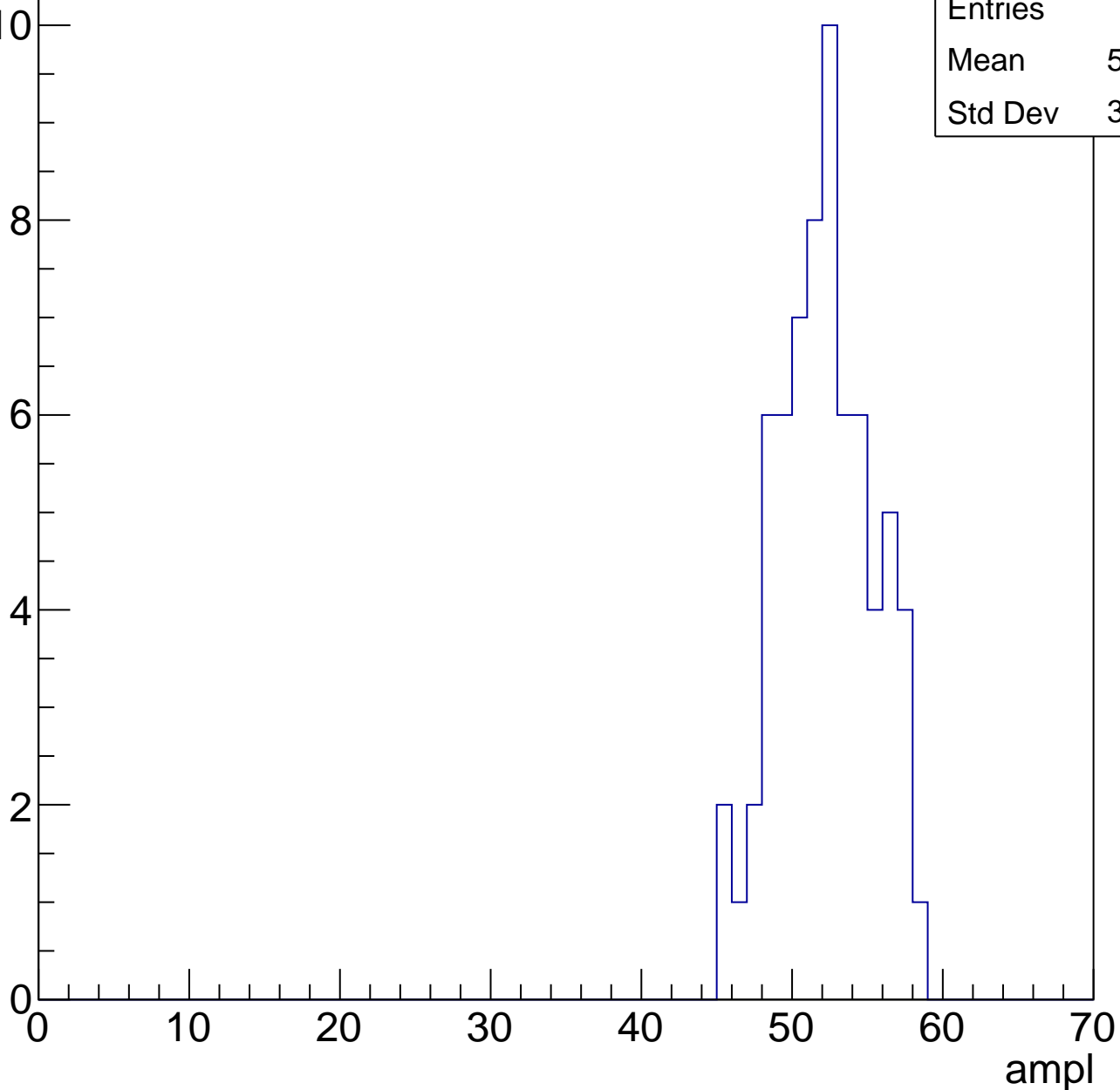


# B1L002S, U26-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	51.74
Std Dev	3.085



# B1L002S, U26-ch50, adc5

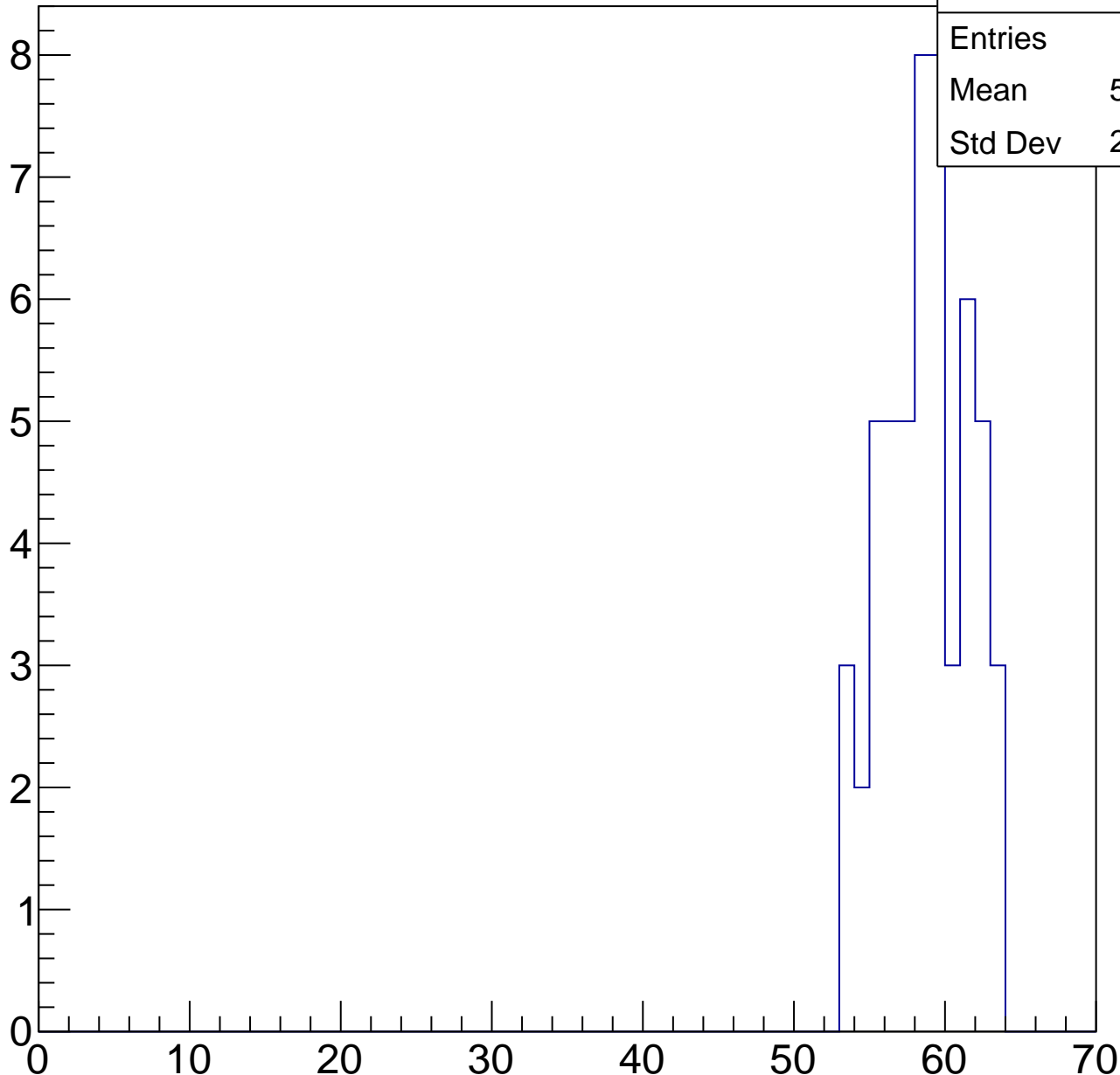
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	58.26
Std Dev	2.755

ampl



# B1L002S, U26-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

6

5

4

3

2

1

0

Entries	20
Mean	57.2
Std Dev	13.26

ampl

10

20

30

40

50

60

70

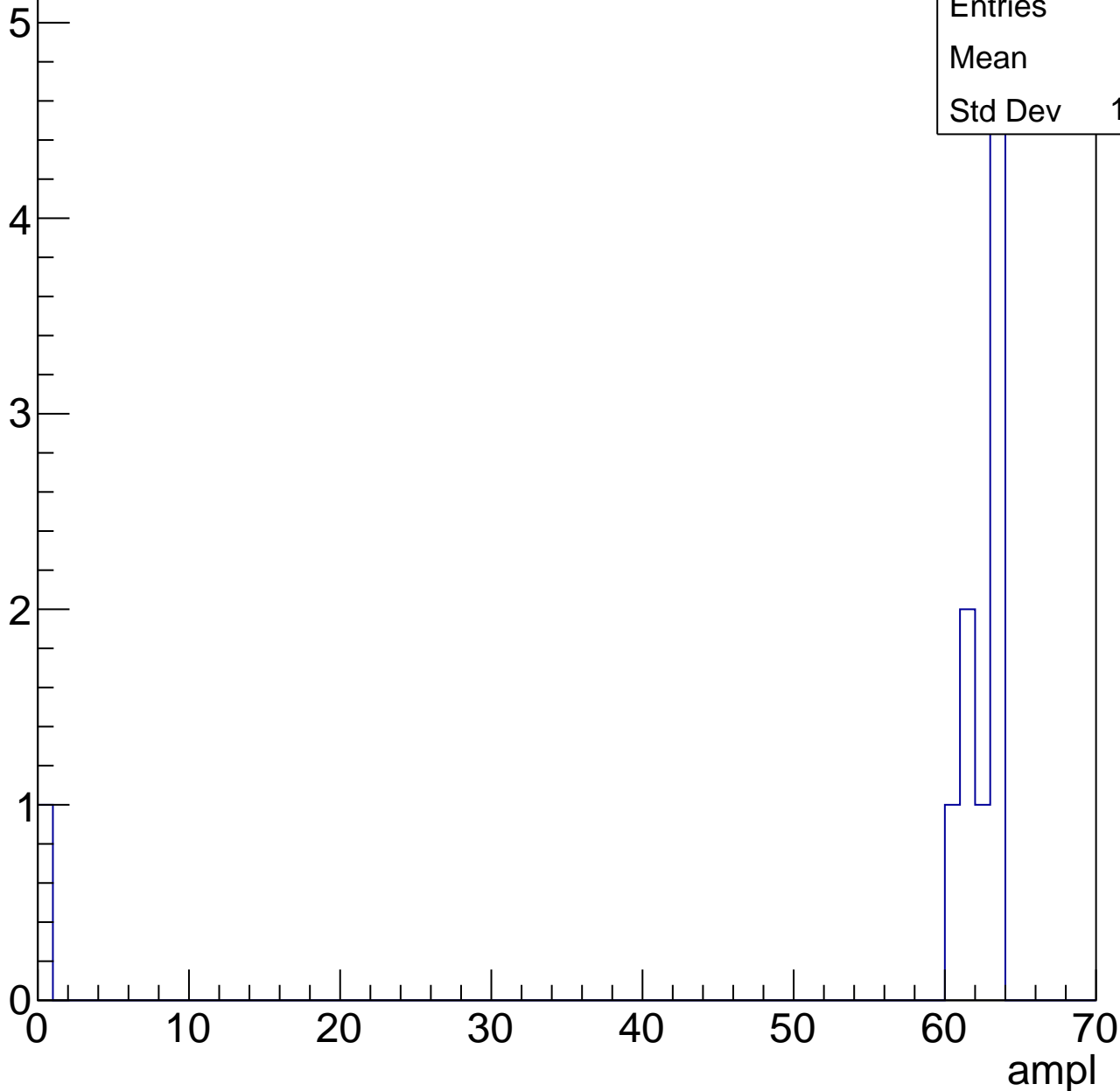


# B1L002S, U26-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	10
Mean	55.9
Std Dev	18.66



# B1L002S, U26-ch51, adc0

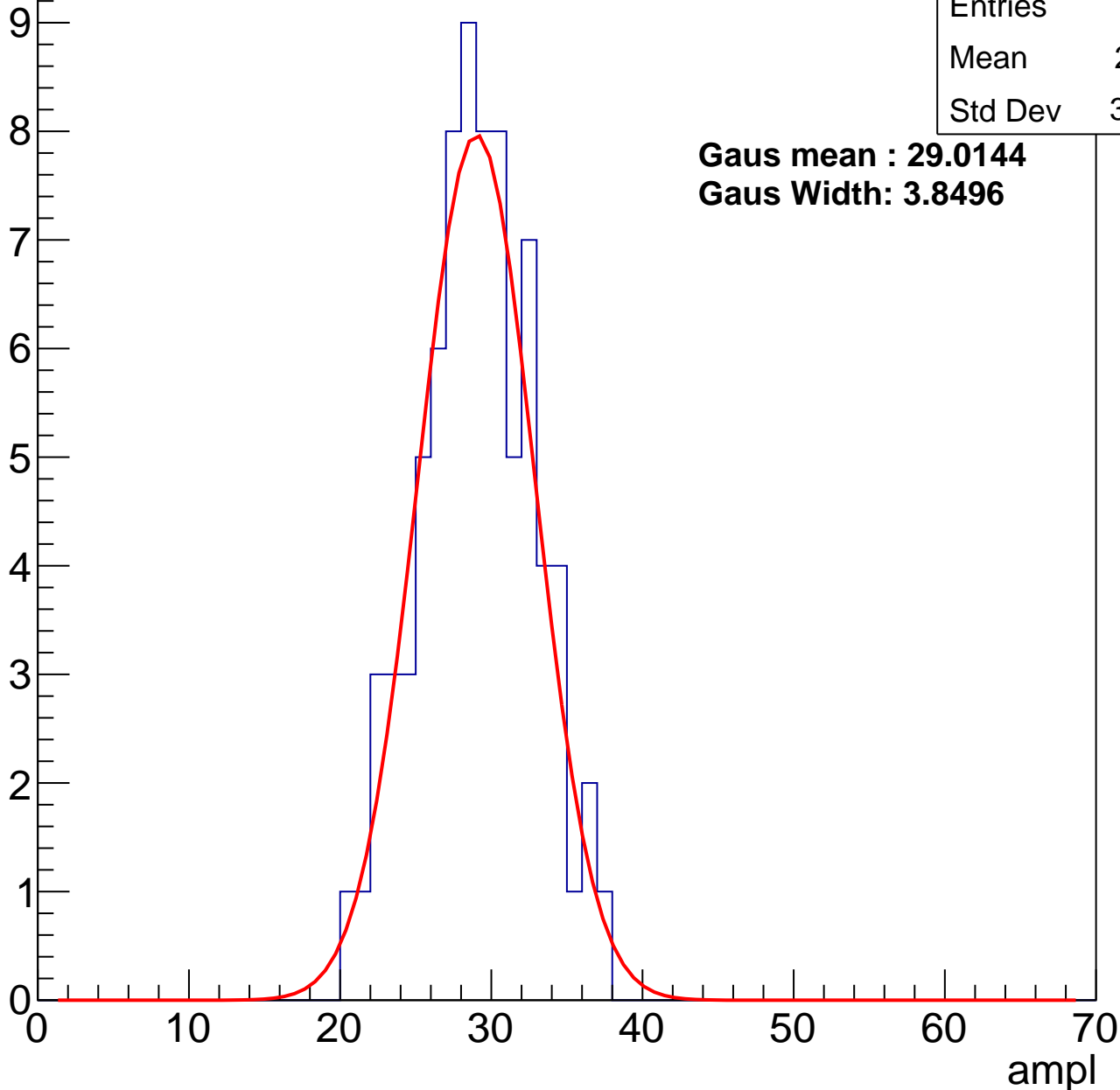
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	28.61
Std Dev	3.716

**Gaus mean : 29.0144**

**Gaus Width: 3.8496**



# B1L002S, U26-ch51, adc1

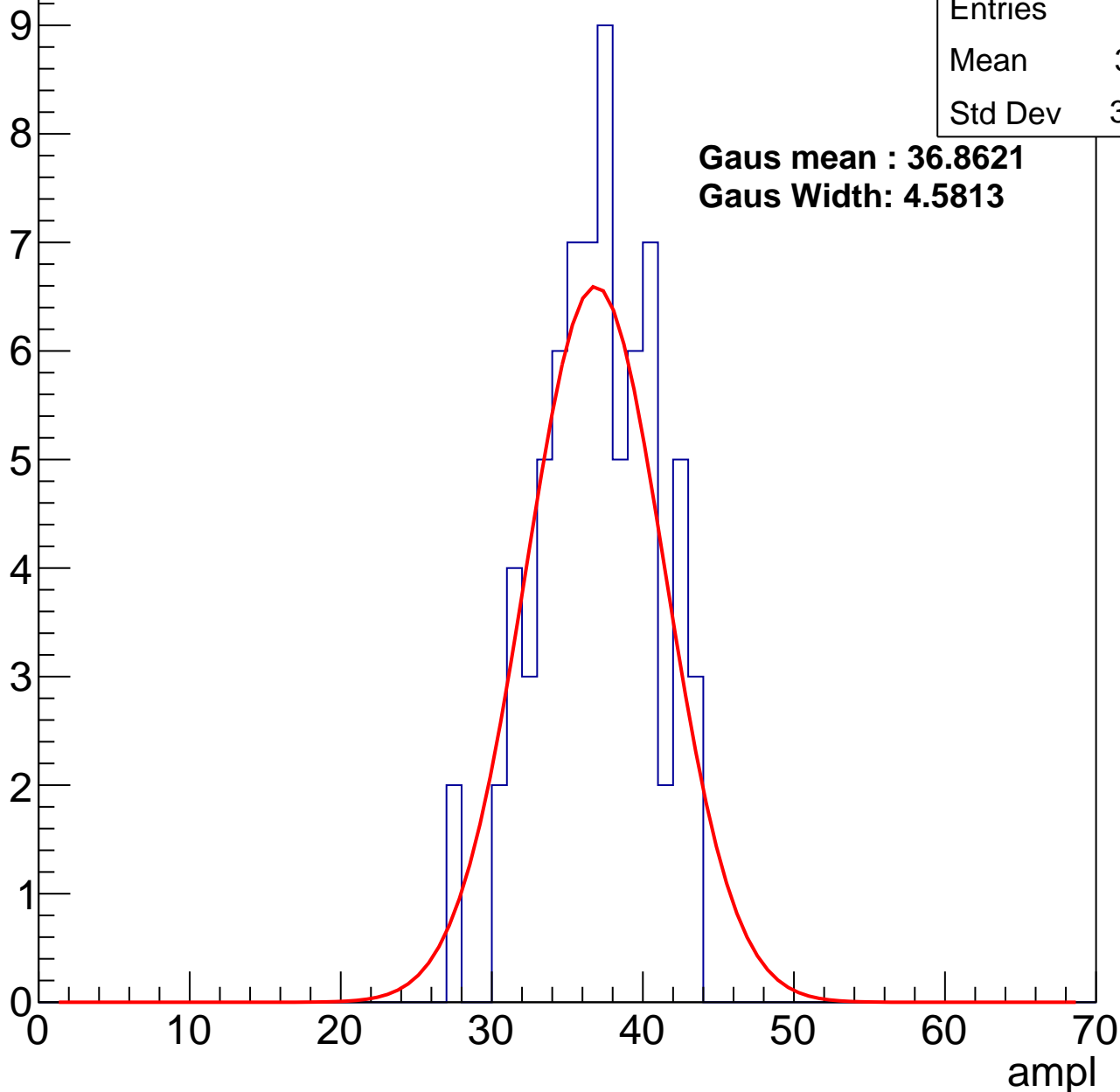
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	36.41
Std Dev	3.734

**Gaus mean : 36.8621**

**Gaus Width: 4.5813**



# B1L002S, U26-ch51, adc2

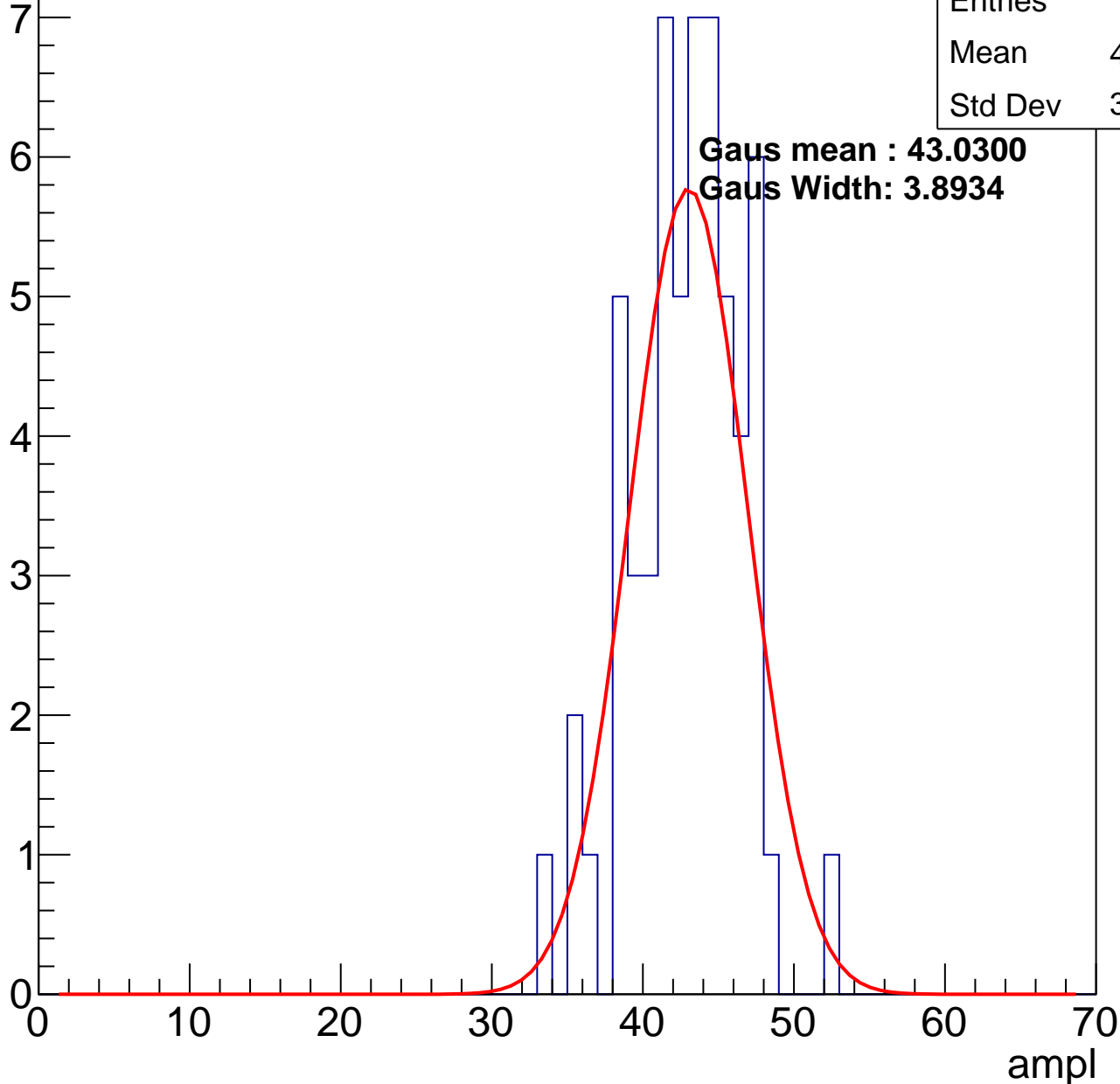
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	42.47
Std Dev	3.616

**Gaus mean : 43.0300**

**Gaus Width: 3.8934**

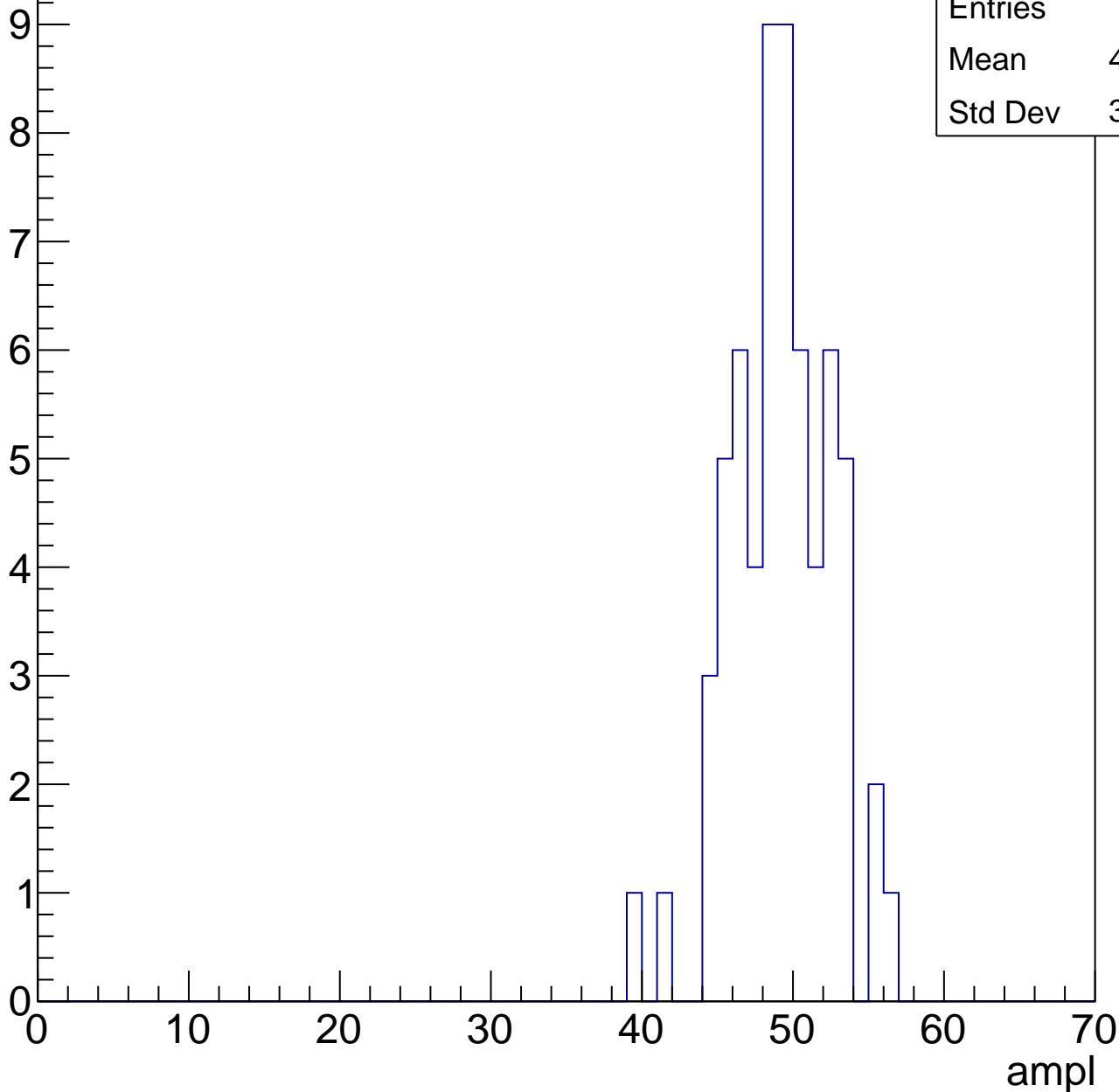


# B1L002S, U26-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

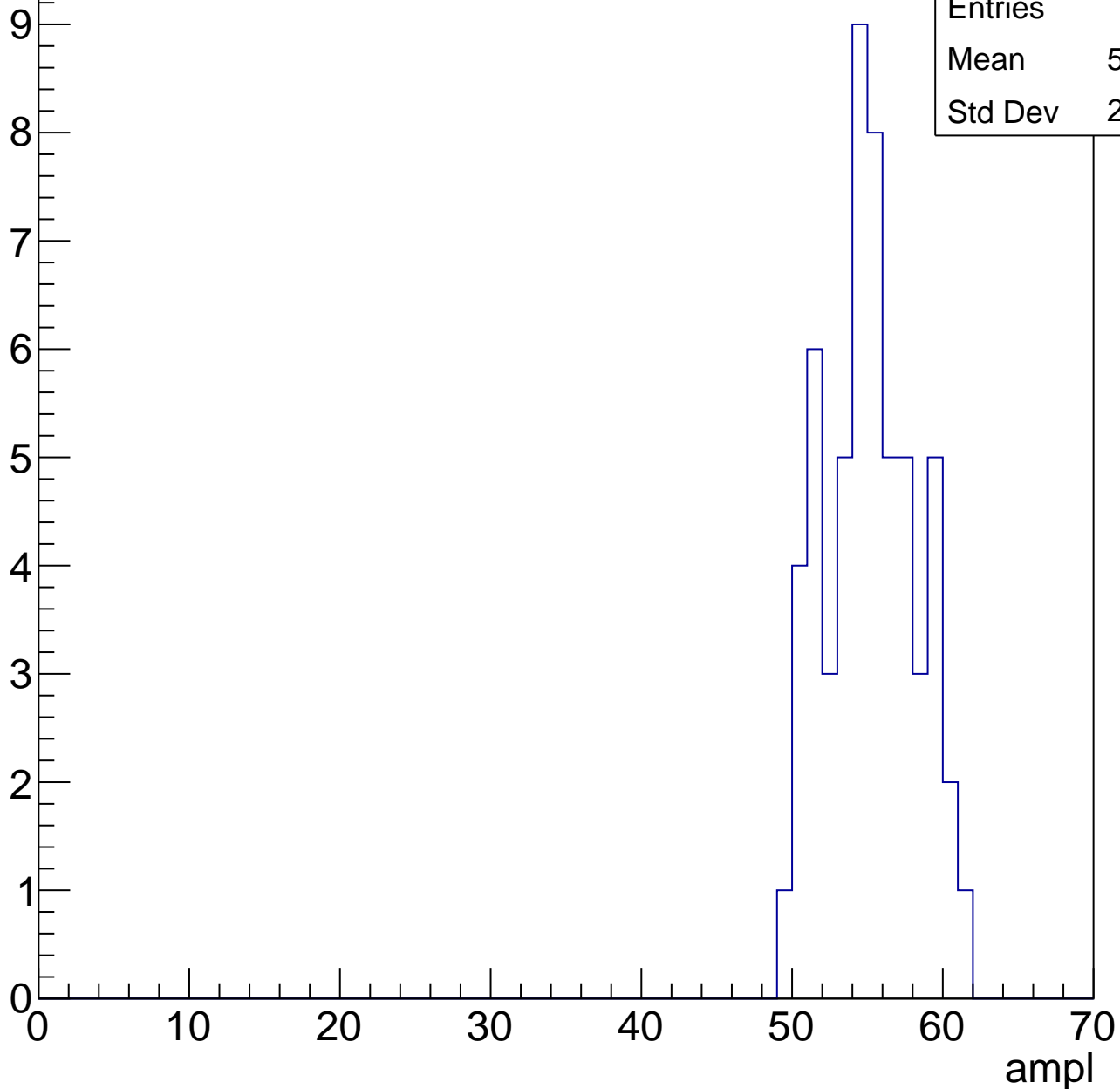
Entries	62
Mean	48.73
Std Dev	3.288



# B1L002S, U26-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



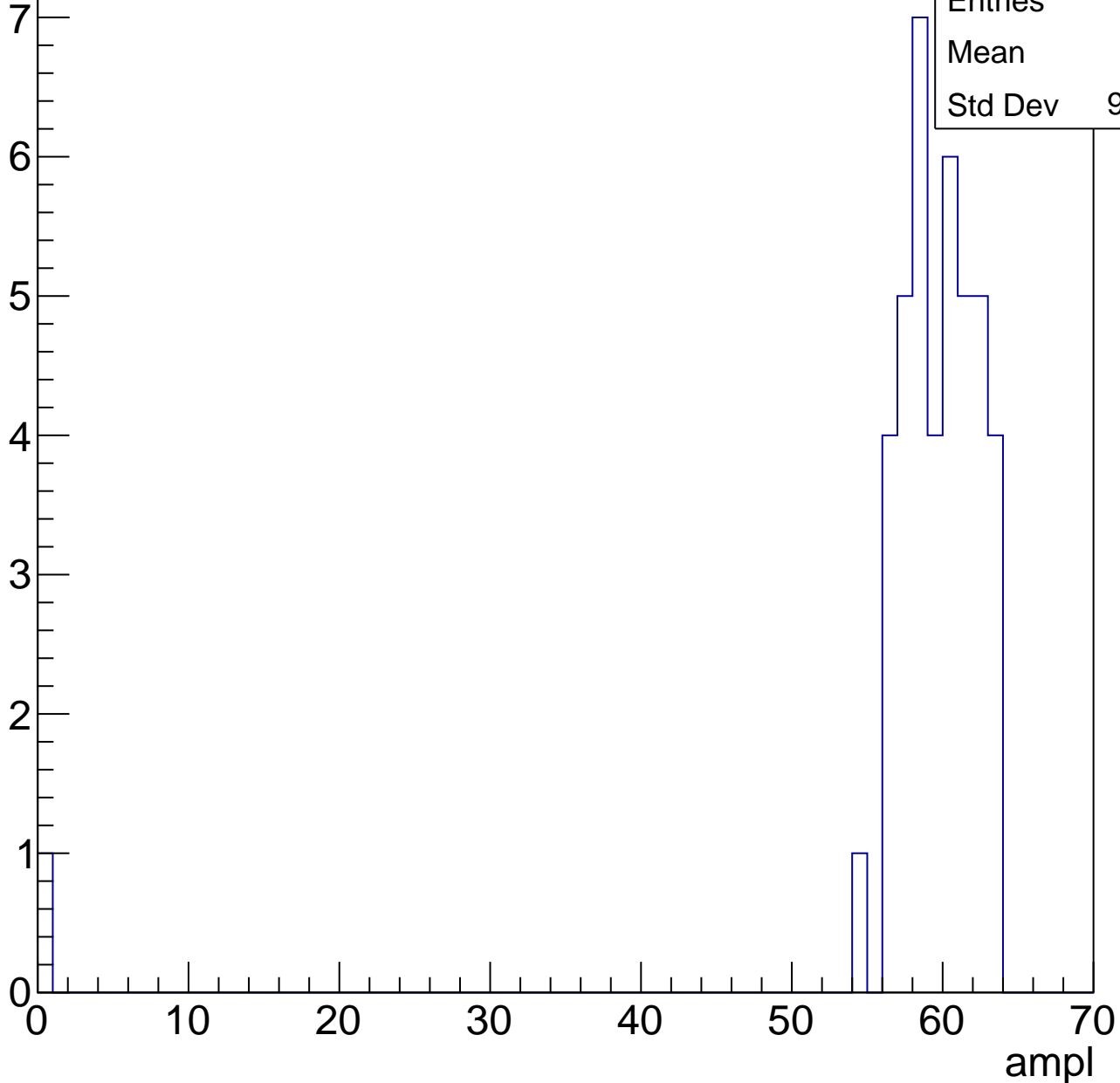
Entries	57
Mean	54.68
Std Dev	2.963

# B1L002S, U26-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	57.9
Std Dev	9.327

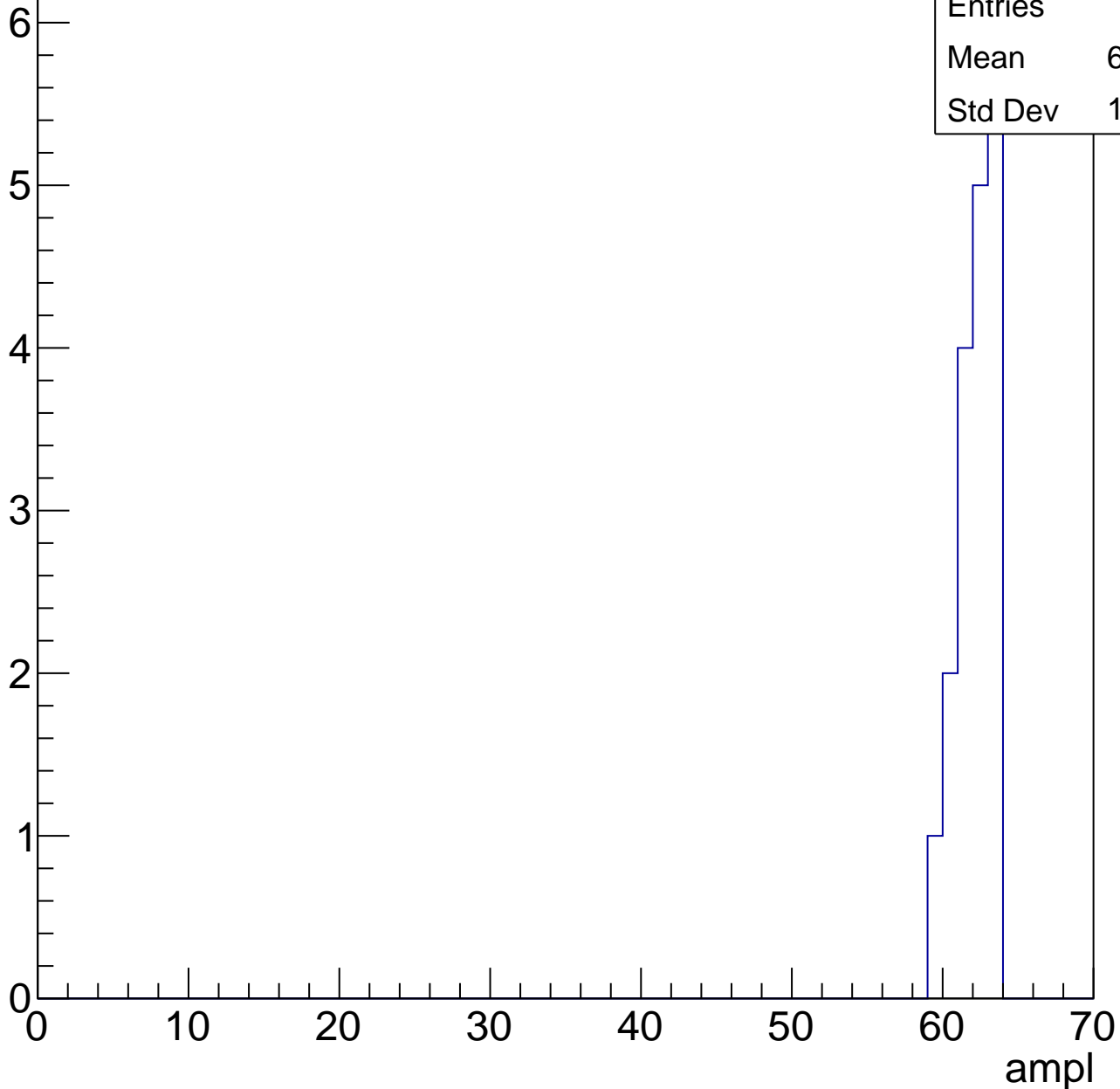


# B1L002S, U26-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.72
Std Dev	1.193





# B1L002S, U26-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch52, adc0

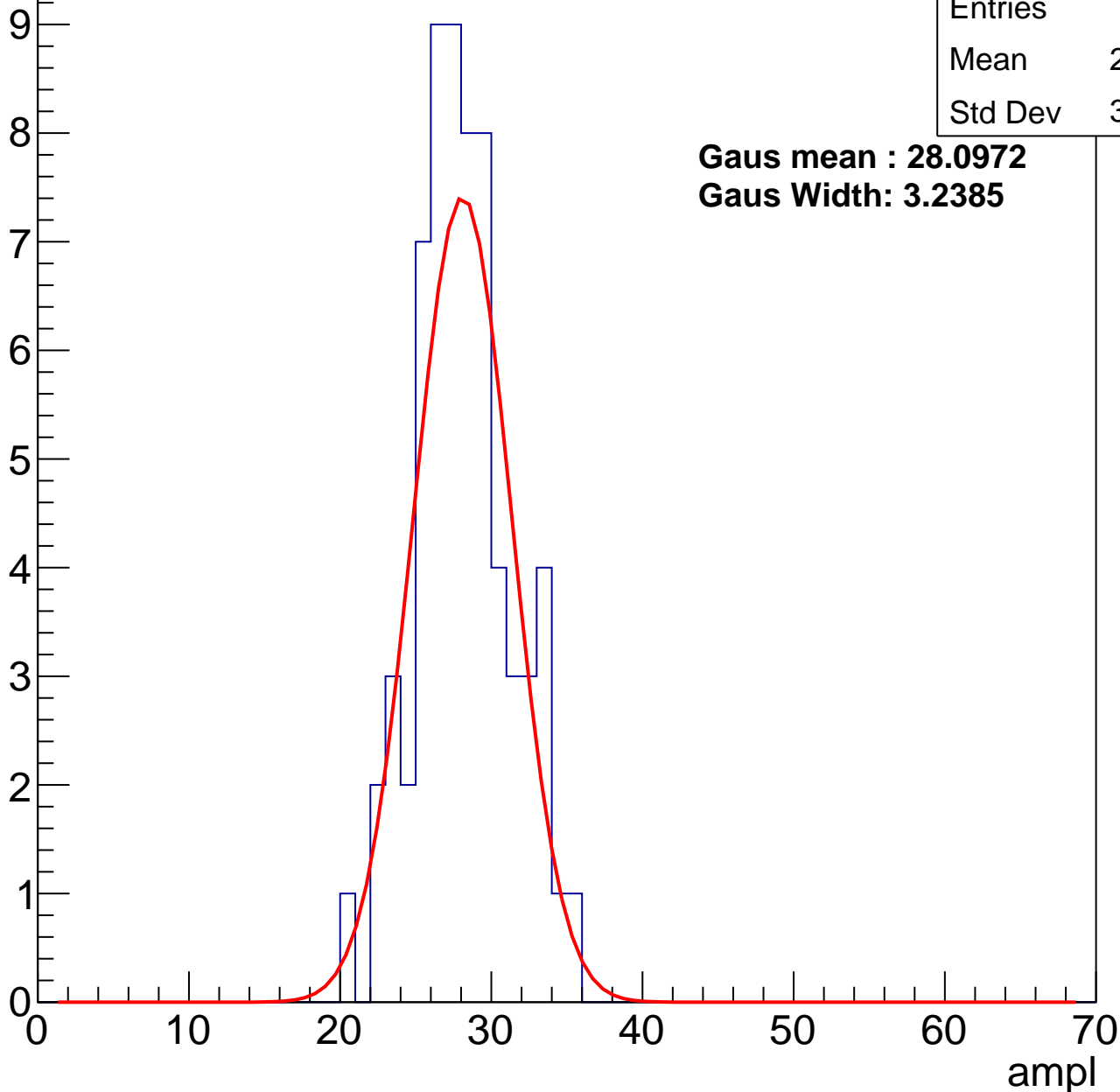
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.68
Std Dev	3.109

**Gaus mean : 28.0972**

**Gaus Width: 3.2385**



# B1L002S, U26-ch52, adc1

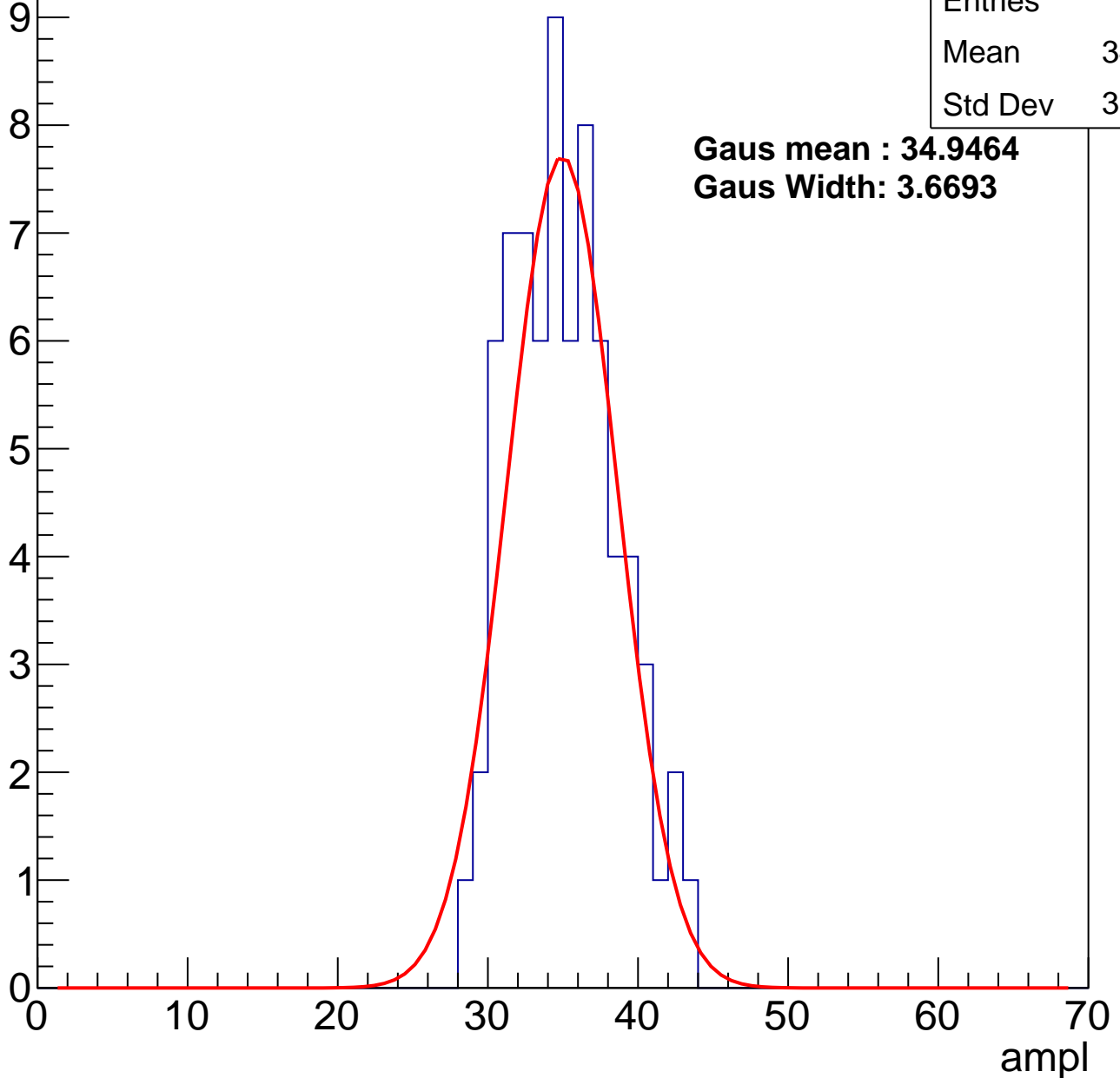
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34.62
Std Dev	3.459

**Gaus mean : 34.9464**

**Gaus Width: 3.6693**



# B1L002S, U26-ch52, adc2

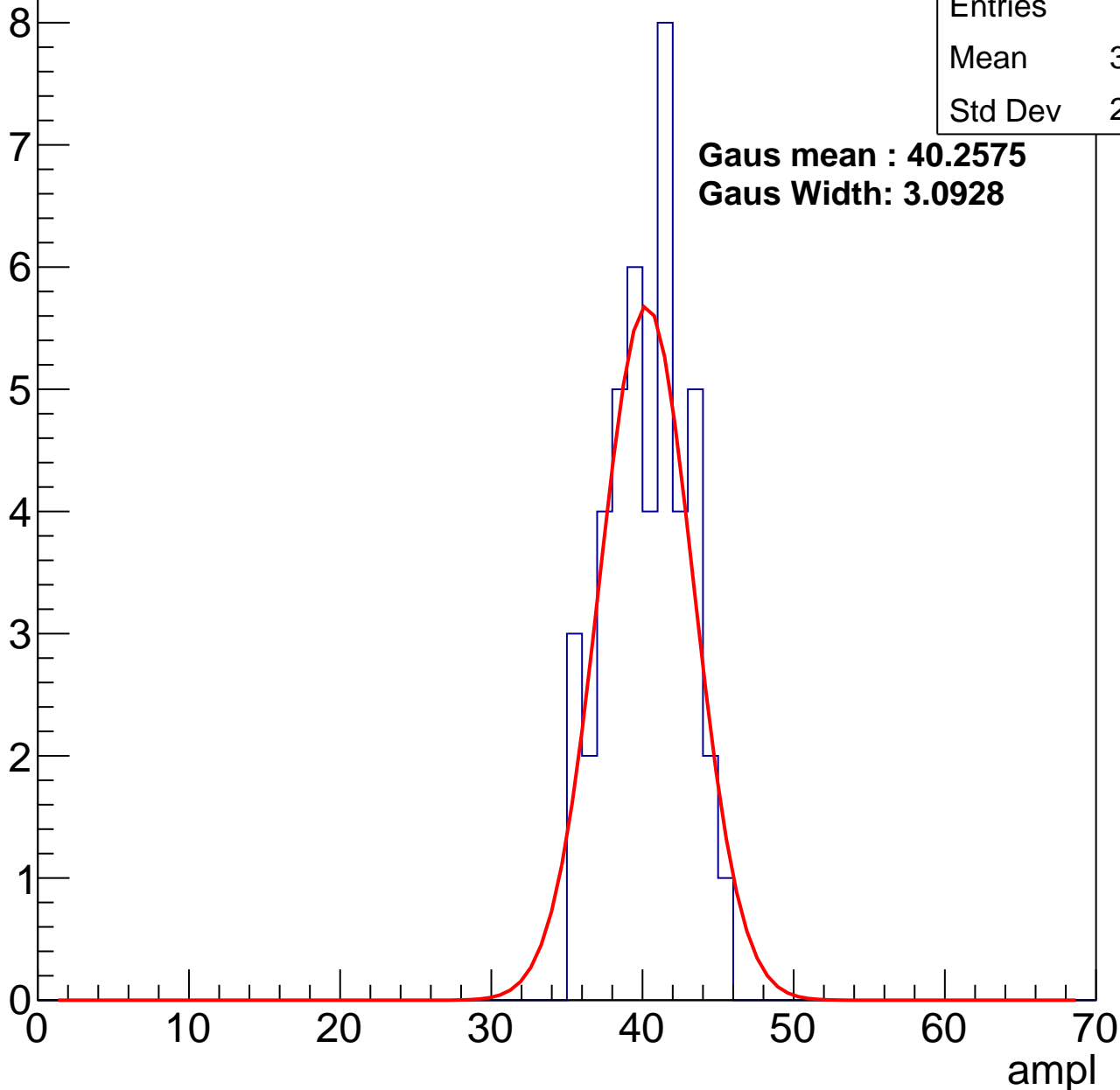
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	39.84
Std Dev	2.584

**Gaus mean : 40.2575**

**Gaus Width: 3.0928**



# B1L002S, U26-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

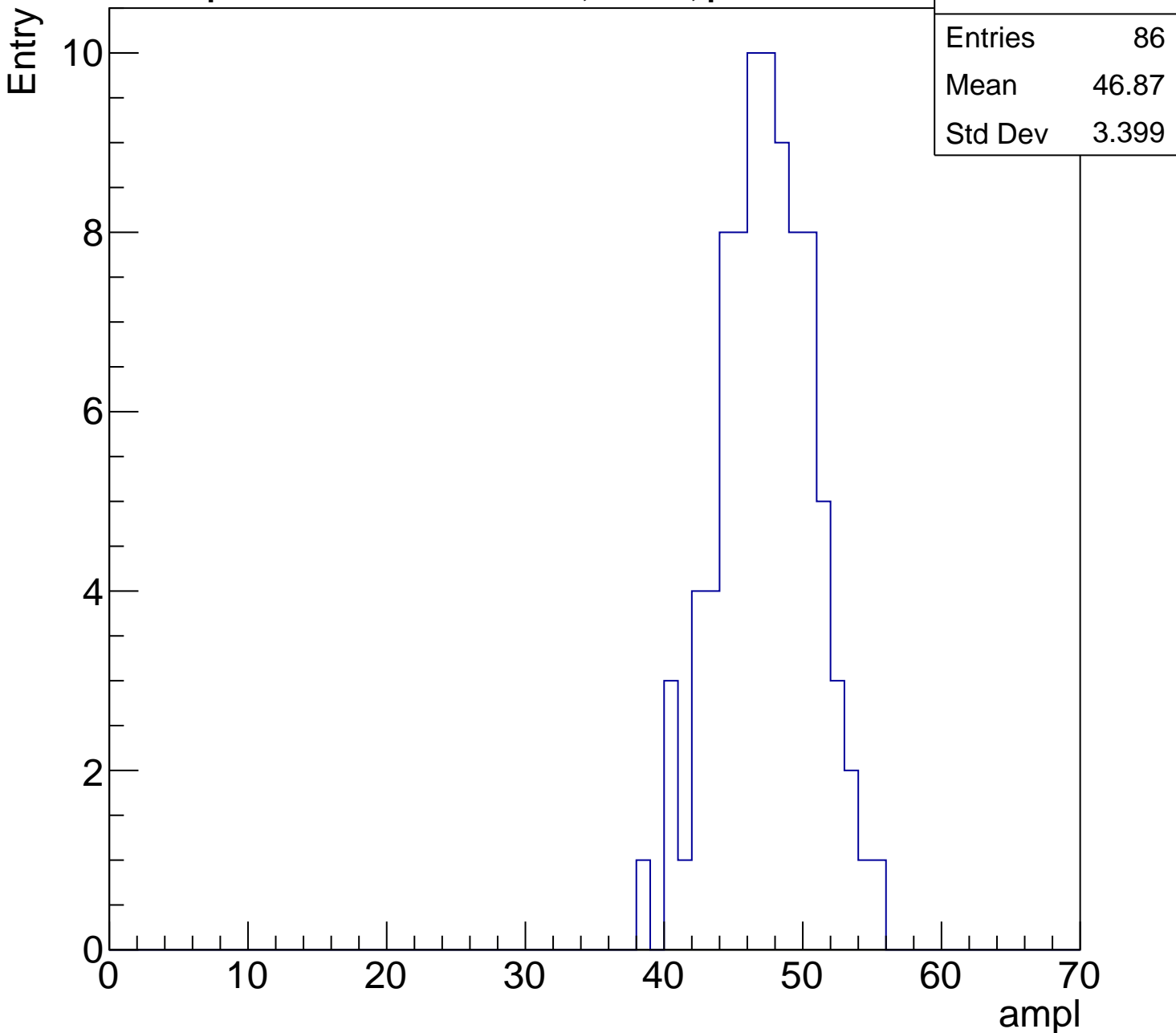
Entries	86
Mean	46.87
Std Dev	3.399

Entry

10  
8  
6  
4  
2  
0

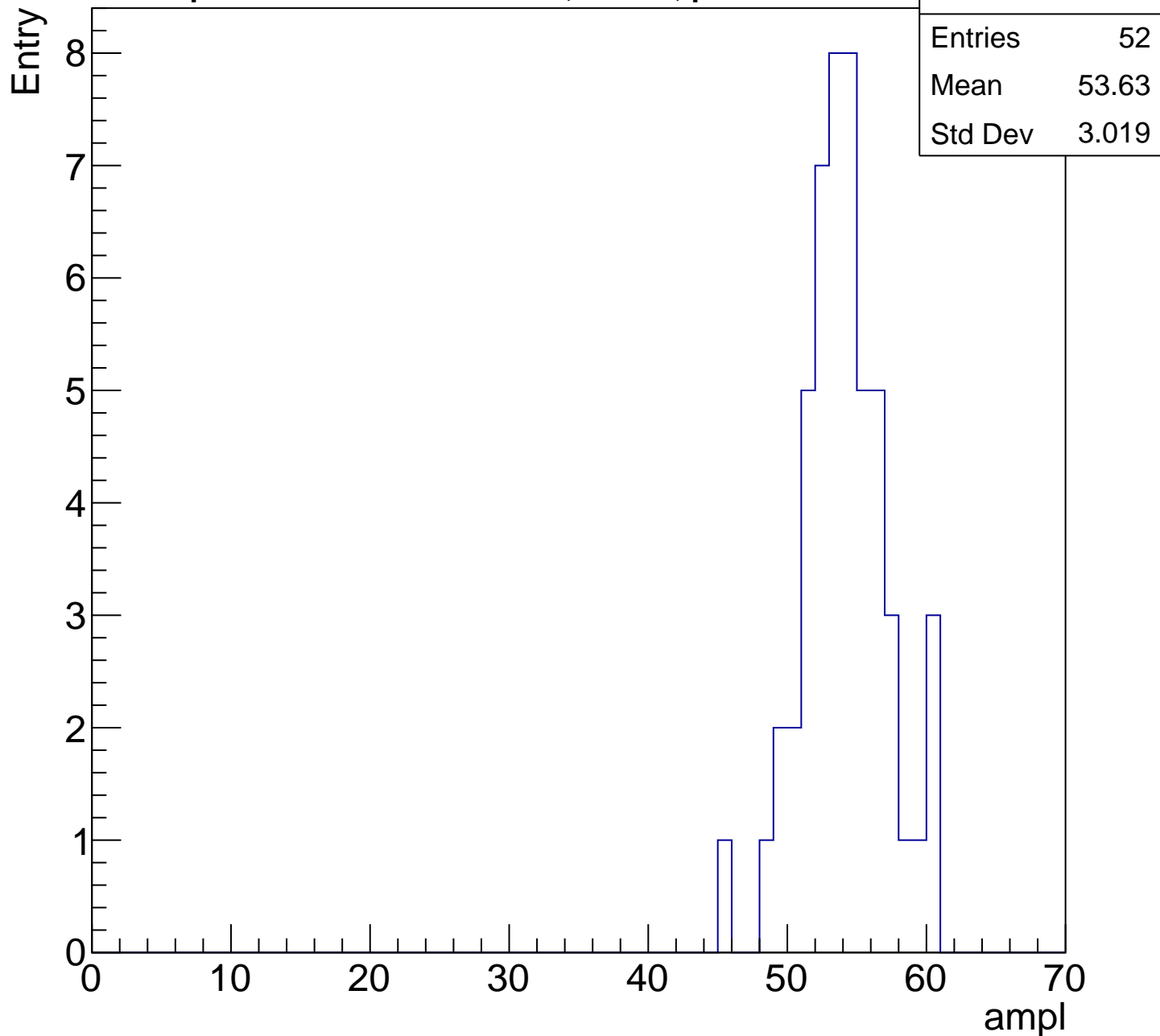
0 10 20 30 40 50 60 70

ampl



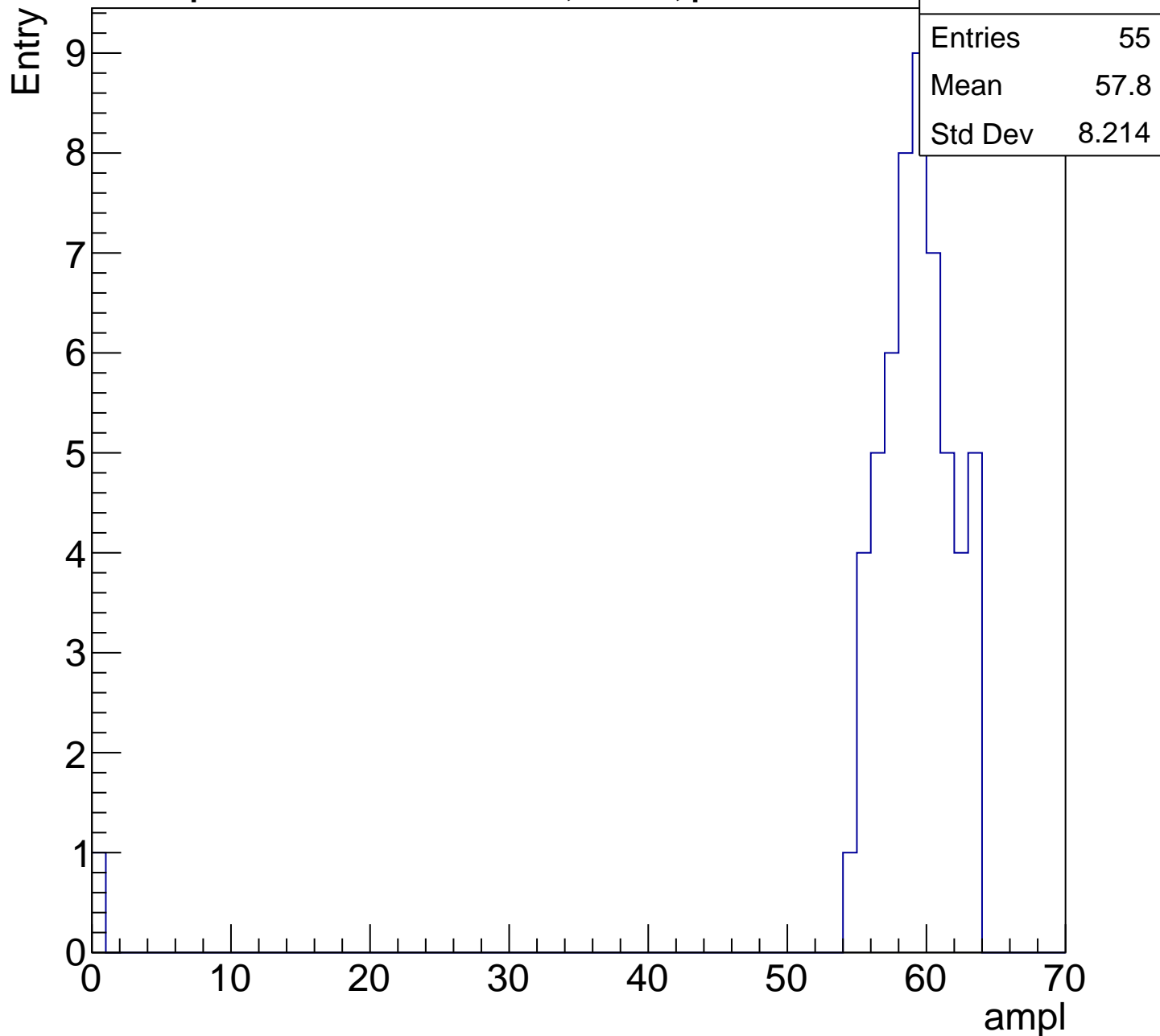
# B1L002S, U26-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

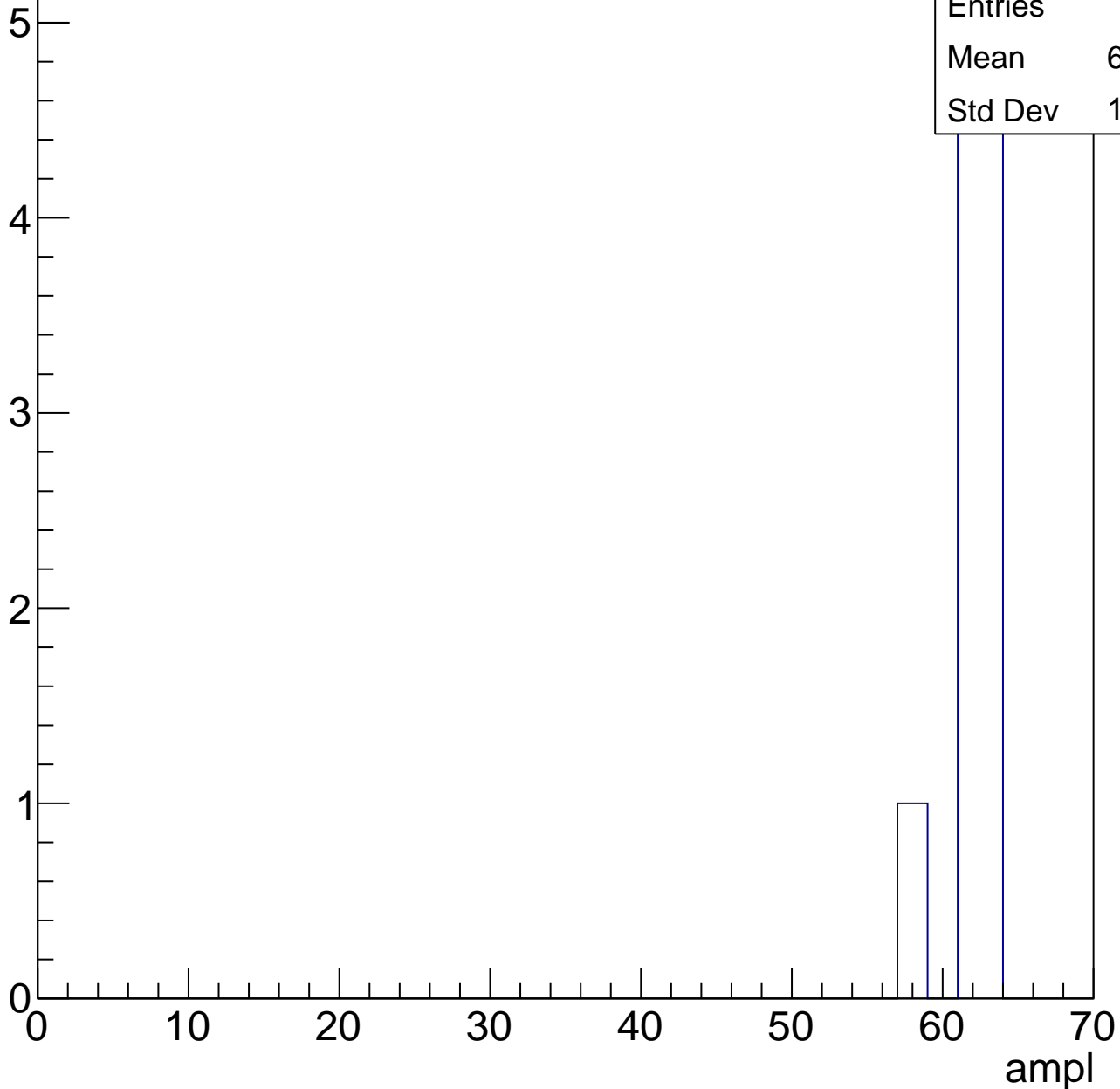


# B1L002S, U26-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.47
Std Dev	1.649





# B1L002S, U26-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch53, adc0

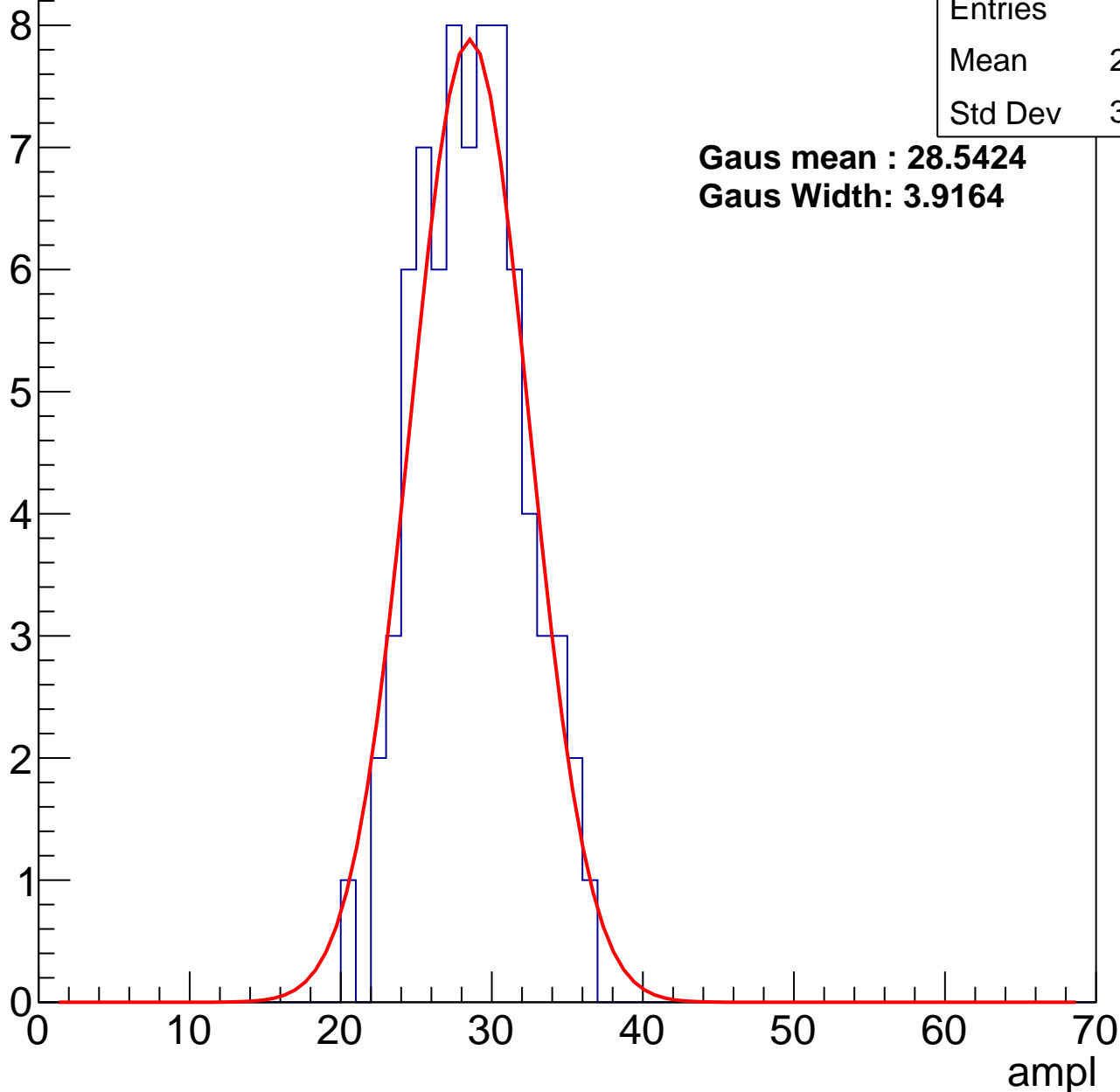
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	28.17
Std Dev	3.469

**Gaus mean : 28.5424**

**Gaus Width: 3.9164**



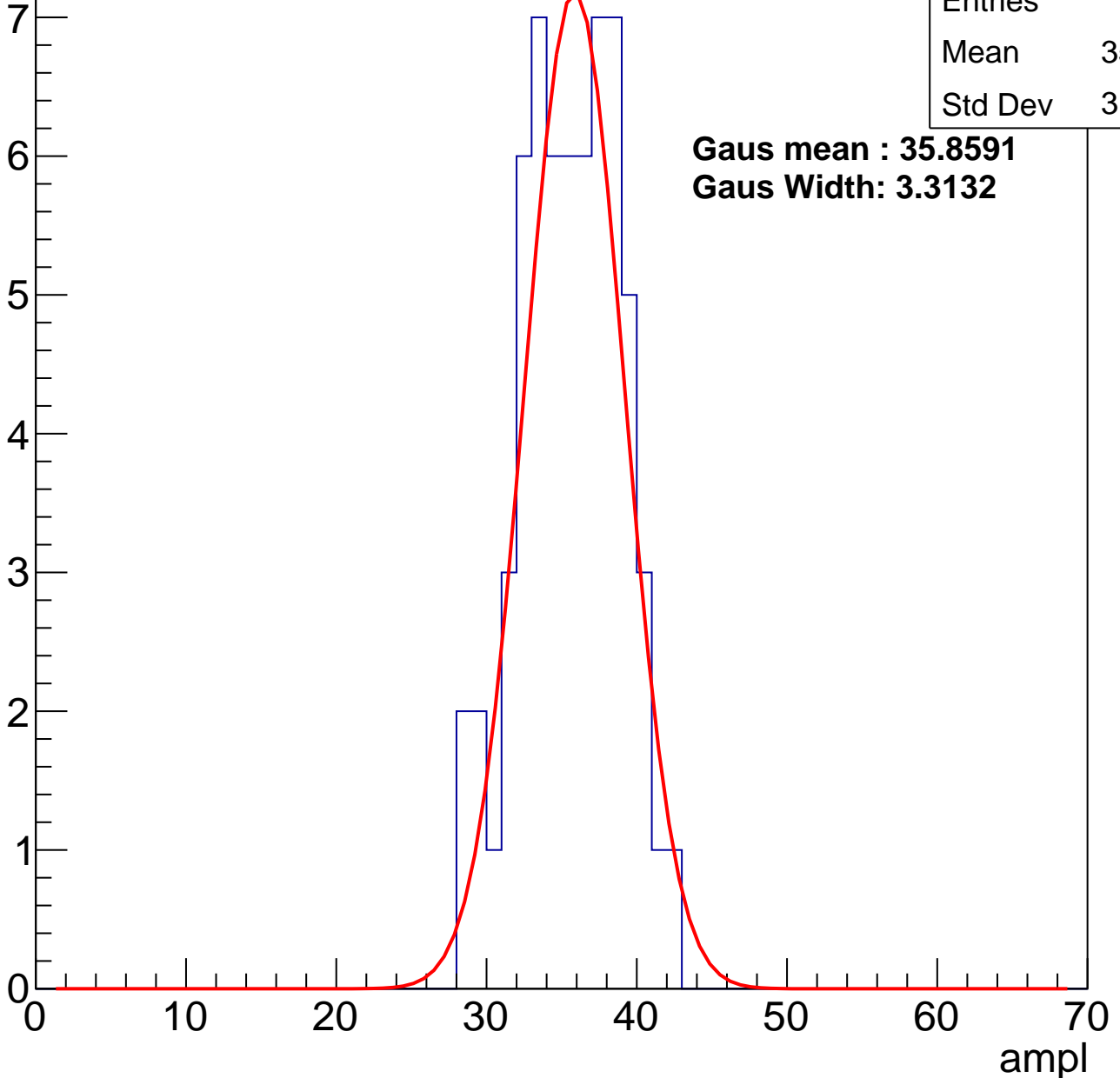
# B1L002S, U26-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.13
Std Dev	3.254

**Gaus mean : 35.8591**  
**Gaus Width: 3.3132**



# B1L002S, U26-ch53, adc2

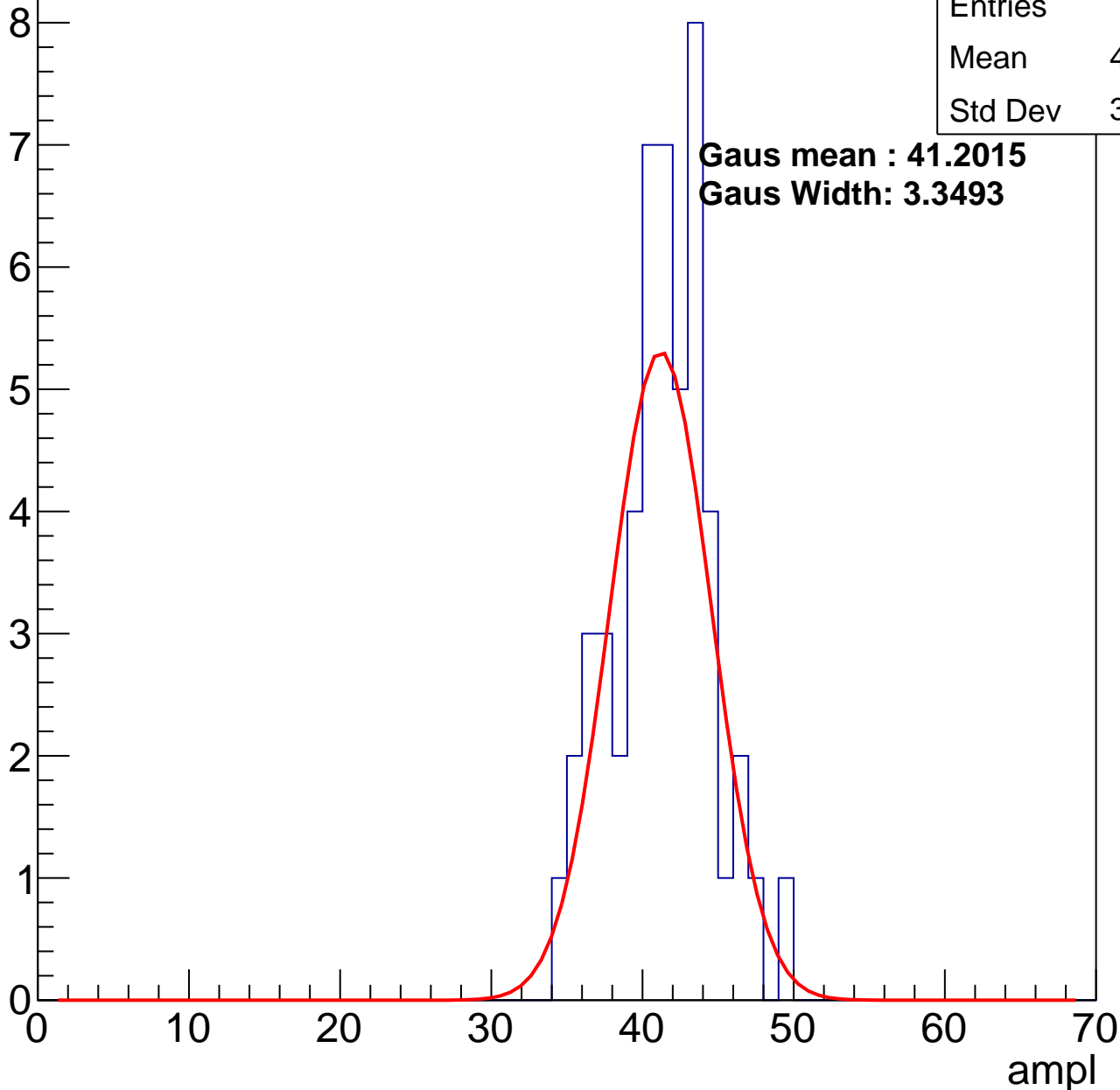
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.88
Std Dev	3.203

**Gaus mean : 41.2015**

**Gaus Width: 3.3493**

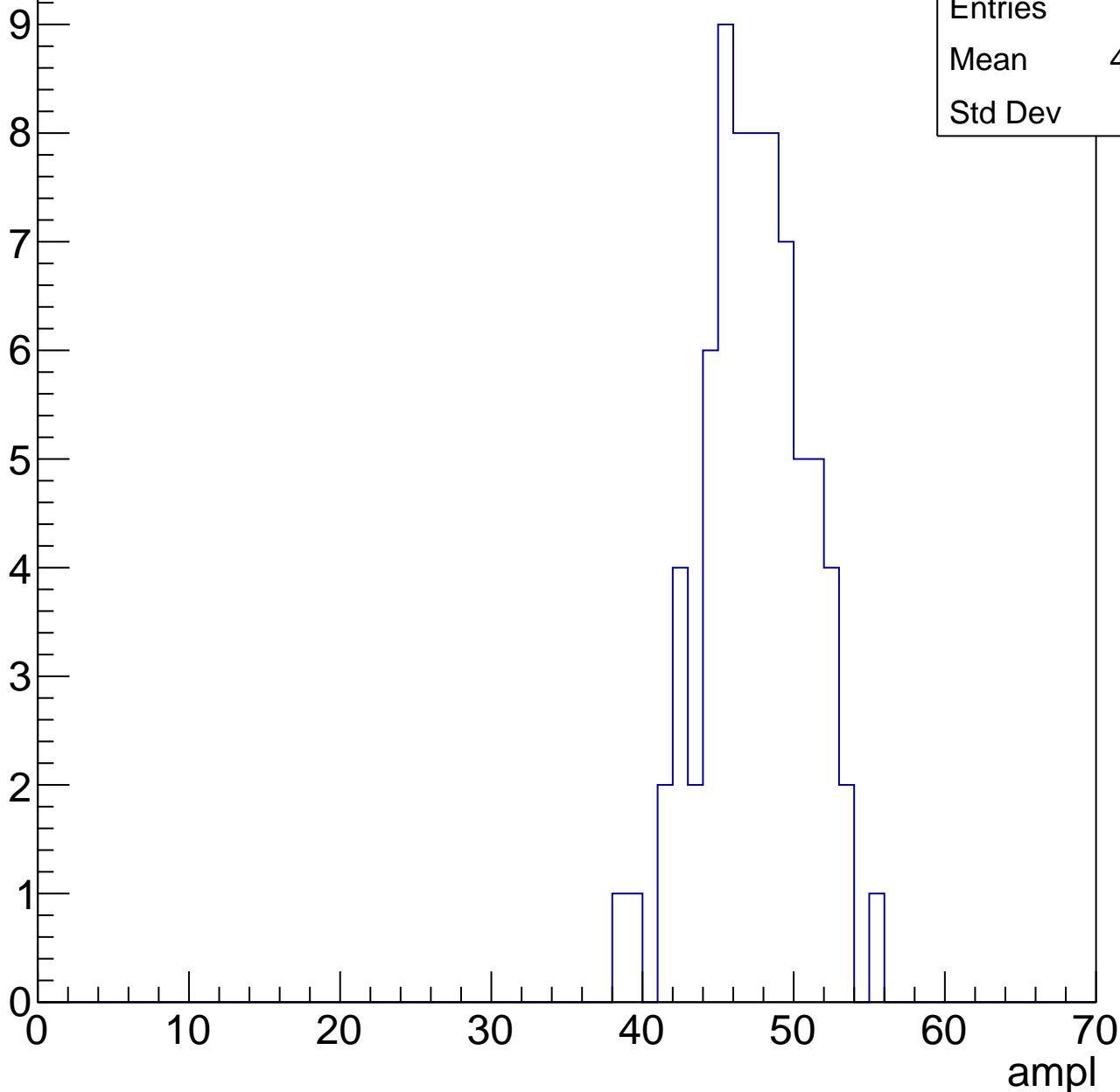


# B1L002S, U26-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	46.95
Std Dev	3.4

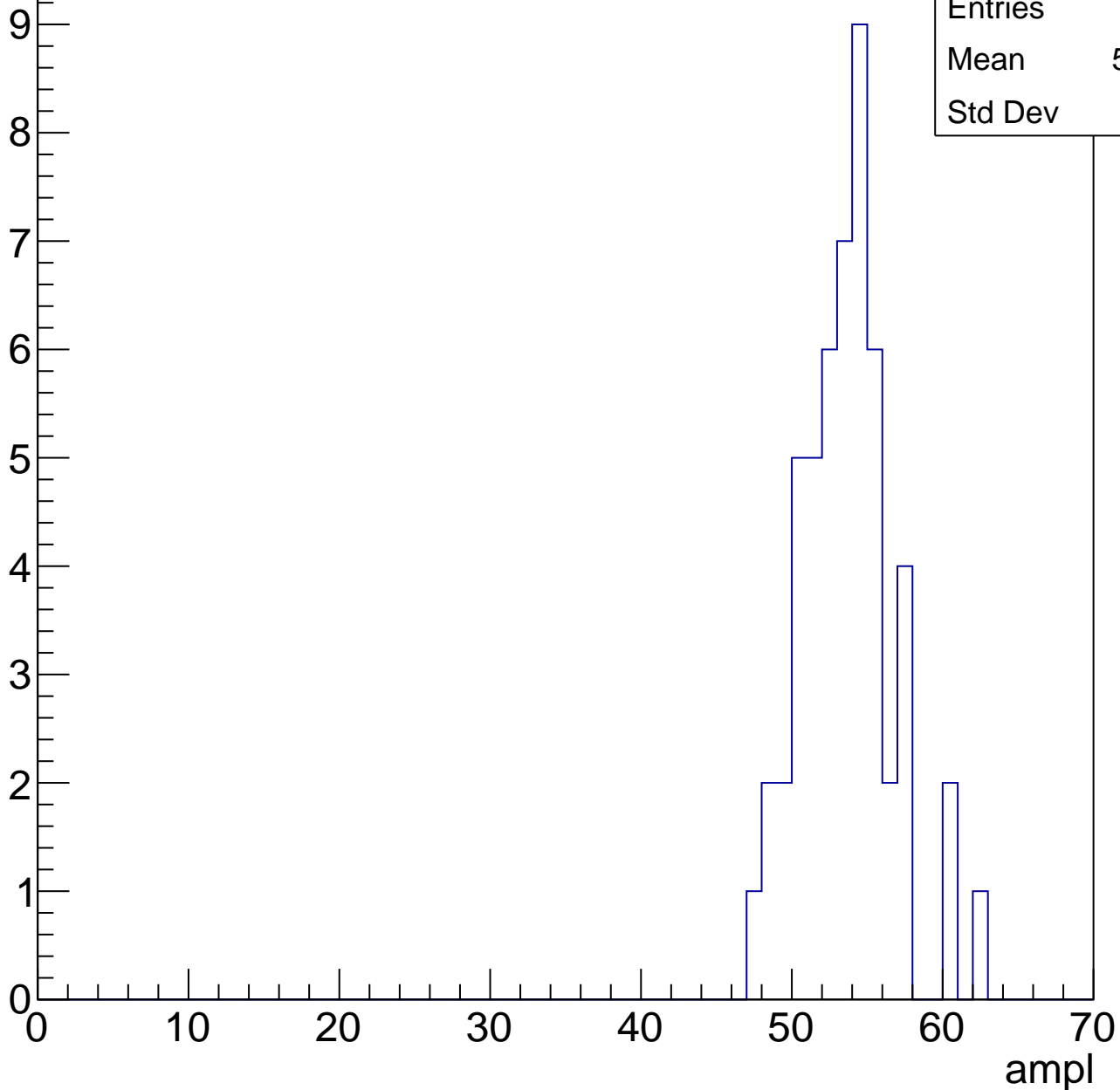


# B1L002S, U26-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.21
Std Dev	3.04

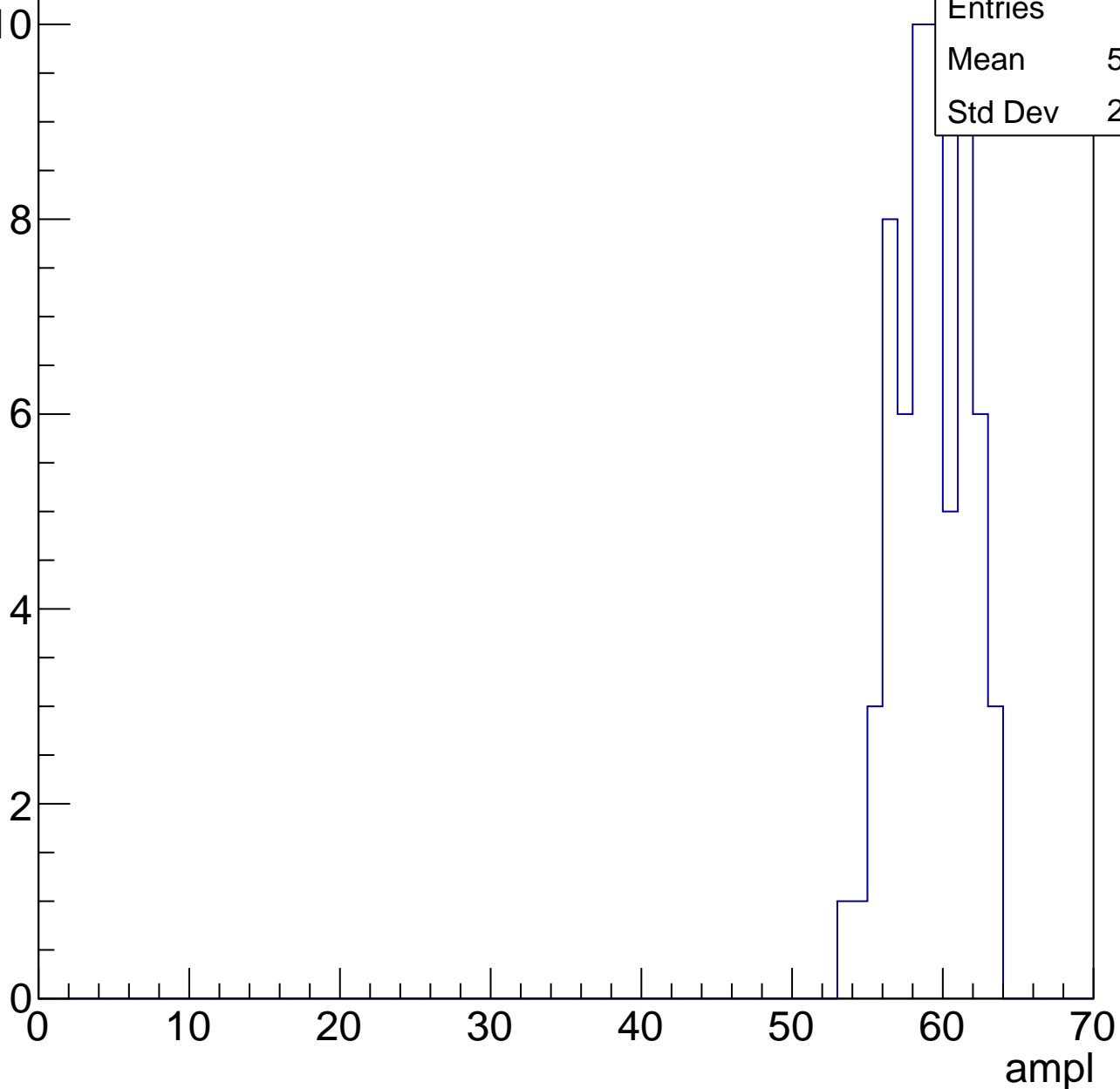


# B1L002S, U26-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.74
Std Dev	2.389

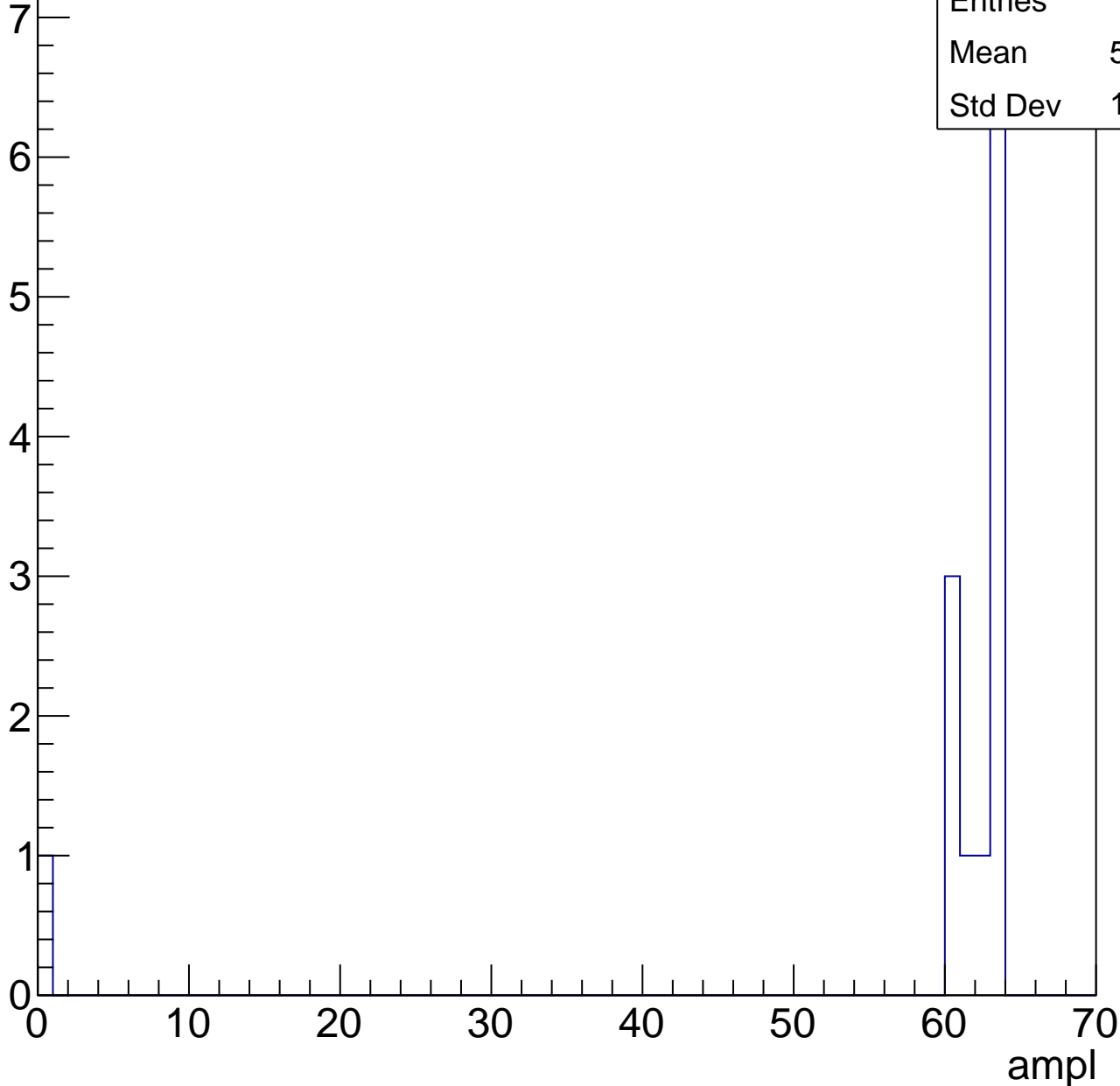


# B1L002S, U26-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	57.23
Std Dev	16.57

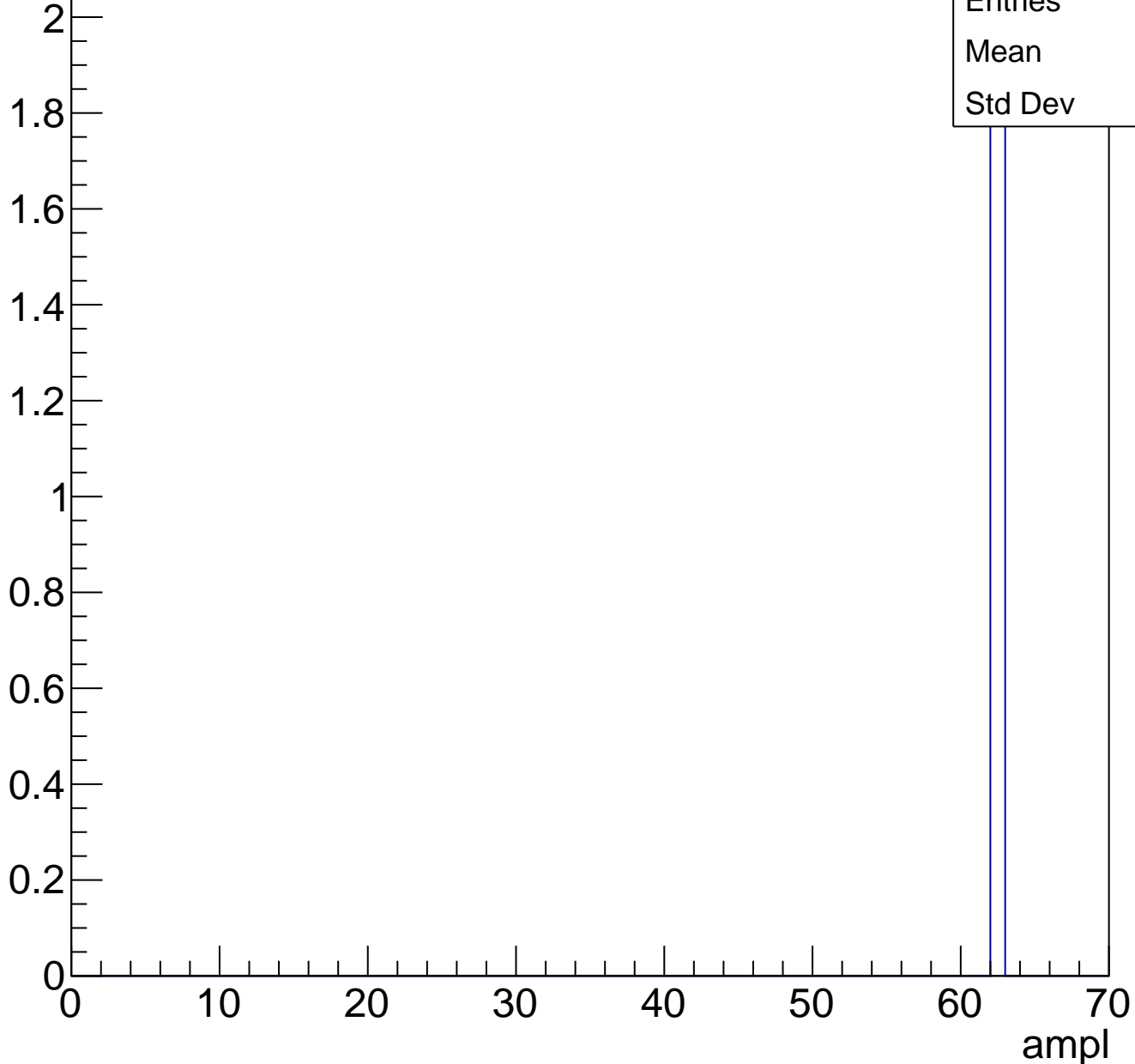




# B1L002S, U26-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch54, adc0

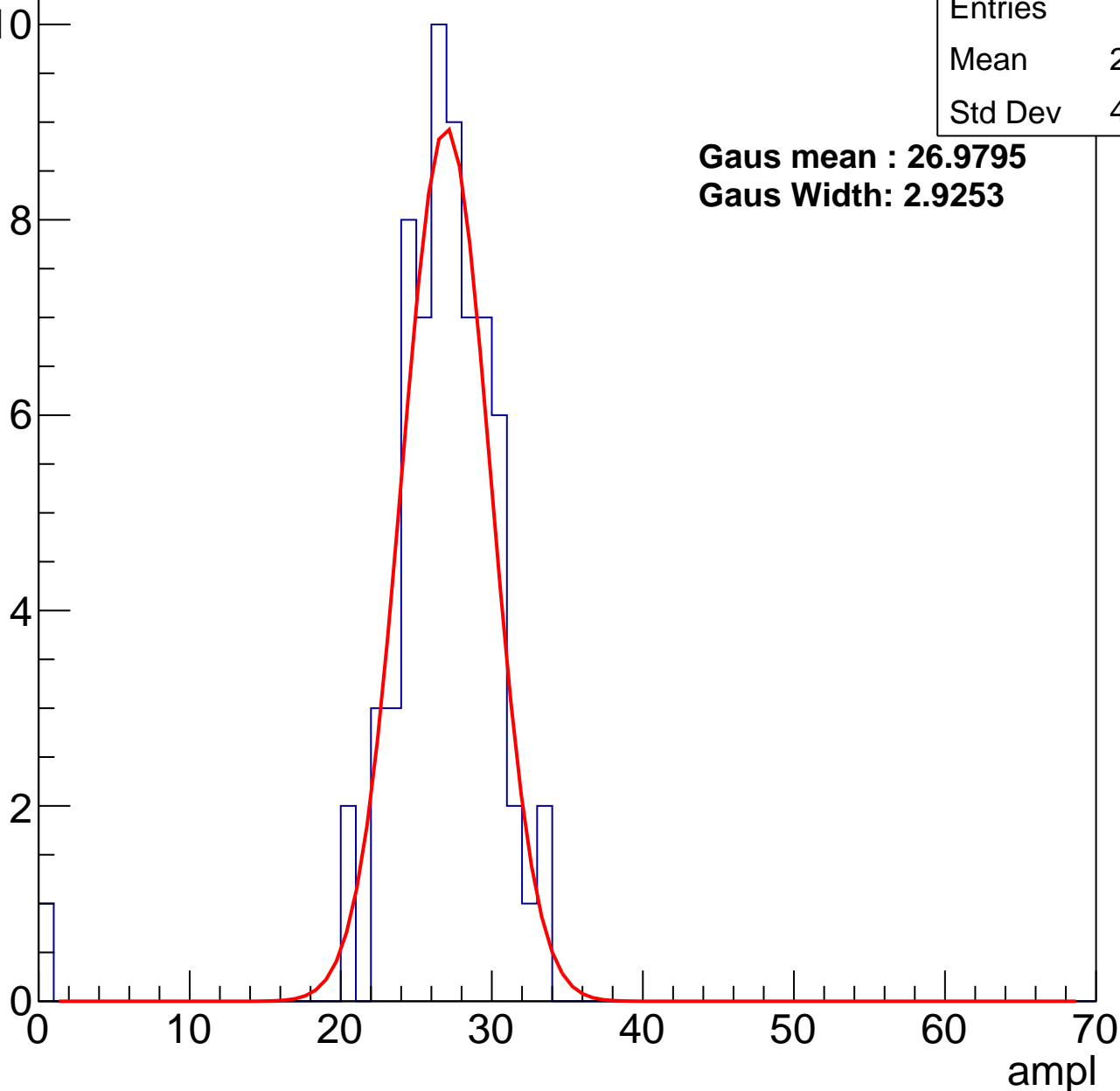
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	26.24
Std Dev	4.267

**Gaus mean : 26.9795**

**Gaus Width: 2.9253**



# B1L002S, U26-ch54, adc1

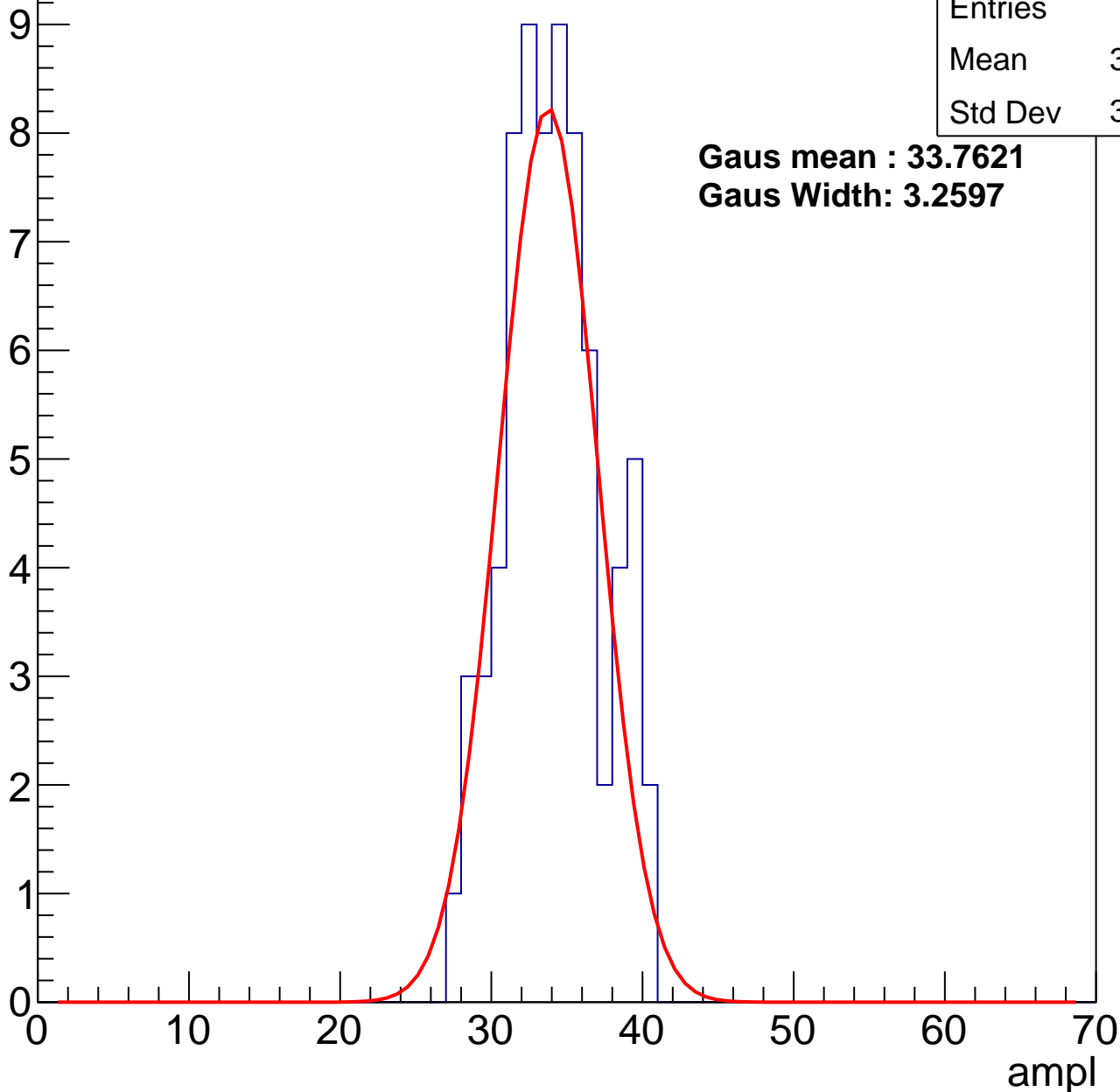
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	33.62
Std Dev	3.147

**Gaus mean : 33.7621**

**Gaus Width: 3.2597**



# B1L002S, U26-ch54, adc2

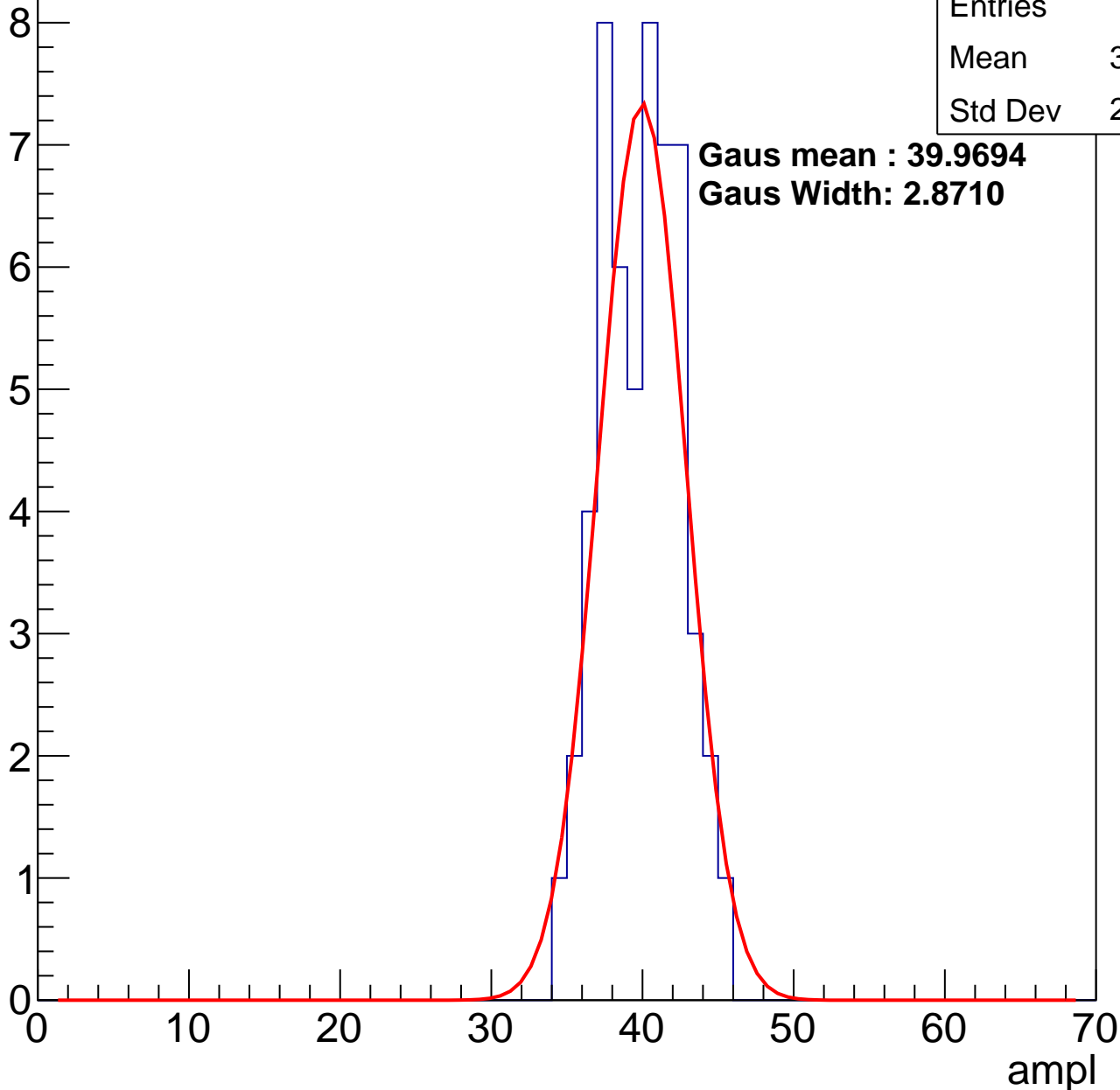
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	39.44
Std Dev	2.558

**Gaus mean : 39.9694**

**Gaus Width: 2.8710**



# B1L002S, U26-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	46.53
Std Dev	2.93

Entry

10

8

6

4

2

0

0

10

20

30

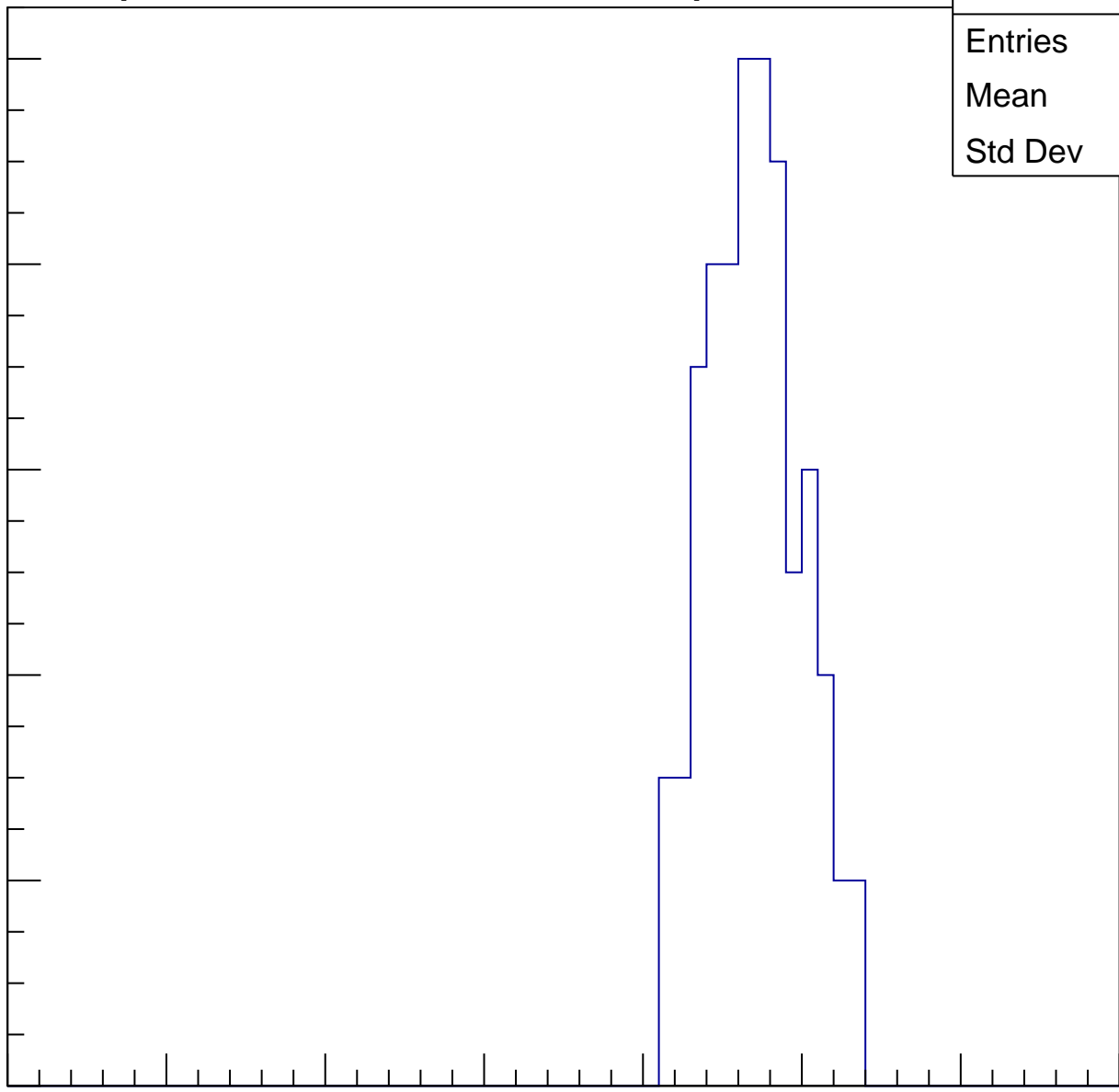
40

50

60

70

ampl

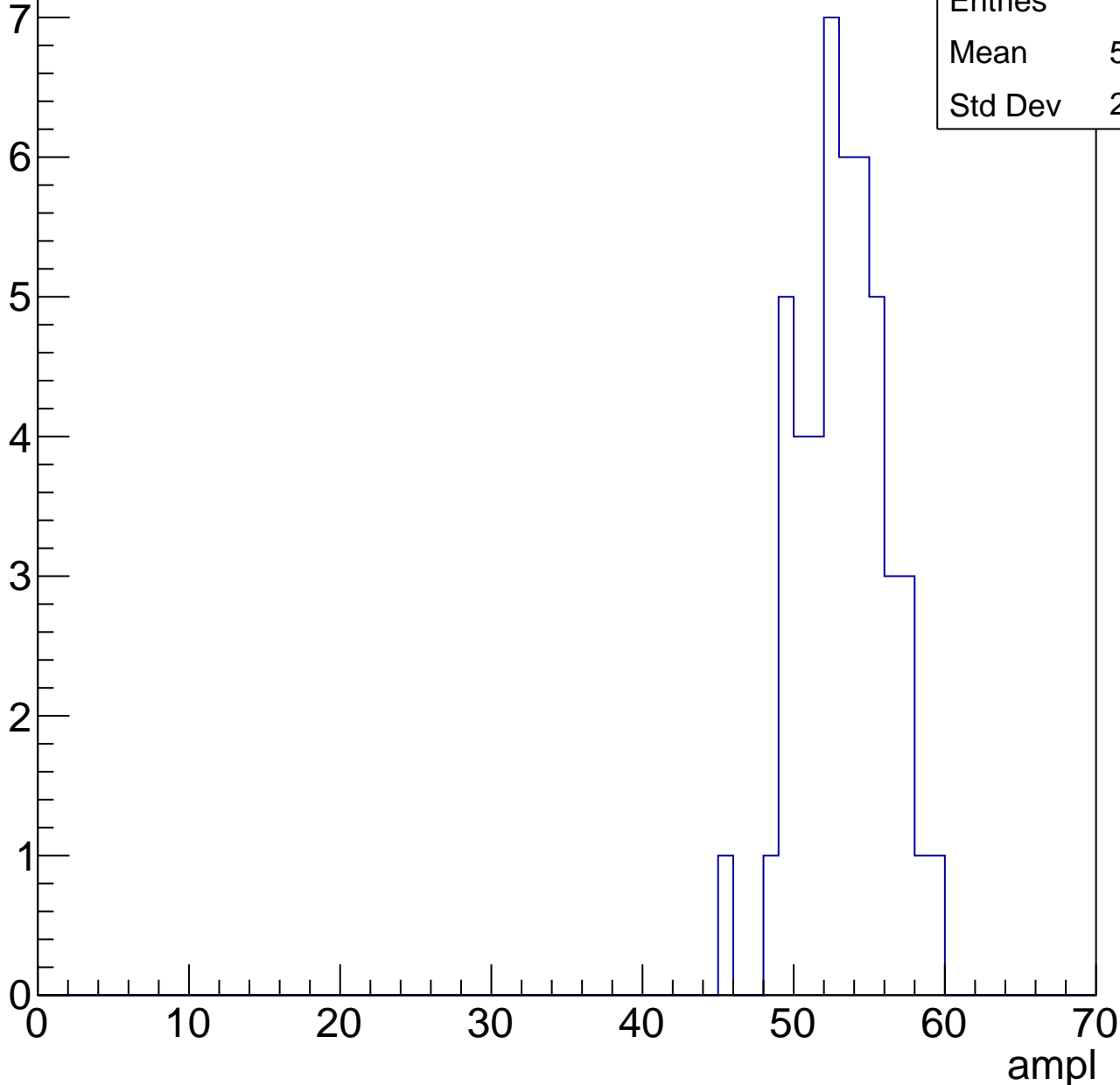


# B1L002S, U26-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

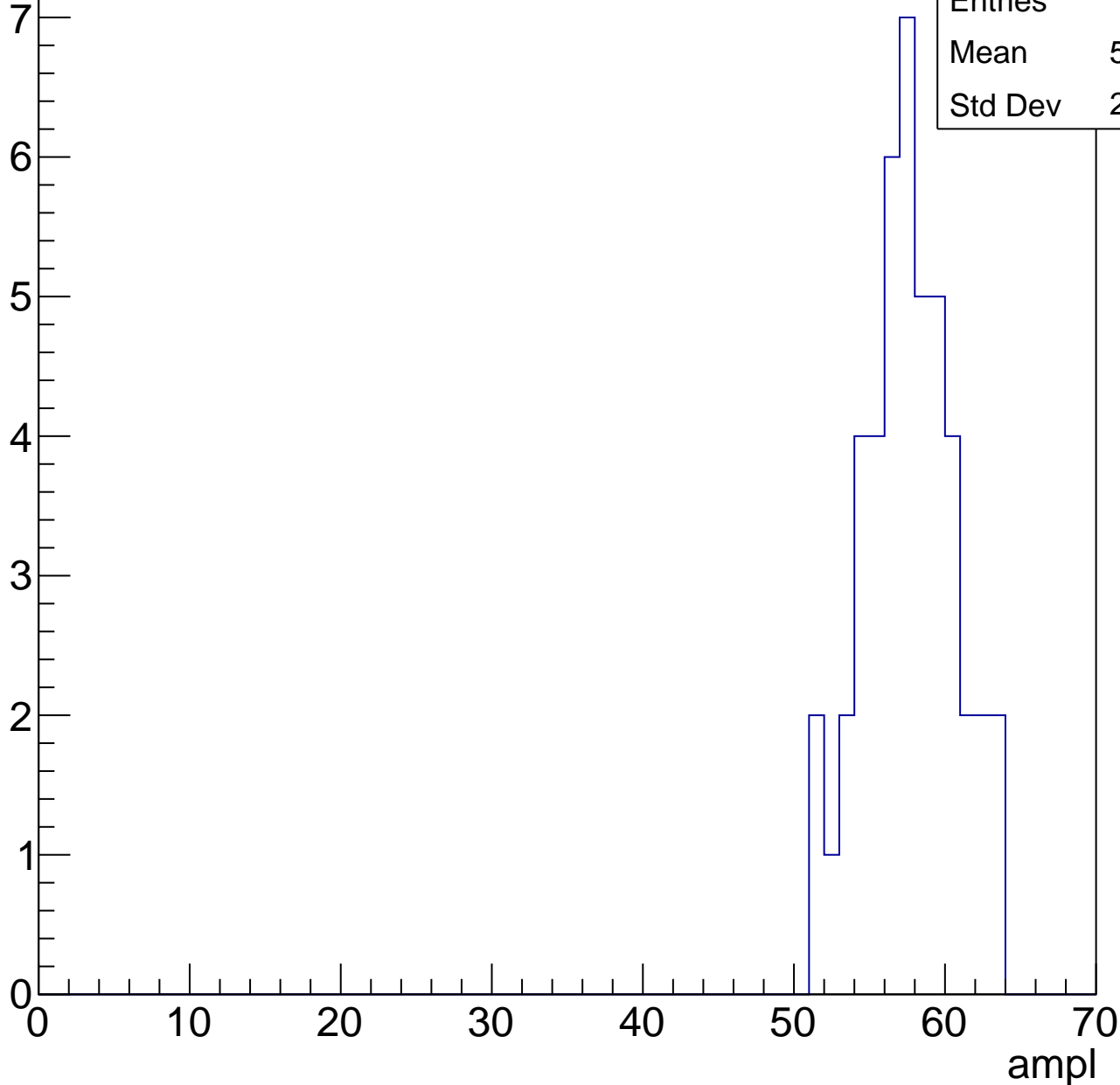
Entries	47
Mean	52.74
Std Dev	2.869



# B1L002S, U26-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

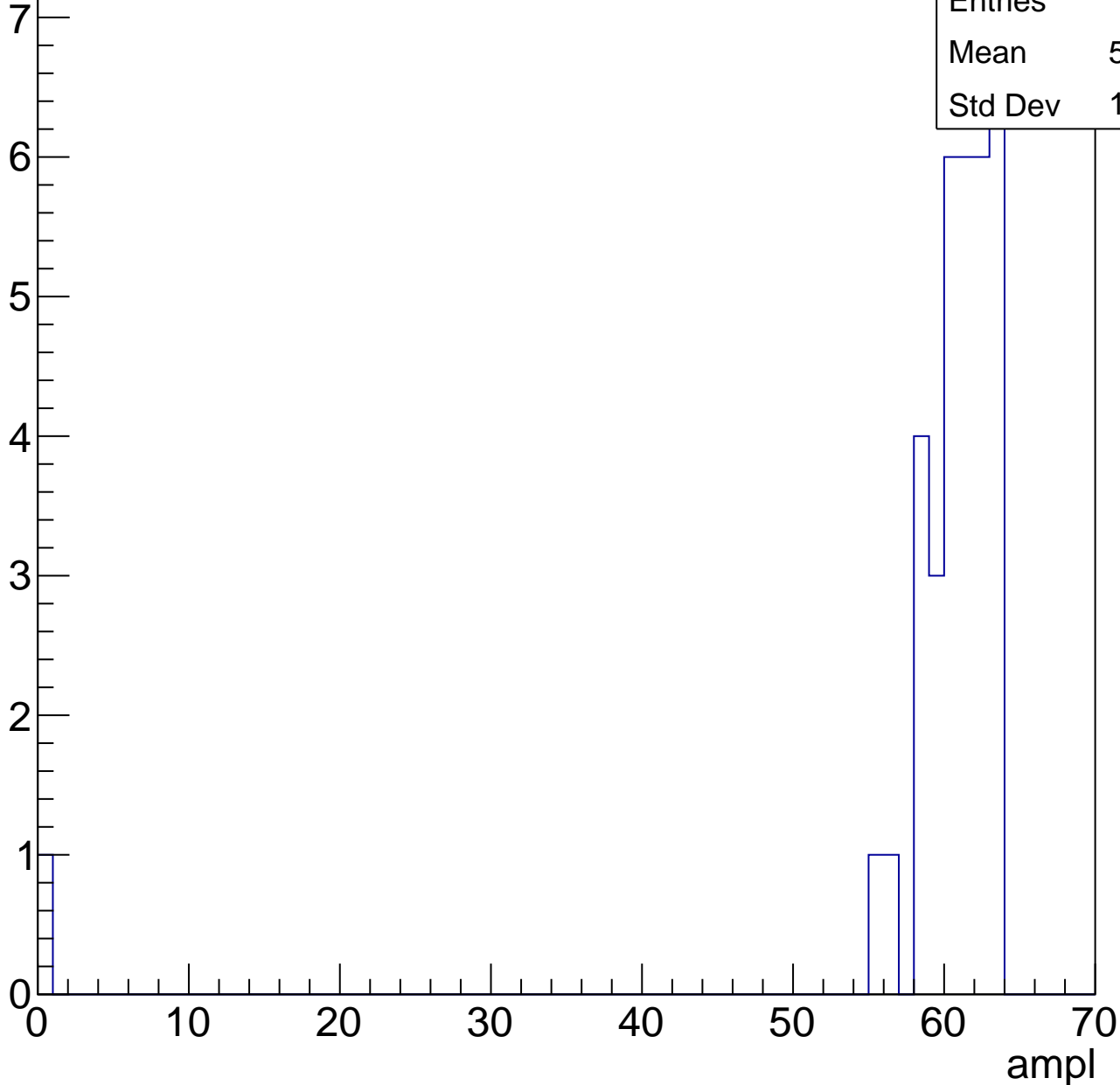


# B1L002S, U26-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.83
Std Dev	10.29





# B1L002S, U26-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

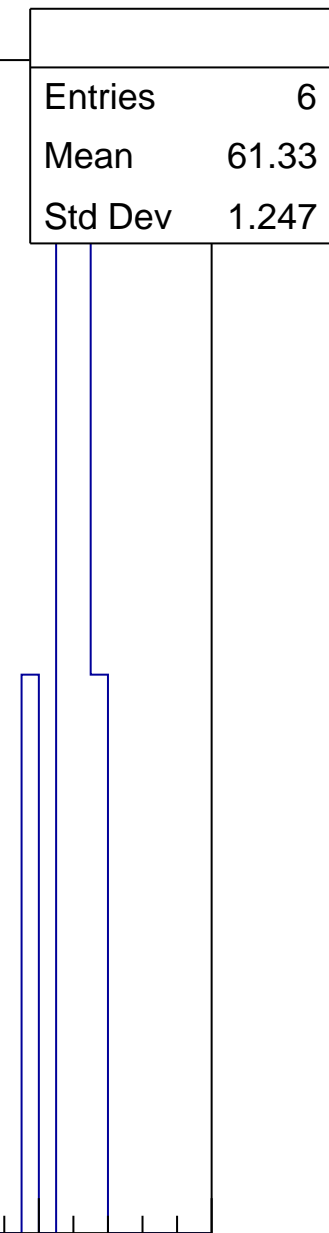
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.33
Std Dev	1.247

0 10 20 30 40 50 60 70

ampl



# B1L002S, U26-ch55, adc0

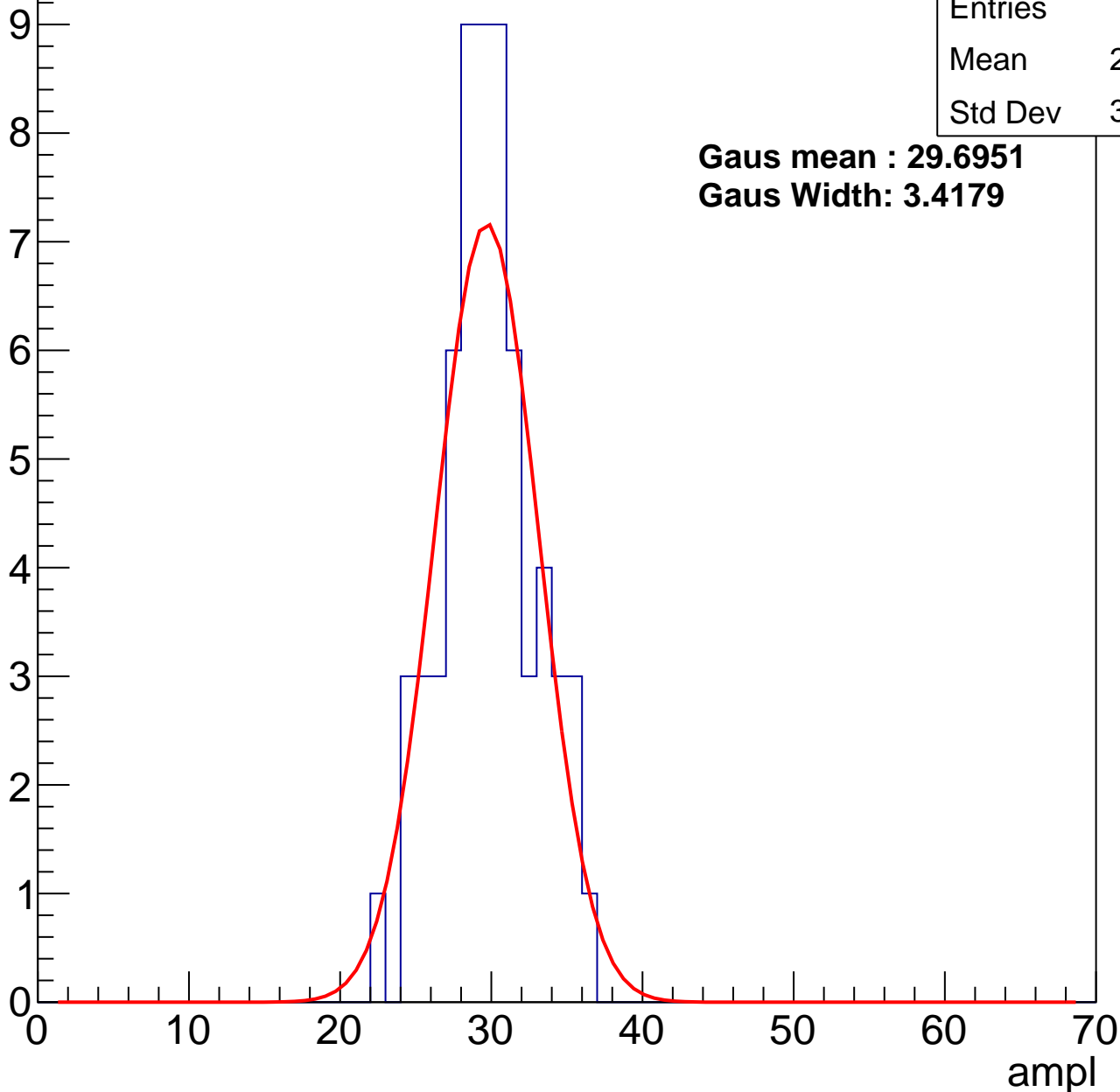
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	29.35
Std Dev	3.035

**Gaus mean : 29.6951**

**Gaus Width: 3.4179**



# B1L002S, U26-ch55, adc1

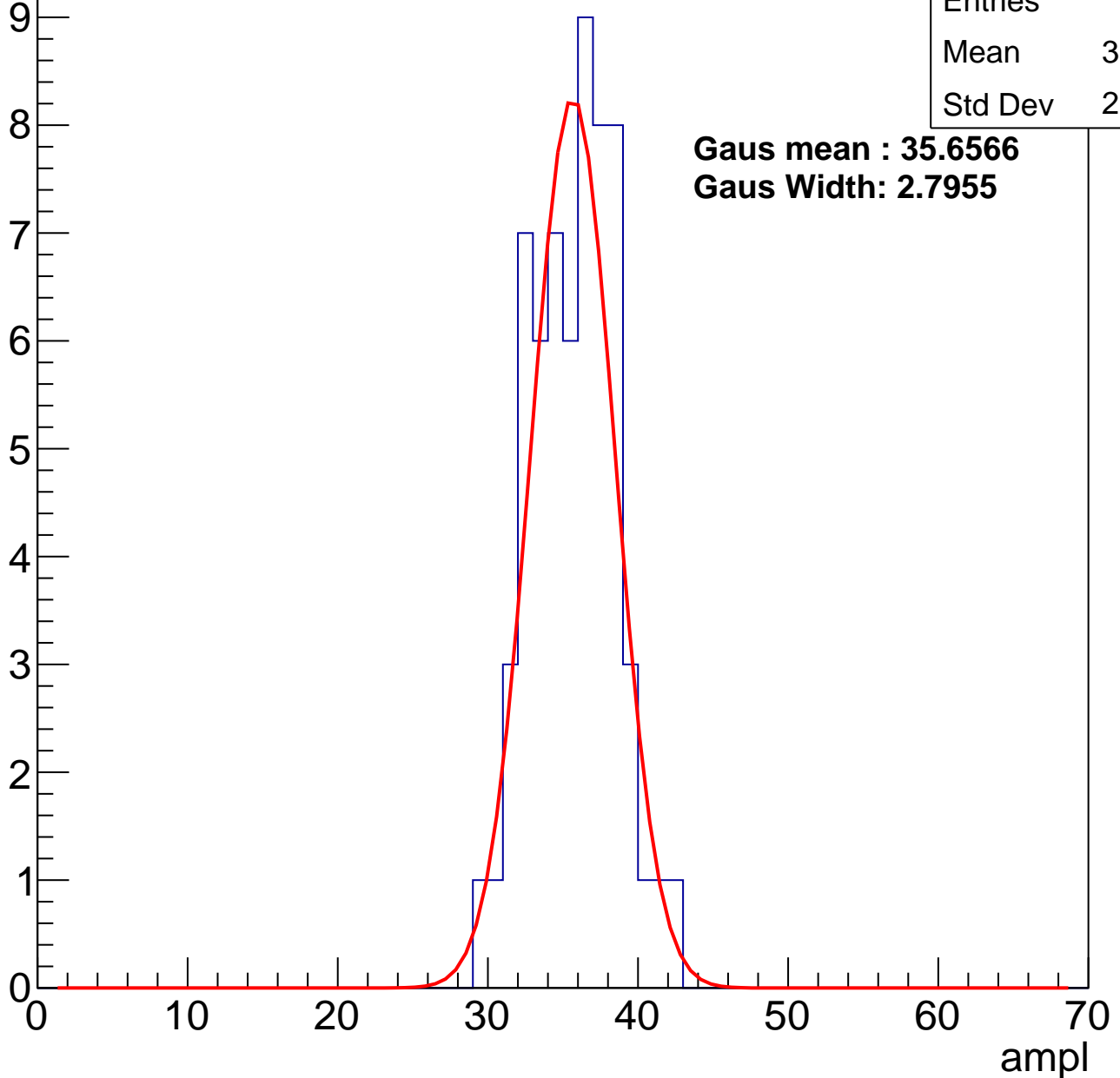
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	35.26
Std Dev	2.753

**Gaus mean : 35.6566**

**Gaus Width: 2.7955**



# B1L002S, U26-ch55, adc2

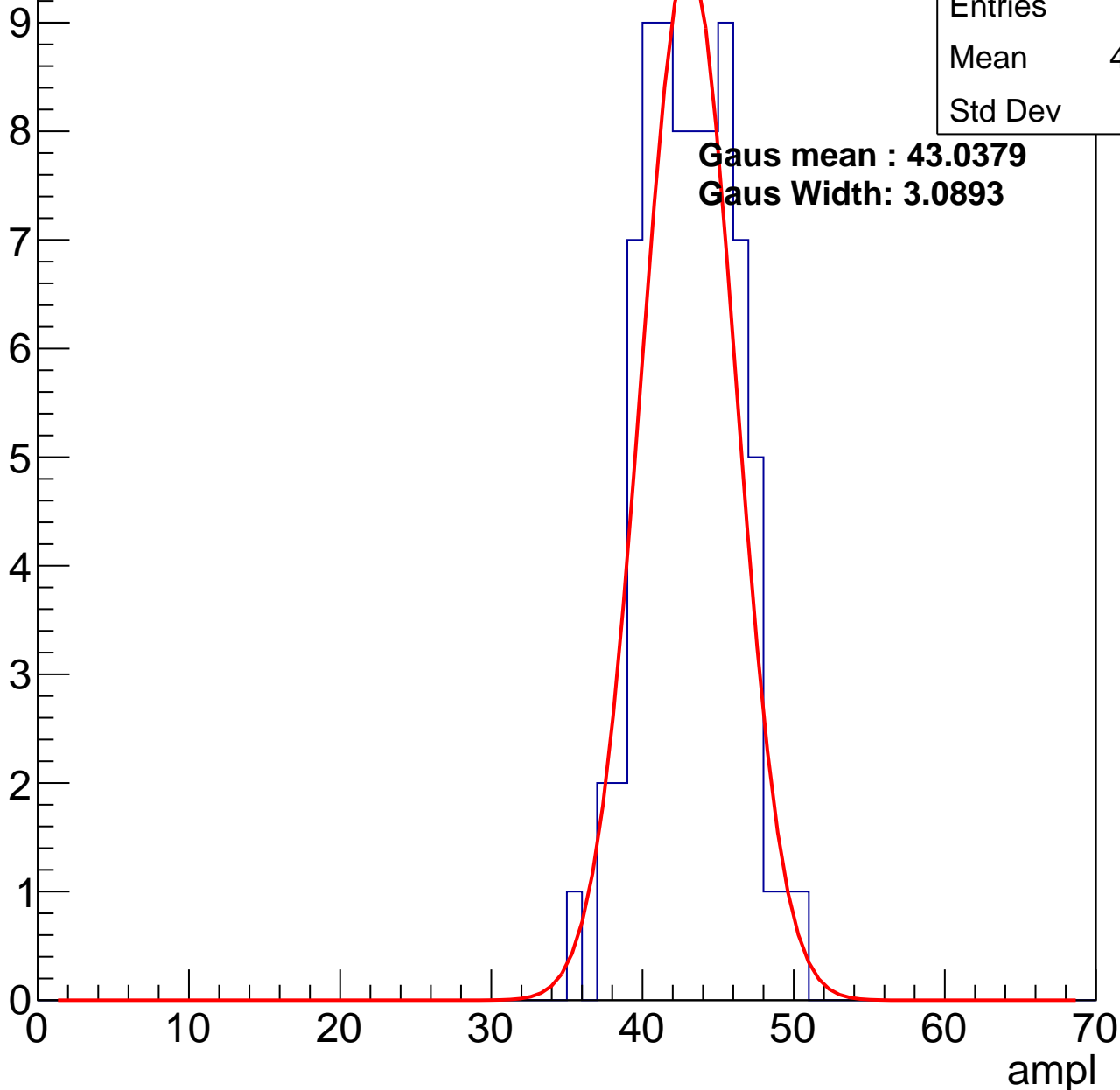
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	42.67
Std Dev	3.02

**Gaus mean : 43.0379**

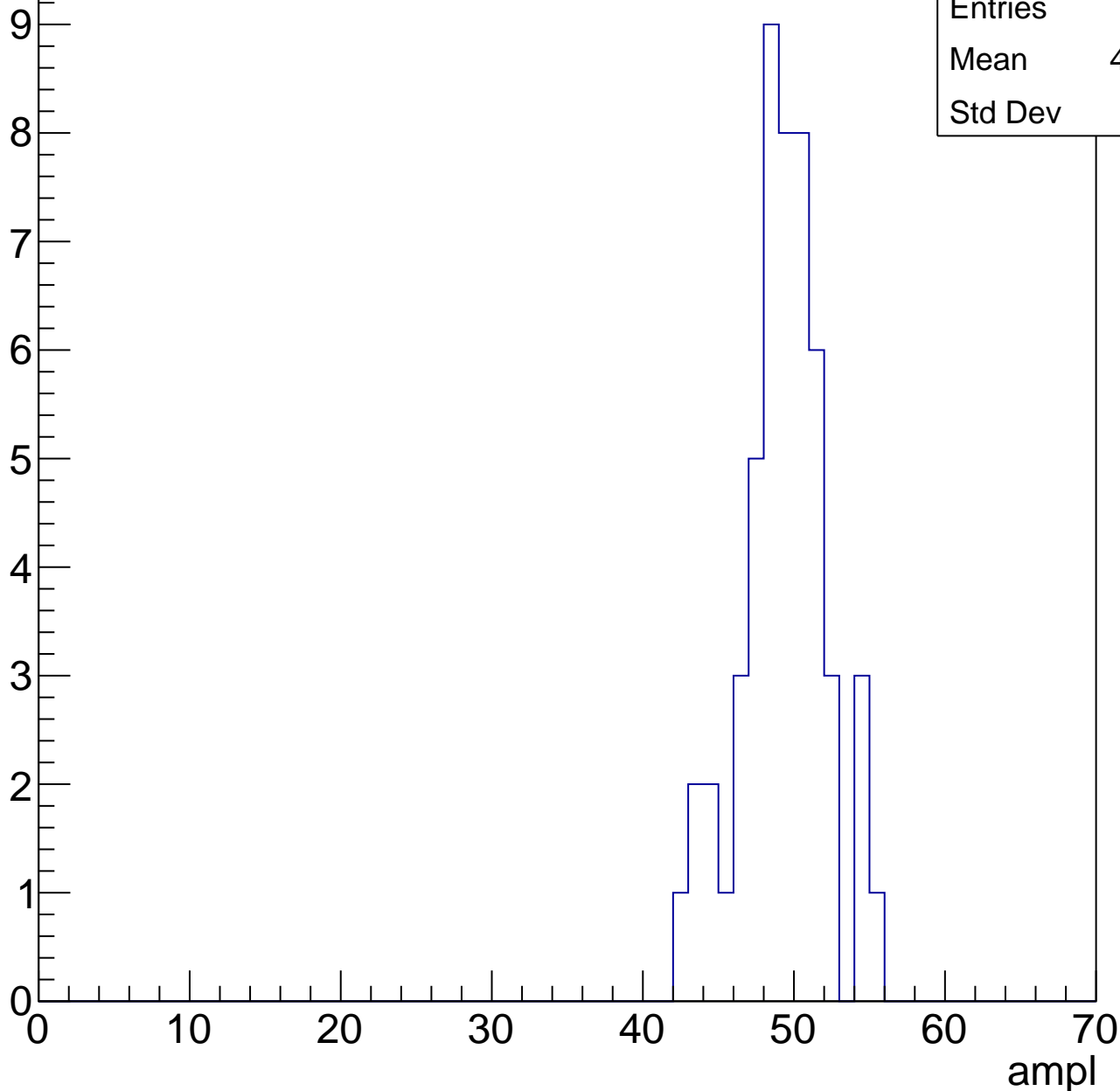
**Gaus Width: 3.0893**



# B1L002S, U26-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	52
Mean	48.79
Std Dev	2.81

# B1L002S, U26-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

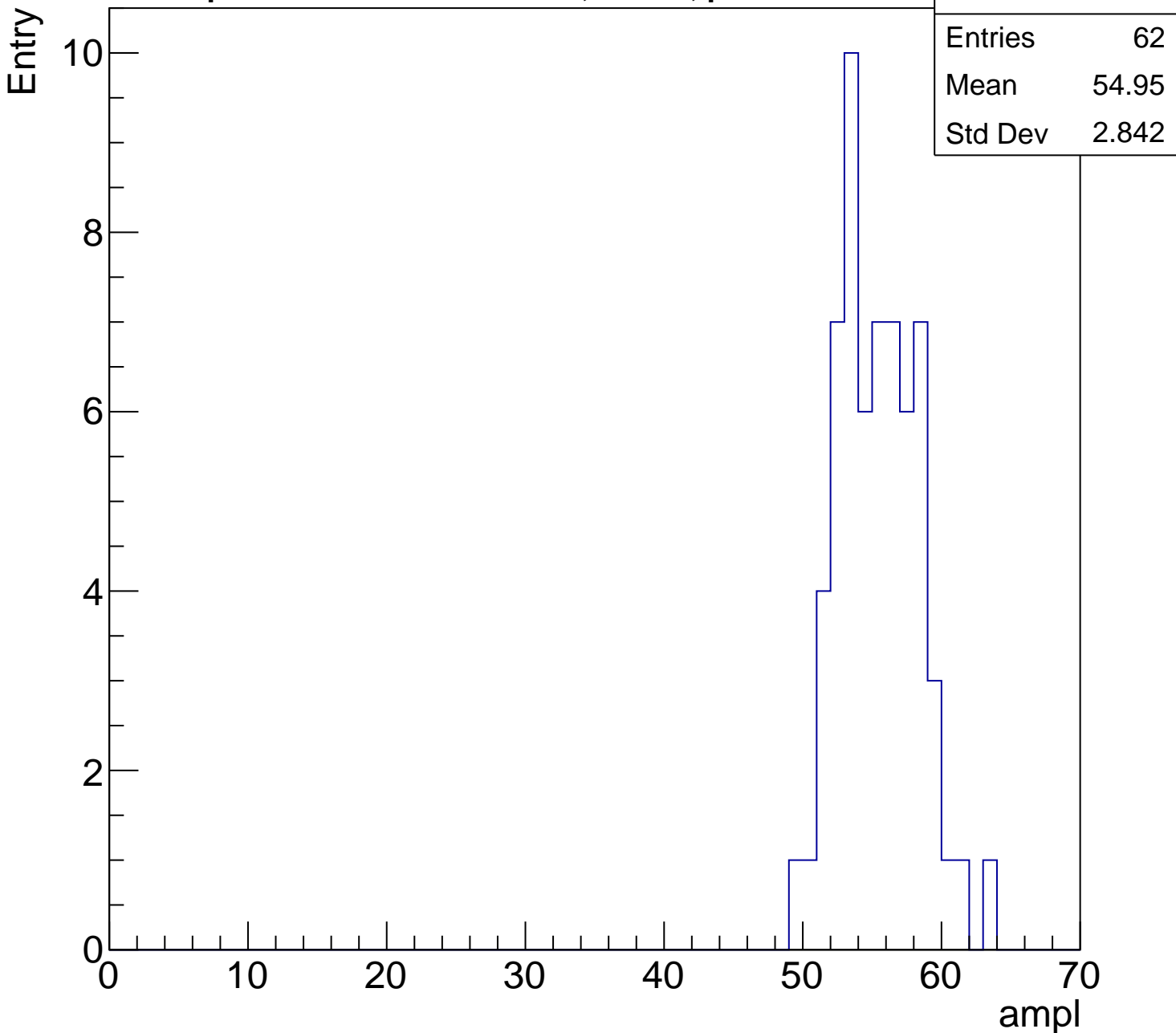
Entries	62
Mean	54.95
Std Dev	2.842

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

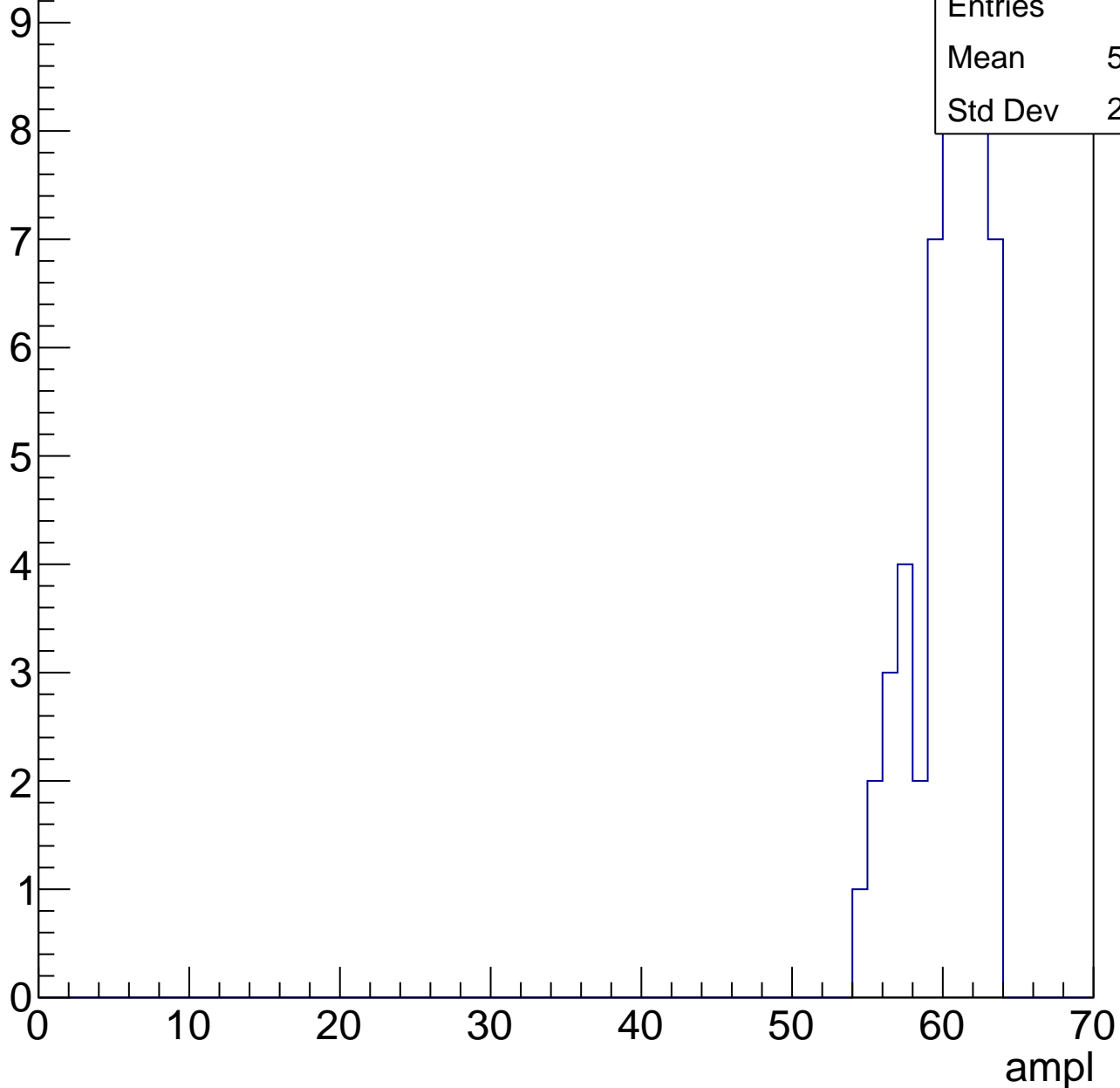
ampl



# B1L002S, U26-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

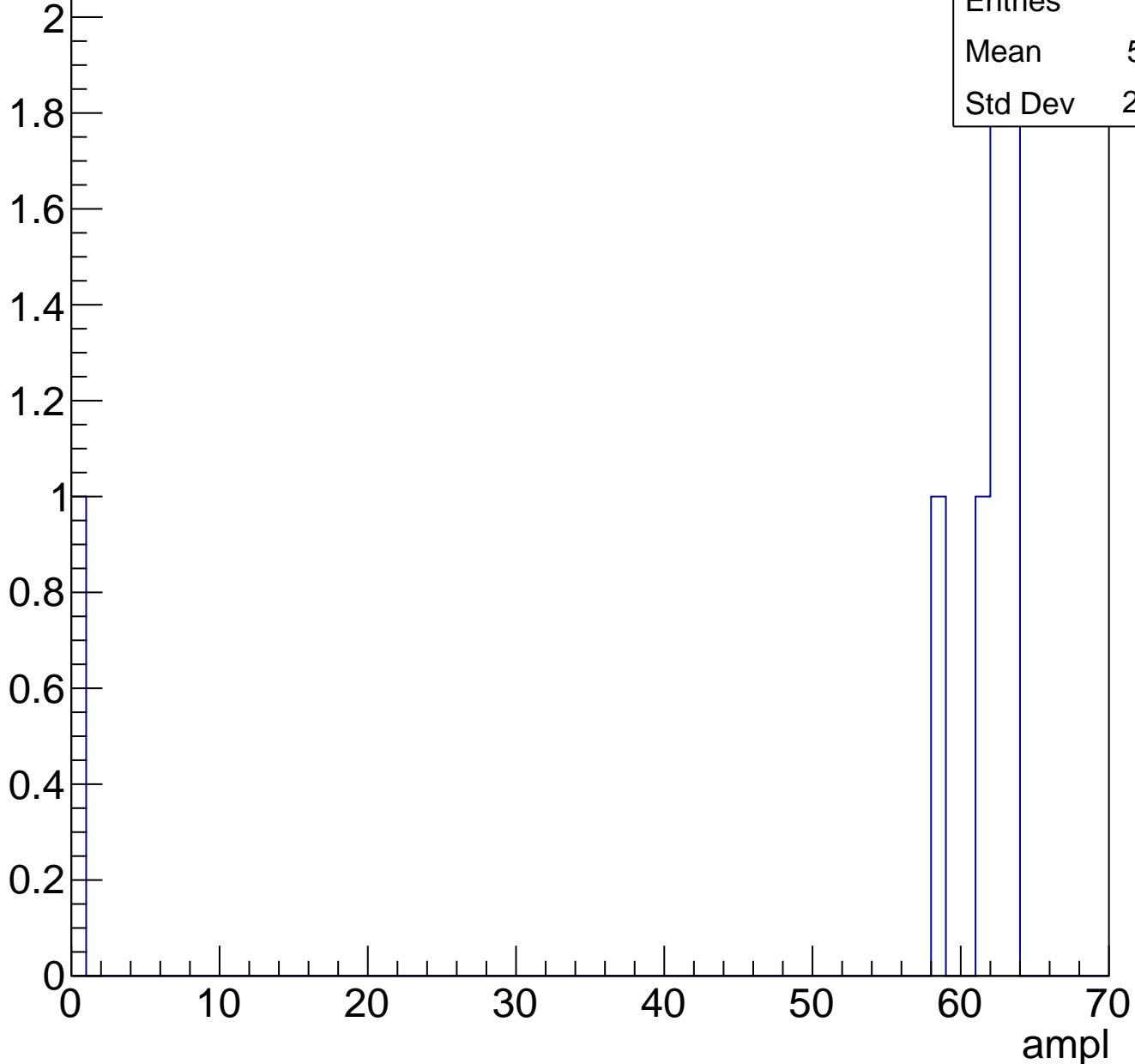
Entry



# B1L002S, U26-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	7
Std Dev	9.899

# B1L002S, U26-ch56, adc0

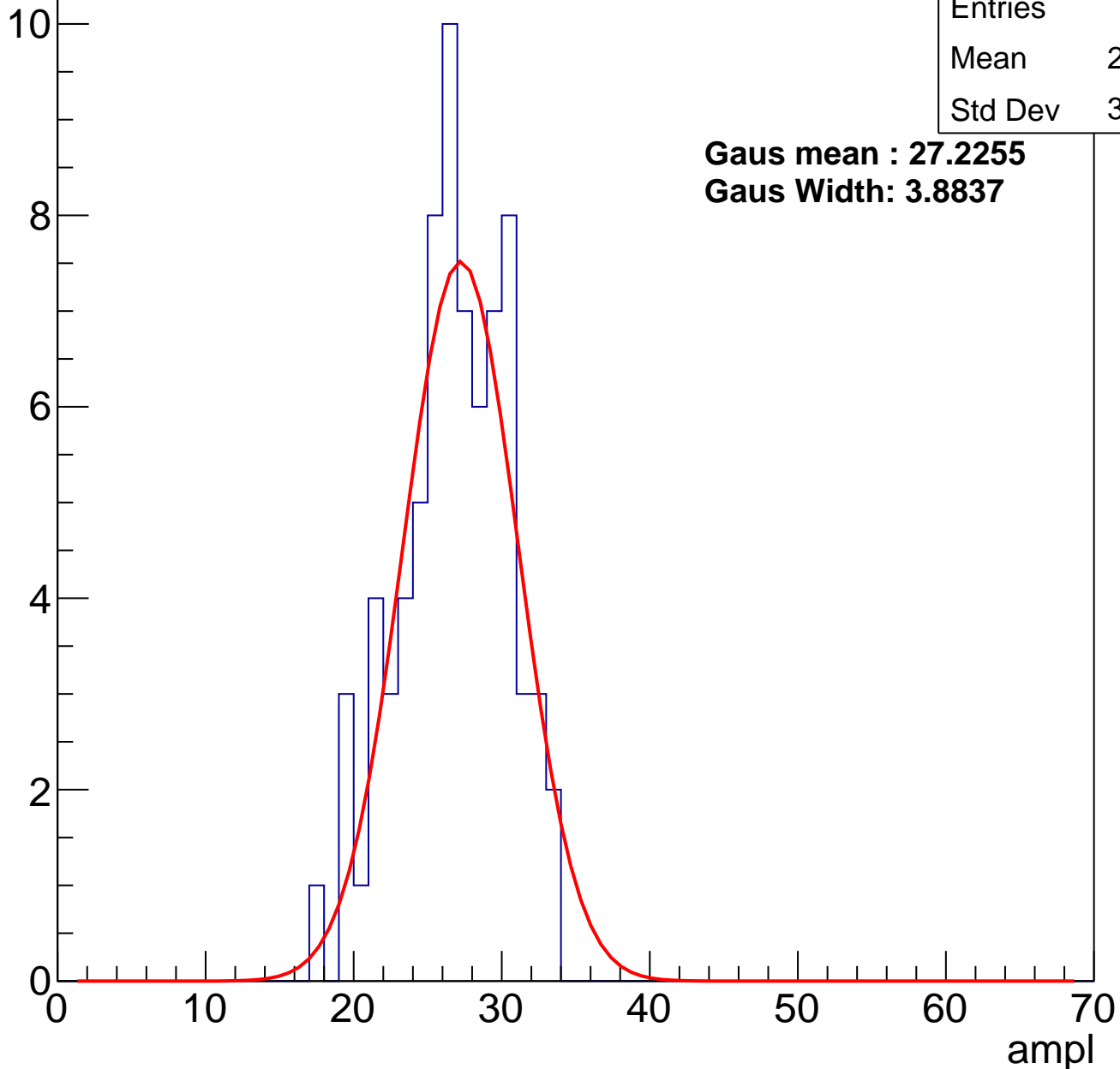
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	26.28
Std Dev	3.602

**Gaus mean : 27.2255**

**Gaus Width: 3.8837**

Entry



# B1L002S, U26-ch56, adc1

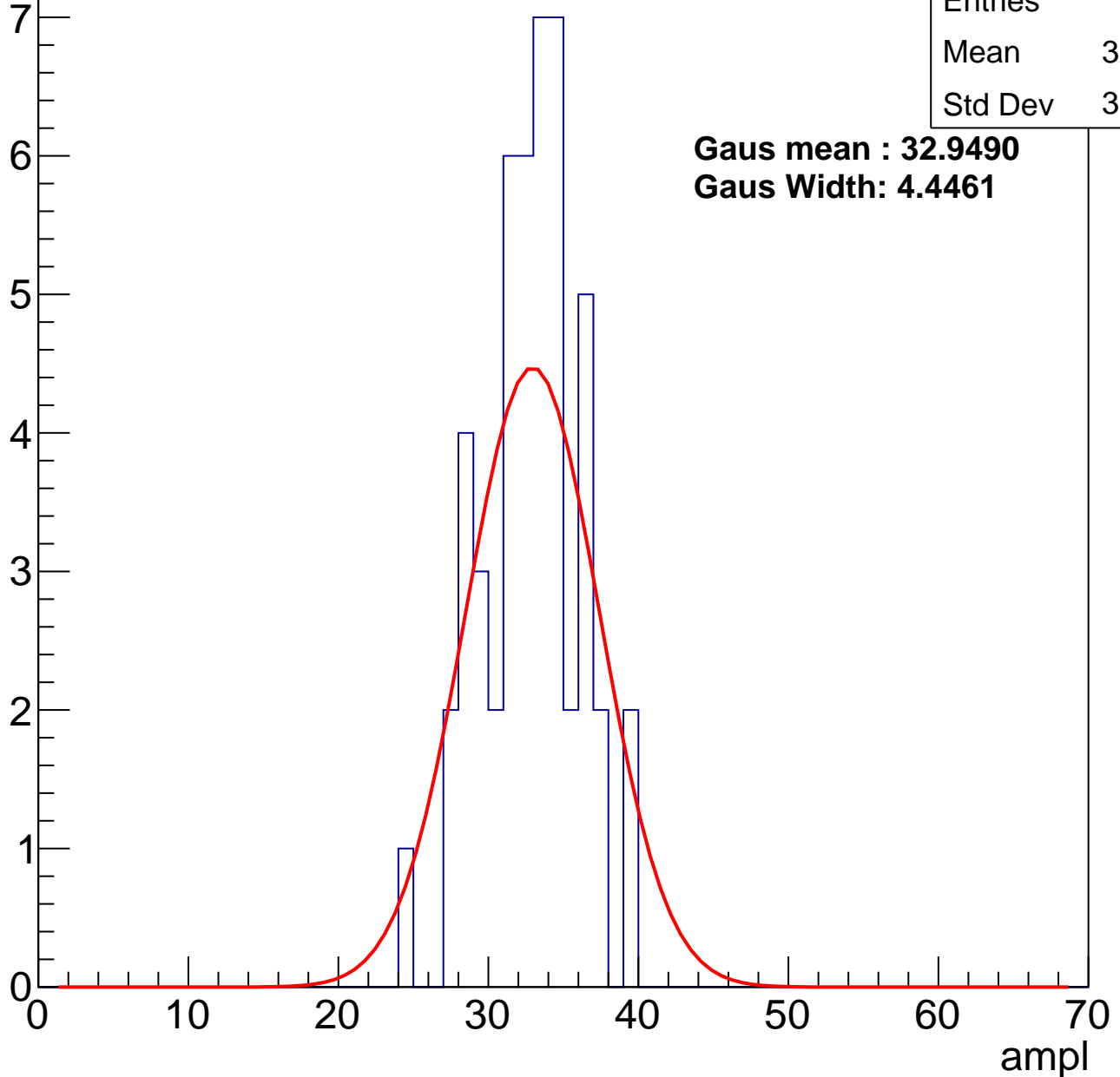
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	32.37
Std Dev	3.173

**Gaus mean : 32.9490**

**Gaus Width: 4.4461**



# B1L002S, U26-ch56, adc2

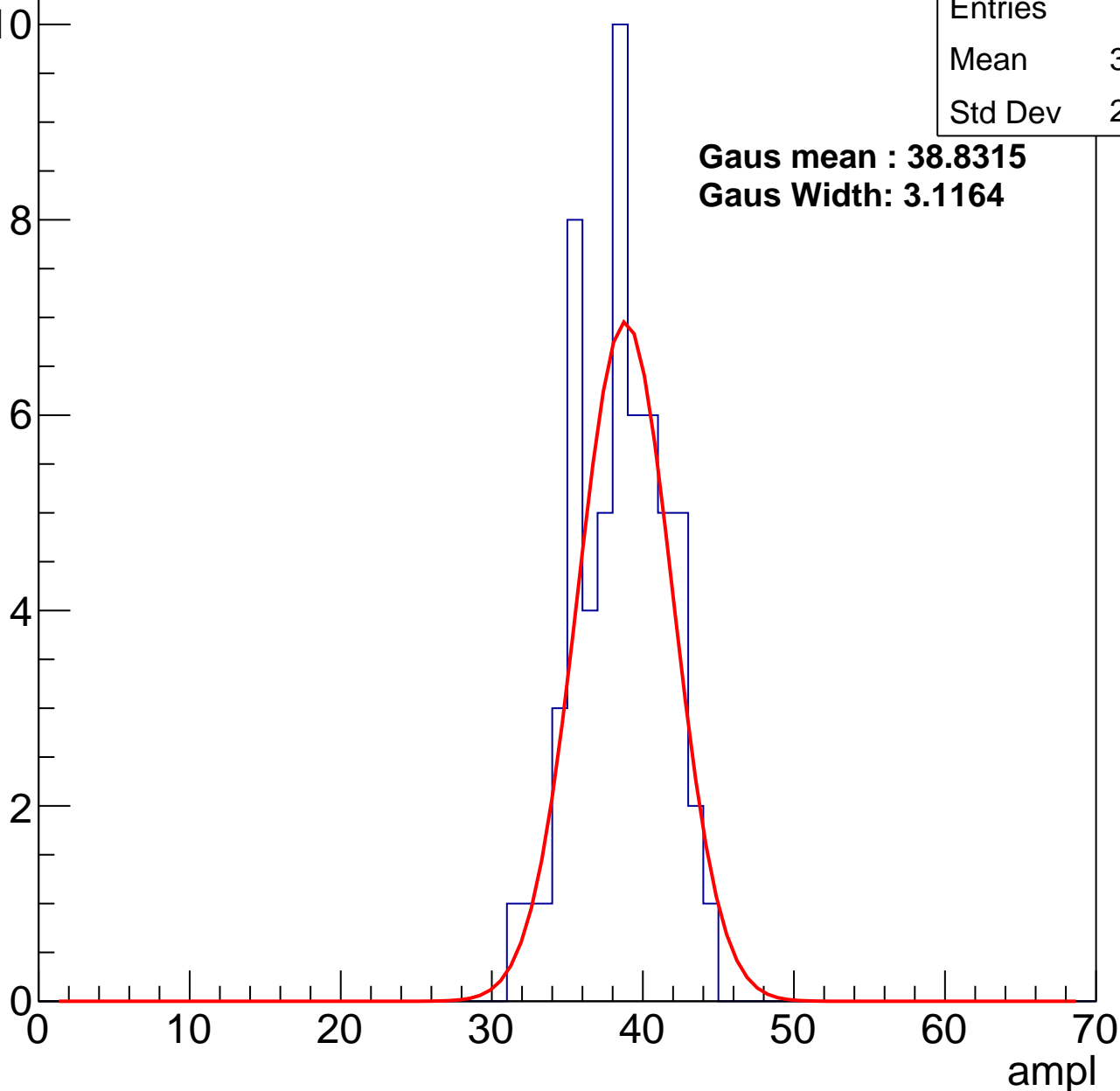
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	38.03
Std Dev	2.912

**Gaus mean : 38.8315**

**Gaus Width: 3.1164**

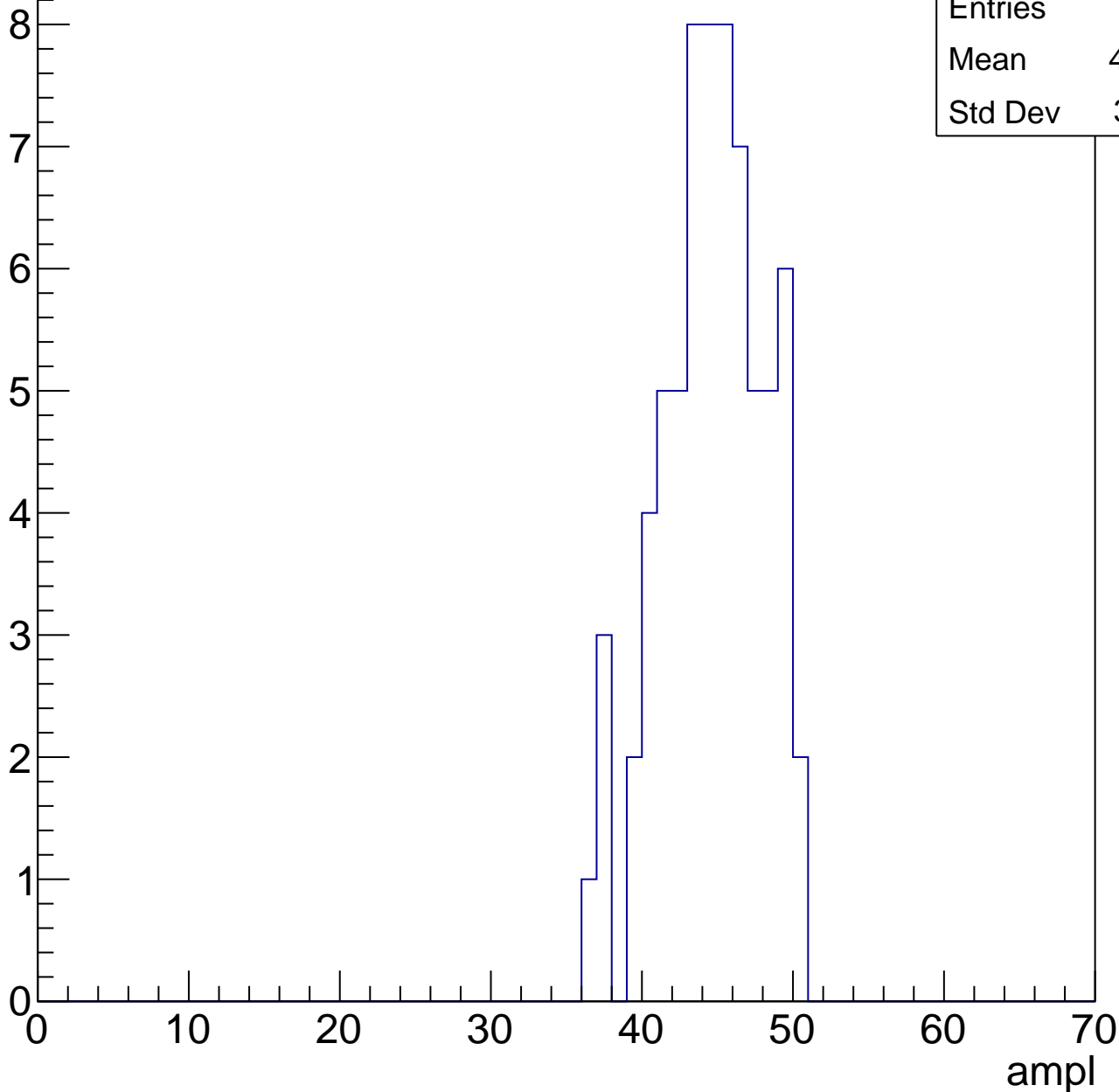


# B1L002S, U26-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

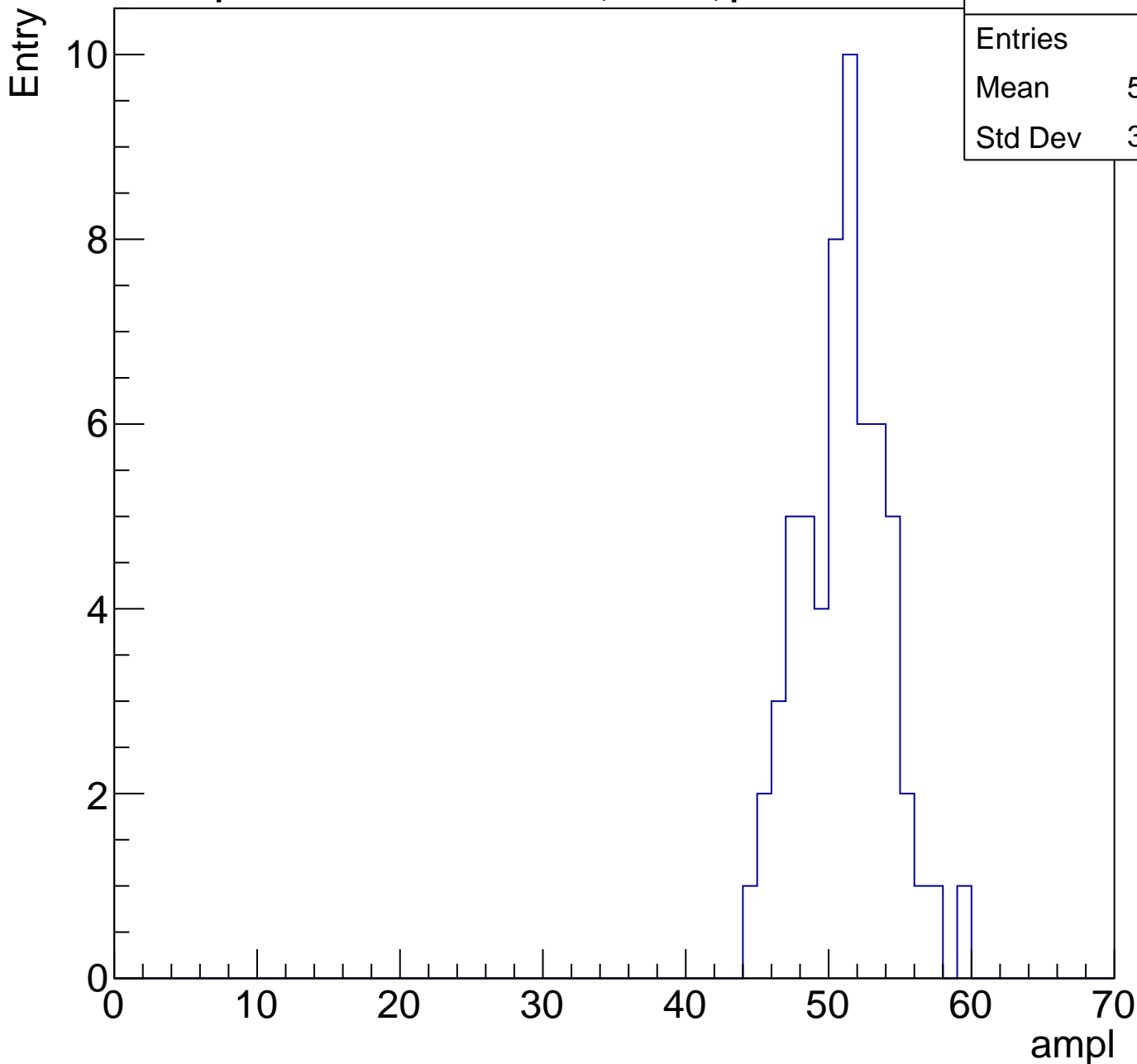
Entries	69
Mean	44.16
Std Dev	3.361



# B1L002S, U26-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

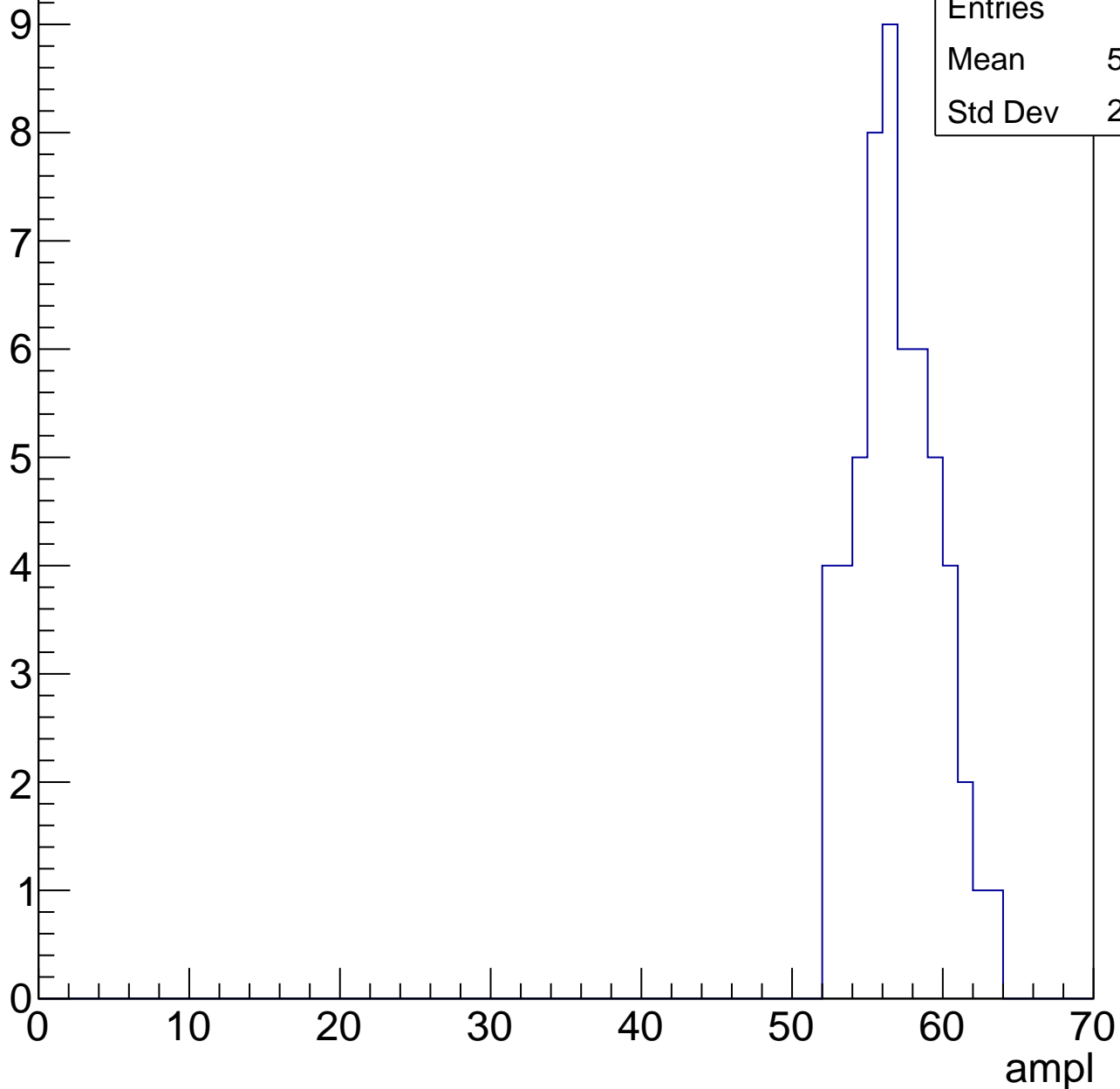
Entries	60
Mean	50.58
Std Dev	3.073



# B1L002S, U26-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

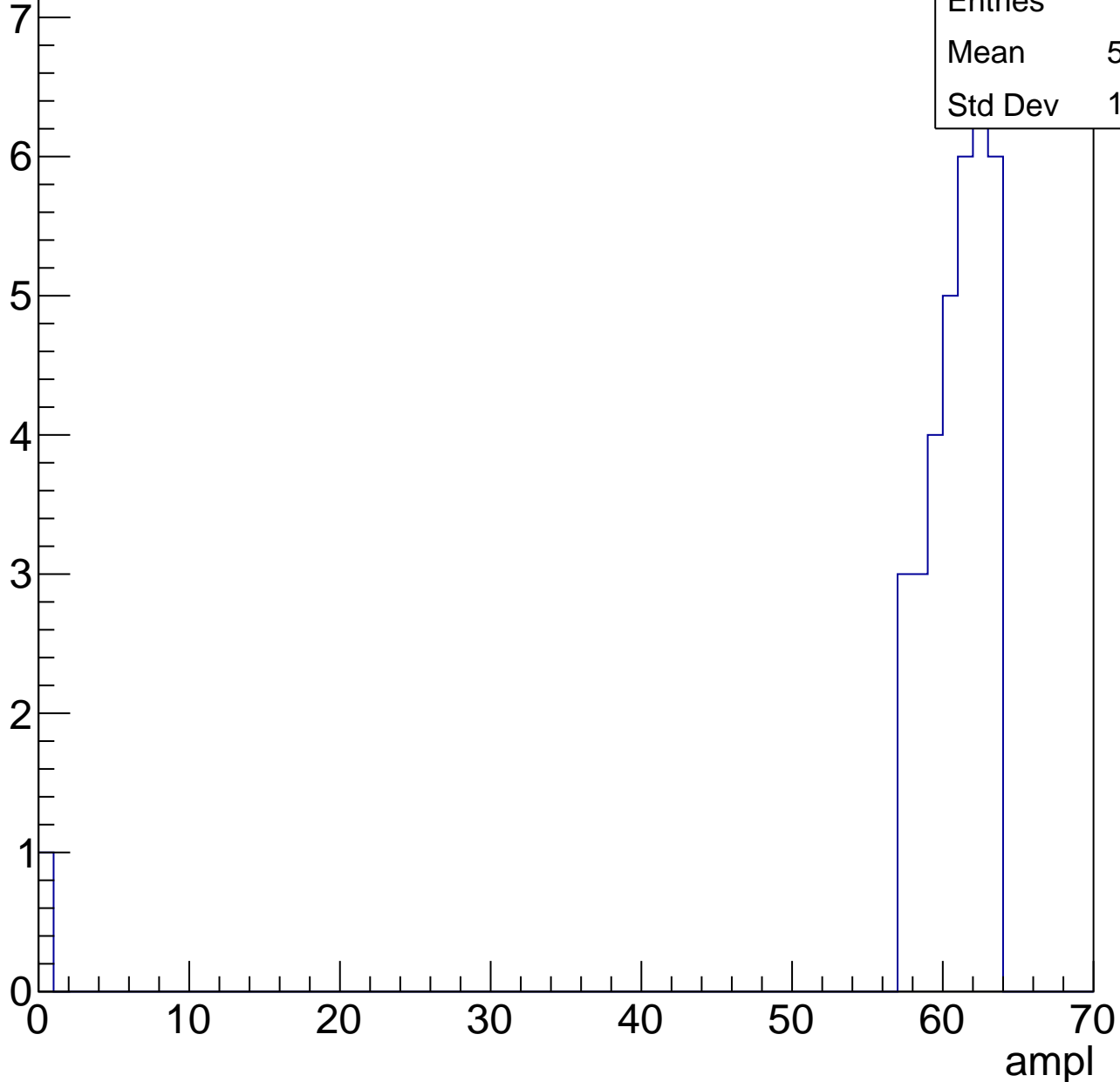


Entries	55
Mean	56.47
Std Dev	2.662

# B1L002S, U26-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

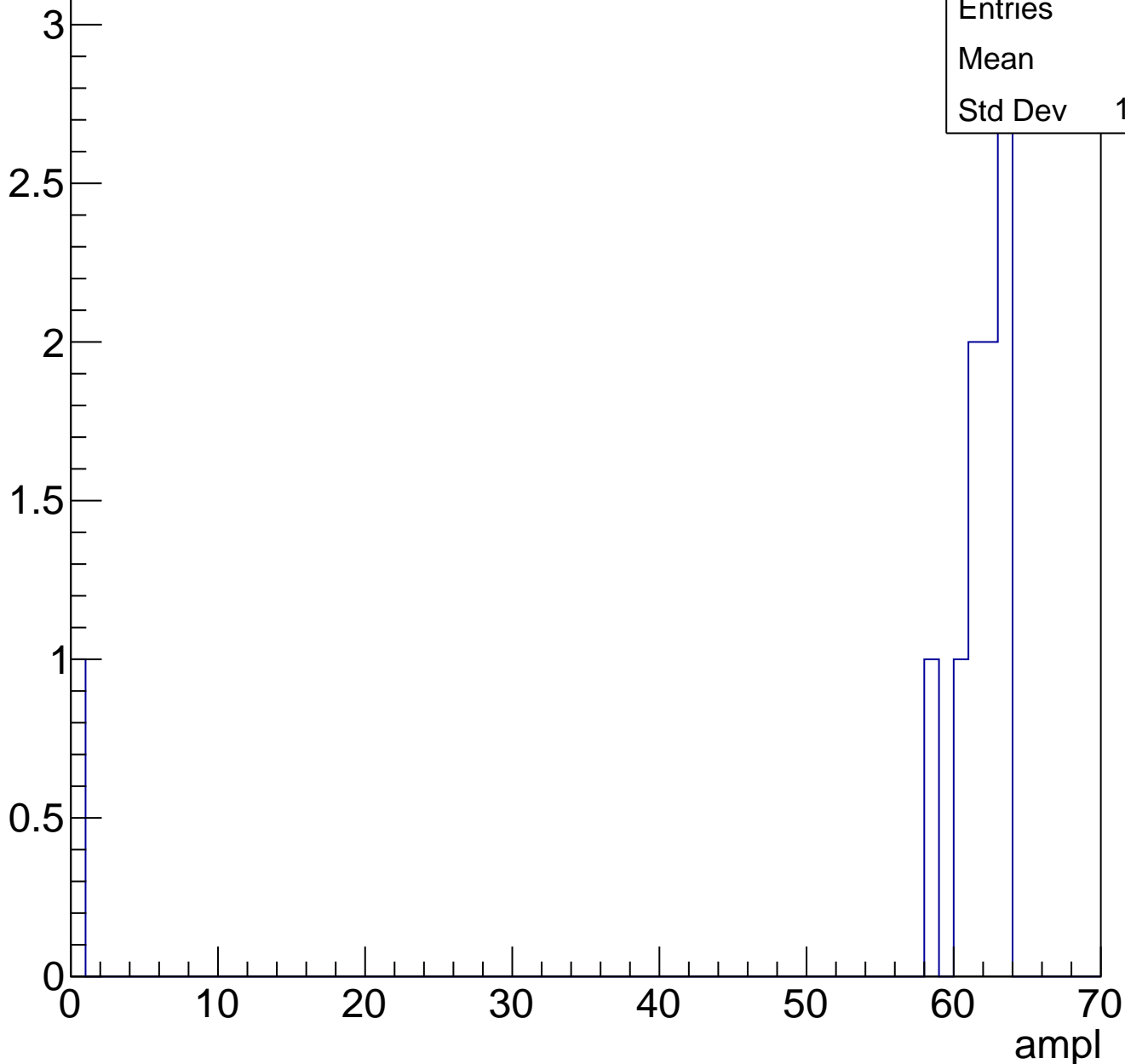




# B1L002S, U26-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch57, adc0

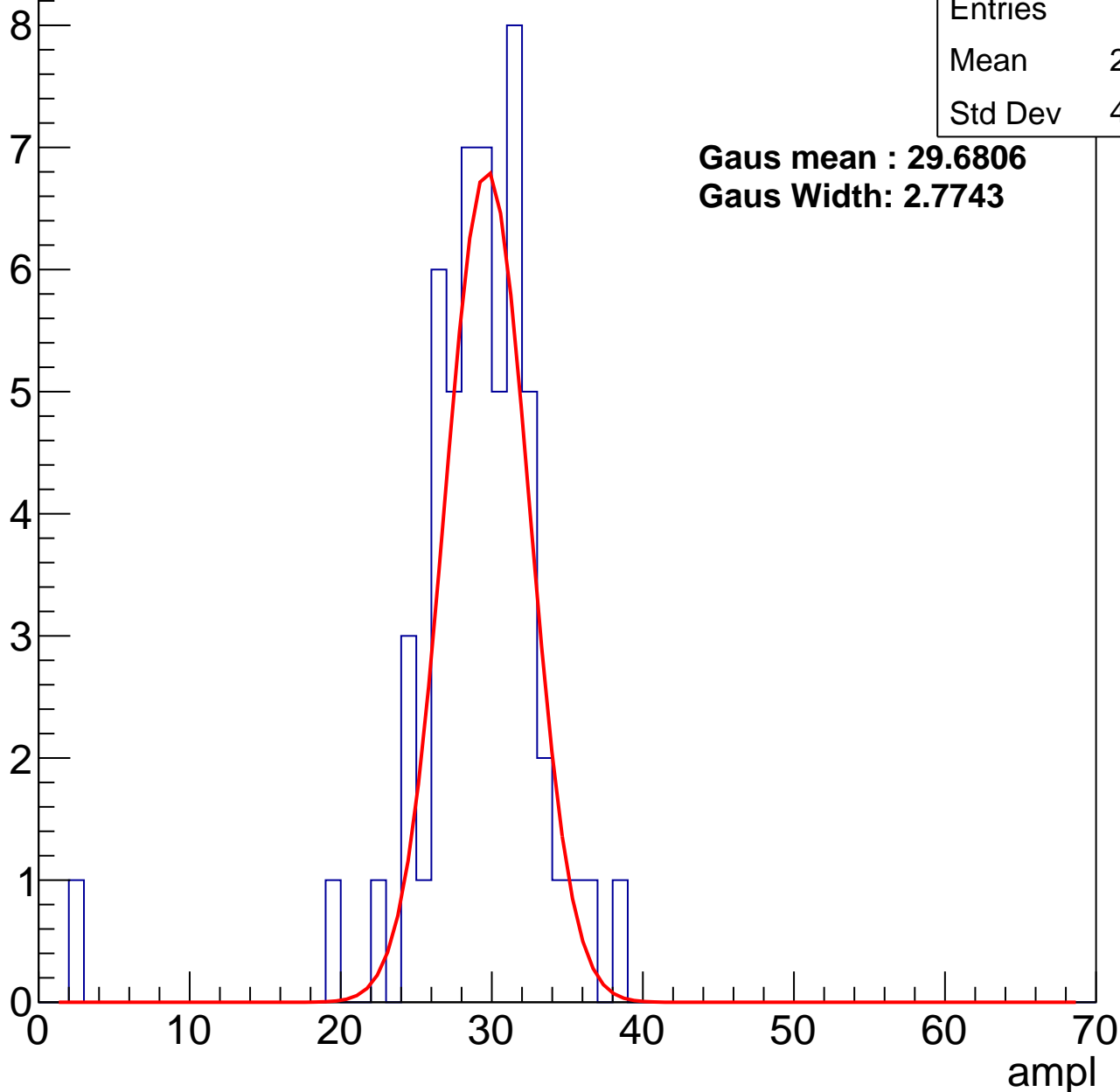
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	28.52
Std Dev	4.895

**Gaus mean : 29.6806**

**Gaus Width: 2.7743**



# B1L002S, U26-ch57, adc1

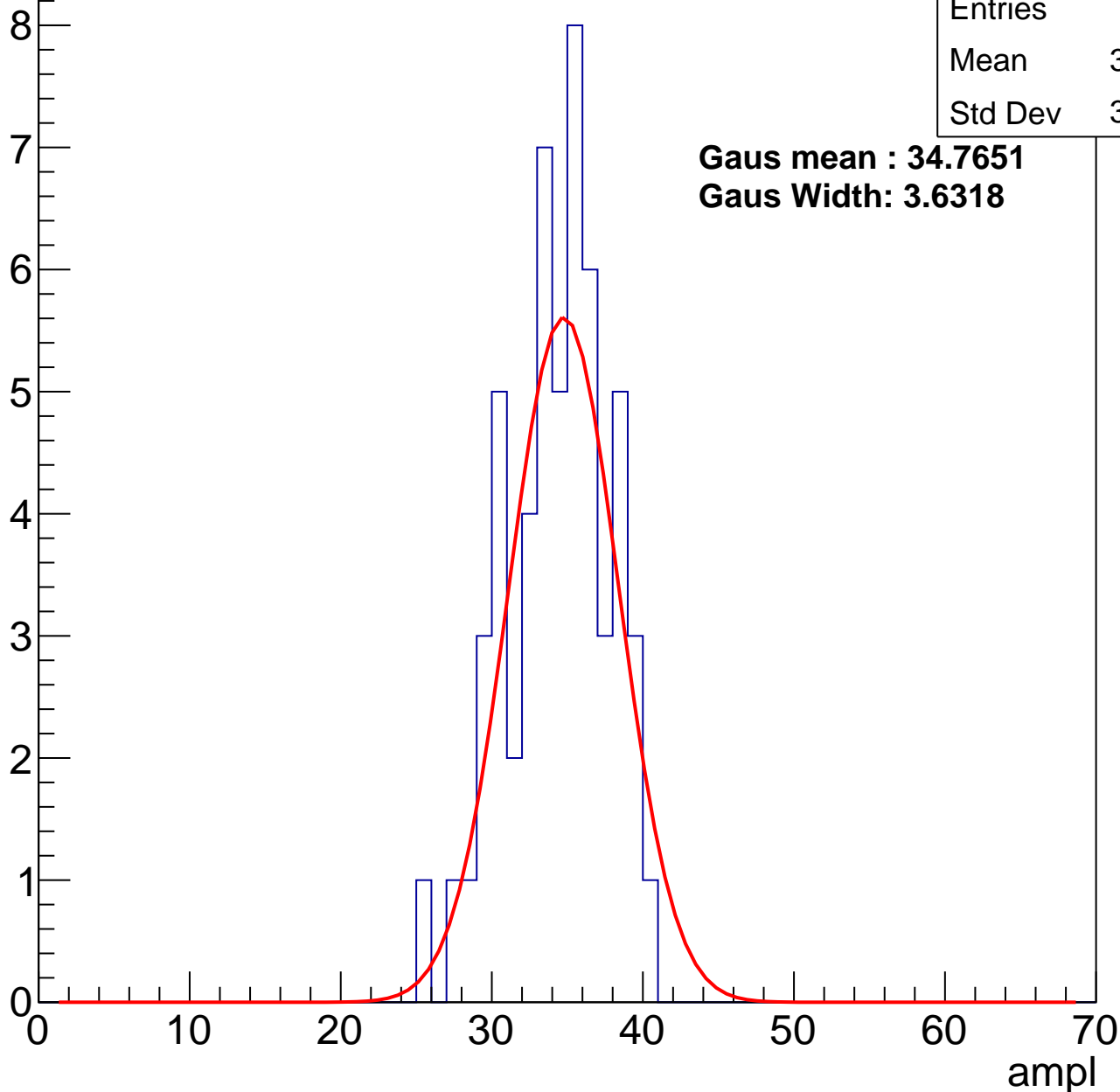
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	33.85
Std Dev	3.333

**Gaus mean : 34.7651**

**Gaus Width: 3.6318**



# B1L002S, U26-ch57, adc2

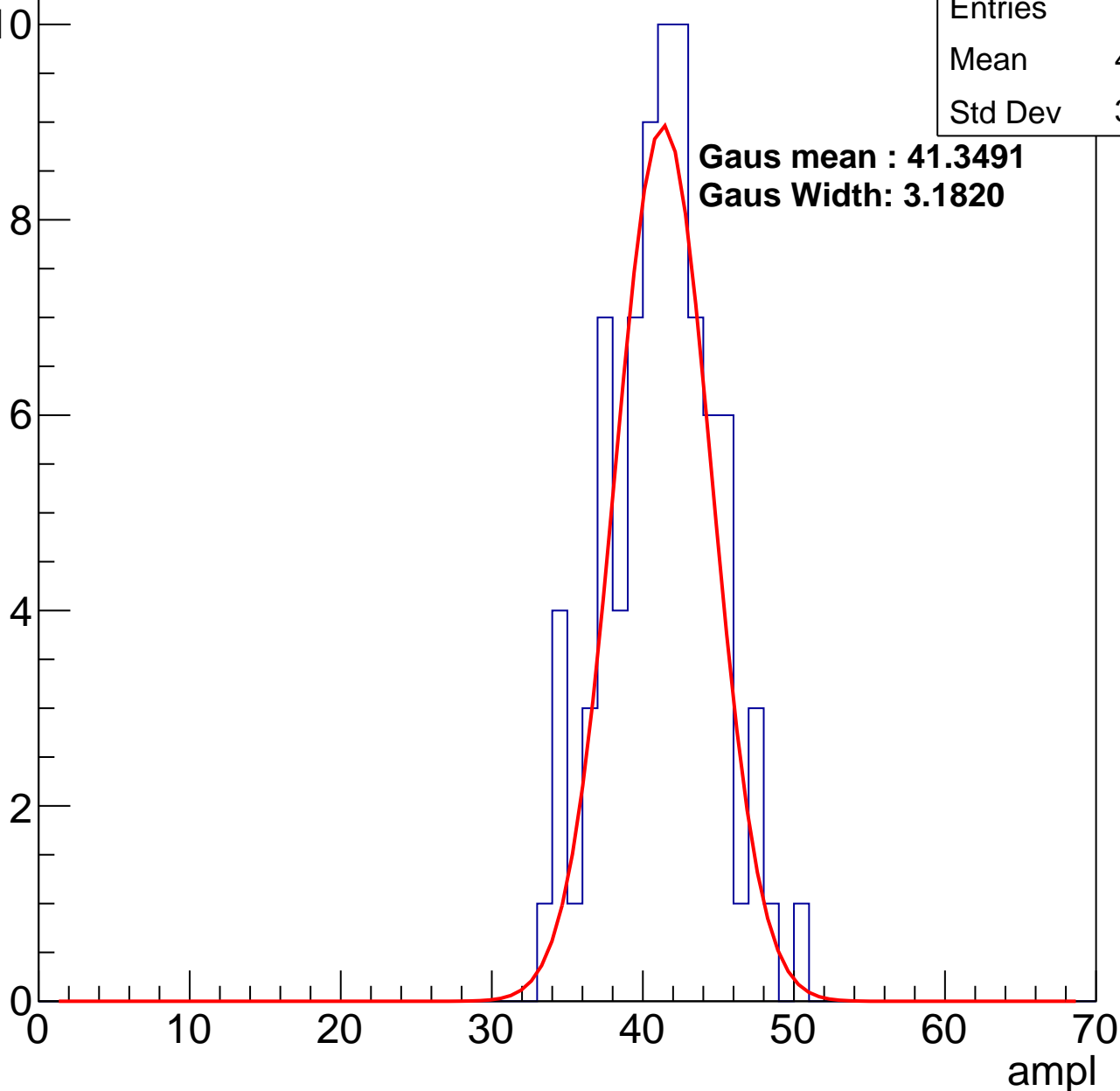
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	40.81
Std Dev	3.521

**Gaus mean : 41.3491**

**Gaus Width: 3.1820**

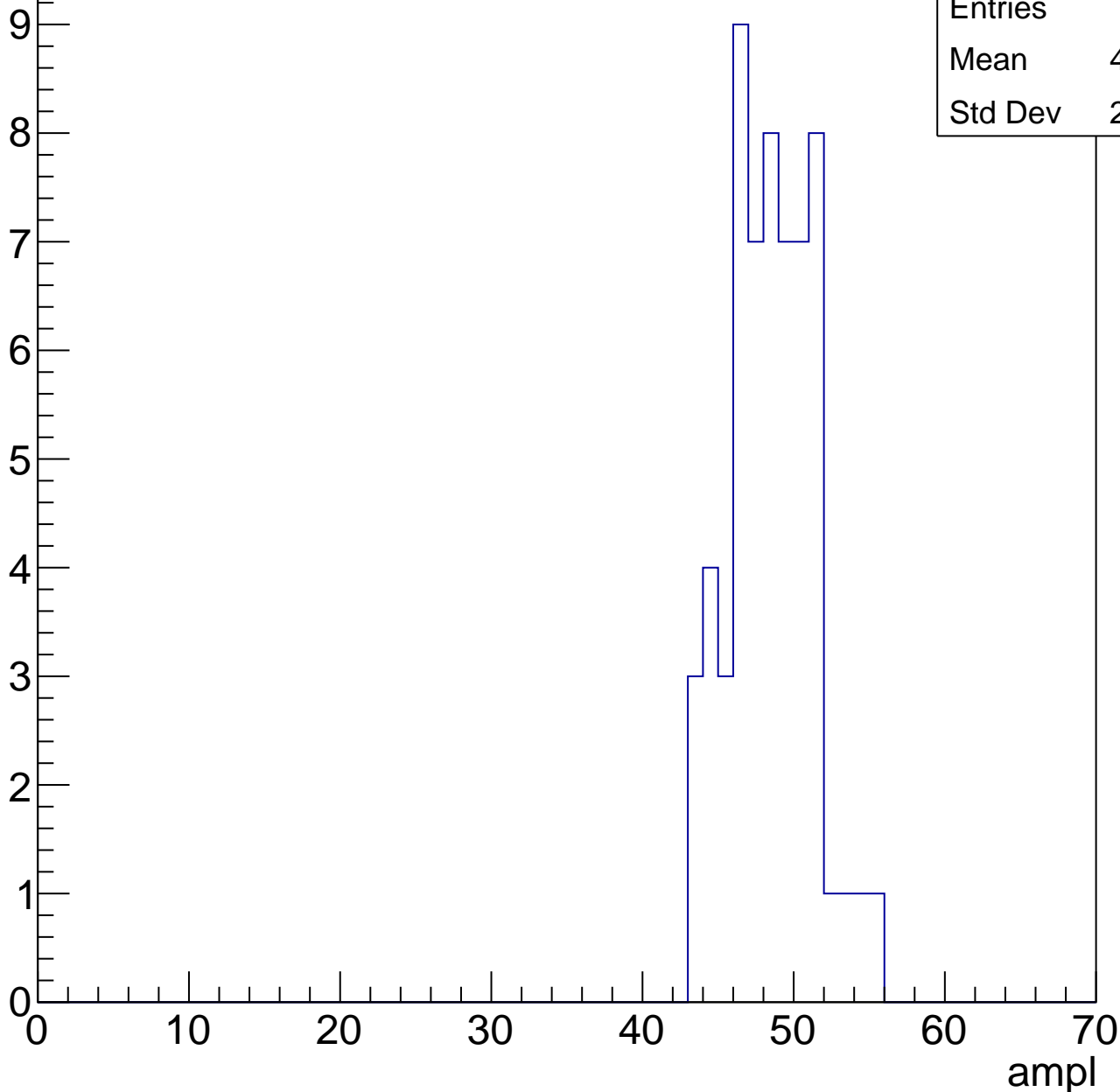


# B1L002S, U26-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.03
Std Dev	2.714

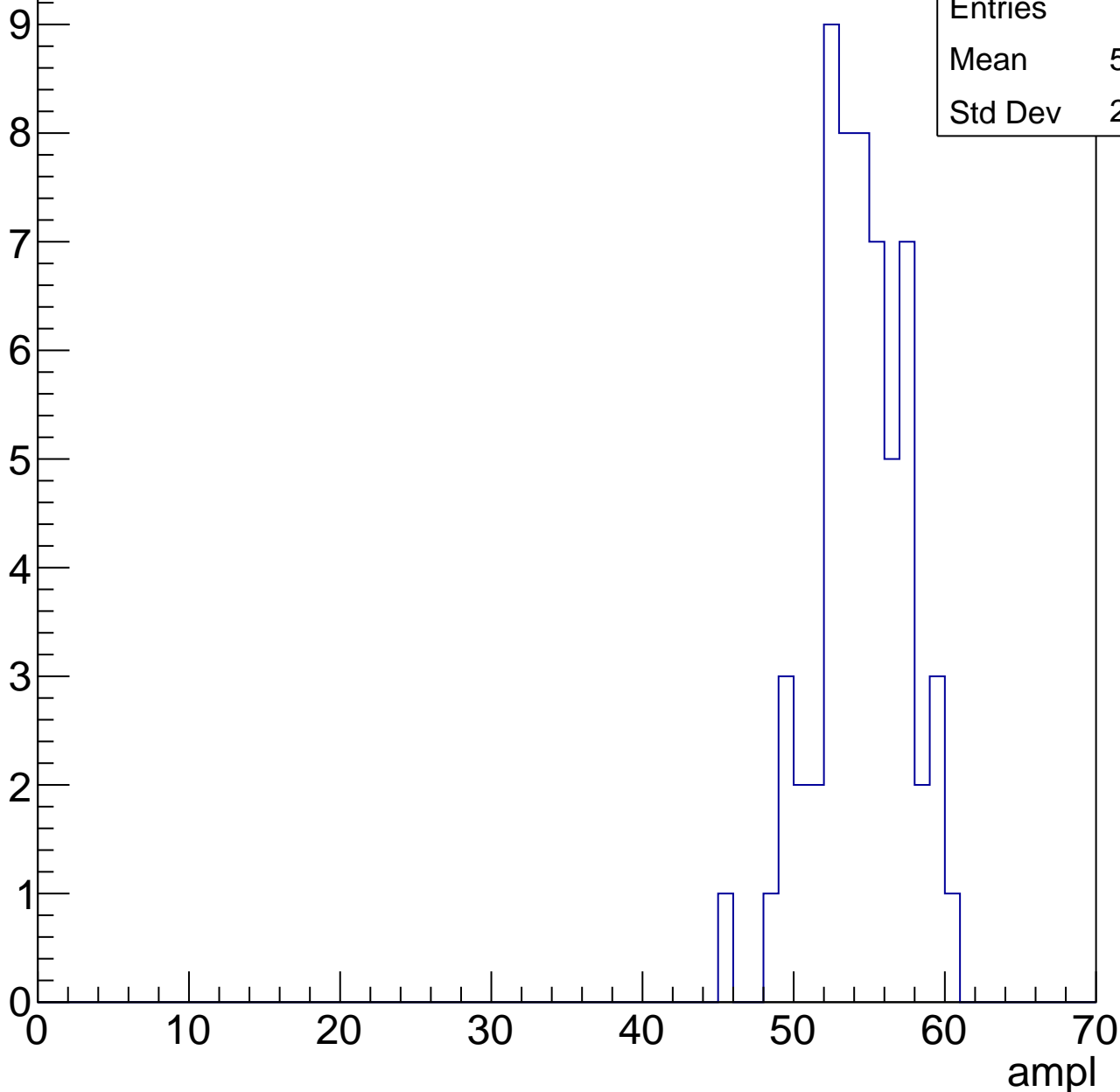


# B1L002S, U26-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

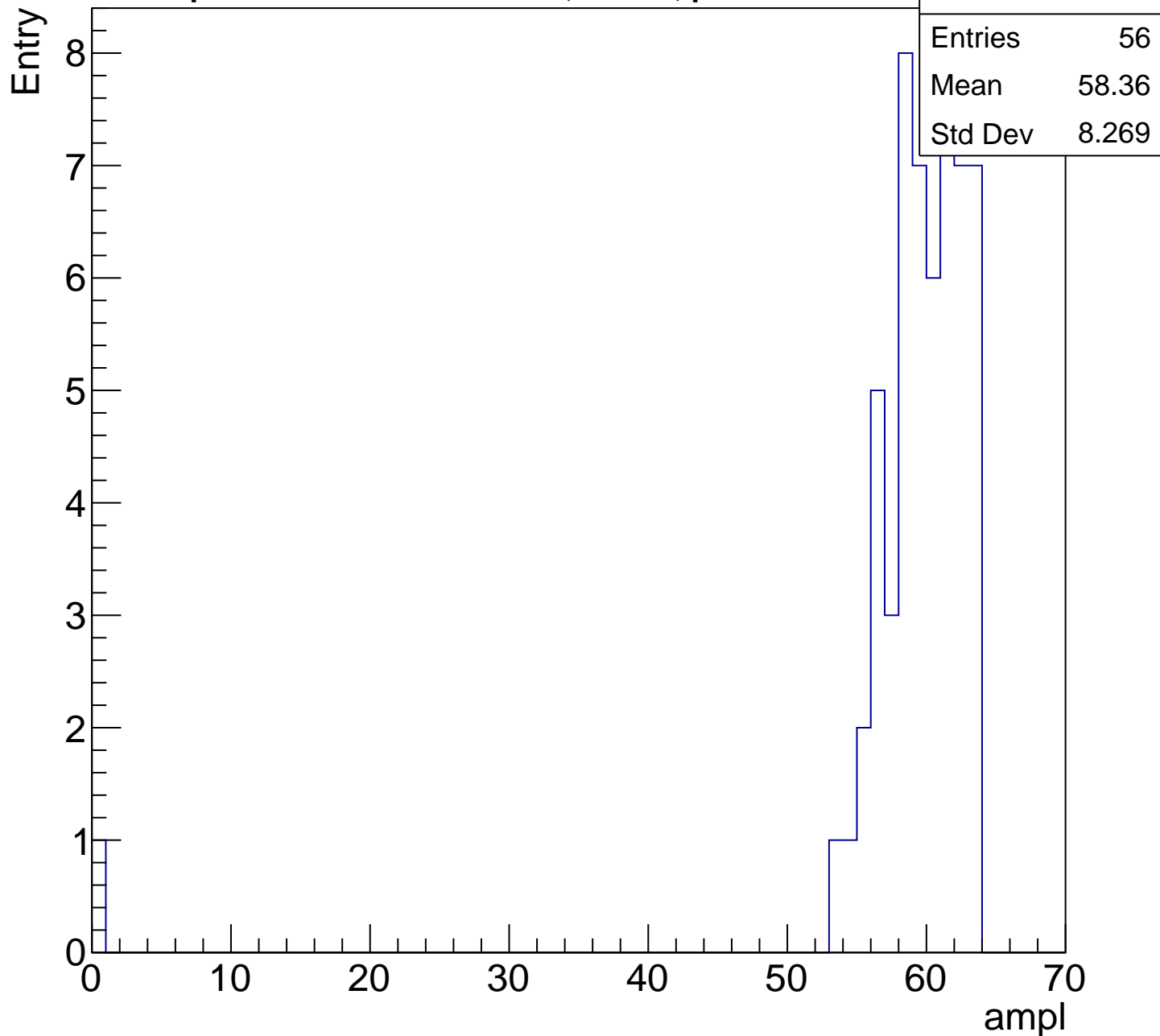
Entry

Entries	59
Mean	53.95
Std Dev	2.965



# B1L002S, U26-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.55
Std Dev	1.157



# B1L002S, U26-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch58, adc0

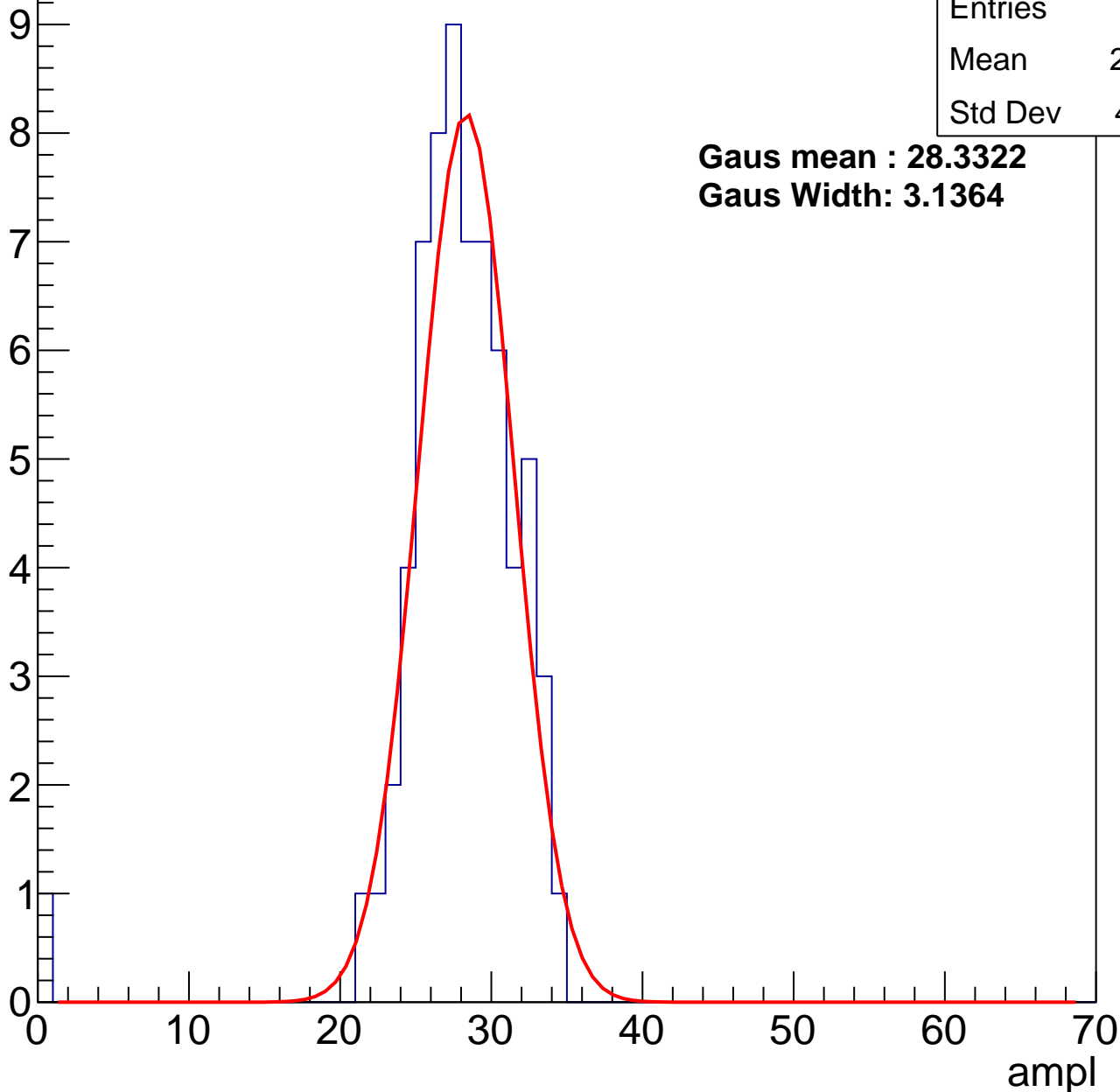
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.38
Std Dev	4.471

**Gaus mean : 28.3322**

**Gaus Width: 3.1364**



# B1L002S, U26-ch58, adc1

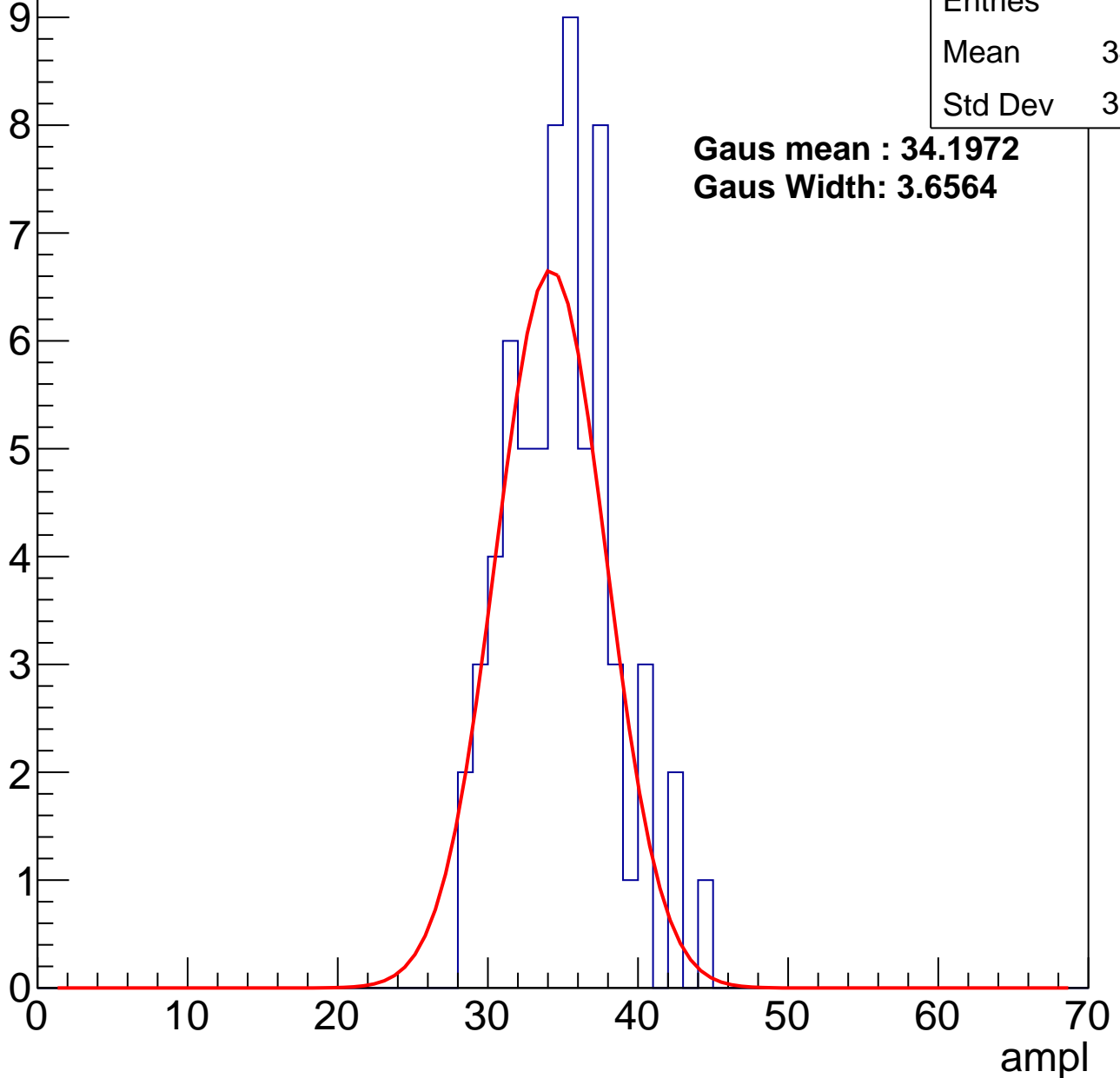
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.43
Std Dev	3.482

**Gaus mean : 34.1972**

**Gaus Width: 3.6564**



# B1L002S, U26-ch58, adc2

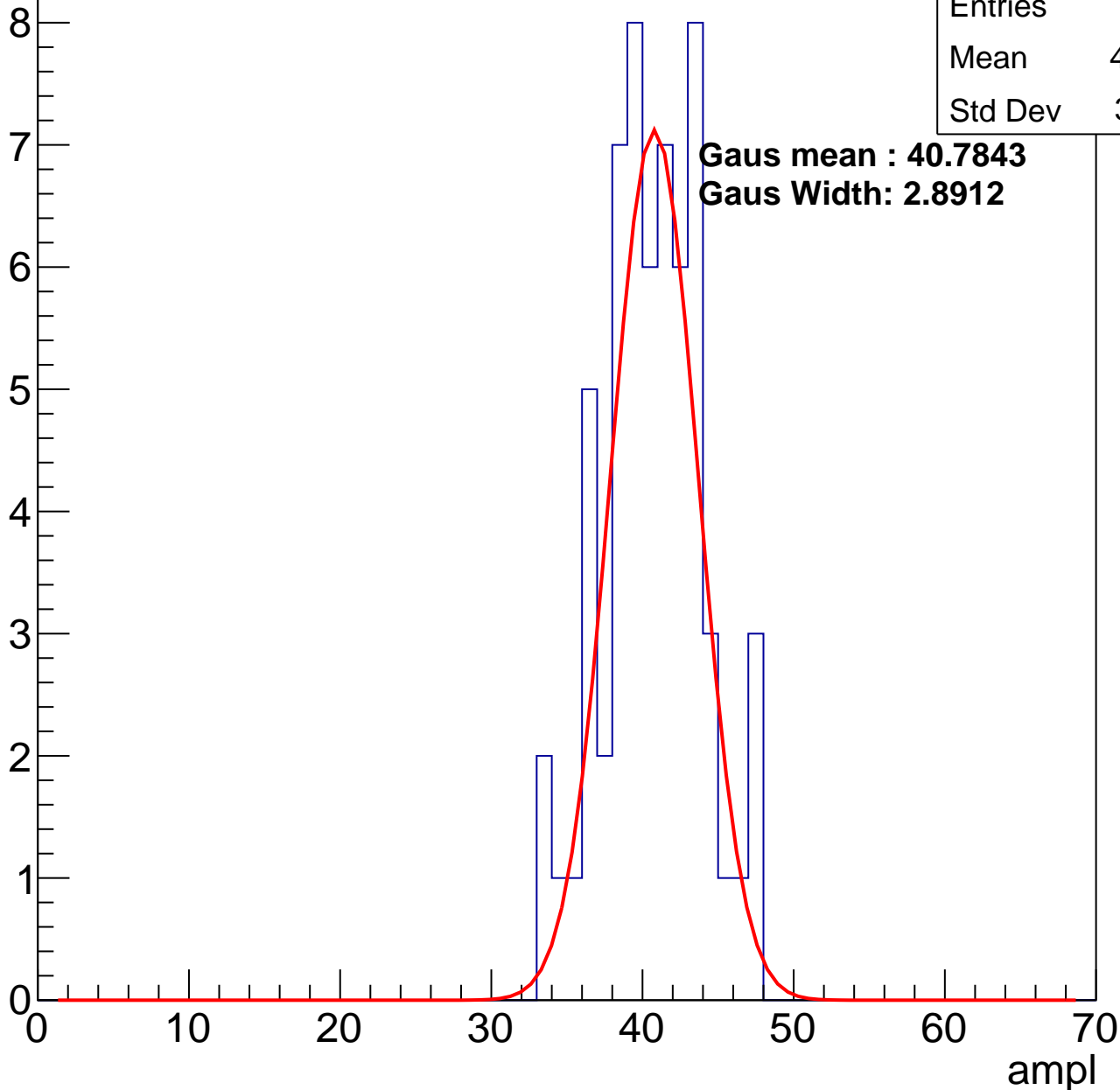
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.23
Std Dev	3.261

**Gaus mean : 40.7843**

**Gaus Width: 2.8912**

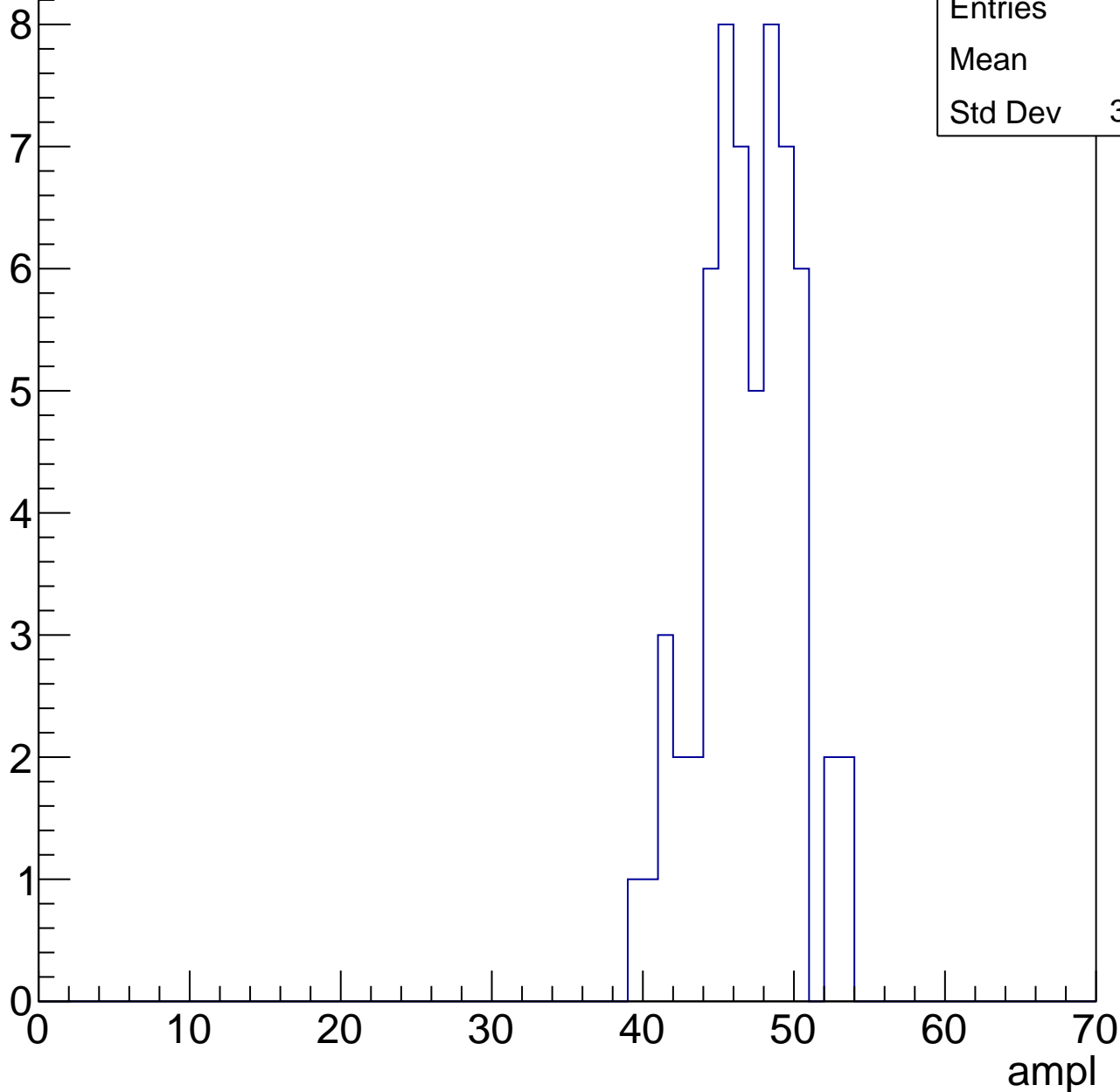


# B1L002S, U26-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.5
Std Dev	3.144

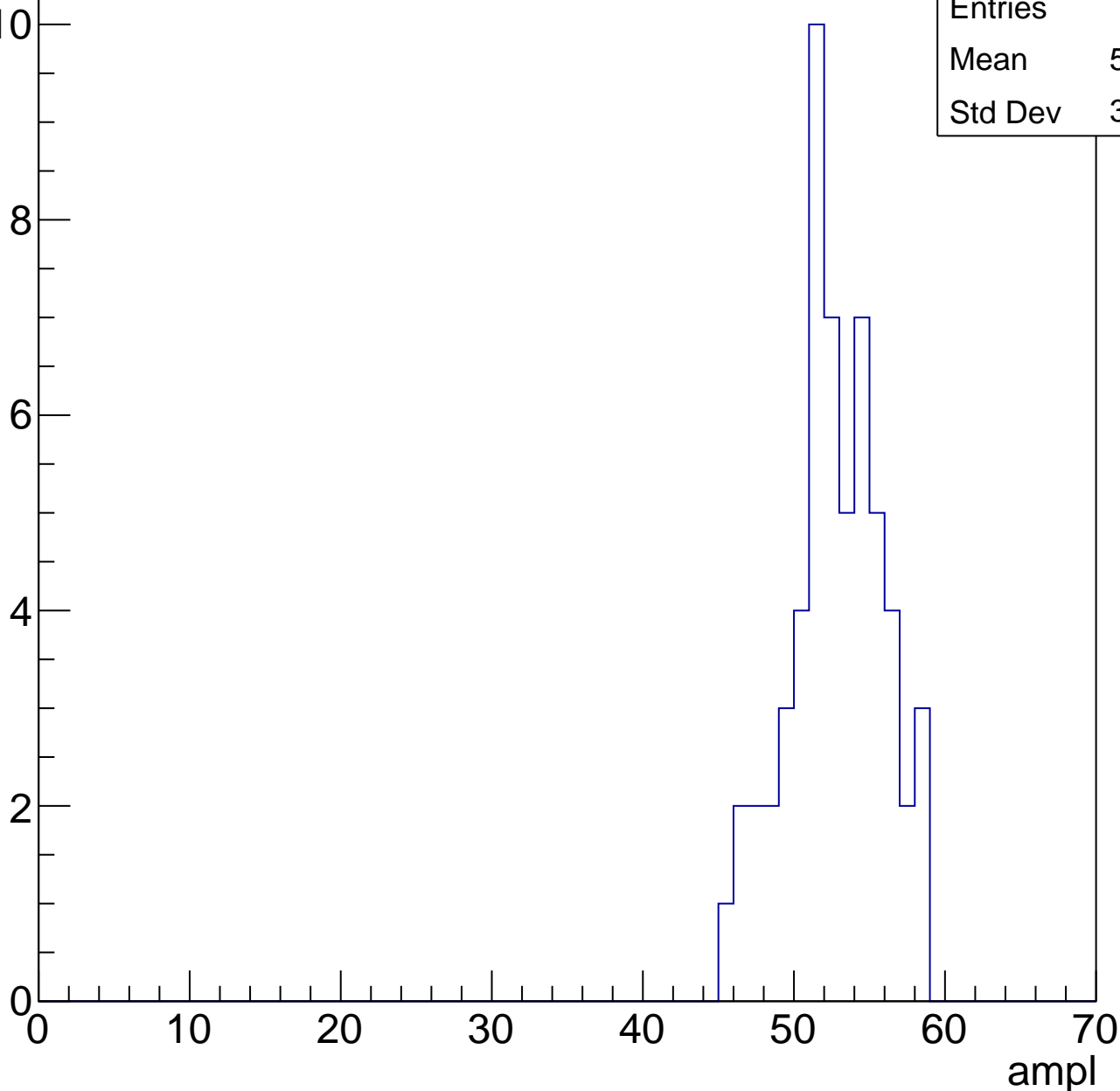


# B1L002S, U26-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.25
Std Dev	3.114

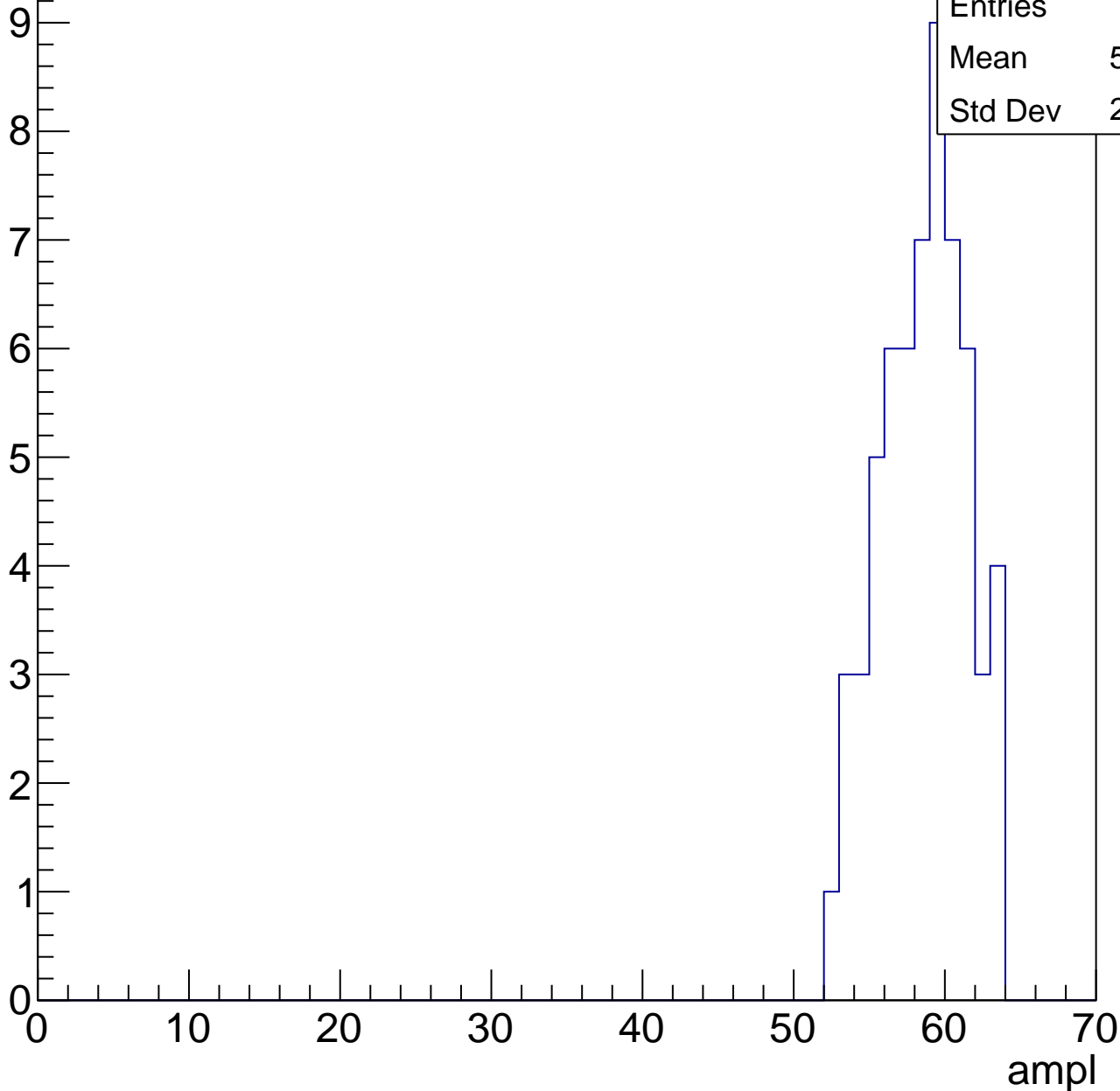


# B1L002S, U26-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	58.12
Std Dev	2.805

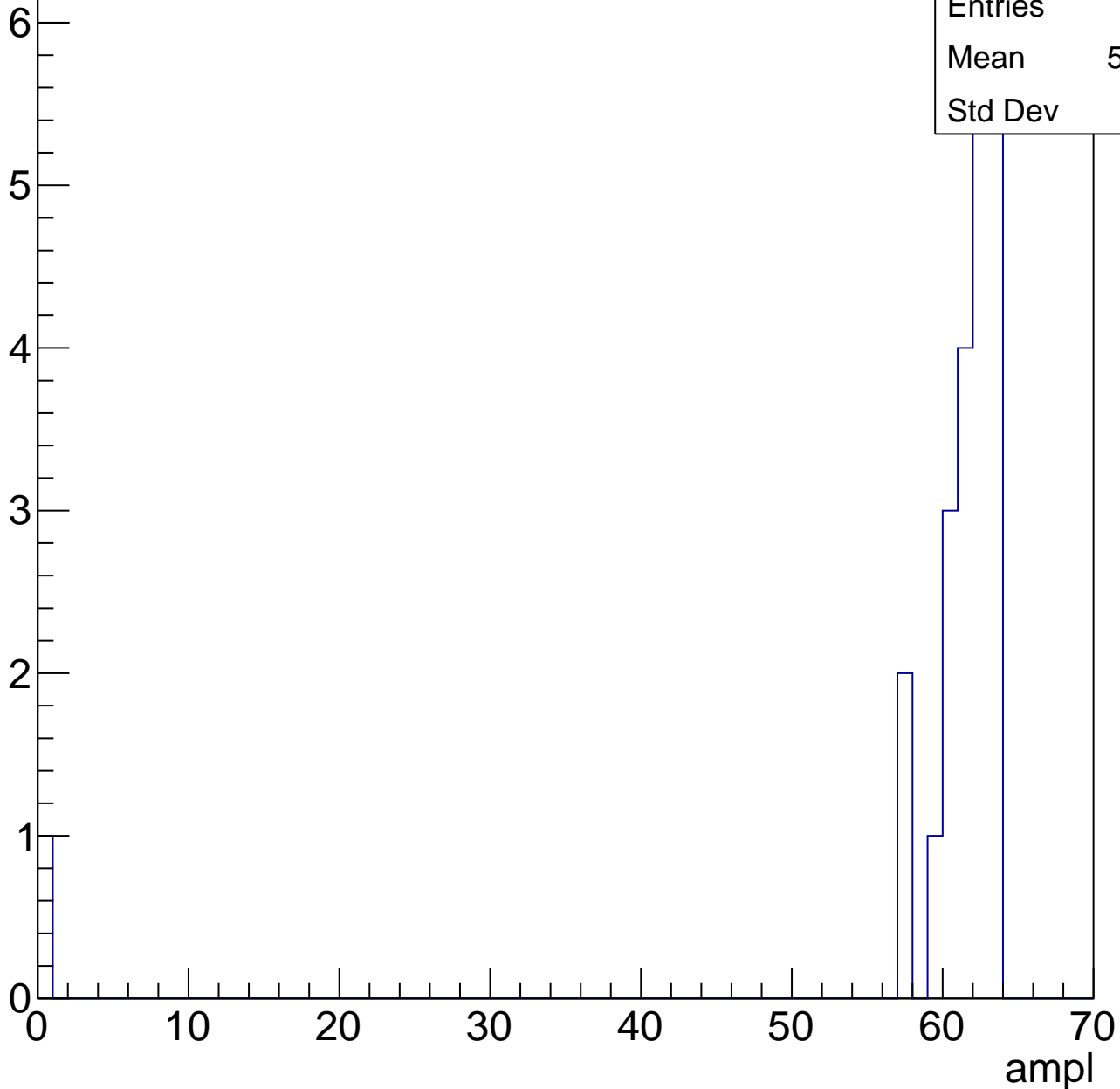


# B1L002S, U26-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.57
Std Dev	12.6

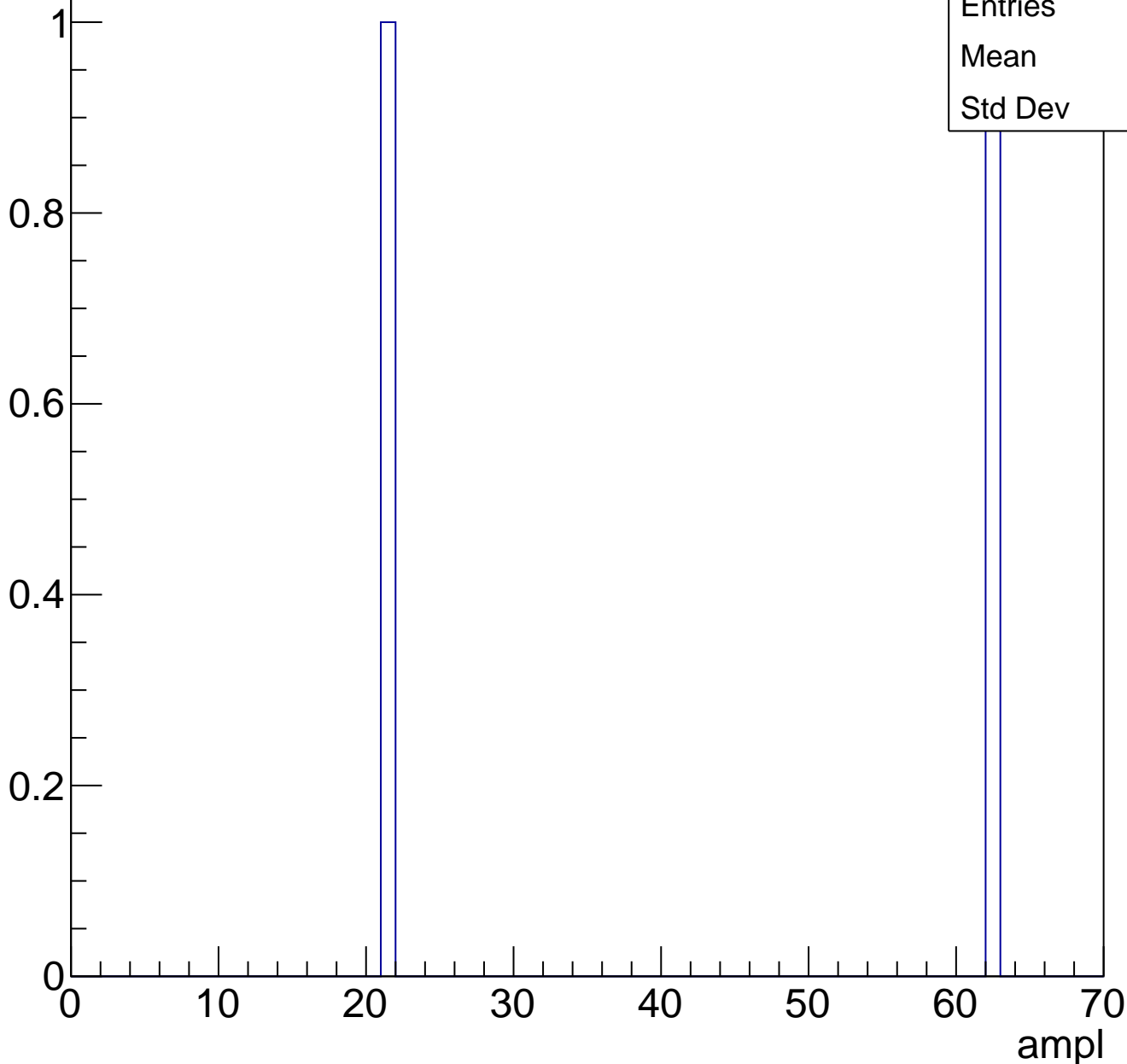




# B1L002S, U26-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch59, adc0

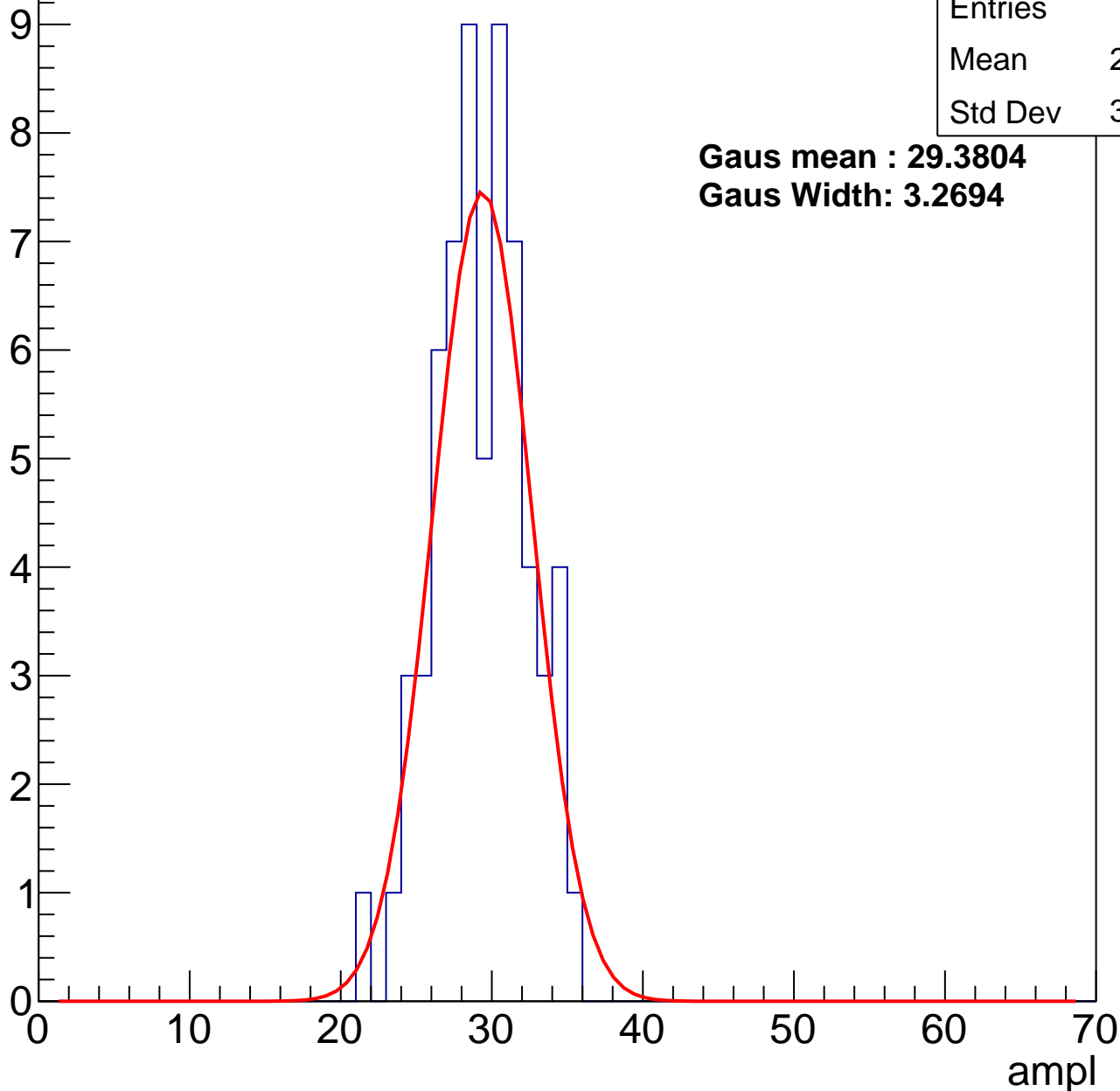
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.86
Std Dev	3.007

**Gaus mean : 29.3804**

**Gaus Width: 3.2694**



# B1L002S, U26-ch59, adc1

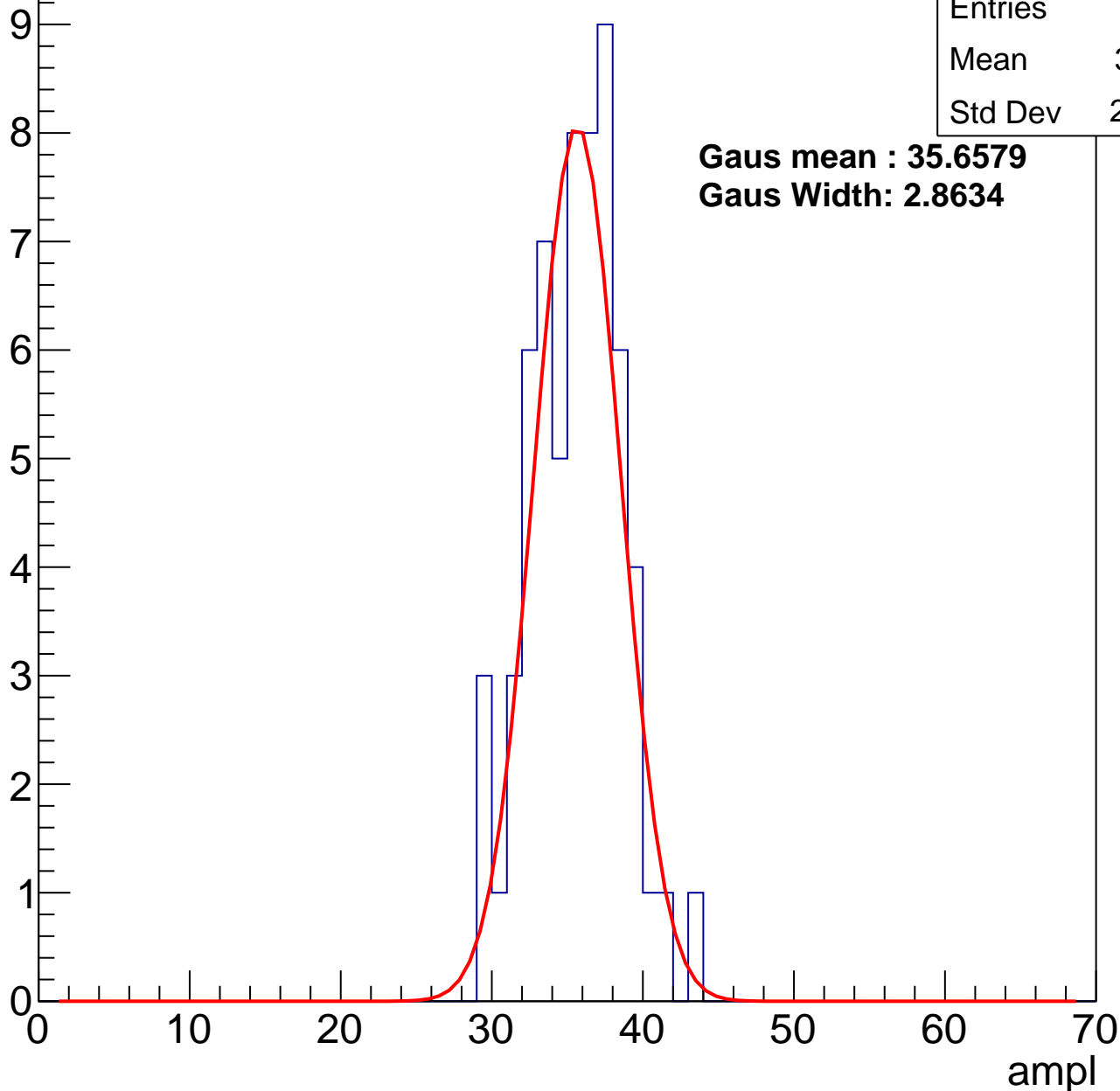
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.11
Std Dev	2.966

**Gaus mean : 35.6579**

**Gaus Width: 2.8634**



# B1L002S, U26-ch59, adc2

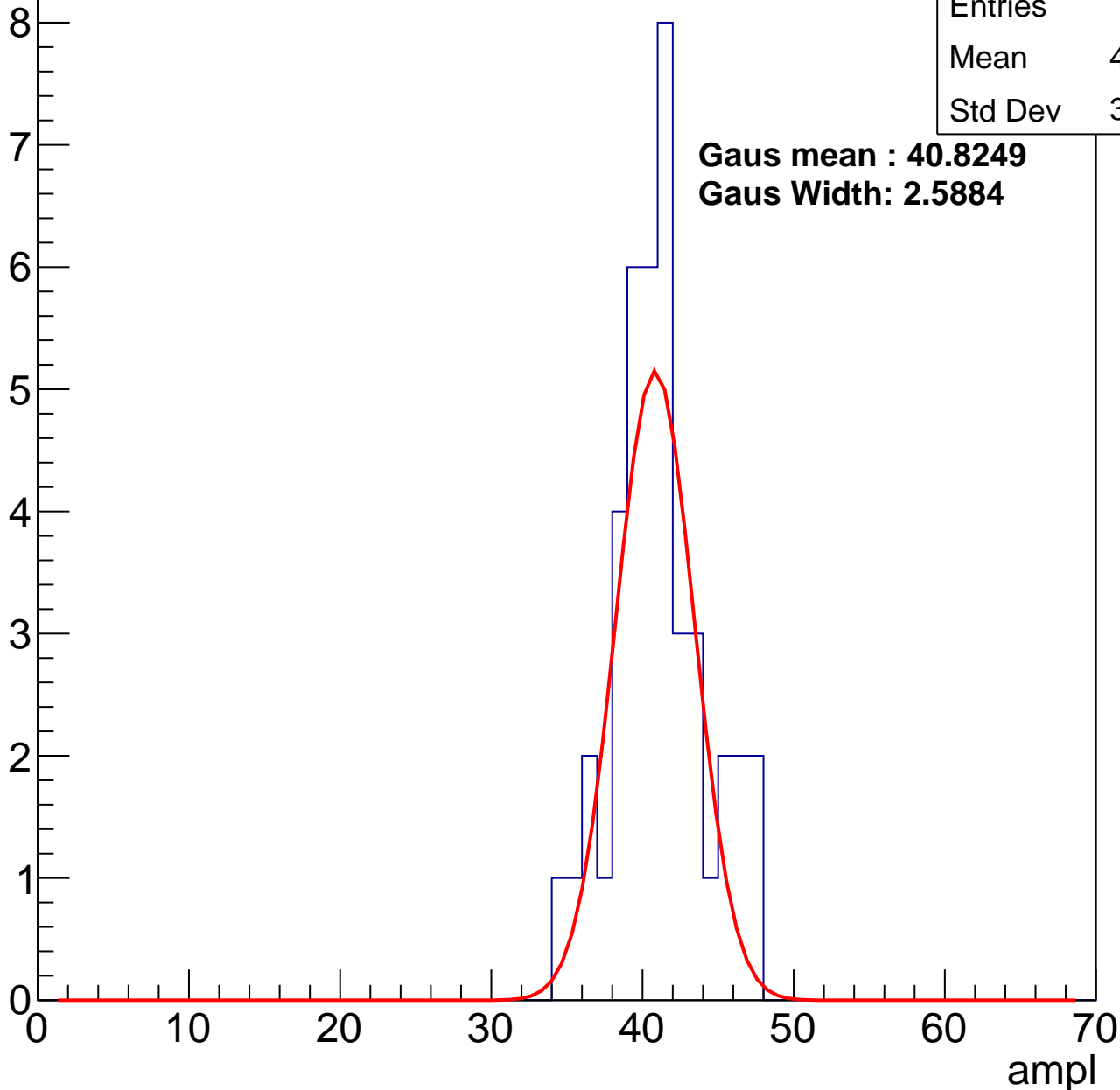
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	40.64
Std Dev	3.046

**Gaus mean : 40.8249**

**Gaus Width: 2.5884**

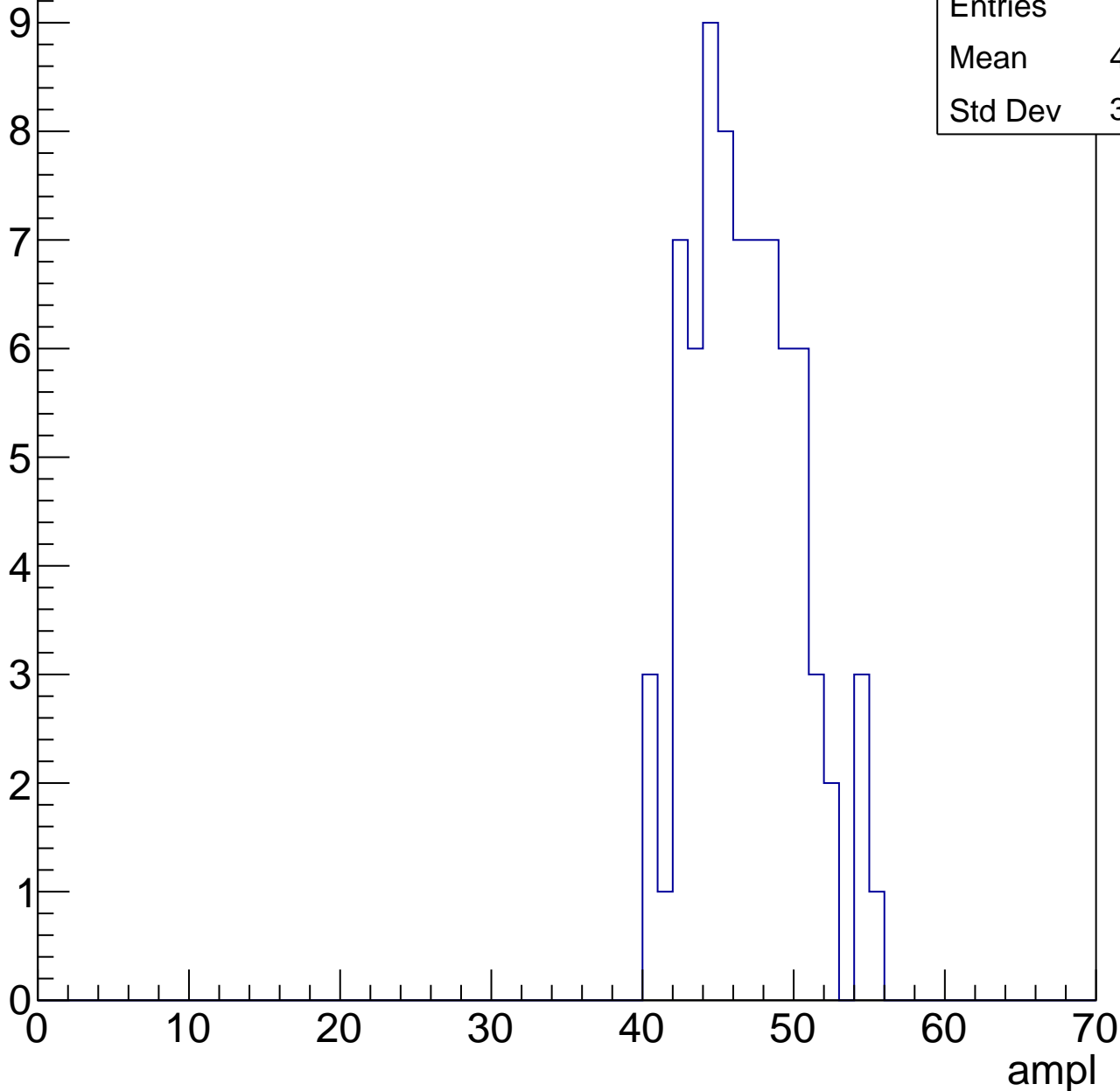


# B1L002S, U26-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

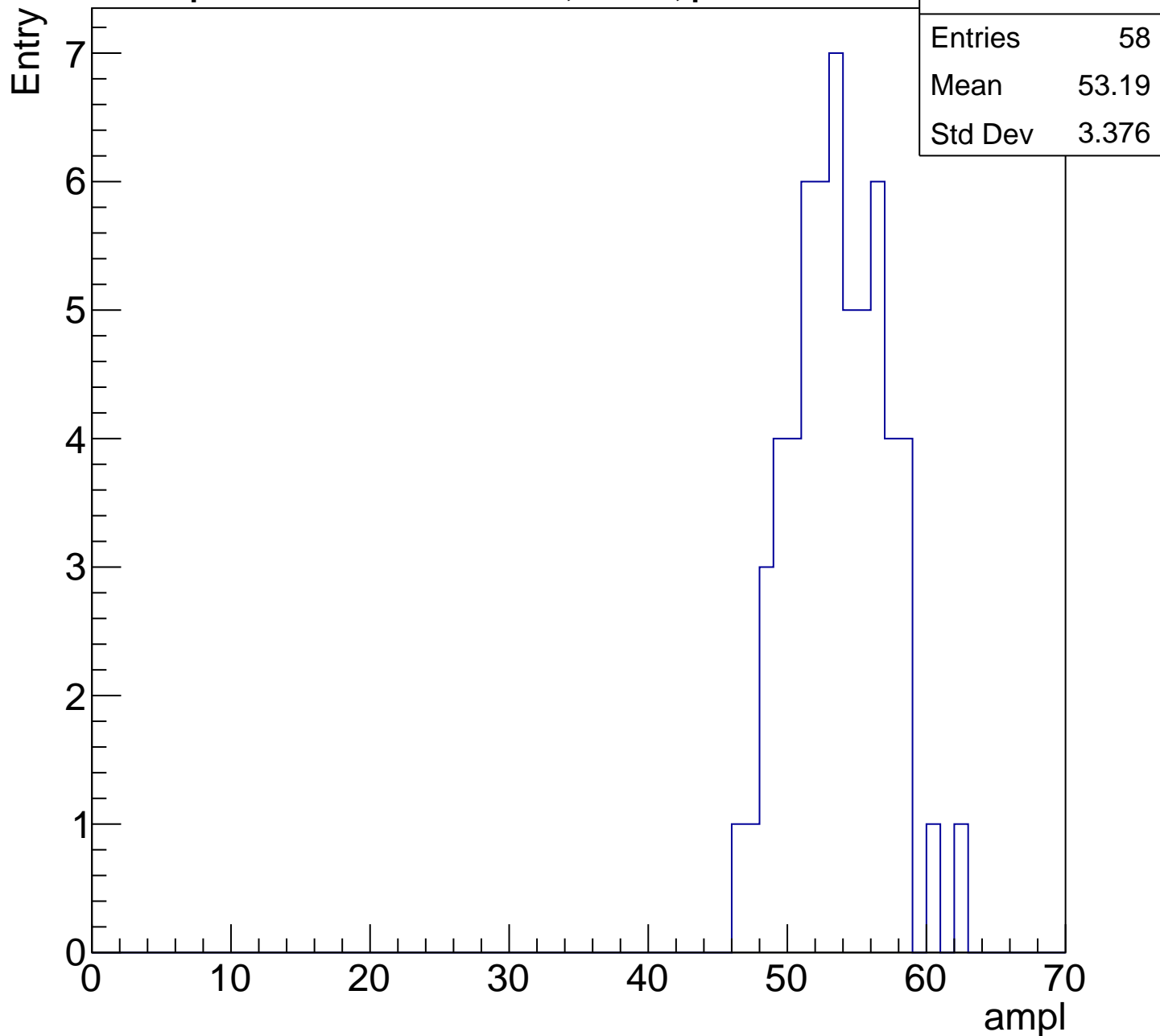
Entry

Entries	76
Mean	46.37
Std Dev	3.513



# B1L002S, U26-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

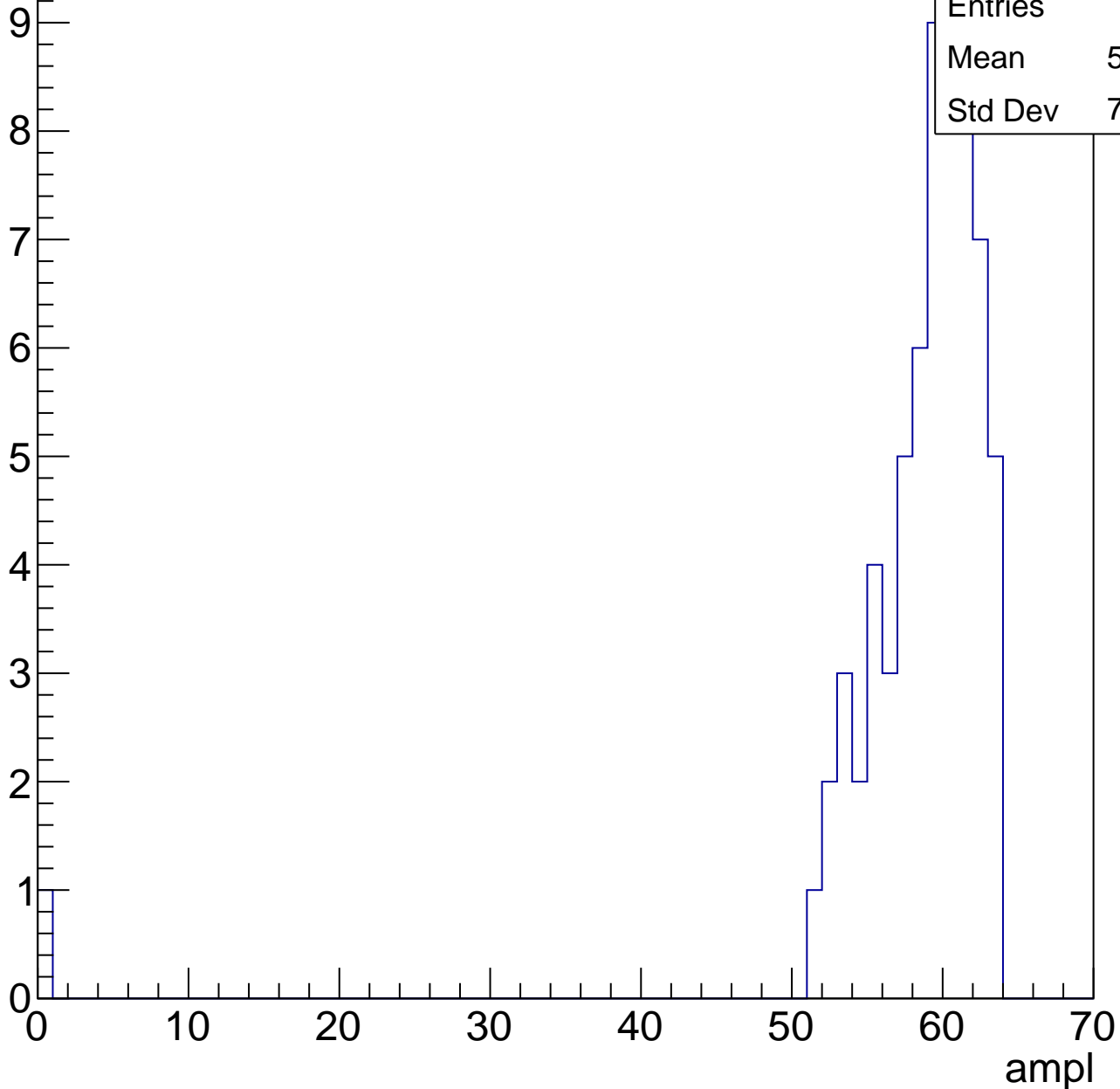


# B1L002S, U26-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.76
Std Dev	7.789

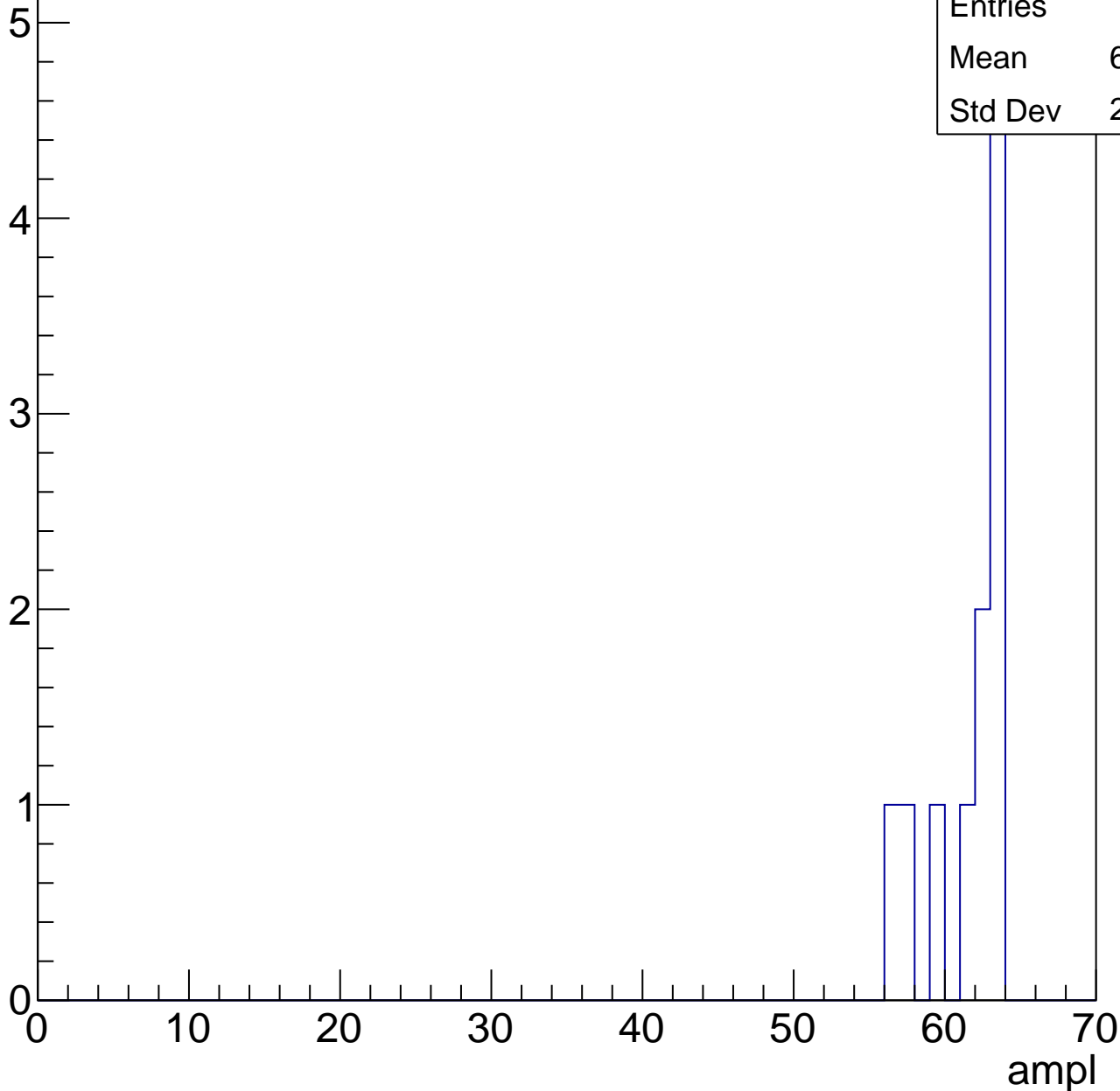


# B1L002S, U26-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	61.09
Std Dev	2.466





# B1L002S, U26-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch60, adc0

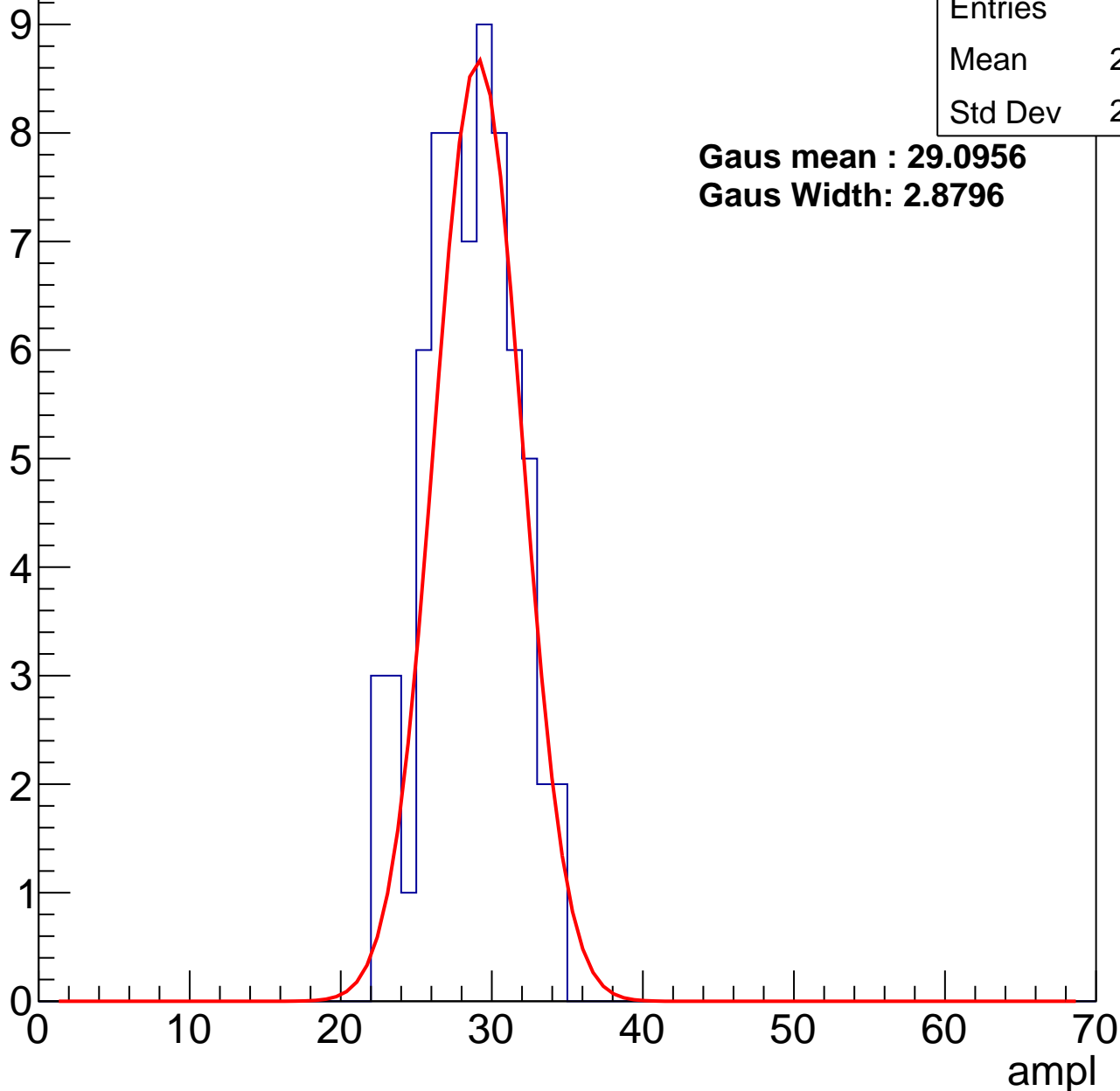
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	28.09
Std Dev	2.944

**Gaus mean : 29.0956**

**Gaus Width: 2.8796**



# B1L002S, U26-ch60, adc1

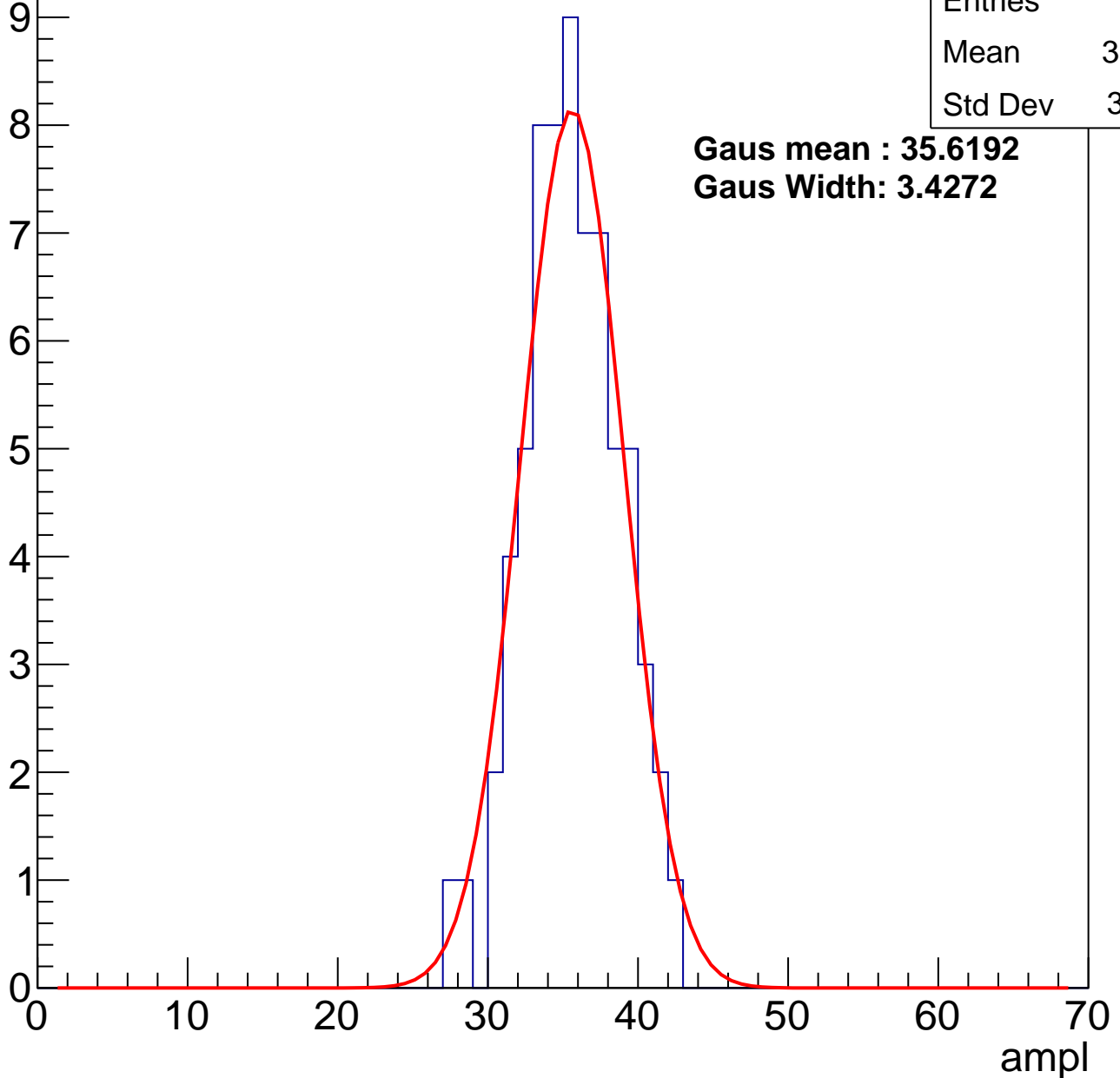
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.15
Std Dev	3.131

**Gaus mean : 35.6192**

**Gaus Width: 3.4272**



# B1L002S, U26-ch60, adc2

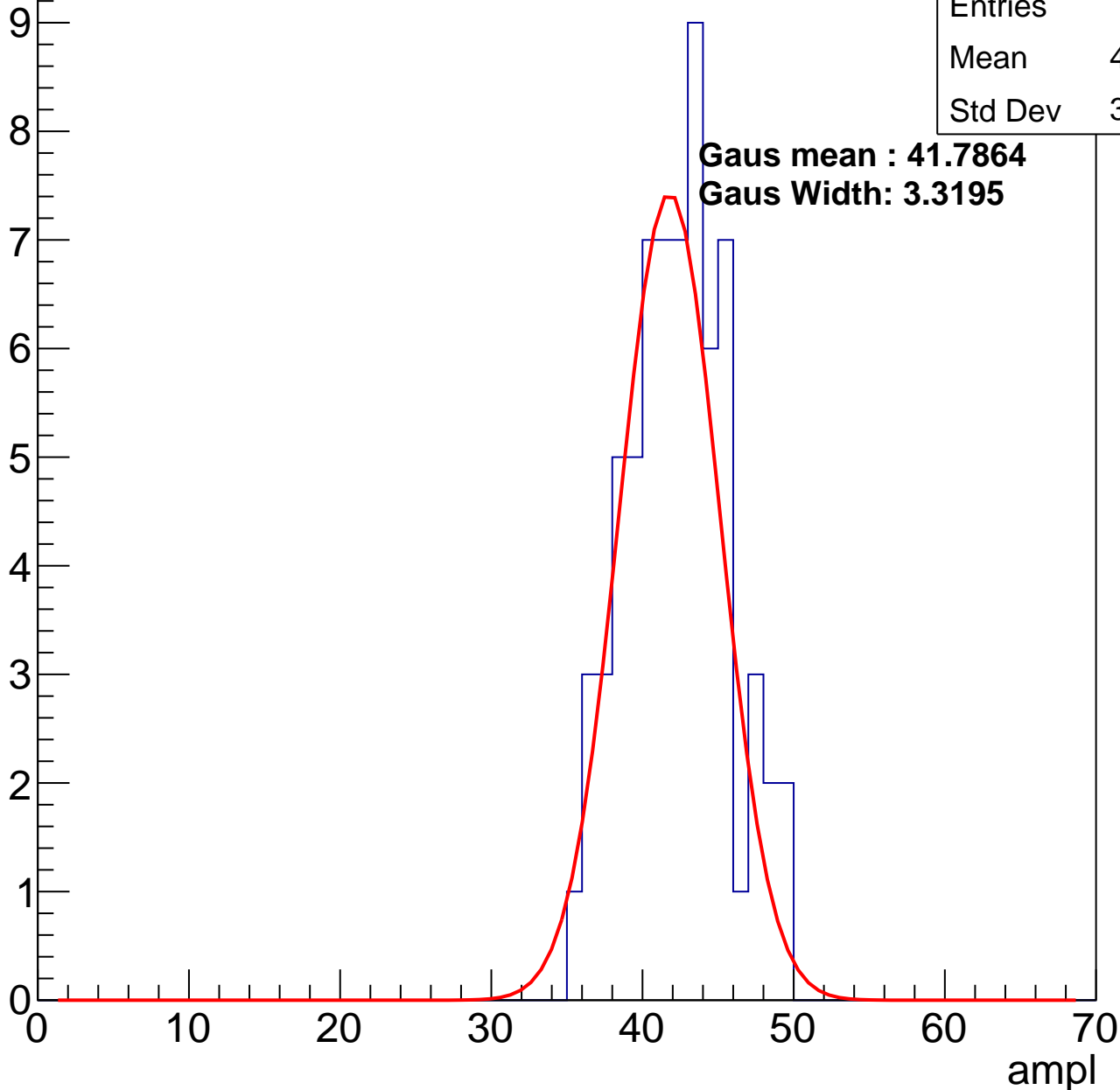
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	41.87
Std Dev	3.316

**Gaus mean : 41.7864**

**Gaus Width: 3.3195**

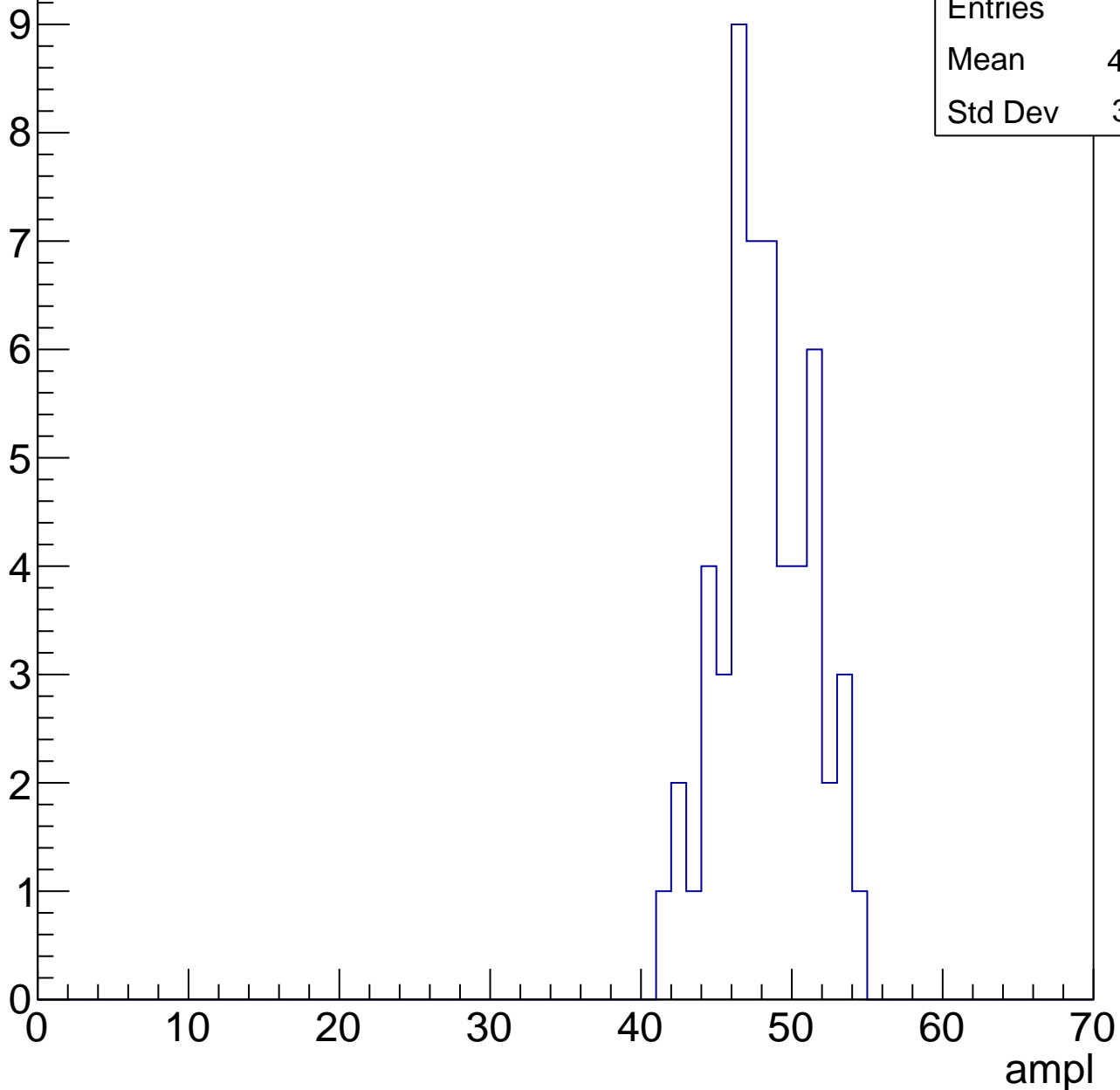


# B1L002S, U26-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	47.72
Std Dev	3.021

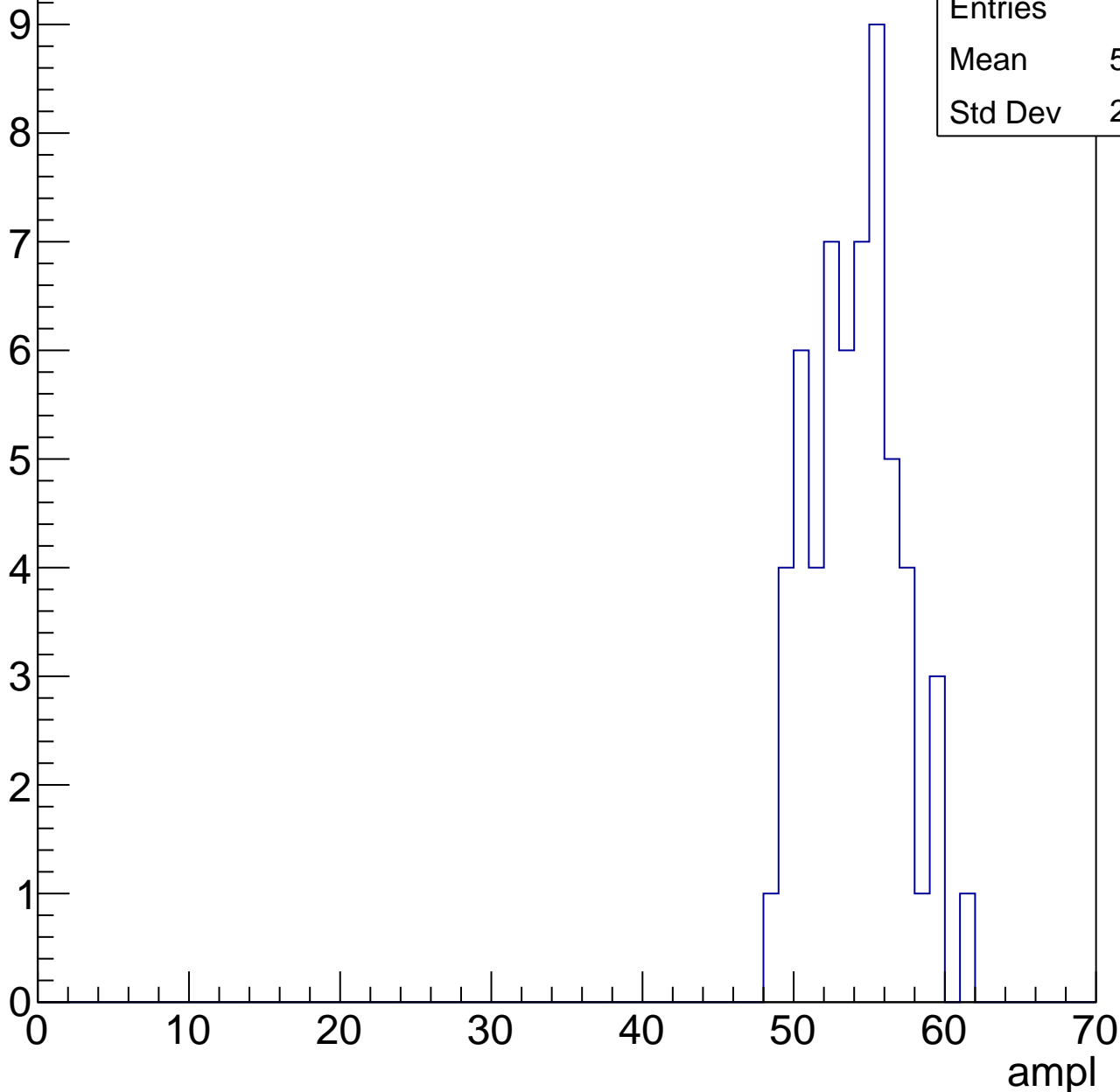


# B1L002S, U26-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.57
Std Dev	2.925

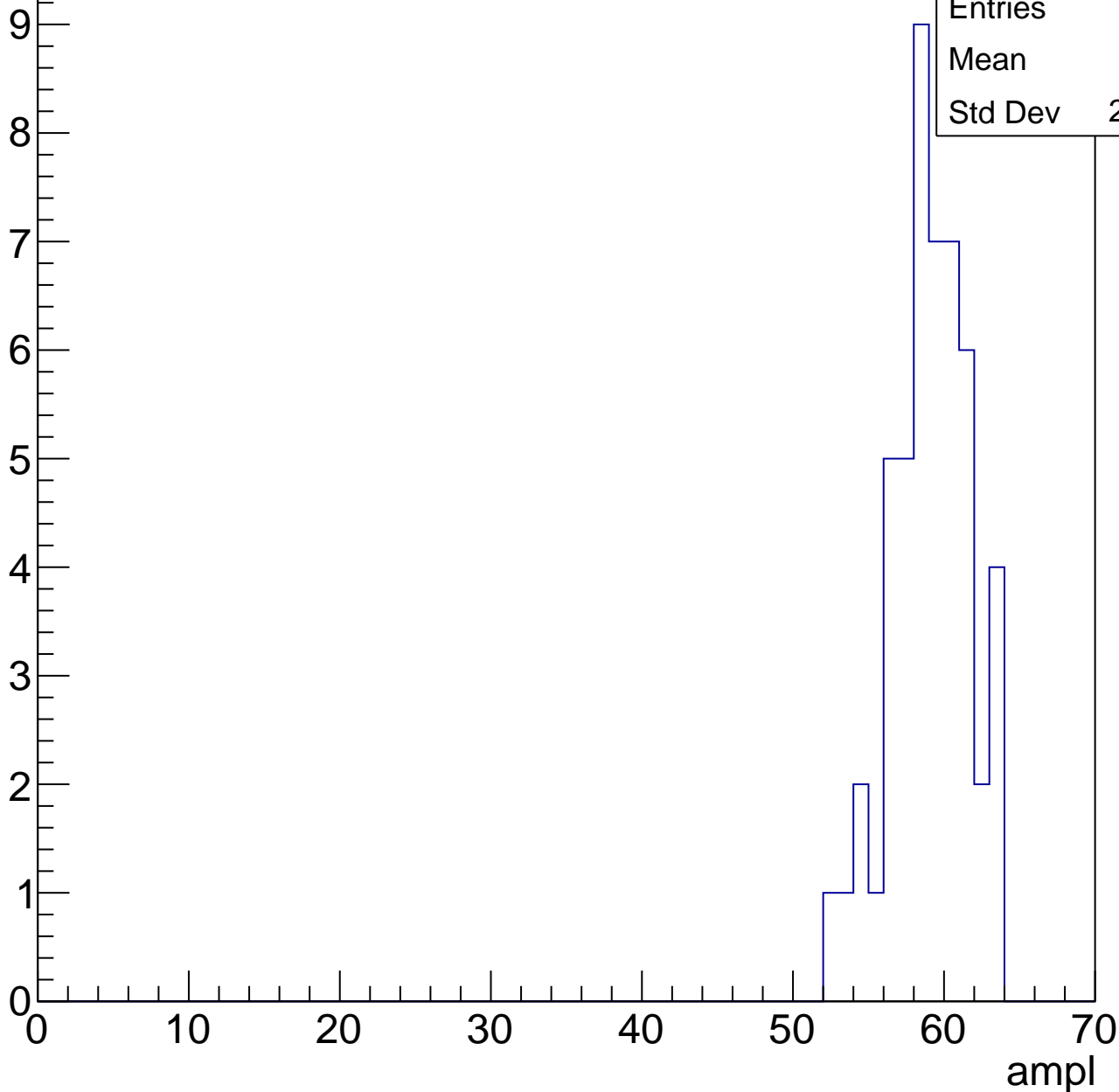


# B1L002S, U26-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	58.6
Std Dev	2.569

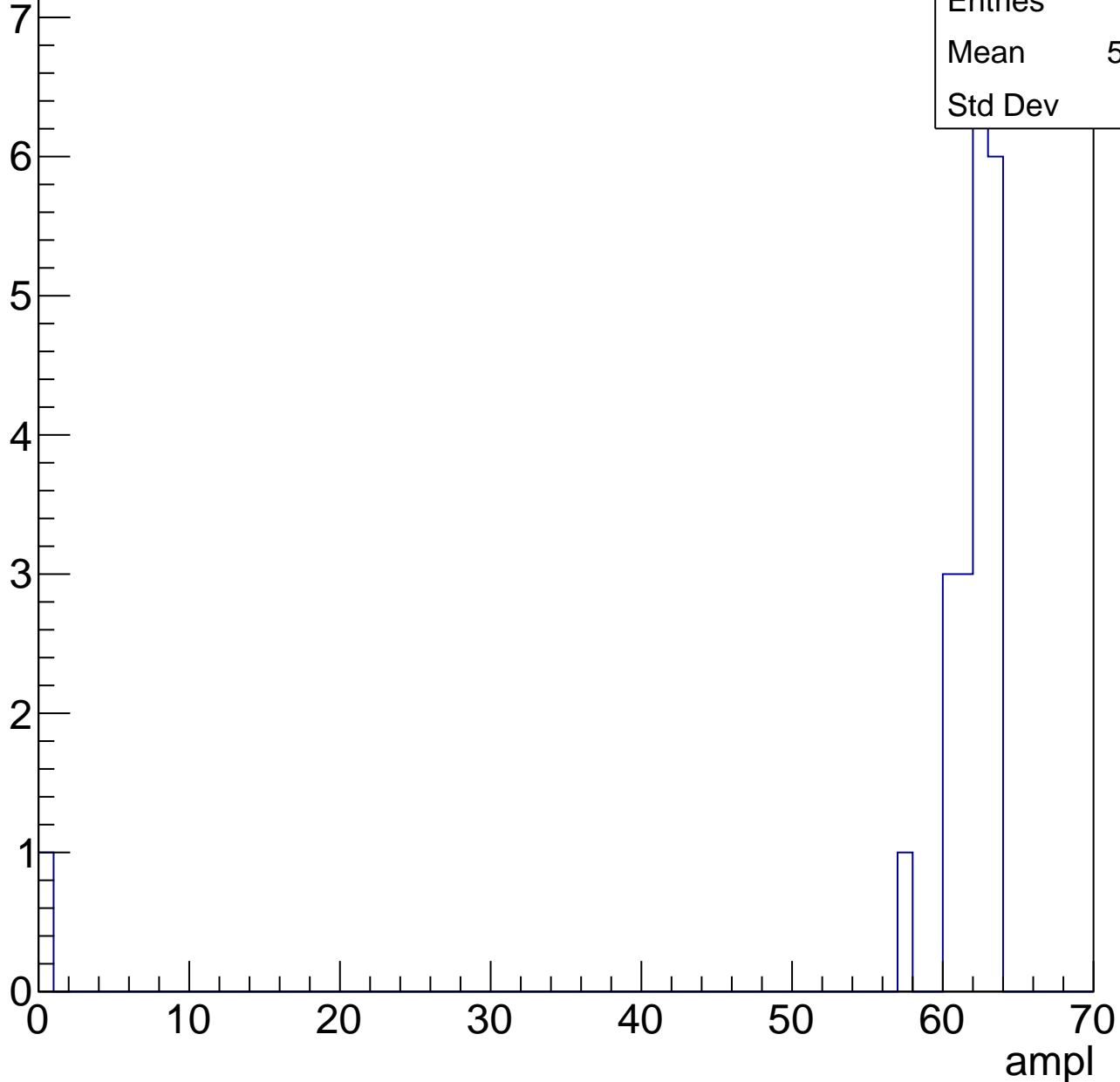


# B1L002S, U26-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.67
Std Dev	13.2

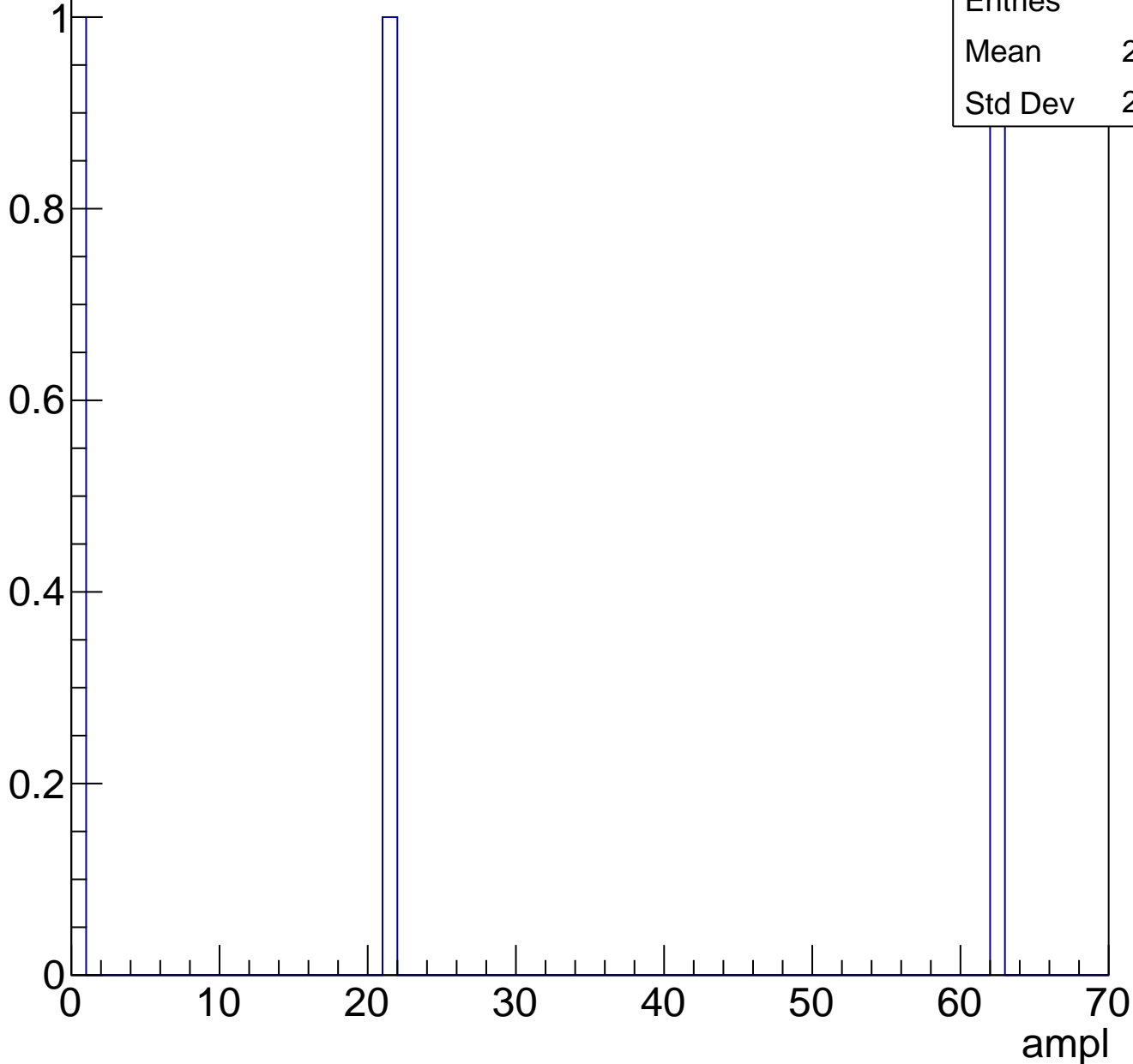




# B1L002S, U26-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L002S, U26-ch61, adc0

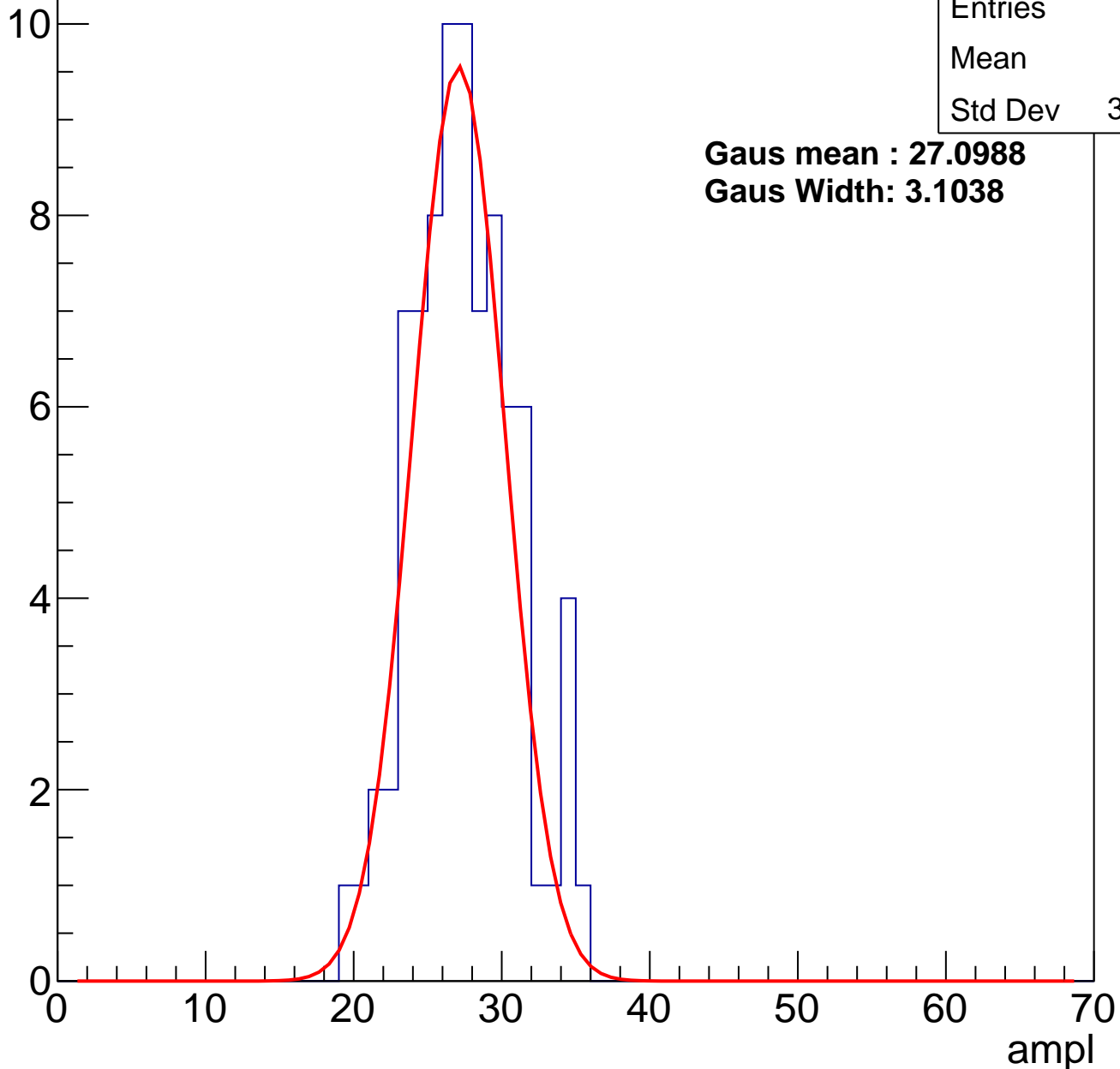
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	27
Std Dev	3.425

**Gaus mean : 27.0988**

**Gaus Width: 3.1038**

Entry



# B1L002S, U26-ch61, adc1

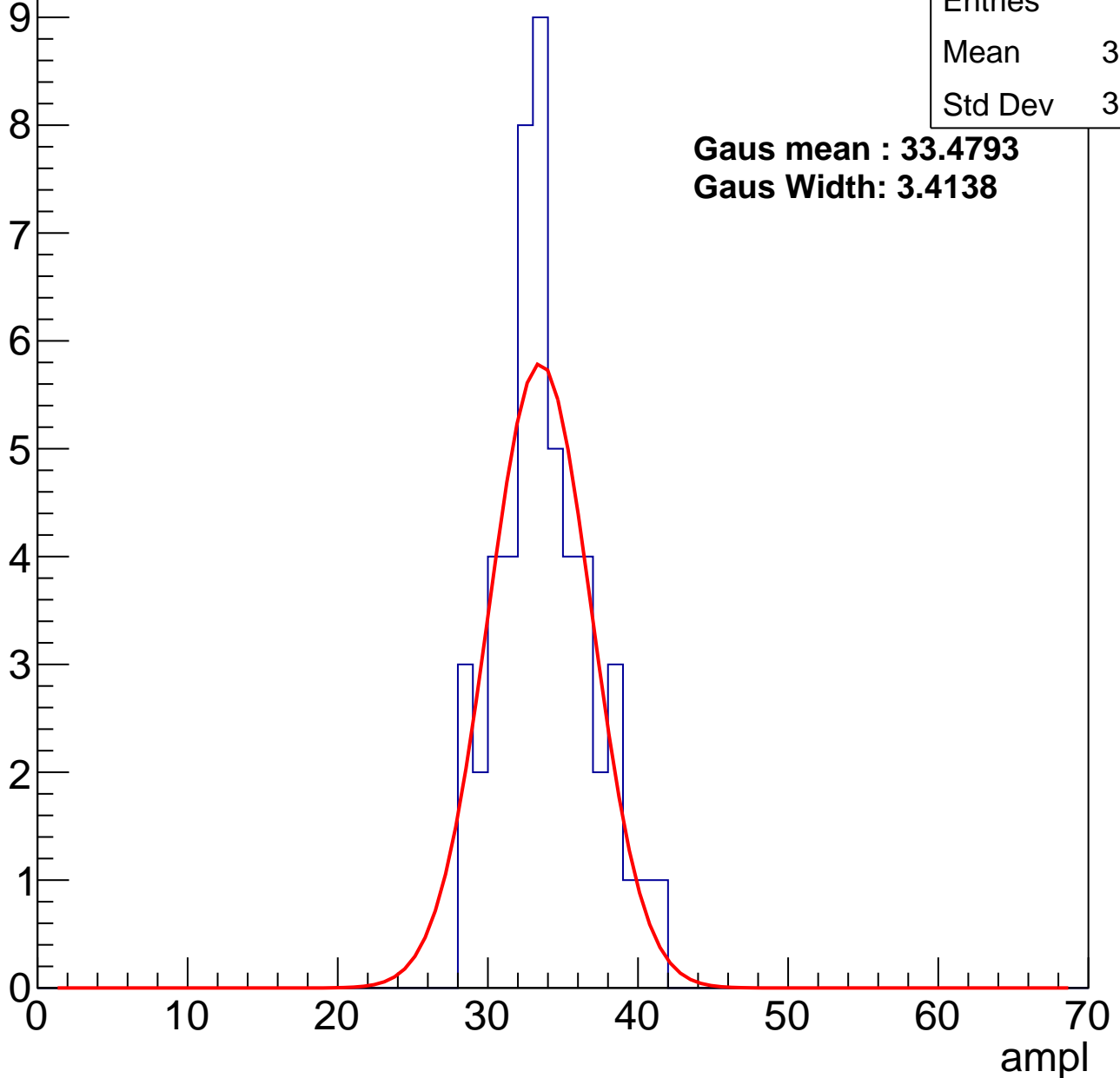
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	33.35
Std Dev	3.047

**Gaus mean : 33.4793**

**Gaus Width: 3.4138**



# B1L002S, U26-ch61, adc2

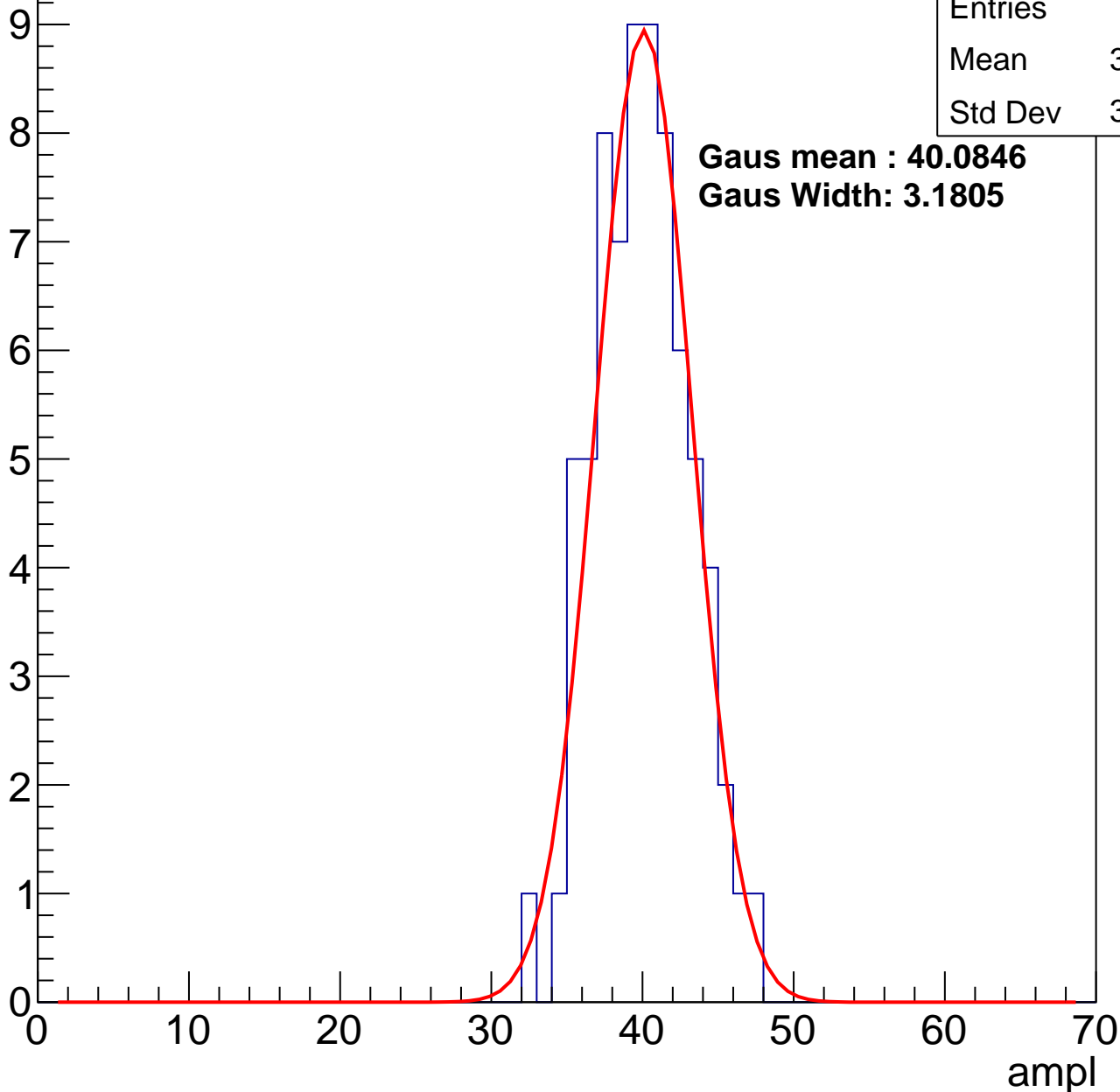
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	39.56
Std Dev	3.064

**Gaus mean : 40.0846**

**Gaus Width: 3.1805**

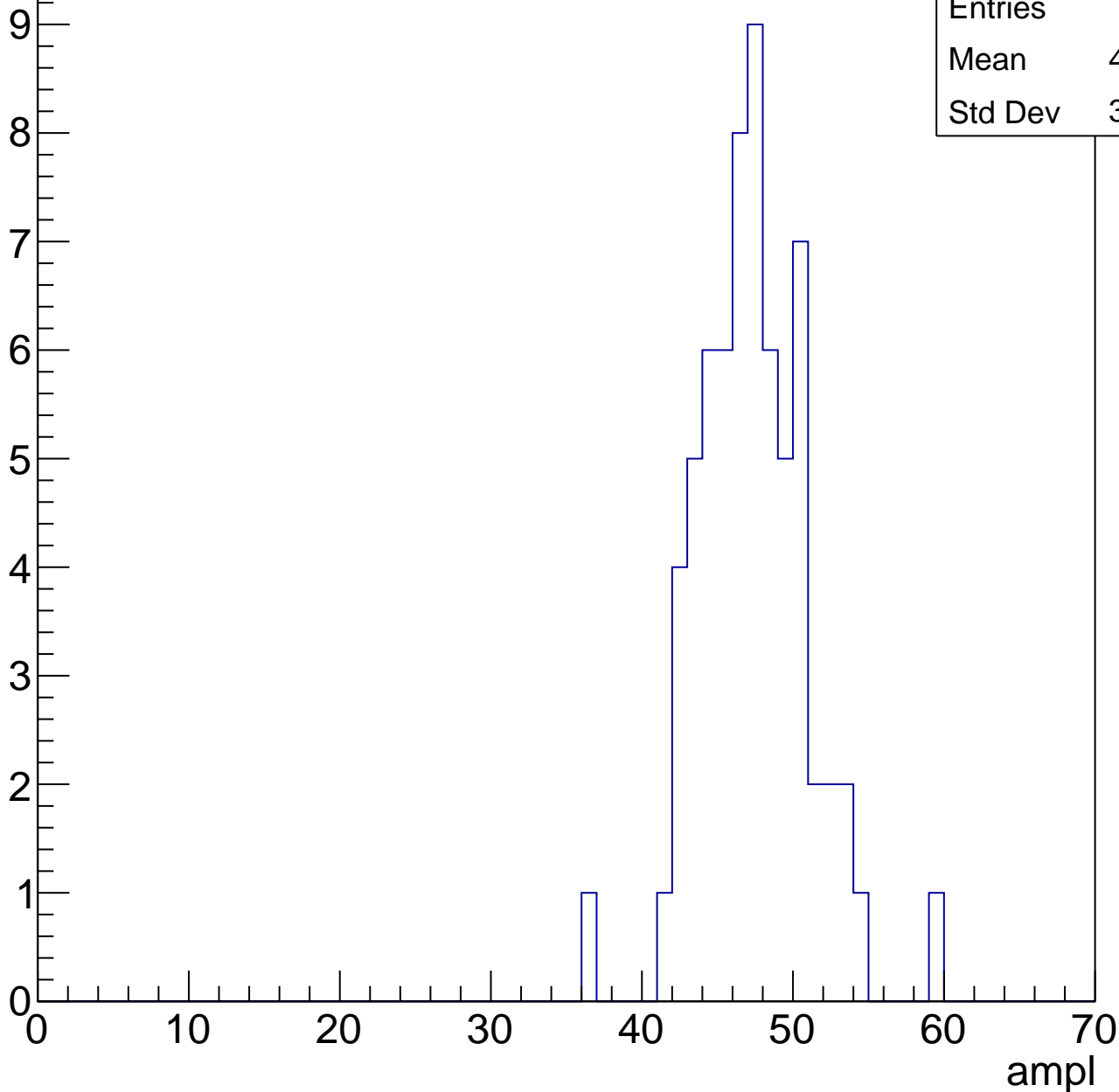


# B1L002S, U26-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.86
Std Dev	3.605

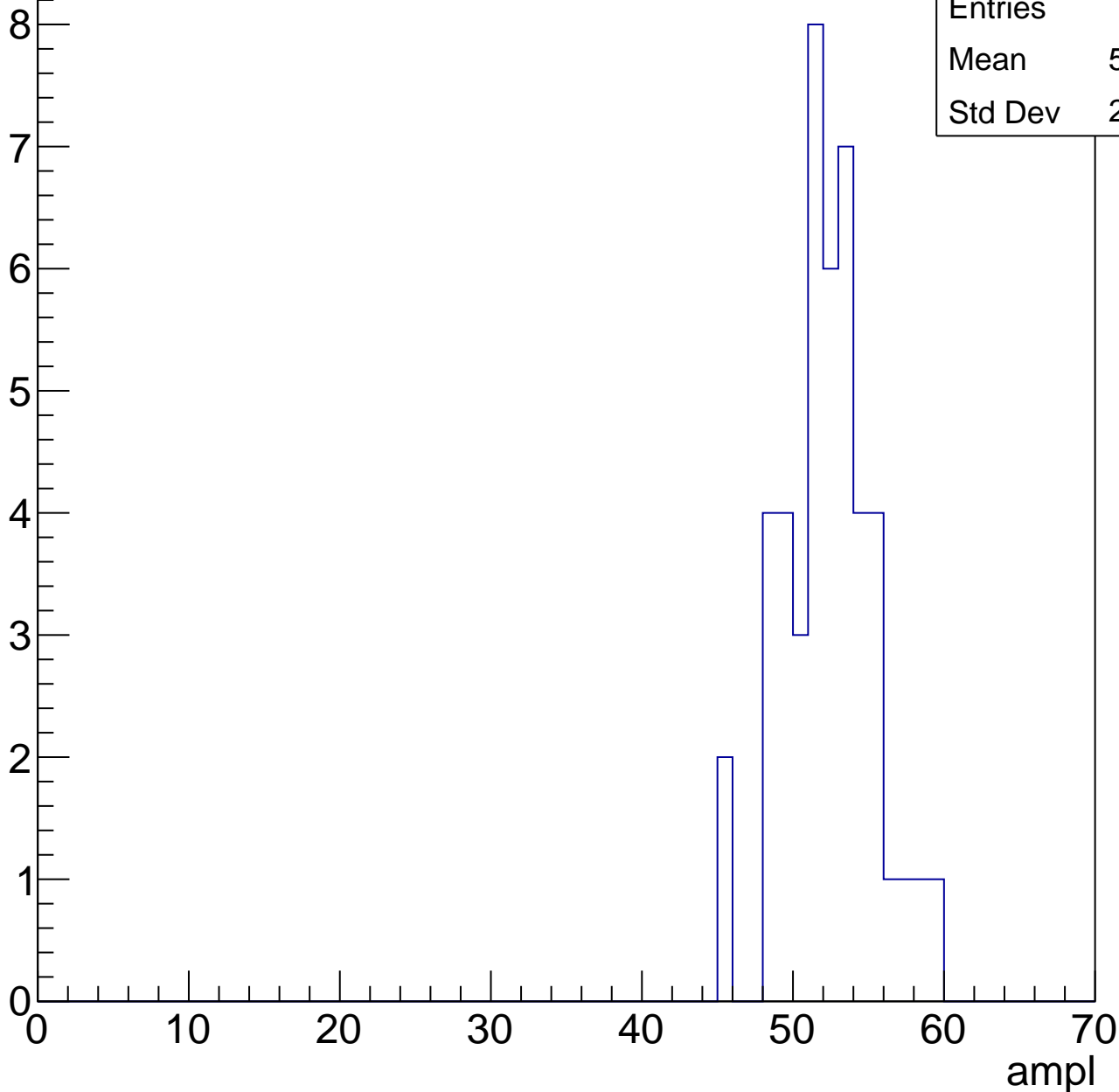


# B1L002S, U26-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	51.85
Std Dev	2.956

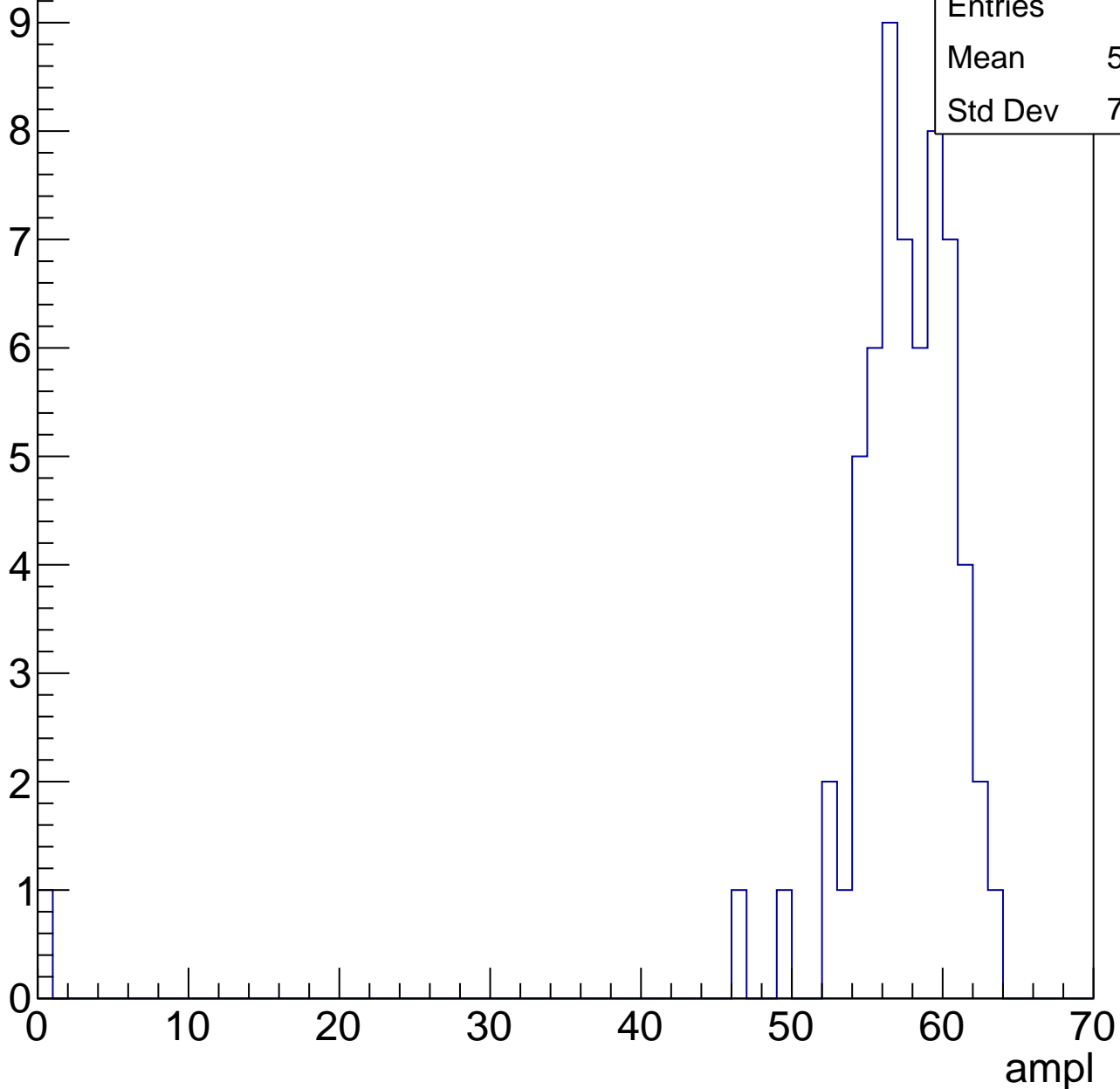


# B1L002S, U26-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	56.16
Std Dev	7.879

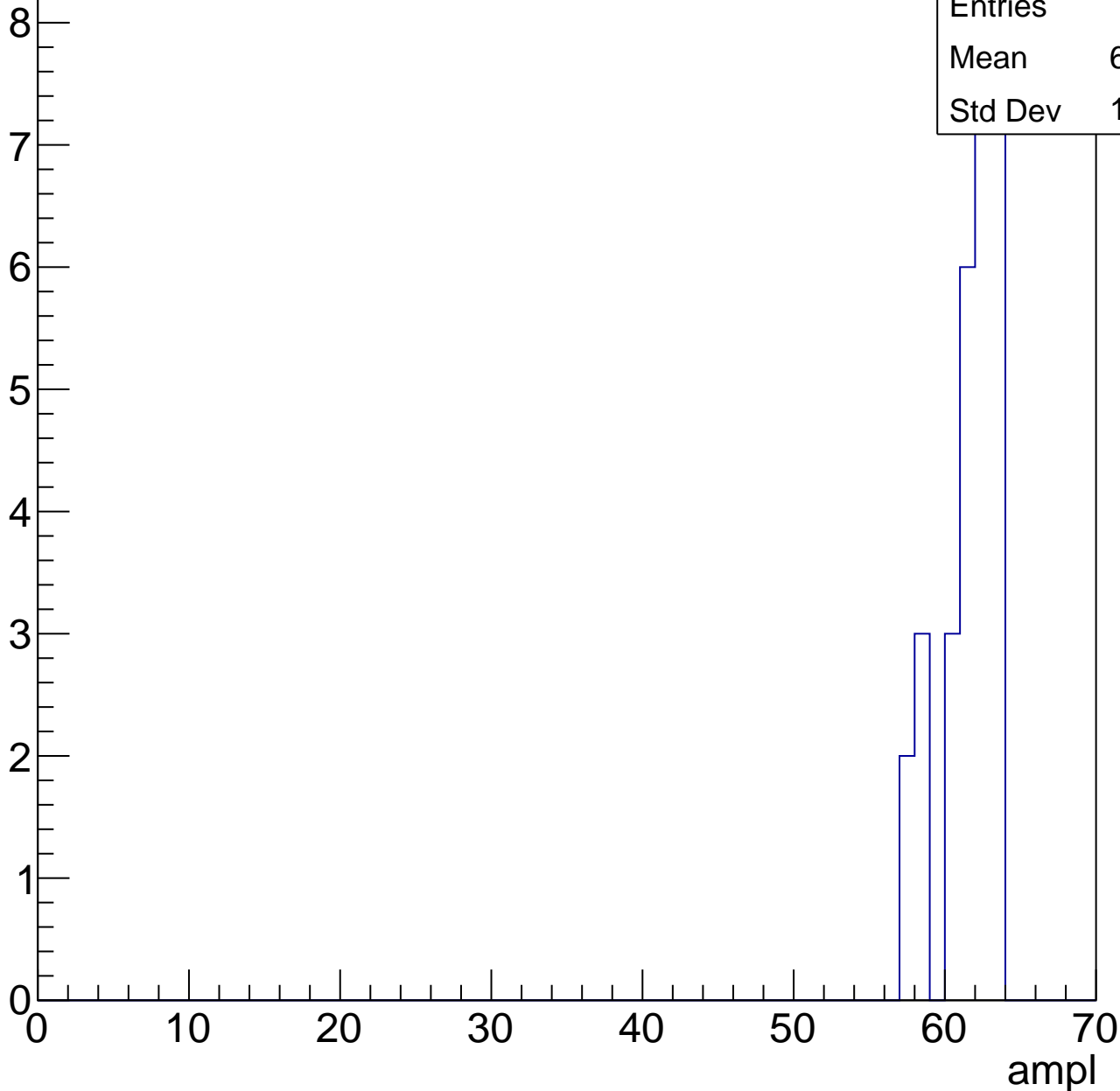


# B1L002S, U26-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	61.13
Std Dev	1.839





# B1L002S, U26-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch62, adc0

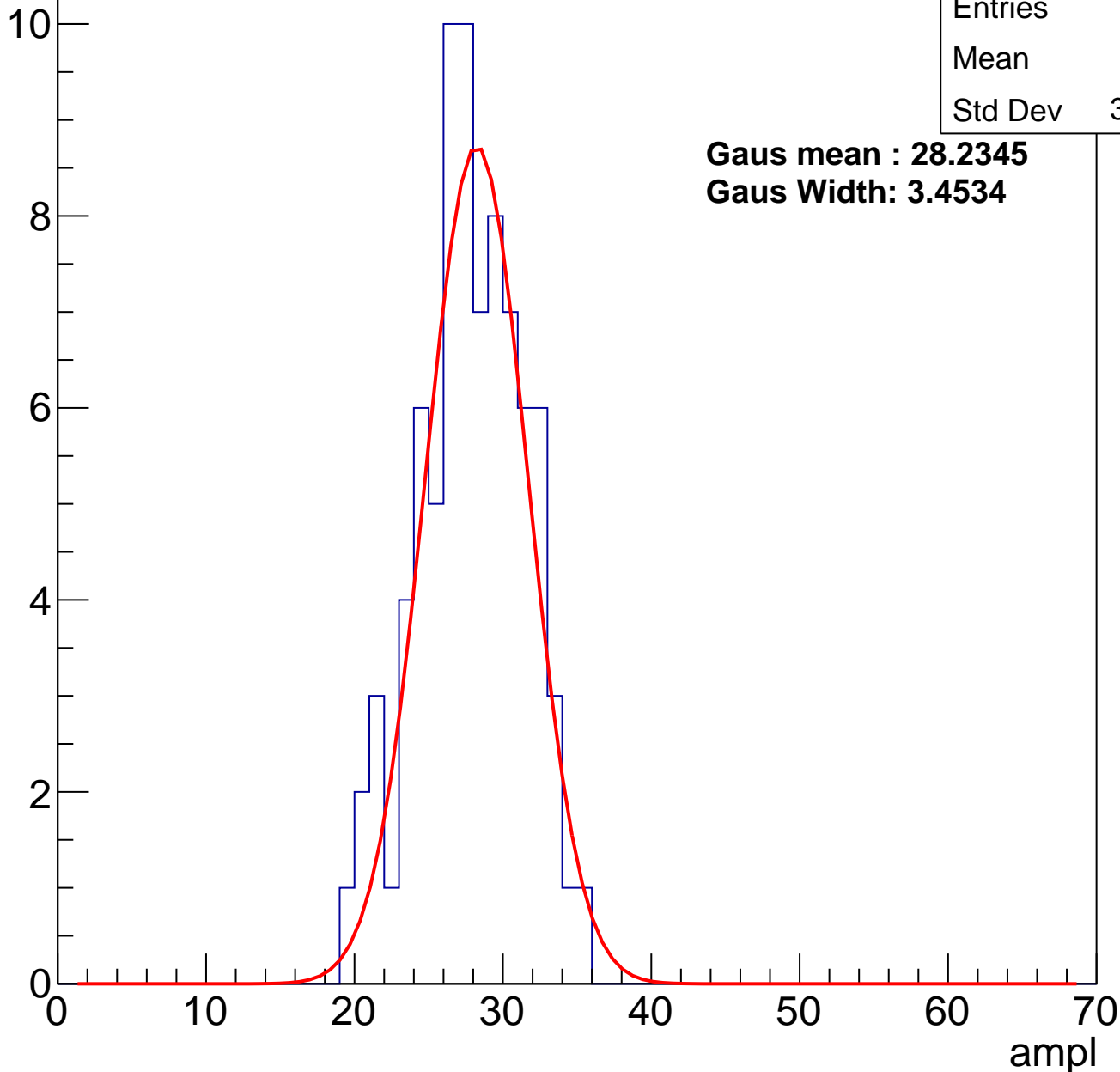
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	27.4
Std Dev	3.513

**Gaus mean : 28.2345**

**Gaus Width: 3.4534**

Entry



# B1L002S, U26-ch62, adc1

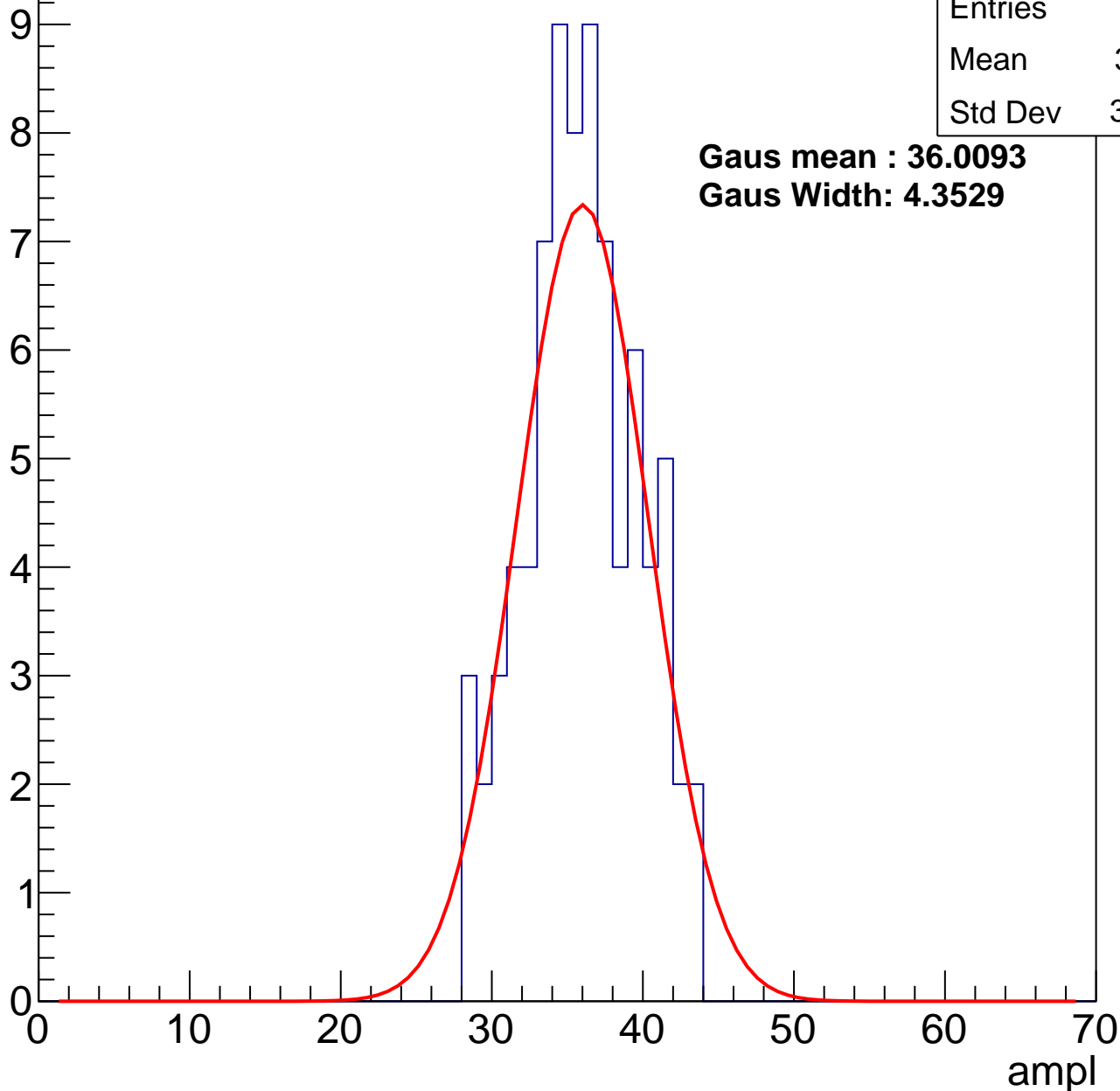
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	35.51
Std Dev	3.707

**Gaus mean : 36.0093**

**Gaus Width: 4.3529**



# B1L002S, U26-ch62, adc2

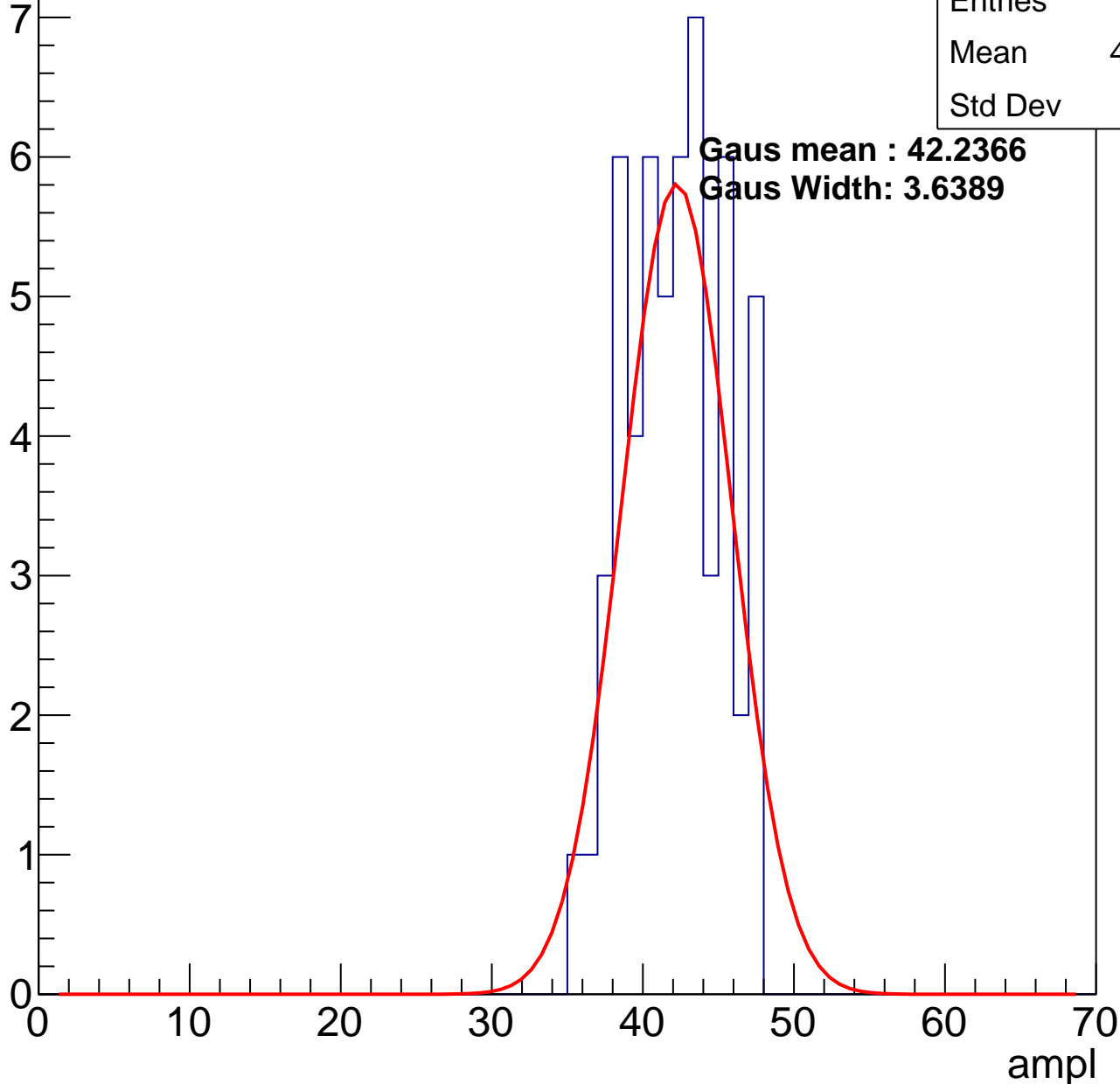
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.69
Std Dev	3.15

**Gaus mean : 42.2366**

**Gaus Width: 3.6389**



# B1L002S, U26-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	47.8
Std Dev	2.956

Entry

10

8

6

4

2

0

0

10

20

30

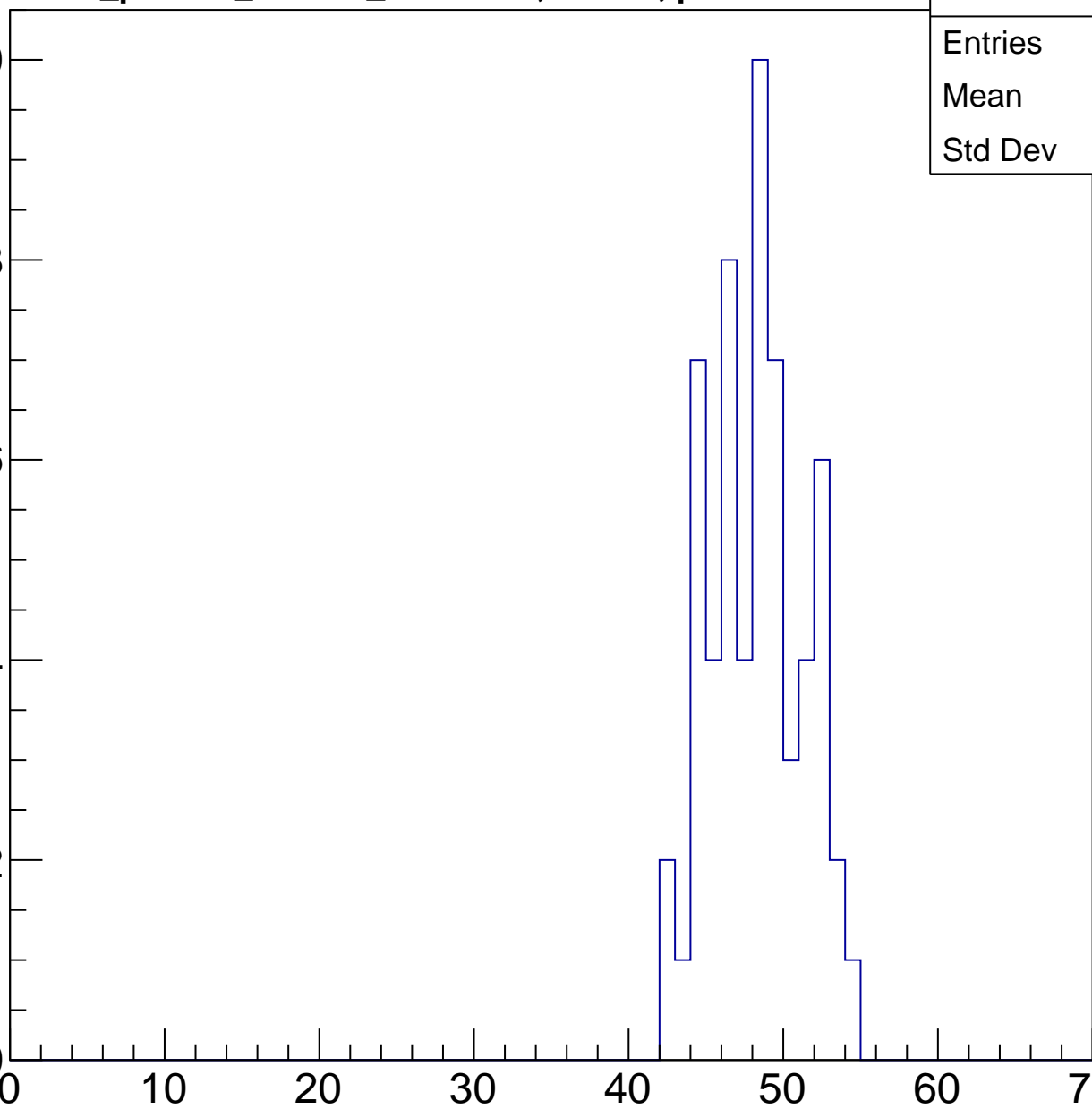
40

50

60

70

ampl

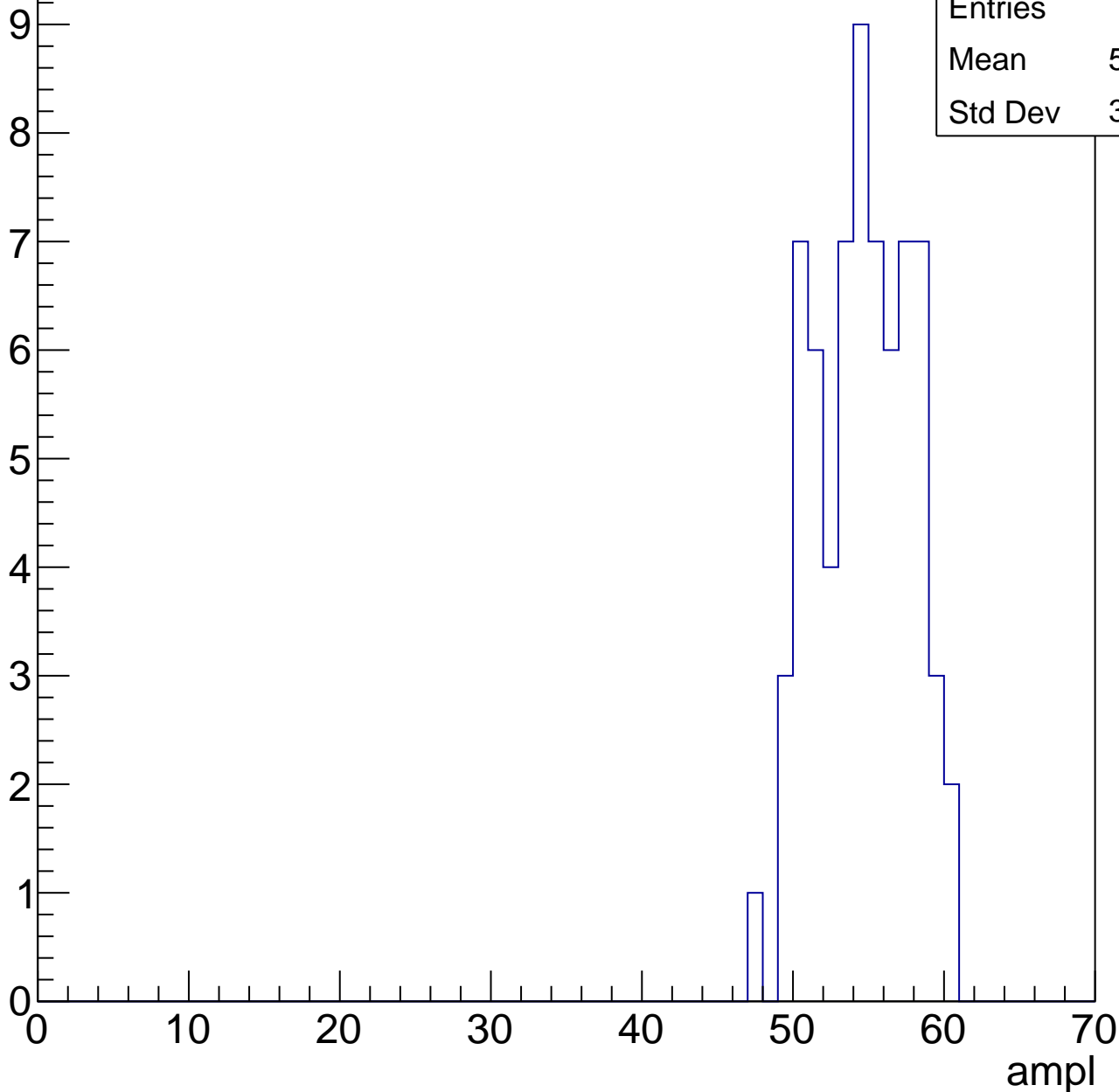


# B1L002S, U26-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	54.17
Std Dev	3.102



# B1L002S, U26-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries 38

Mean 59.08

Std Dev 2.528

0

10

20

30

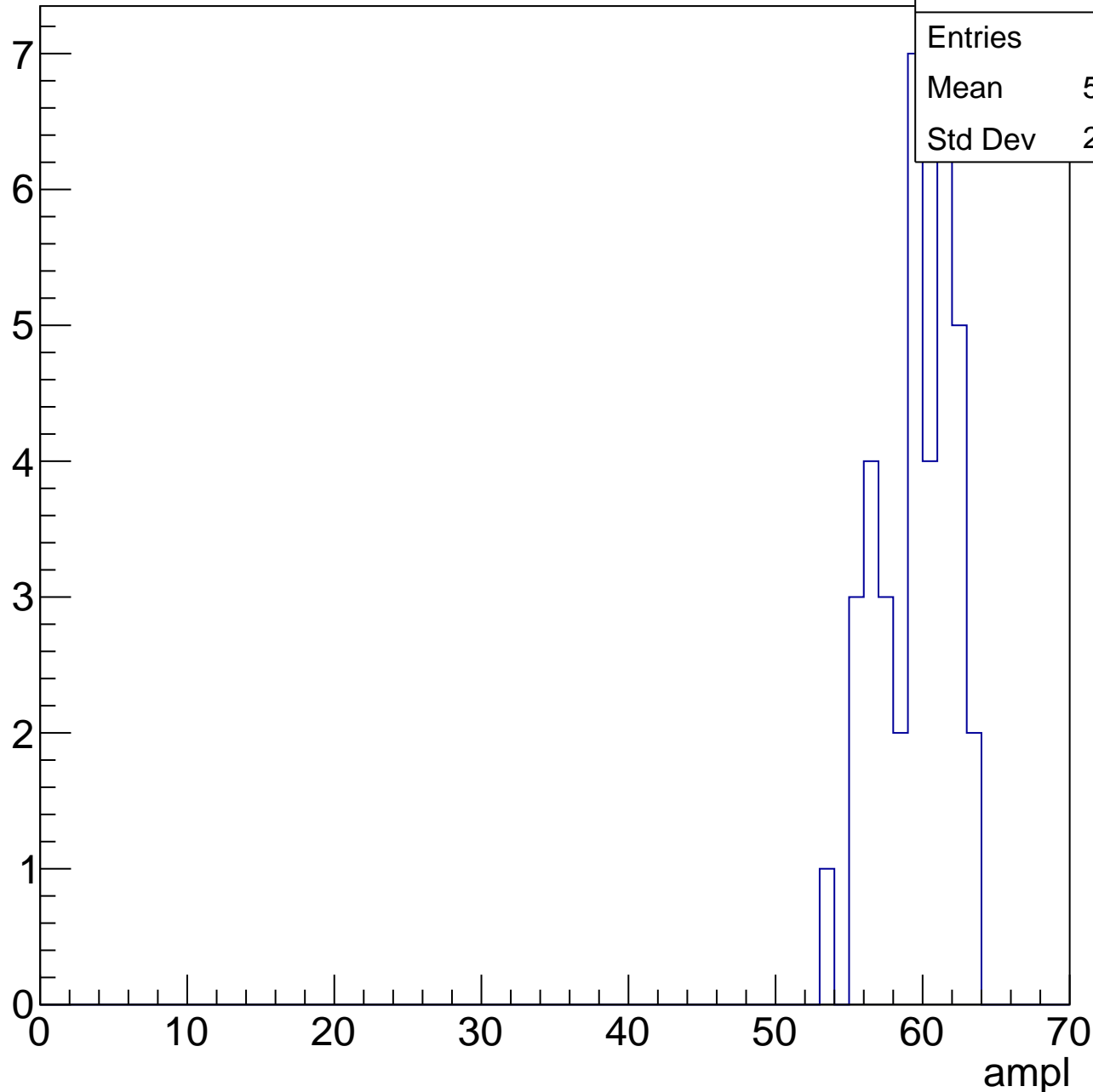
40

50

60

70

ampl

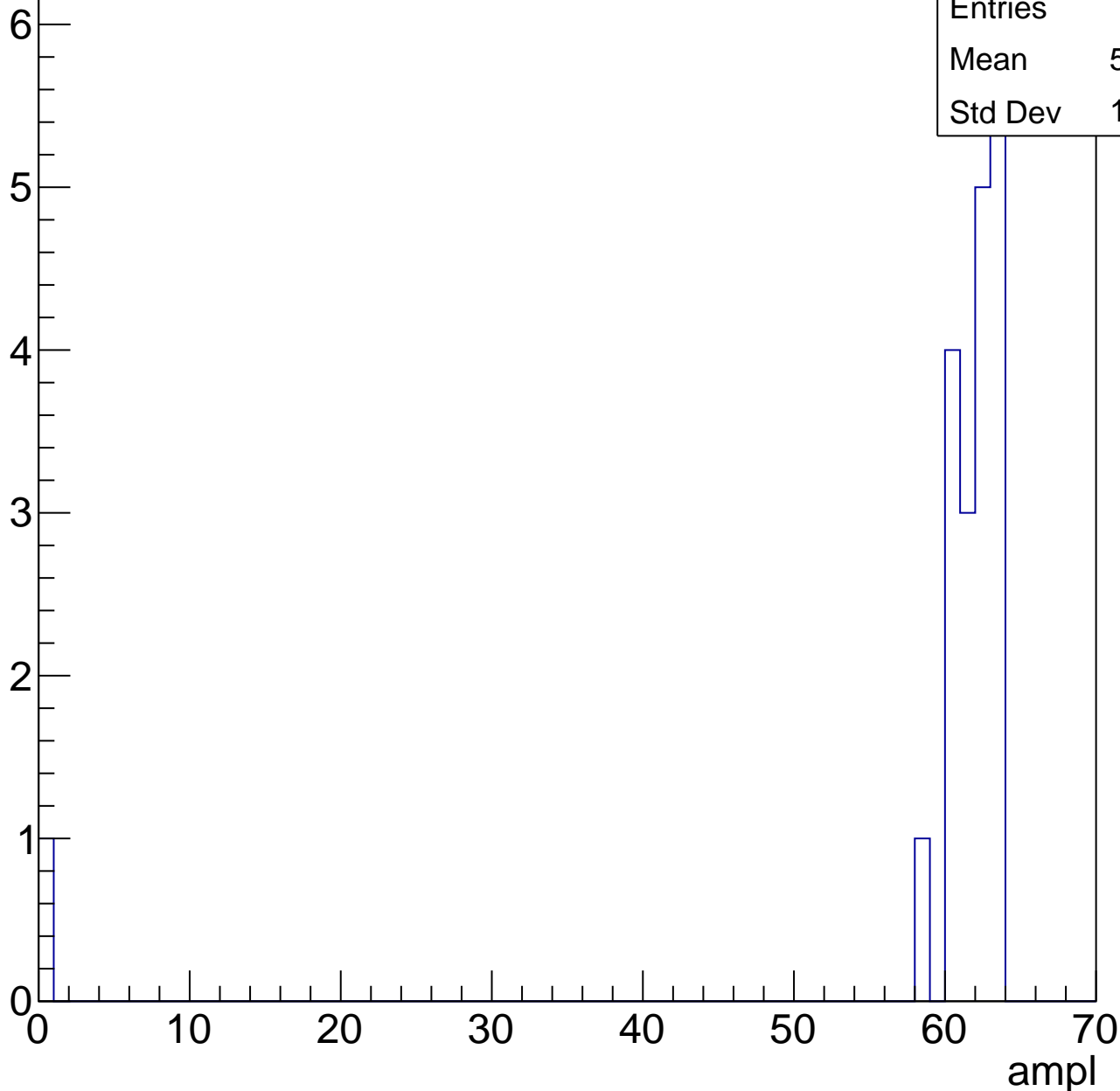


# B1L002S, U26-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.48





# B1L002S, U26-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	63
Std Dev	0

# B1L002S, U26-ch63, adc0

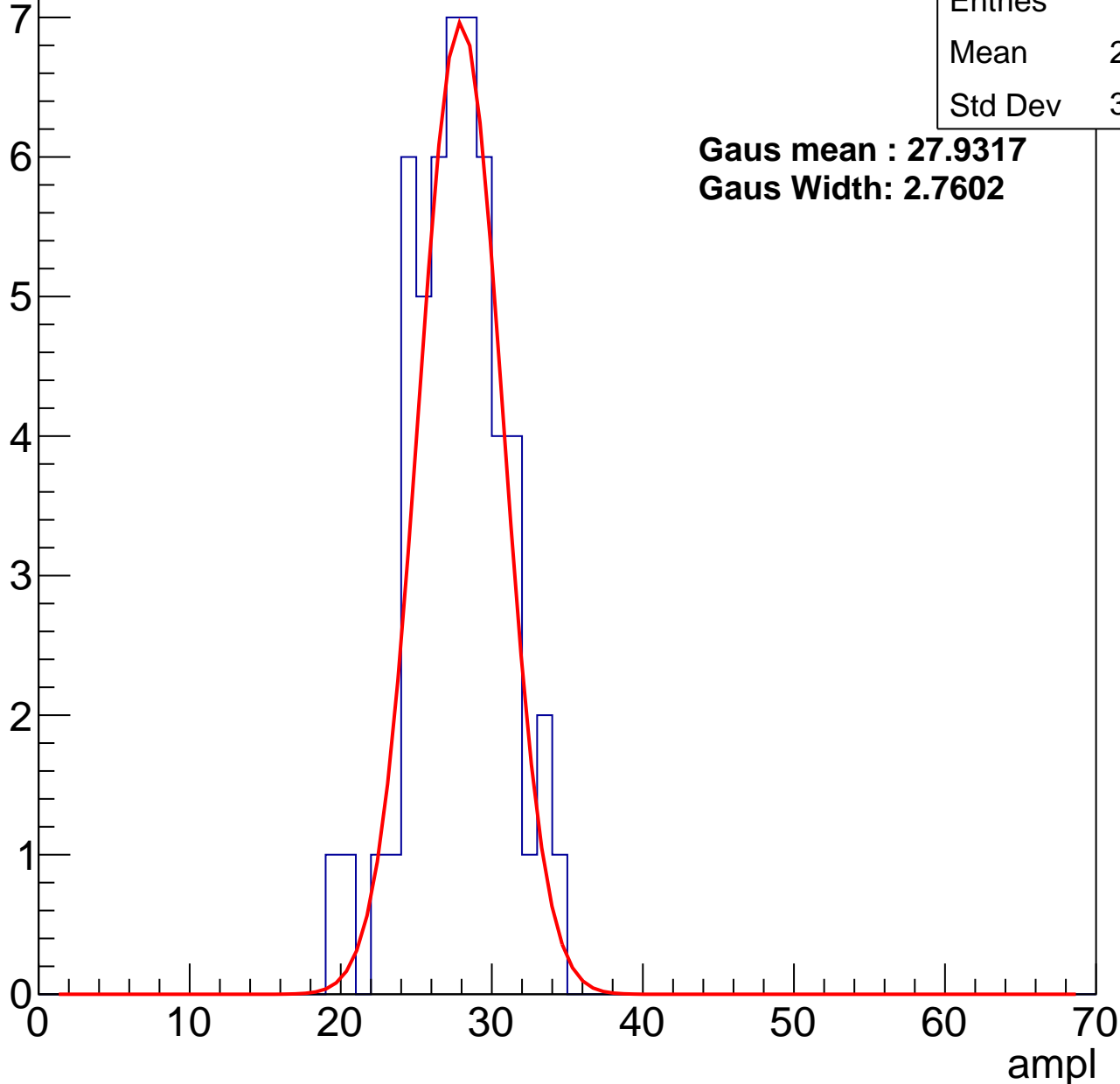
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	27.25
Std Dev	3.095

**Gaus mean : 27.9317**

**Gaus Width: 2.7602**



# B1L002S, U26-ch63, adc1

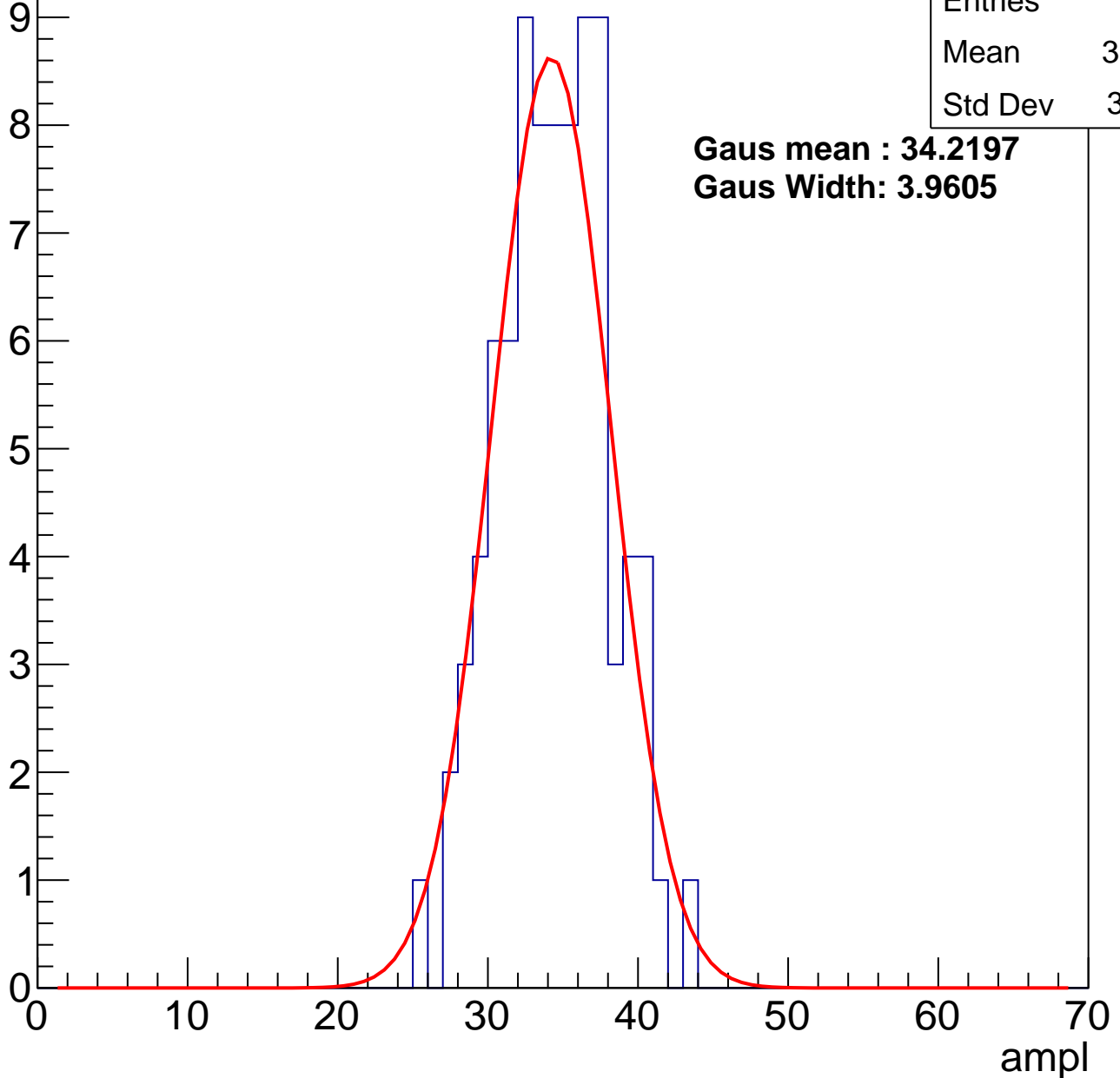
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	86
Mean	33.95
Std Dev	3.621

**Gaus mean : 34.2197**

**Gaus Width: 3.9605**



# B1L002S, U26-ch63, adc2

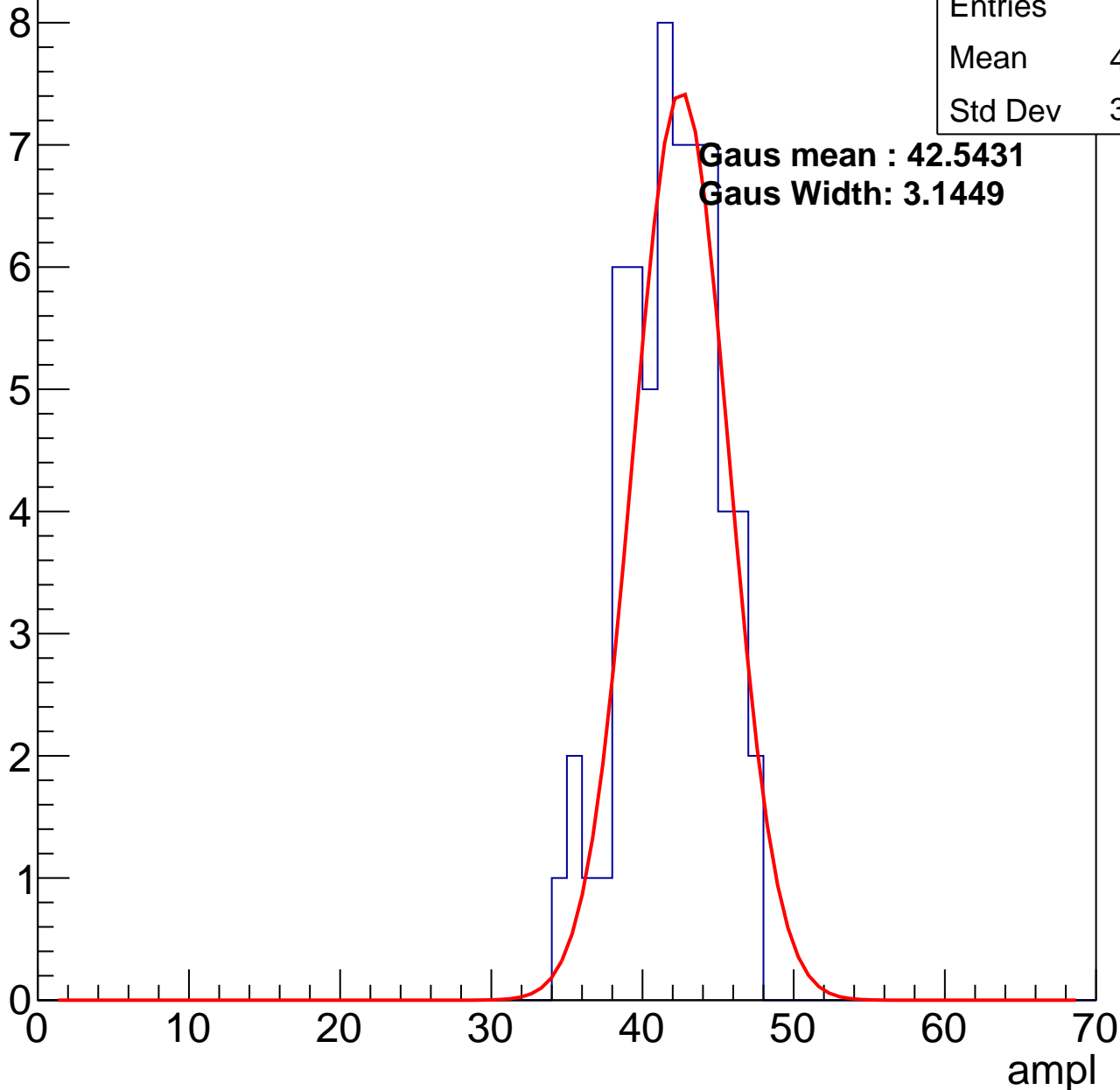
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.44
Std Dev	3.049

**Gaus mean : 42.5431**

**Gaus Width: 3.1449**

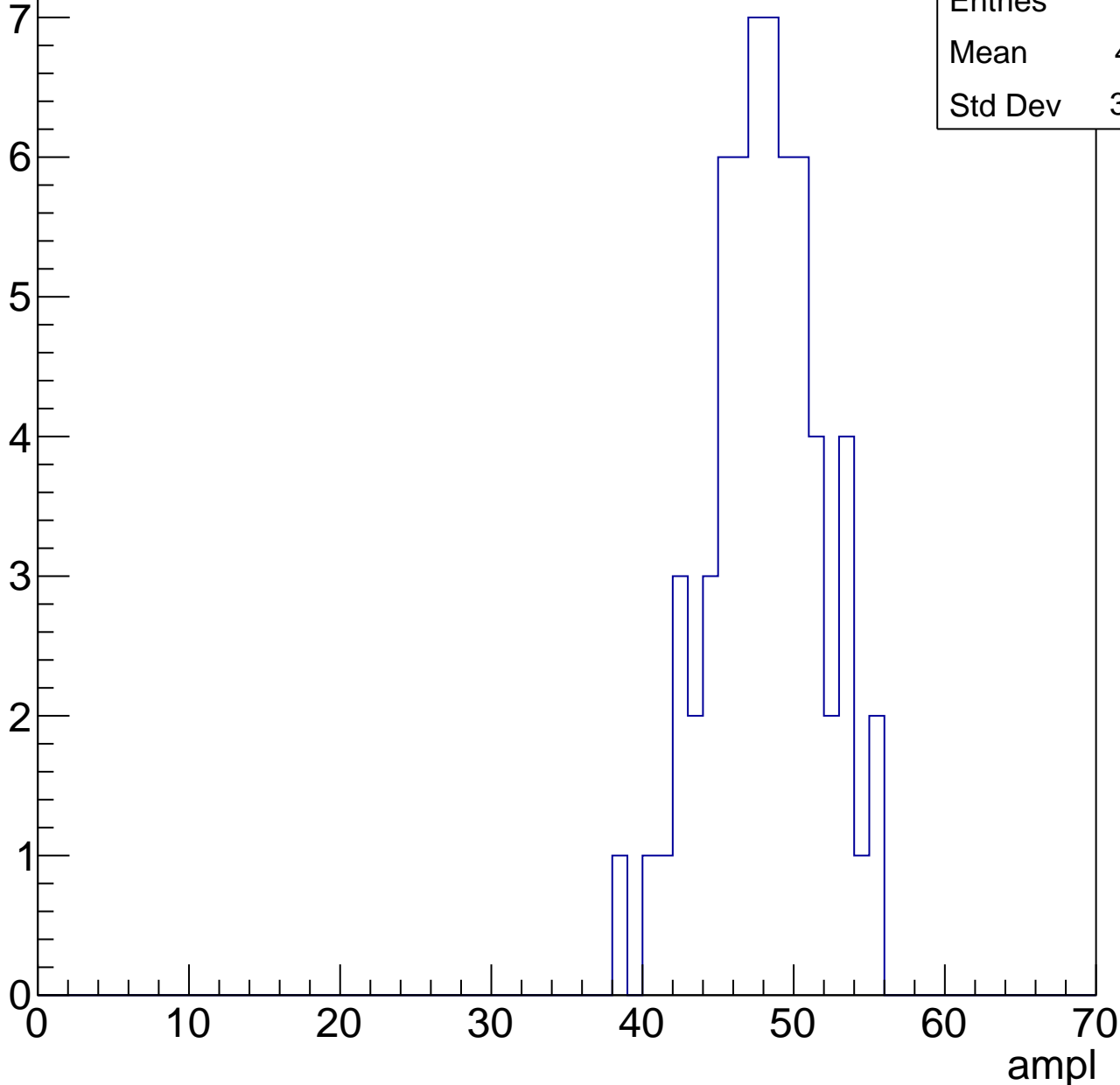


# B1L002S, U26-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	47.61
Std Dev	3.652

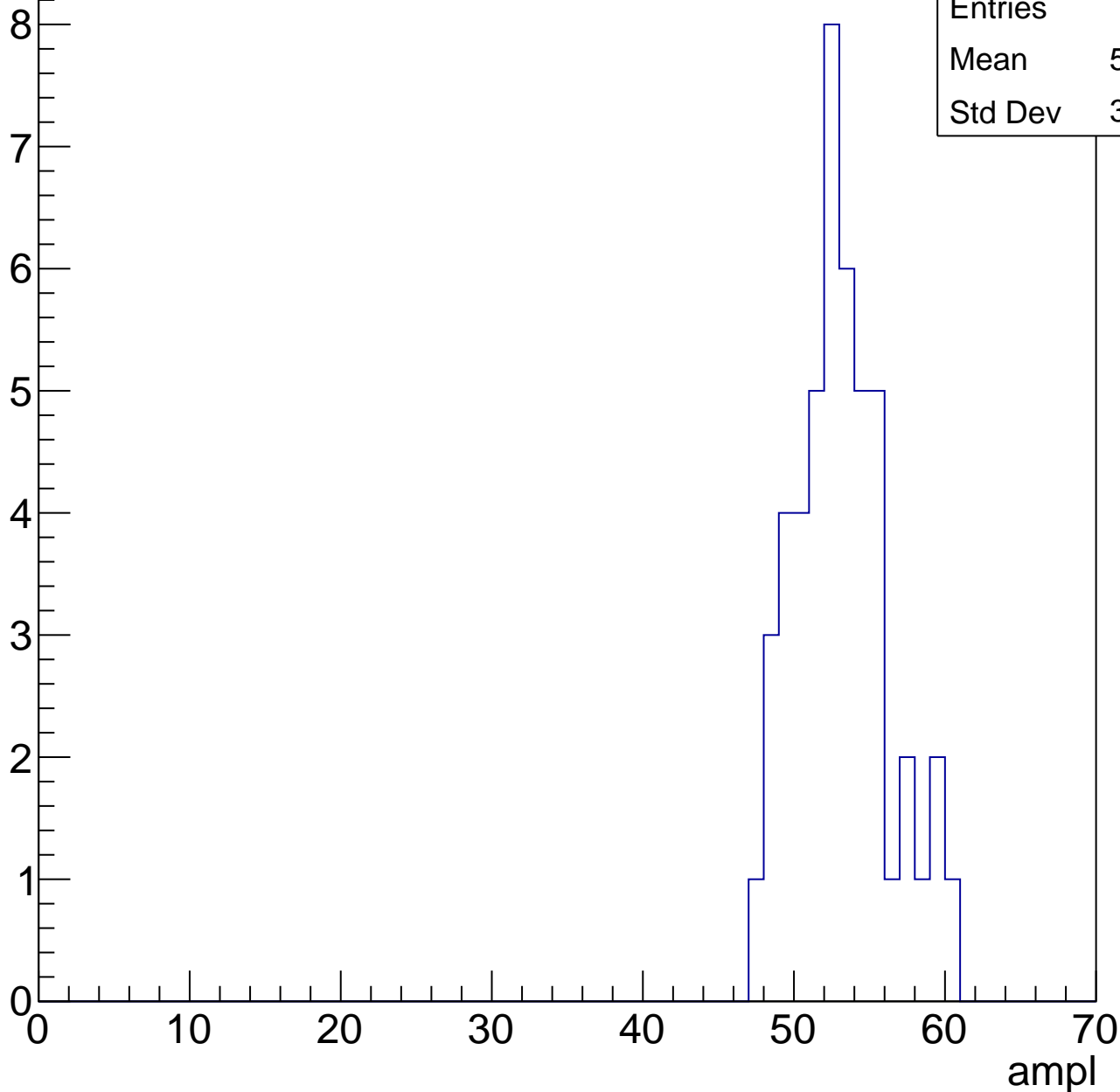


# B1L002S, U26-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	52.65
Std Dev	3.045

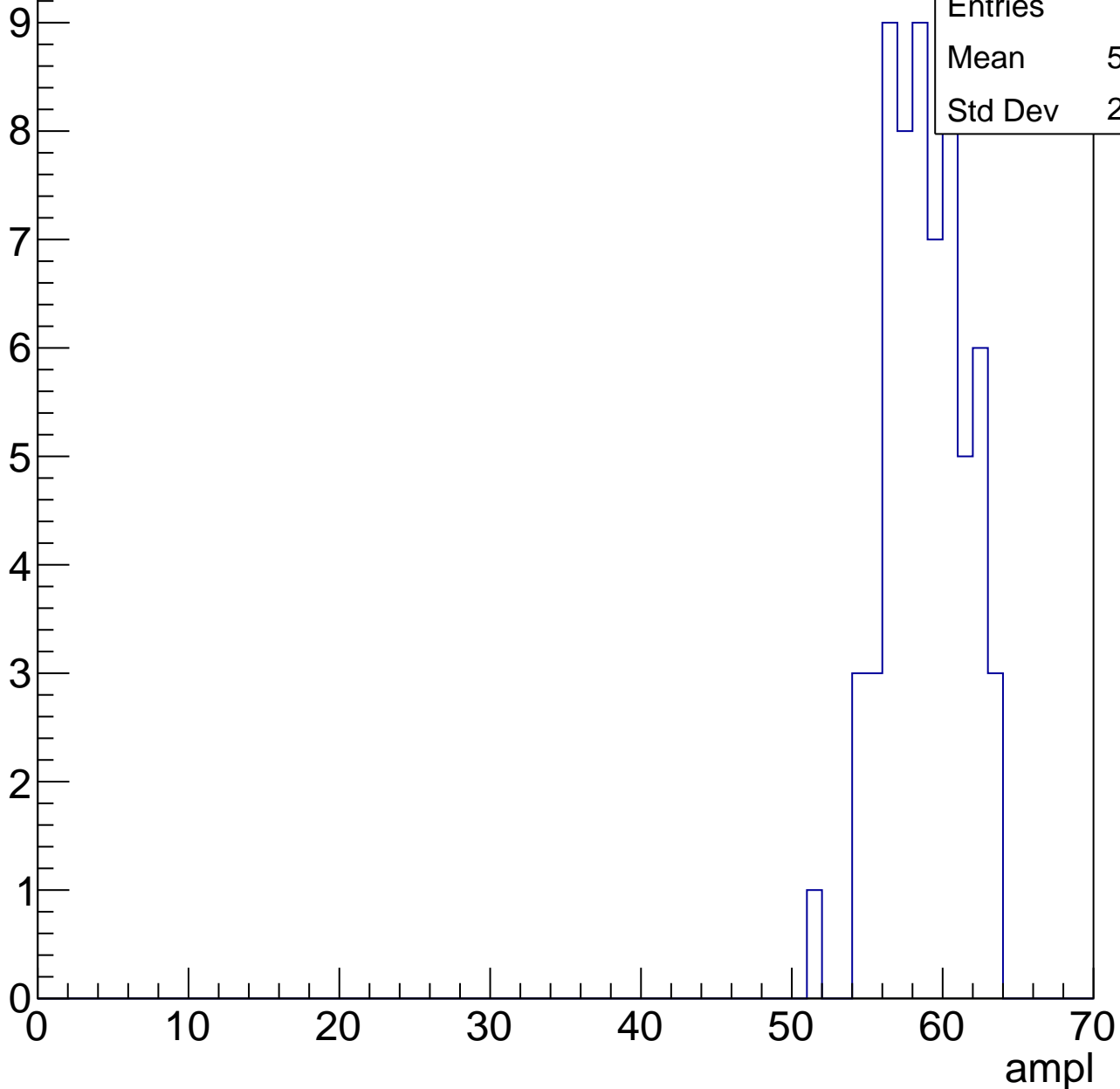


# B1L002S, U26-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.37
Std Dev	2.586

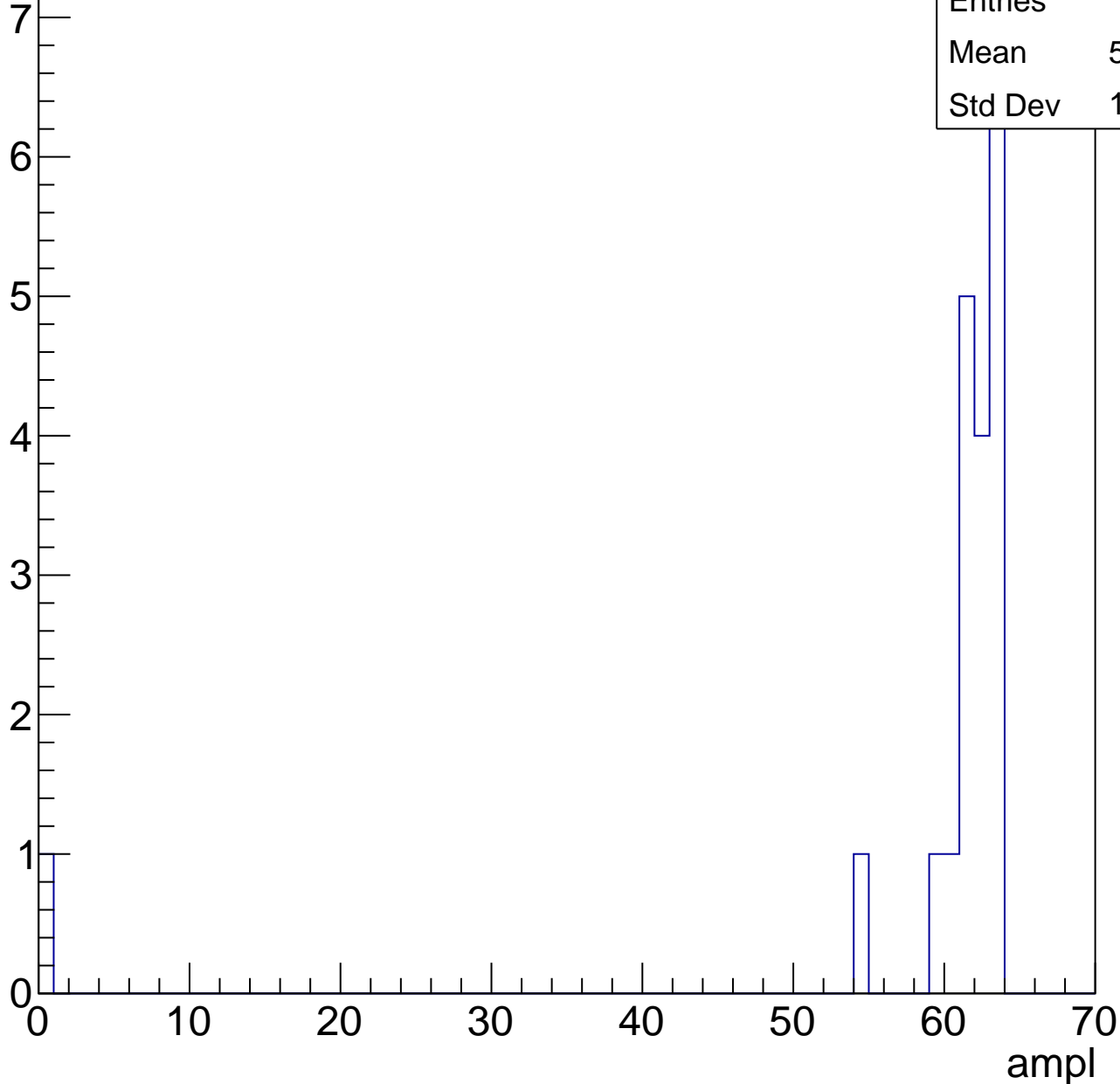


# B1L002S, U26-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.35
Std Dev	13.54





# B1L002S, U26-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch64, adc0

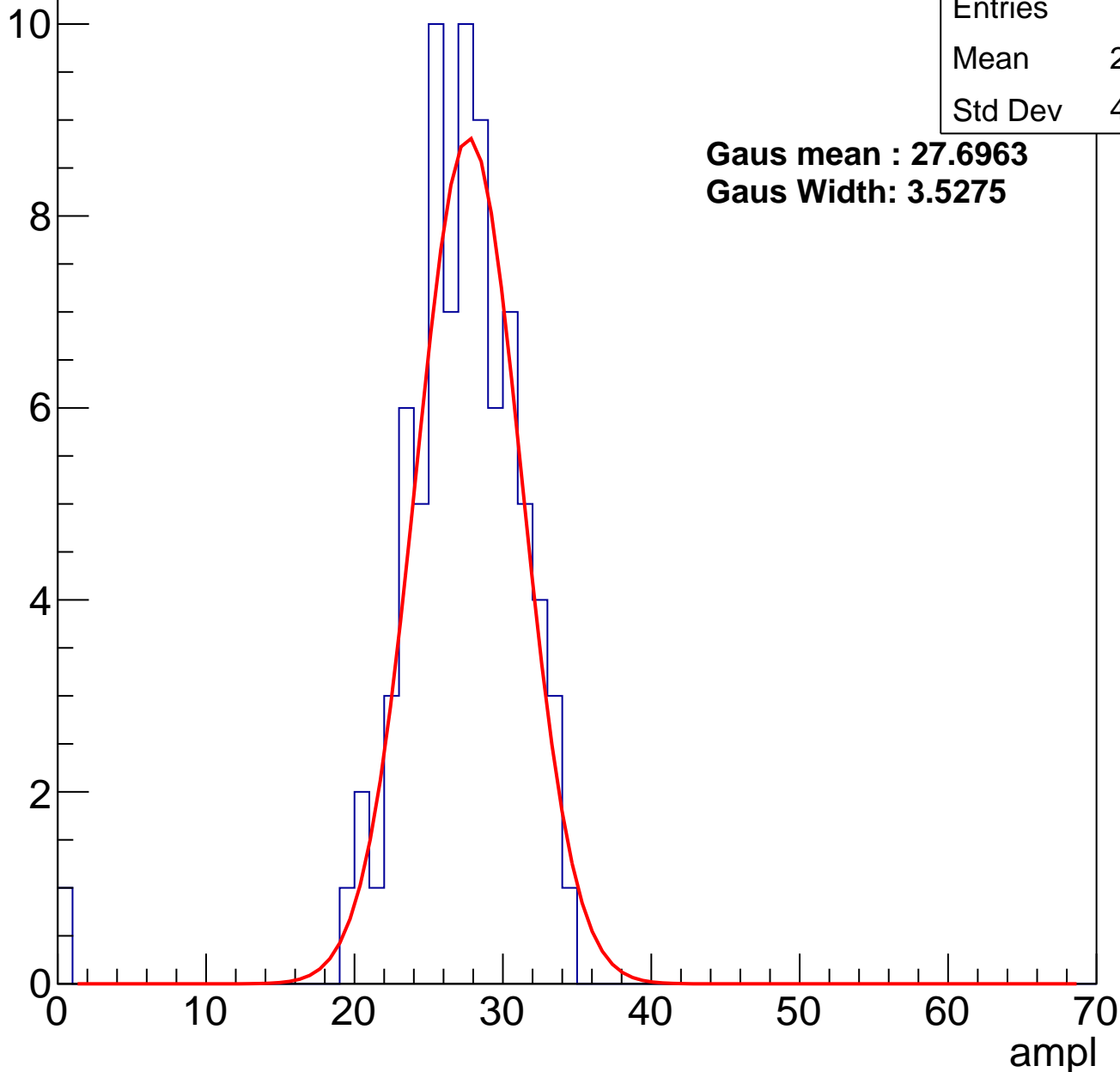
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	81
Mean	26.64
Std Dev	4.462

**Gaus mean : 27.6963**

**Gaus Width: 3.5275**

Entry



# B1L002S, U26-ch64, adc1

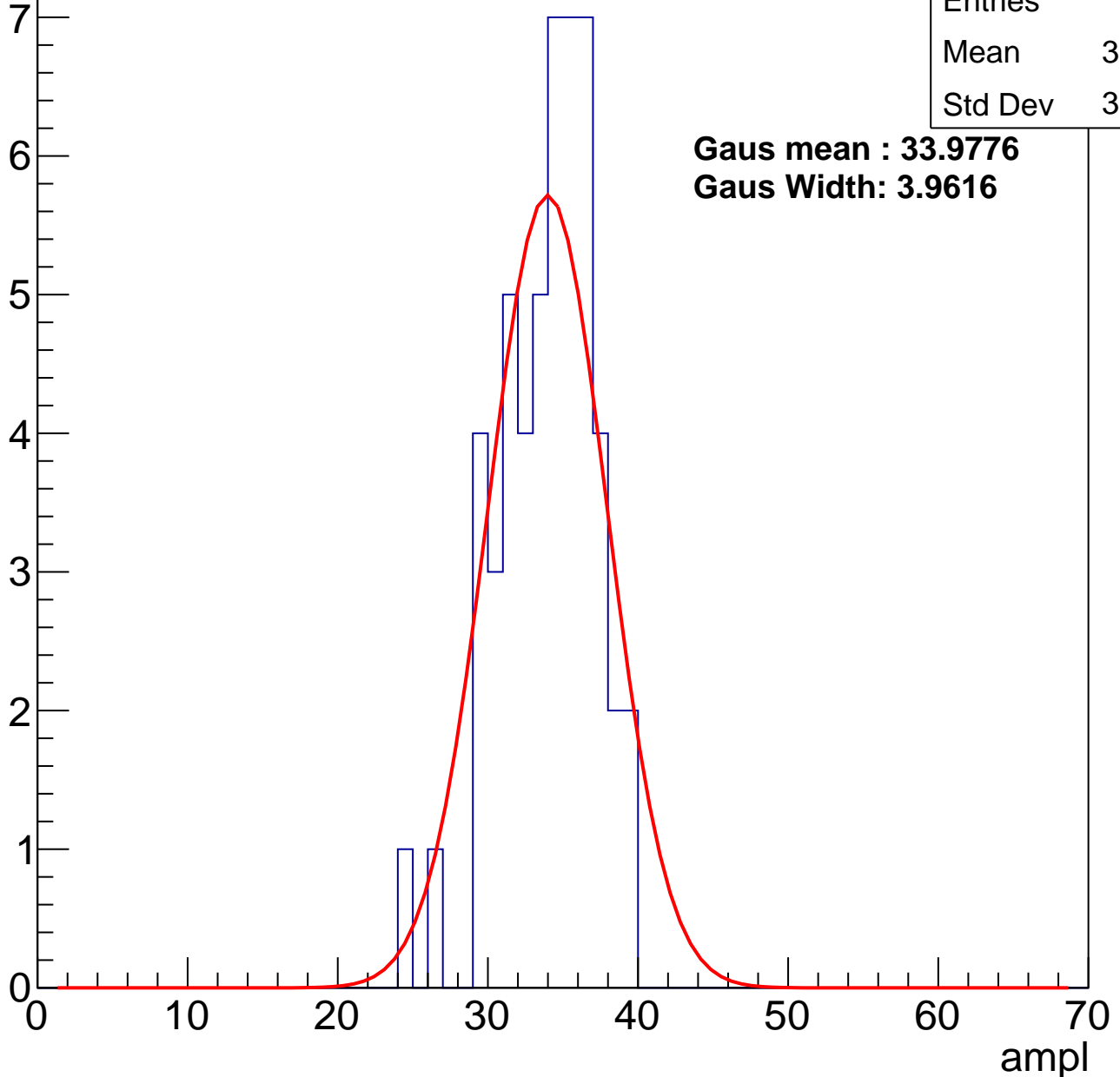
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.48
Std Dev	3.153

**Gaus mean : 33.9776**

**Gaus Width: 3.9616**



# B1L002S, U26-ch64, adc2

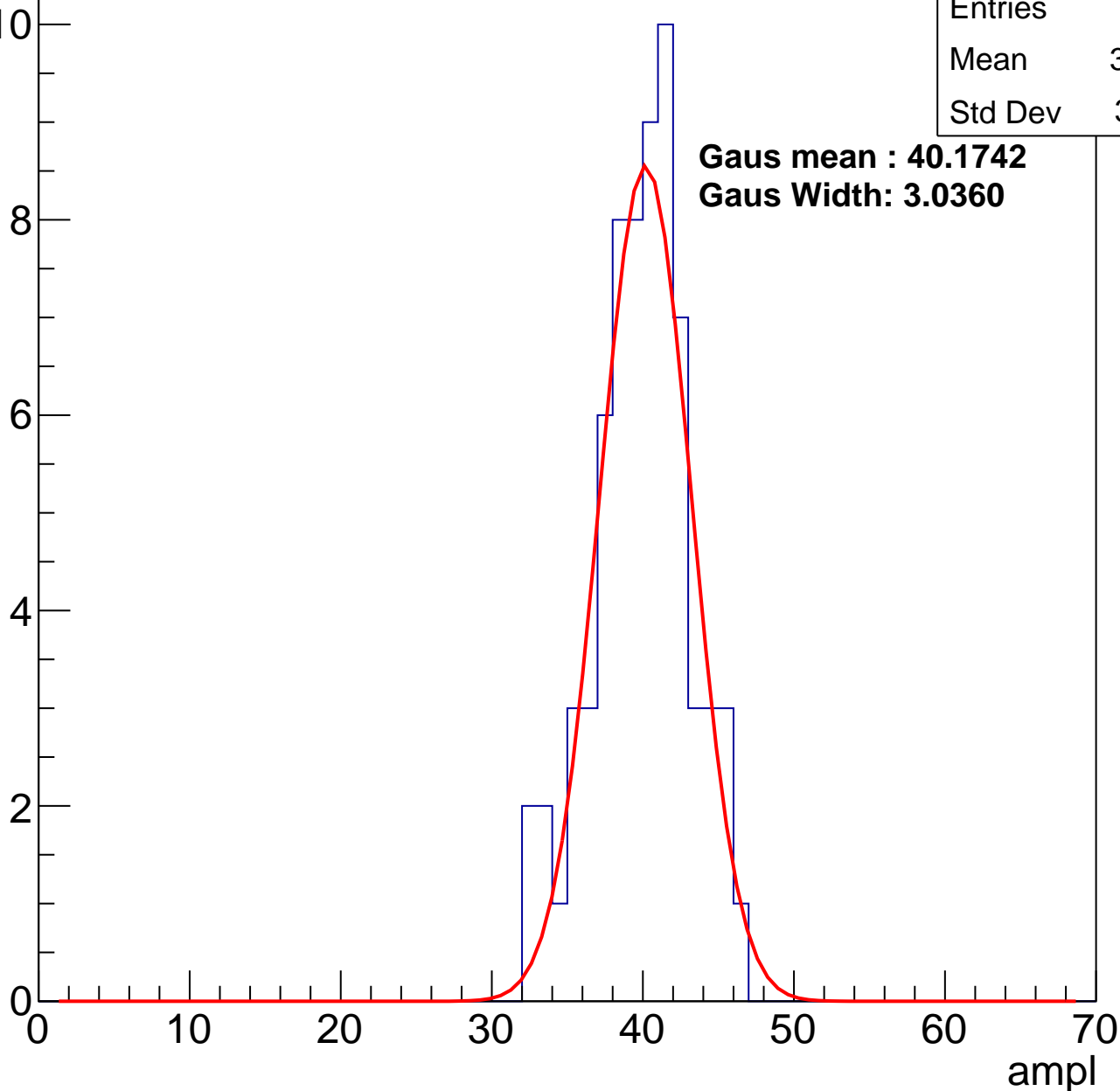
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.43
Std Dev	3.141

**Gaus mean : 40.1742**

**Gaus Width: 3.0360**



# B1L002S, U26-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	46.74
Std Dev	3.455

Entry

10

8

6

4

2

0

0

10

20

30

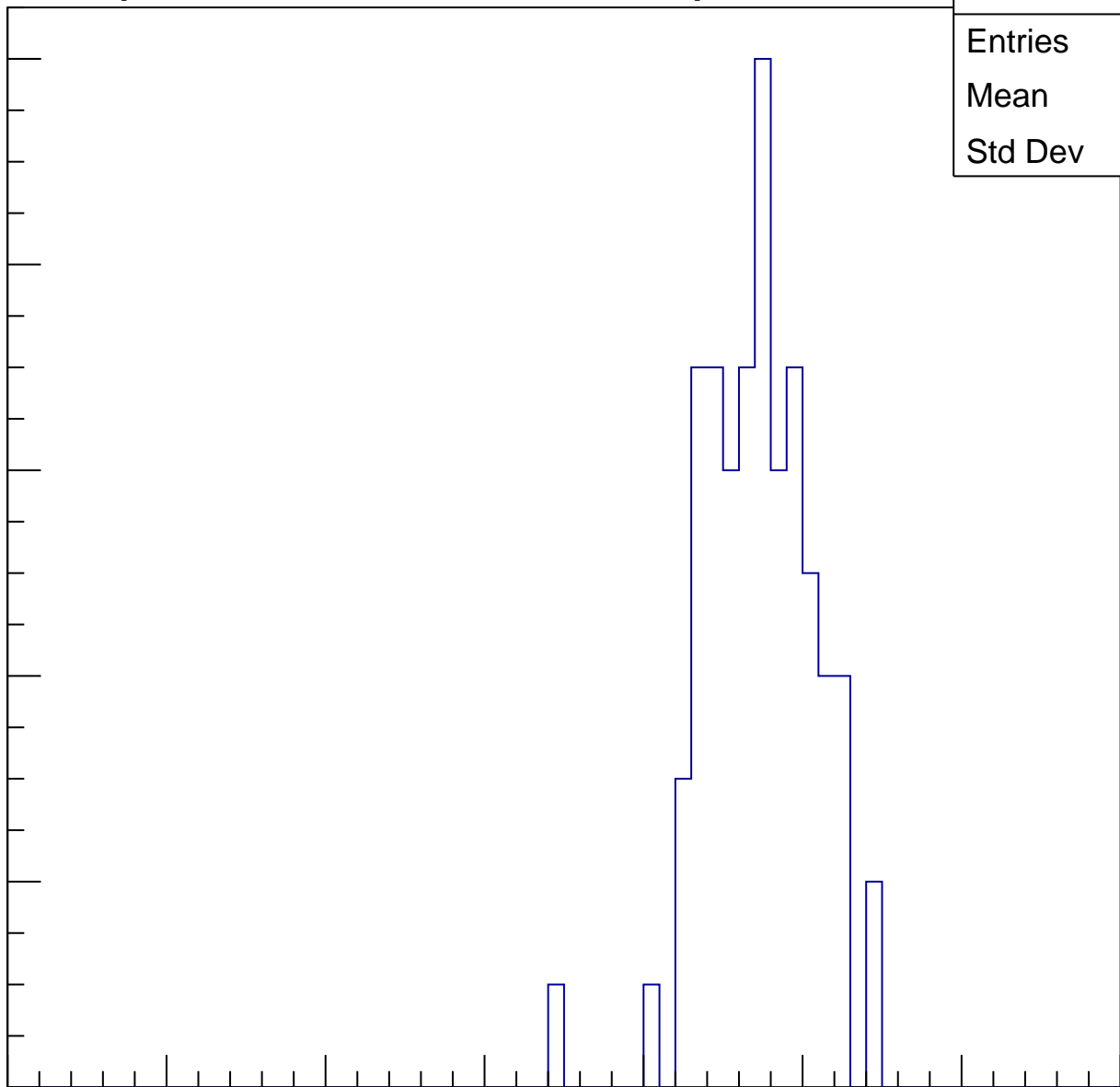
40

50

60

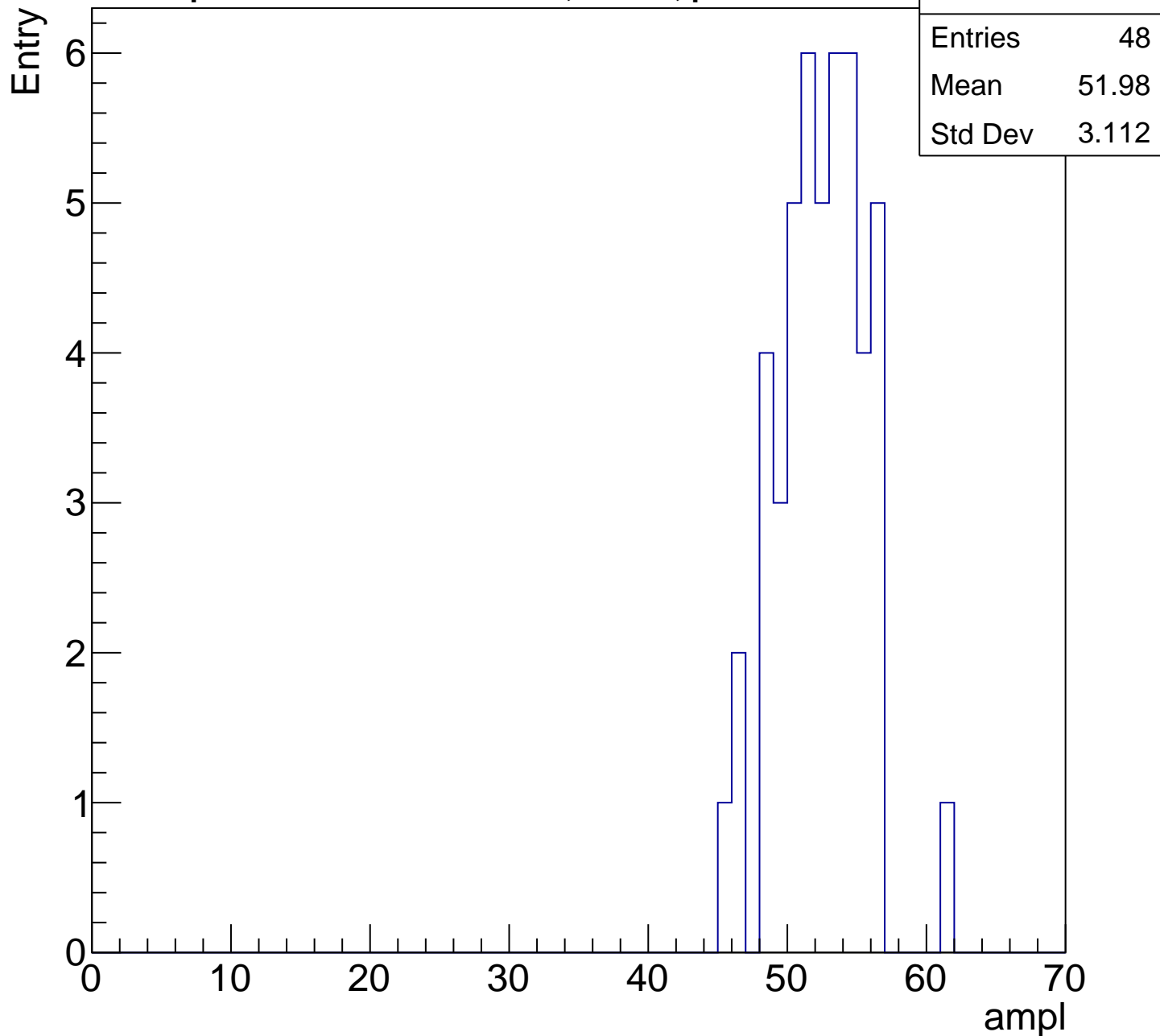
70

ampl



# B1L002S, U26-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

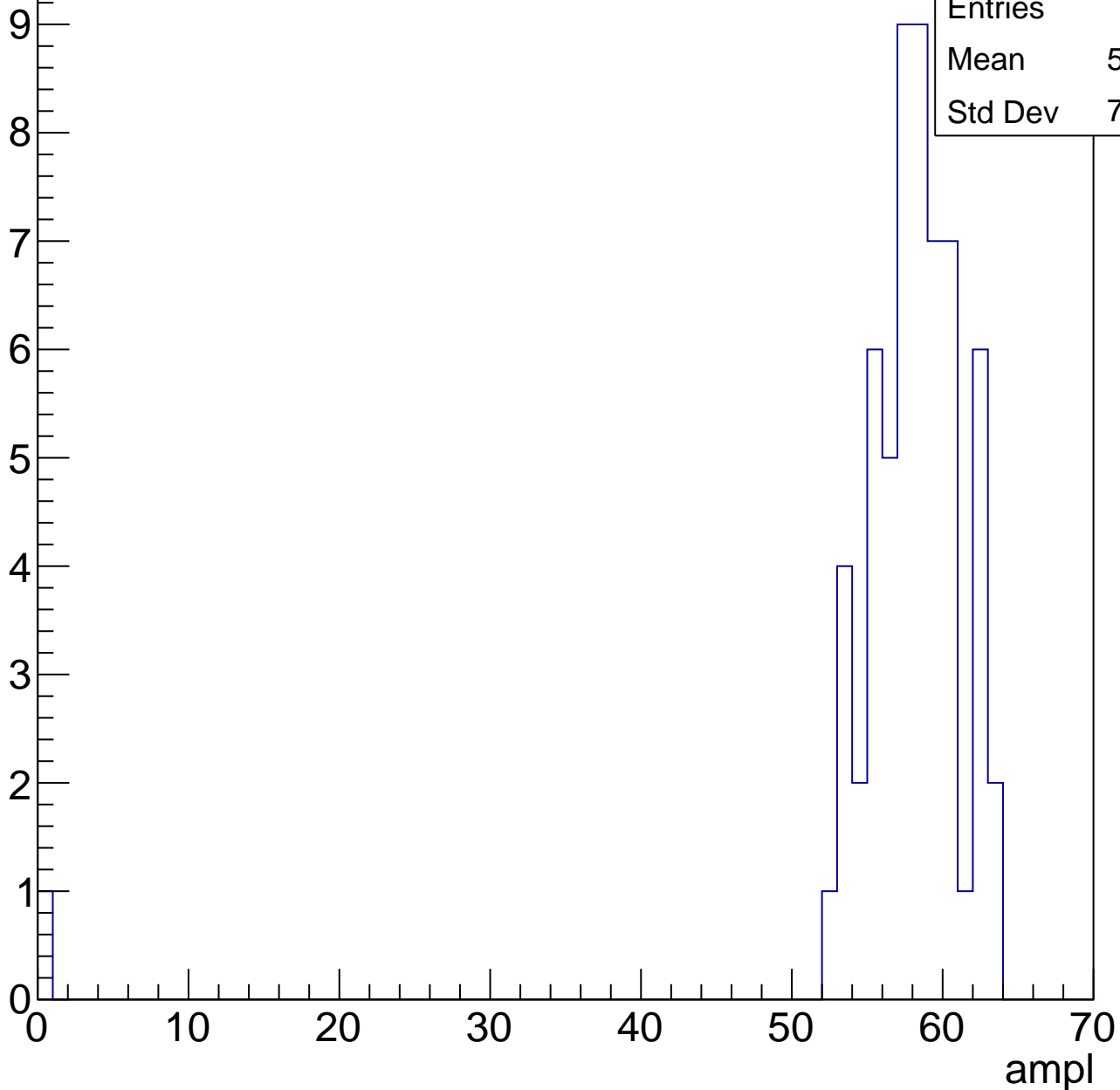


# B1L002S, U26-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.82
Std Dev	7.875

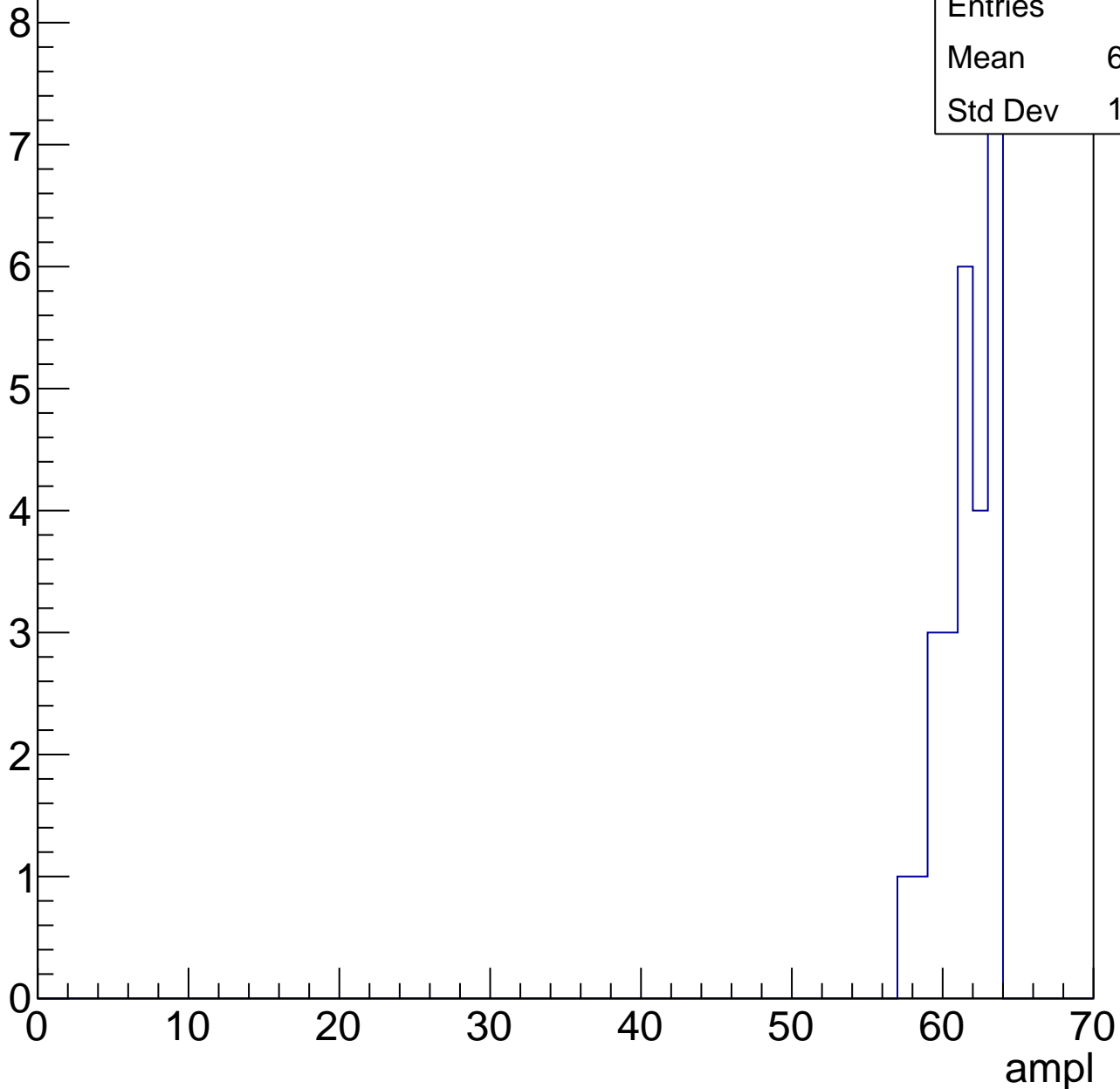


# B1L002S, U26-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.15
Std Dev	1.703

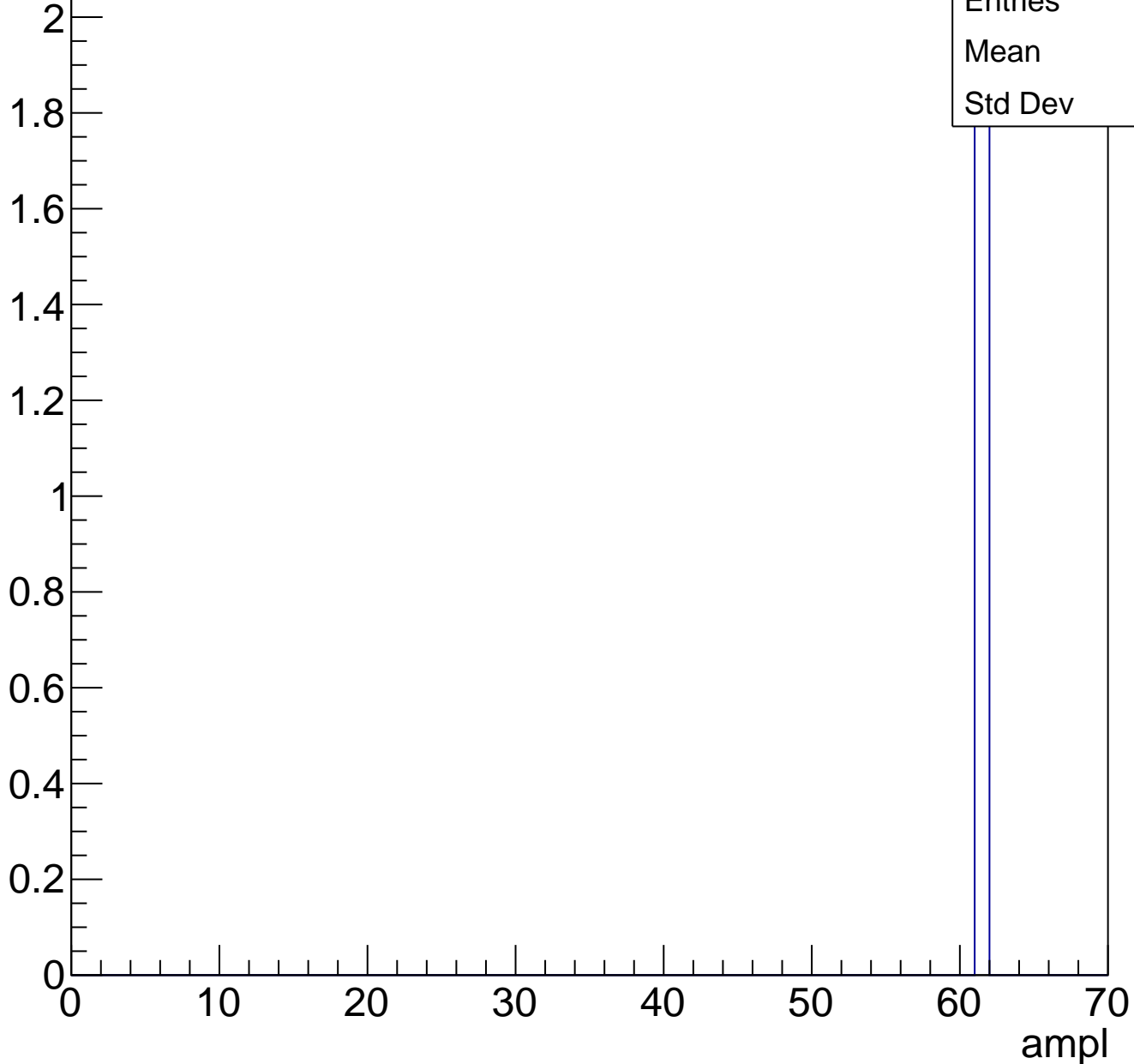




# B1L002S, U26-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch65, adc0

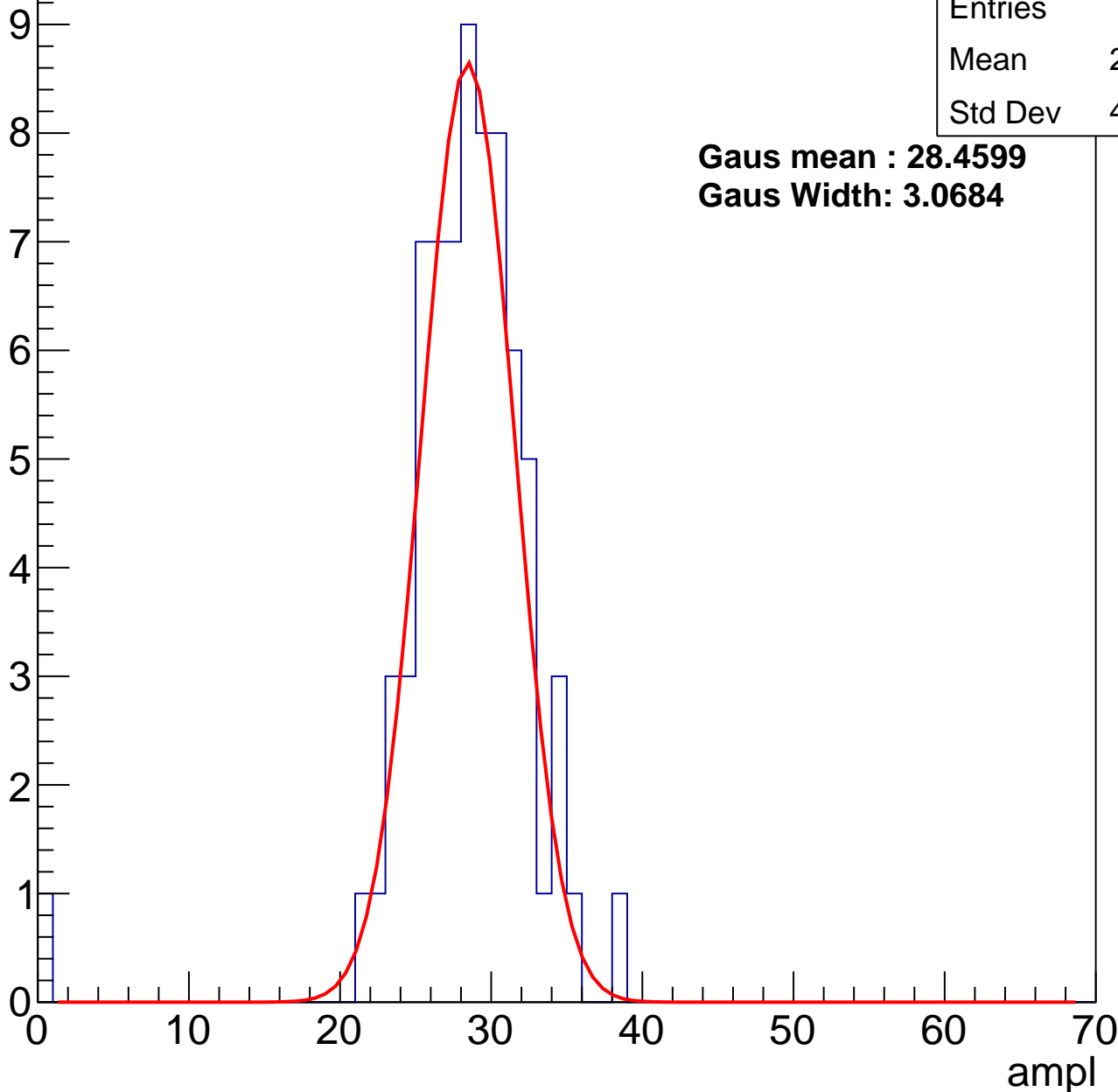
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.89
Std Dev	4.632

**Gaus mean : 28.4599**

**Gaus Width: 3.0684**



# B1L002S, U26-ch65, adc1

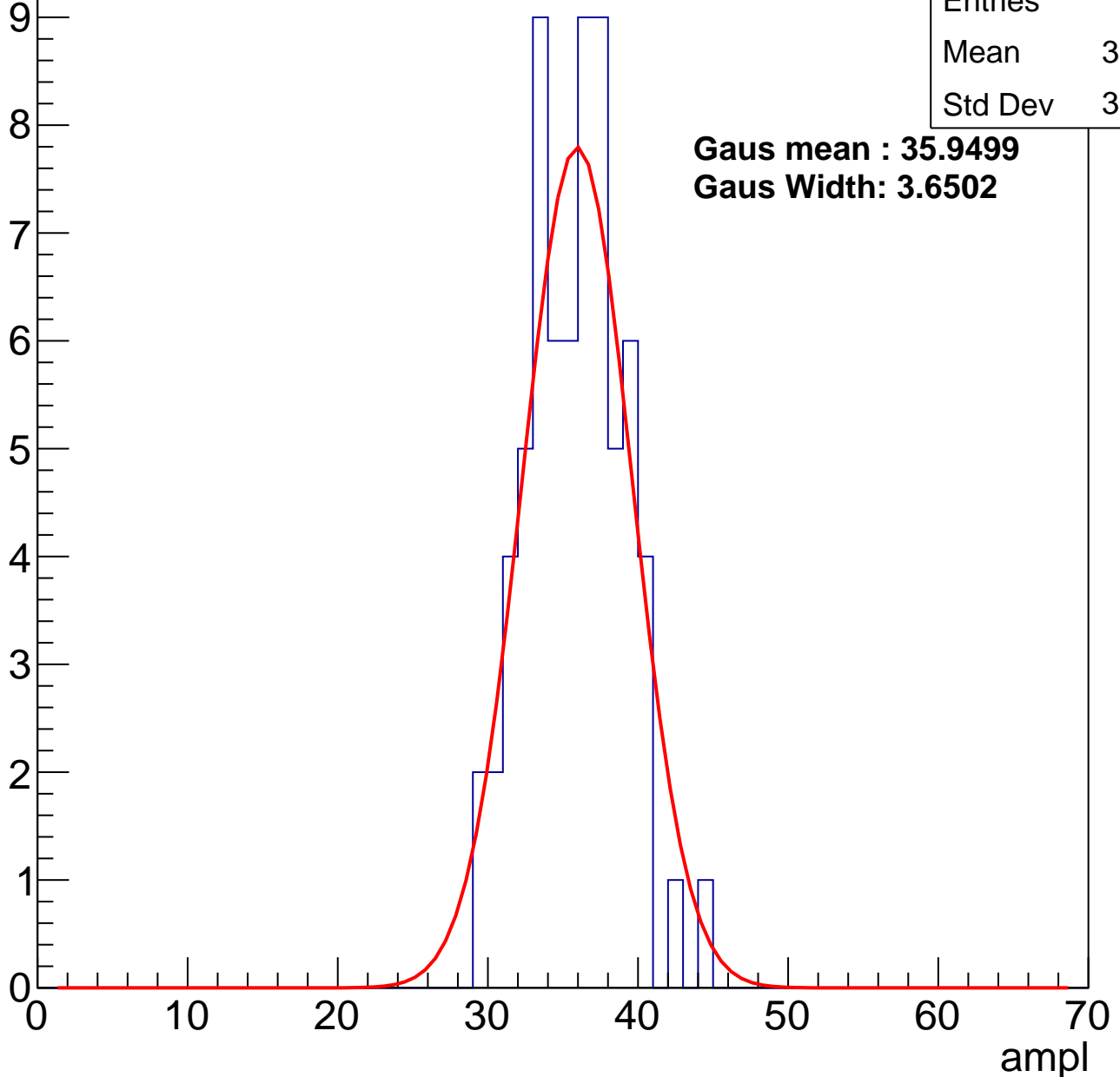
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	35.36
Std Dev	3.139

**Gaus mean : 35.9499**

**Gaus Width: 3.6502**



# B1L002S, U26-ch65, adc2

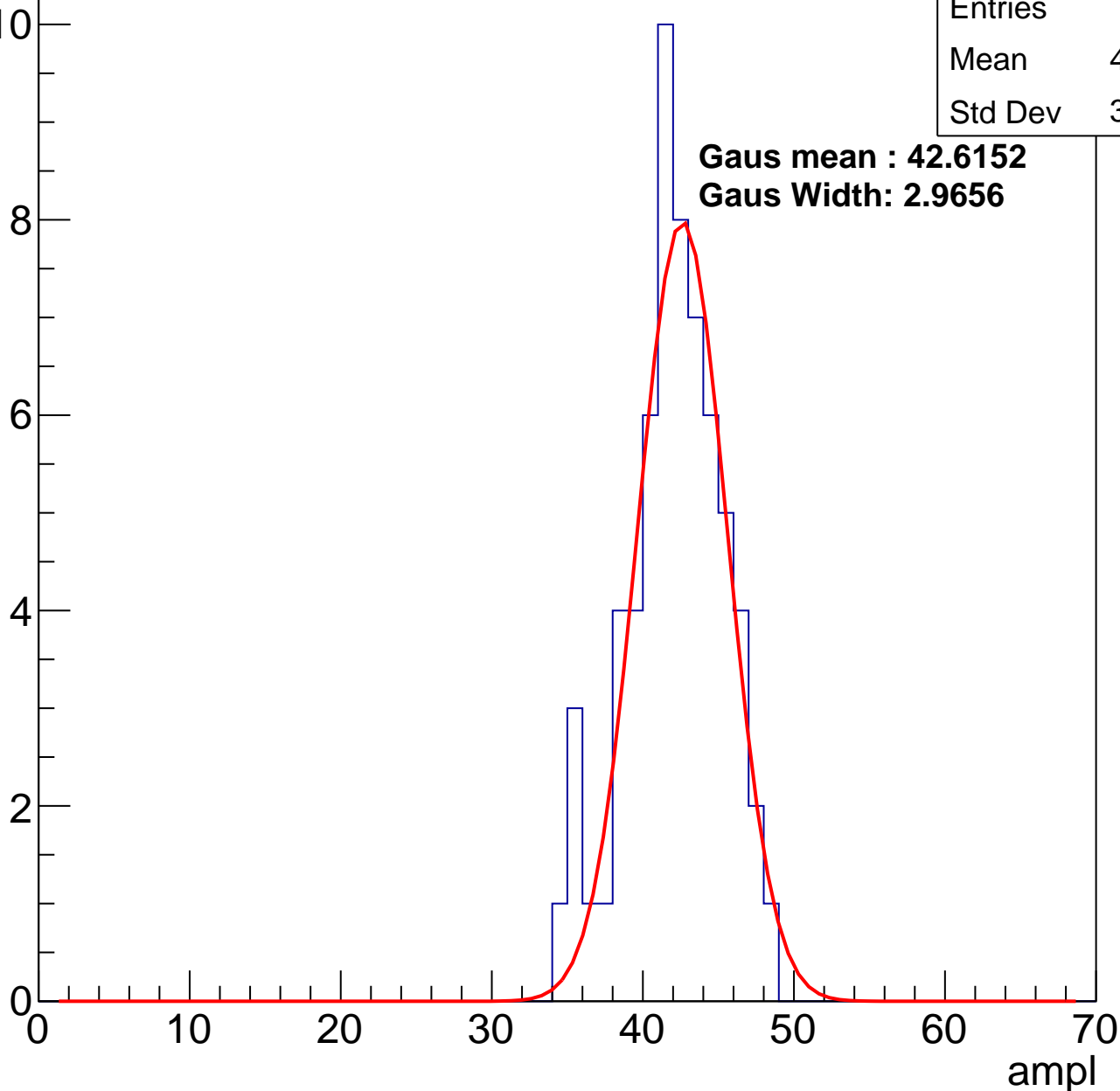
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.62
Std Dev	3.144

**Gaus mean : 42.6152**

**Gaus Width: 2.9656**



# B1L002S, U26-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	49.09
Std Dev	3.367

Entry

10

8

6

4

2

0

0

10

20

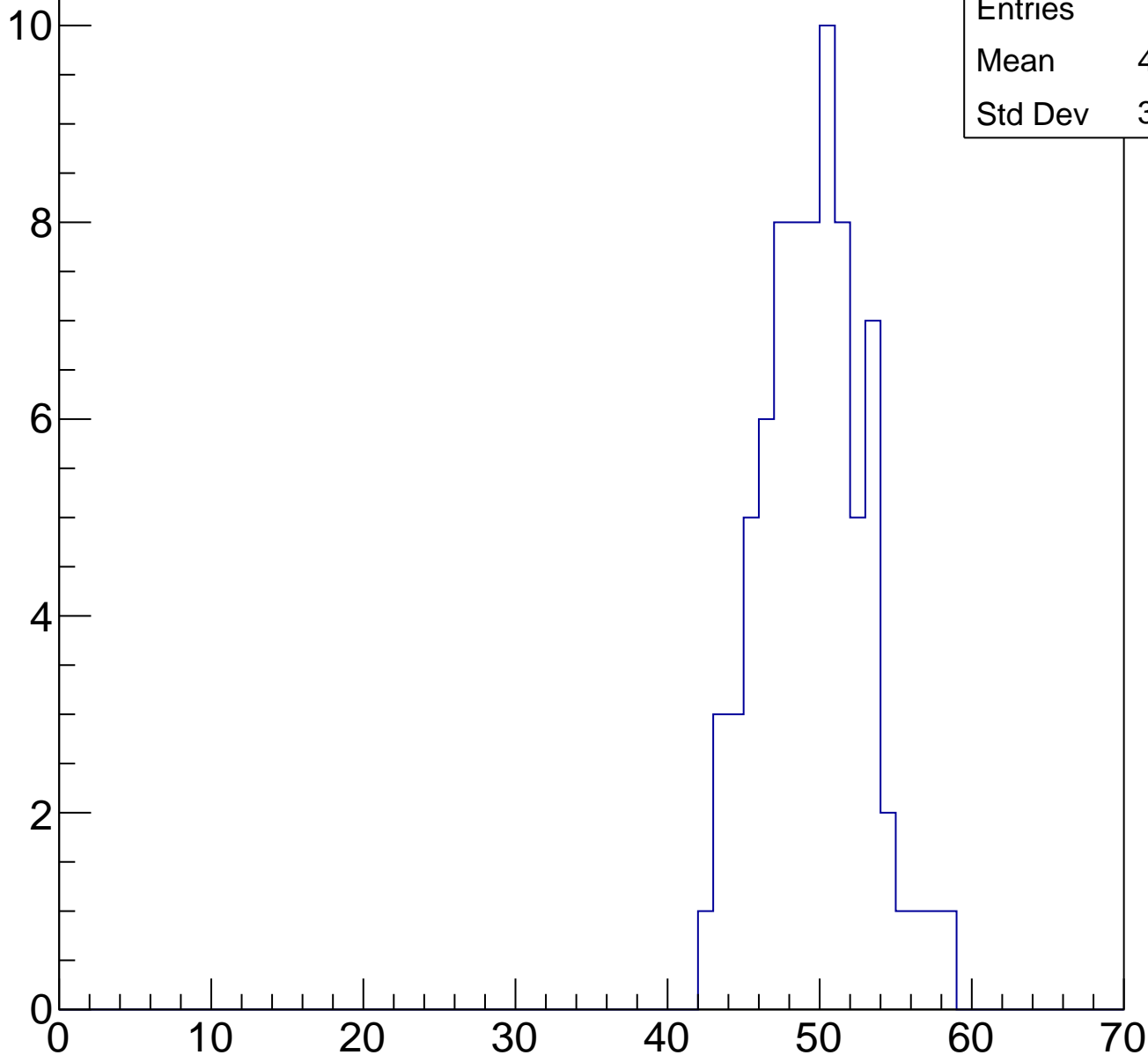
30

40

50

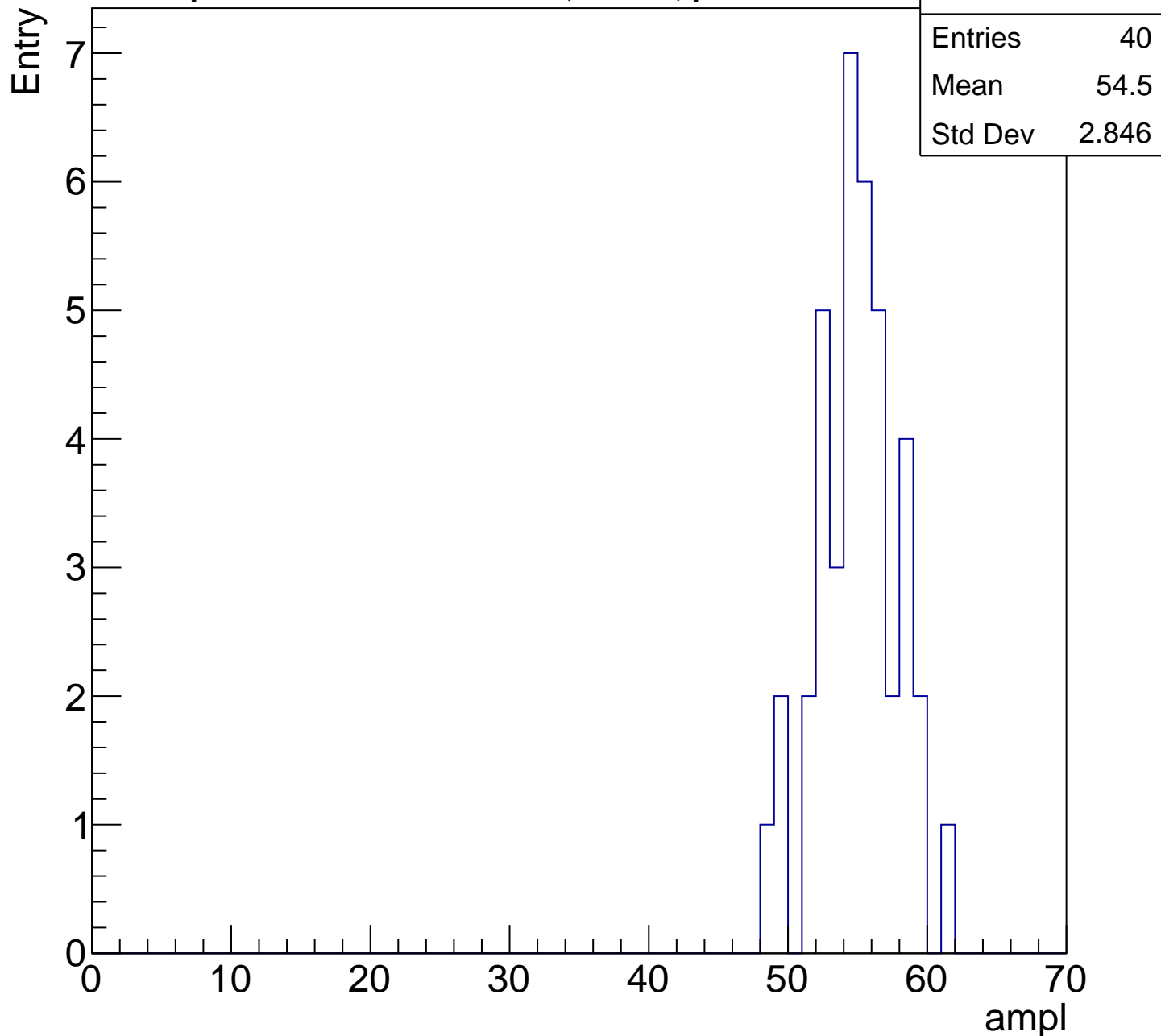
60

ampl



# B1L002S, U26-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

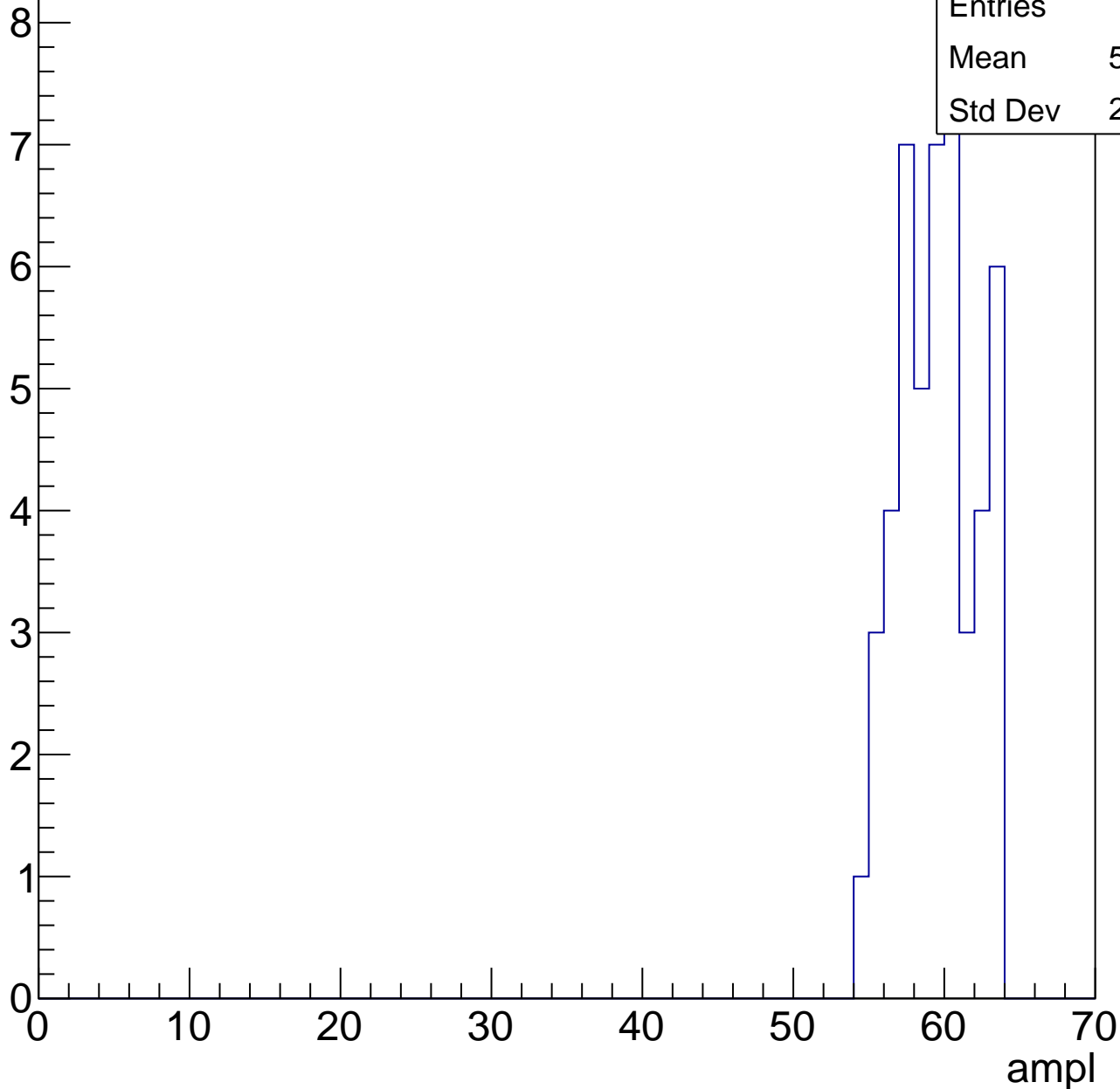


# B1L002S, U26-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	59.04
Std Dev	2.475

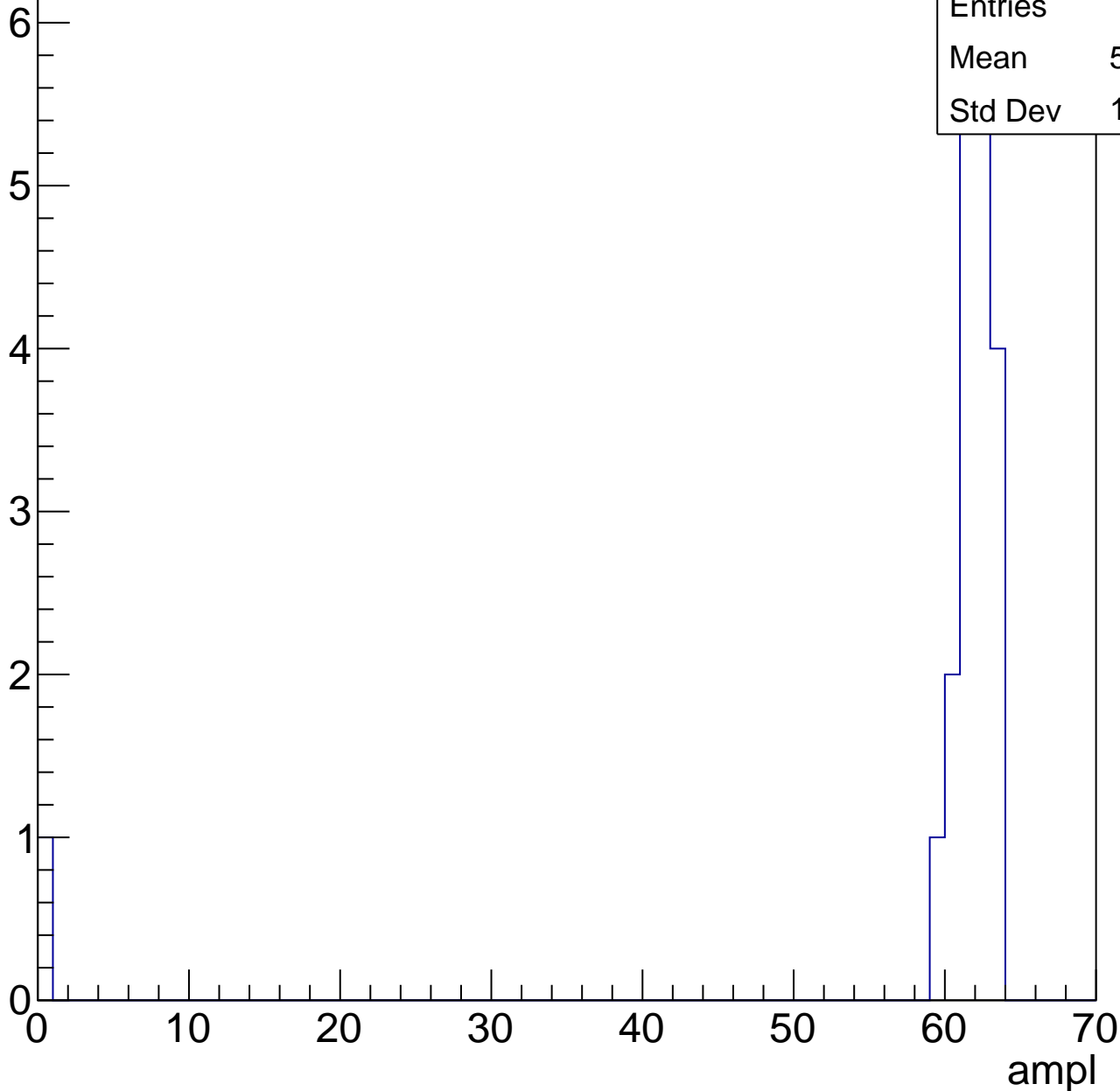


# B1L002S, U26-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.45
Std Dev	13.45





# B1L002S, U26-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch66, adc0

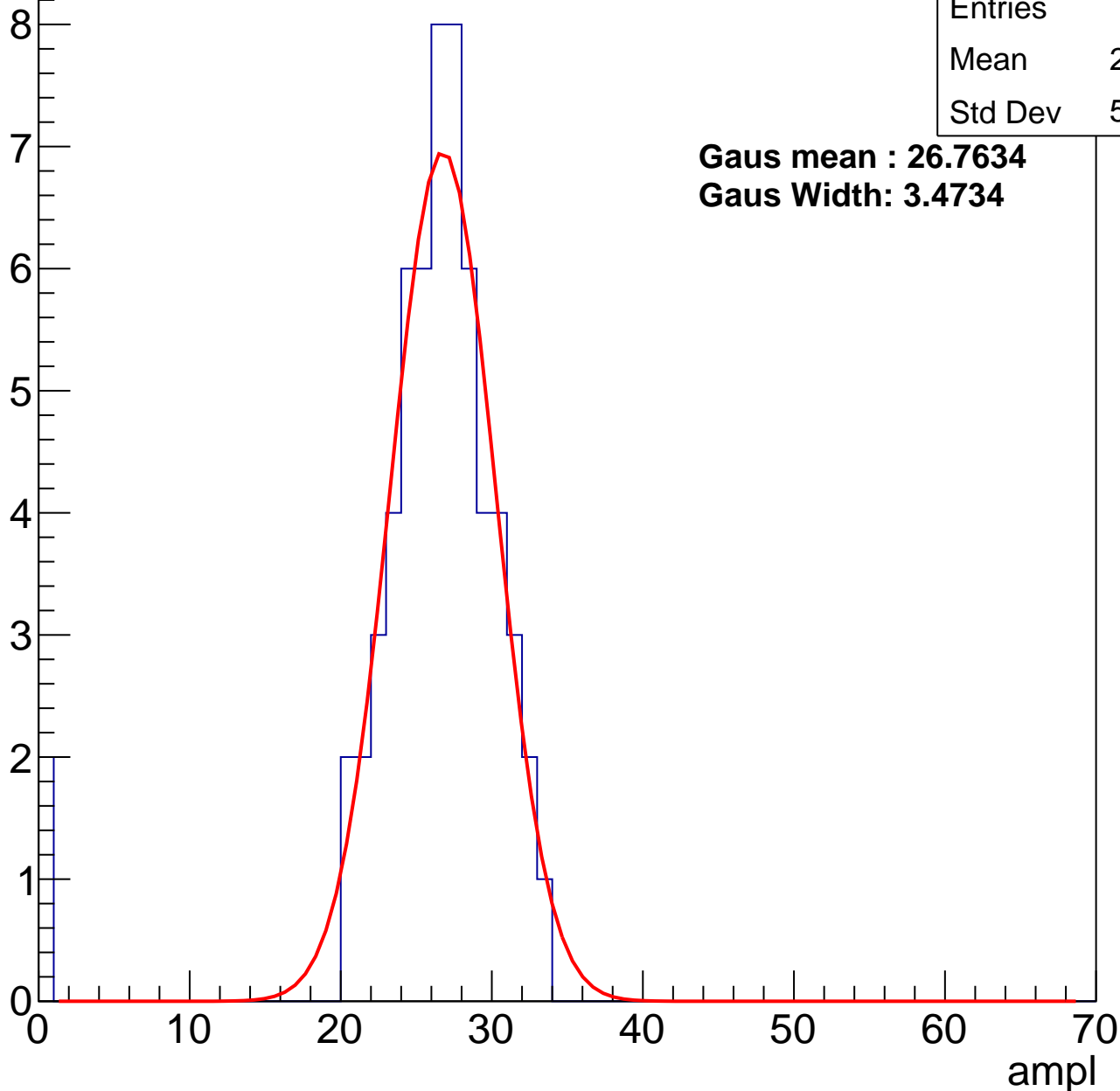
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	25.44
Std Dev	5.577

**Gaus mean : 26.7634**

**Gaus Width: 3.4734**



# B1L002S, U26-ch66, adc1

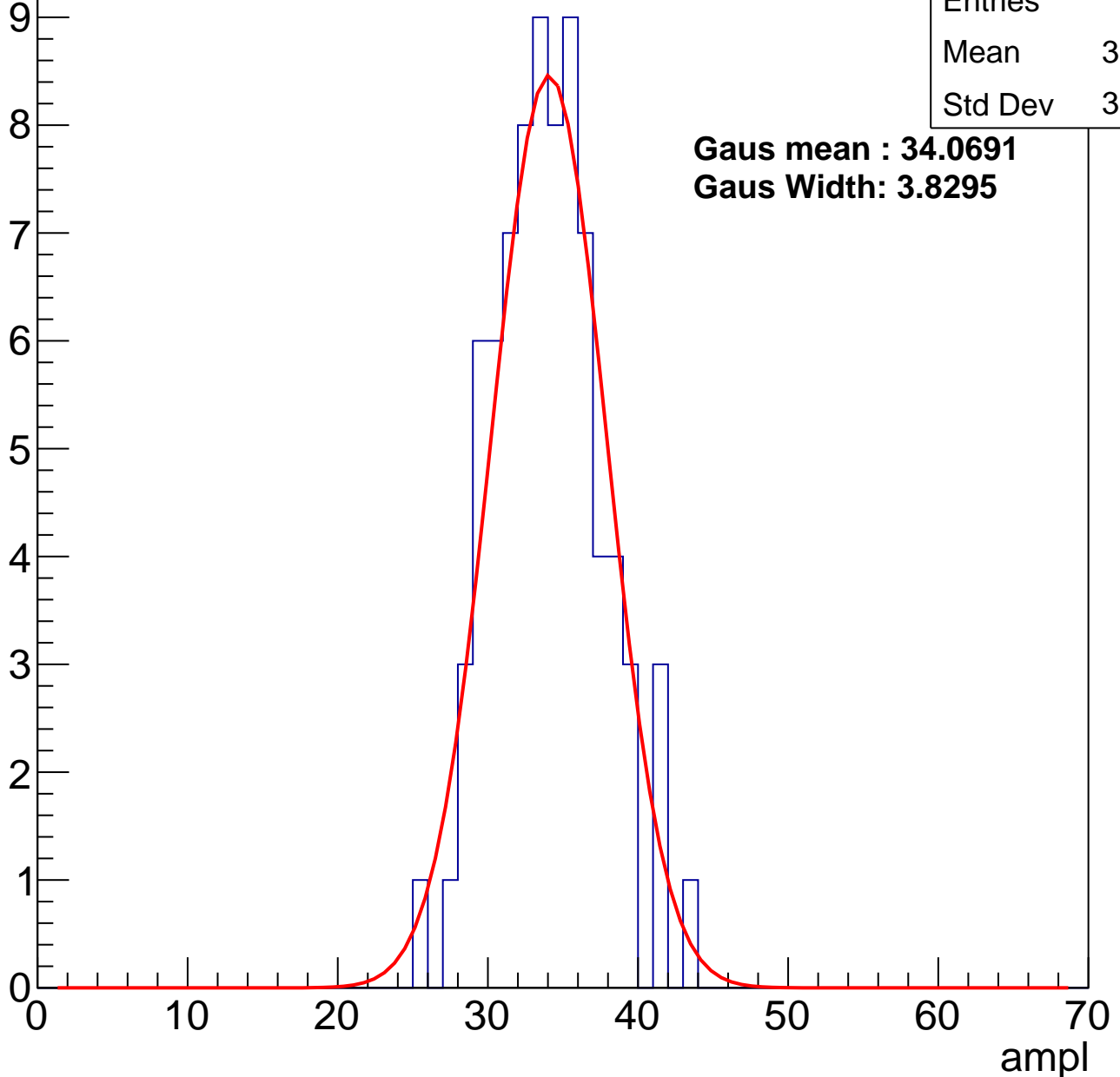
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	33.52
Std Dev	3.553

**Gaus mean : 34.0691**

**Gaus Width: 3.8295**



# B1L002S, U26-ch66, adc2

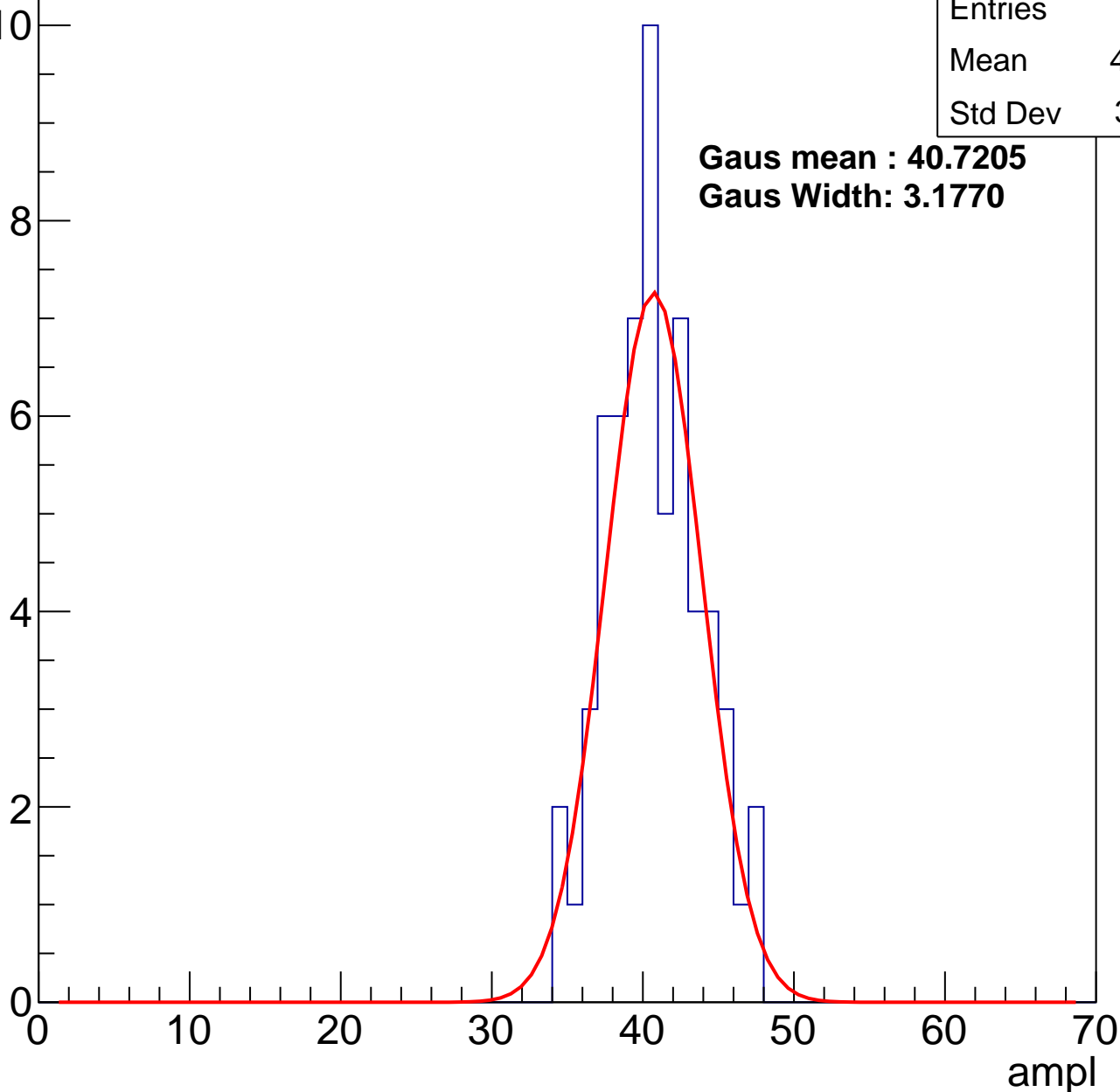
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.26
Std Dev	3.051

**Gaus mean : 40.7205**

**Gaus Width: 3.1770**

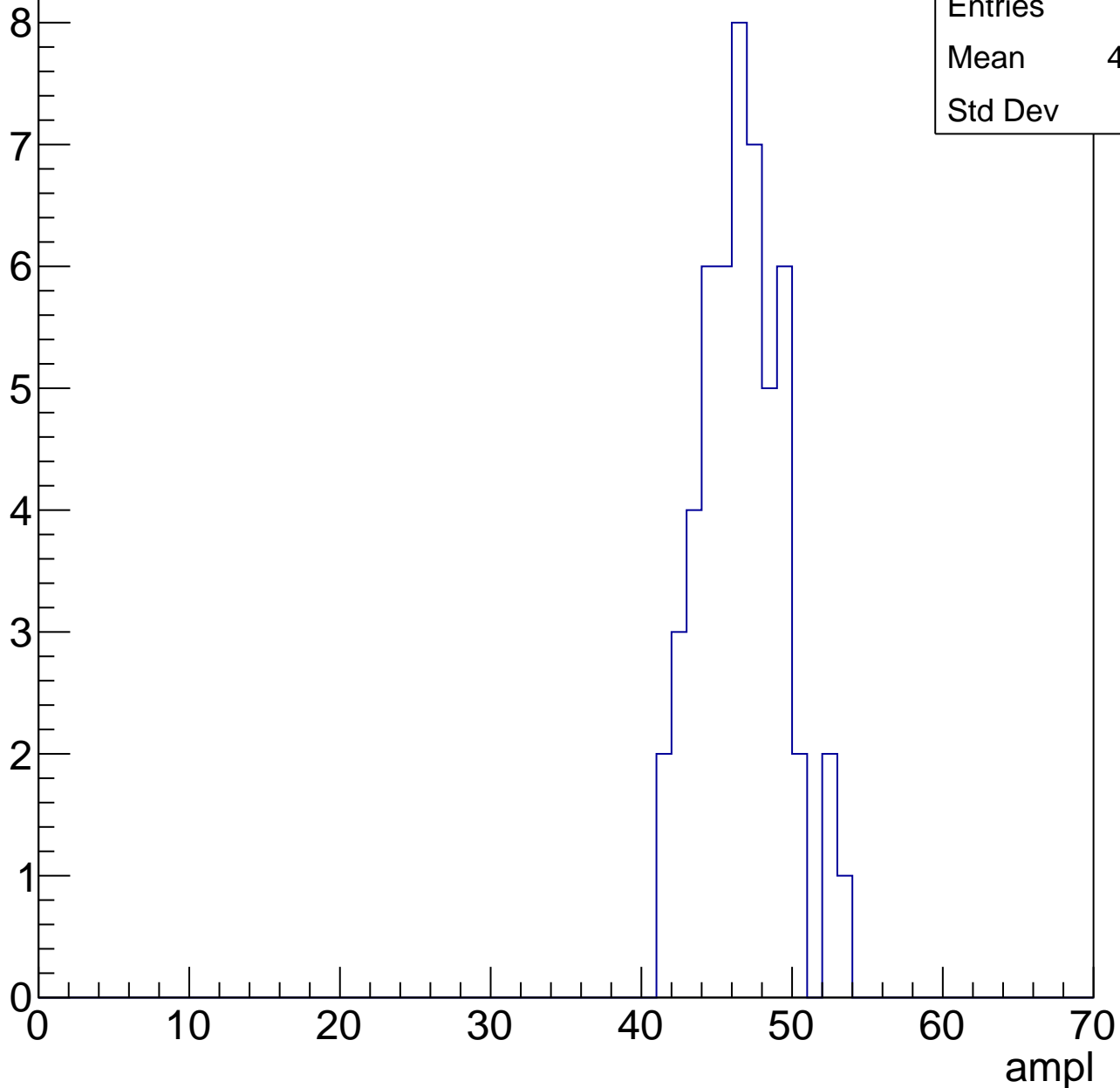


# B1L002S, U26-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	46.19
Std Dev	2.76

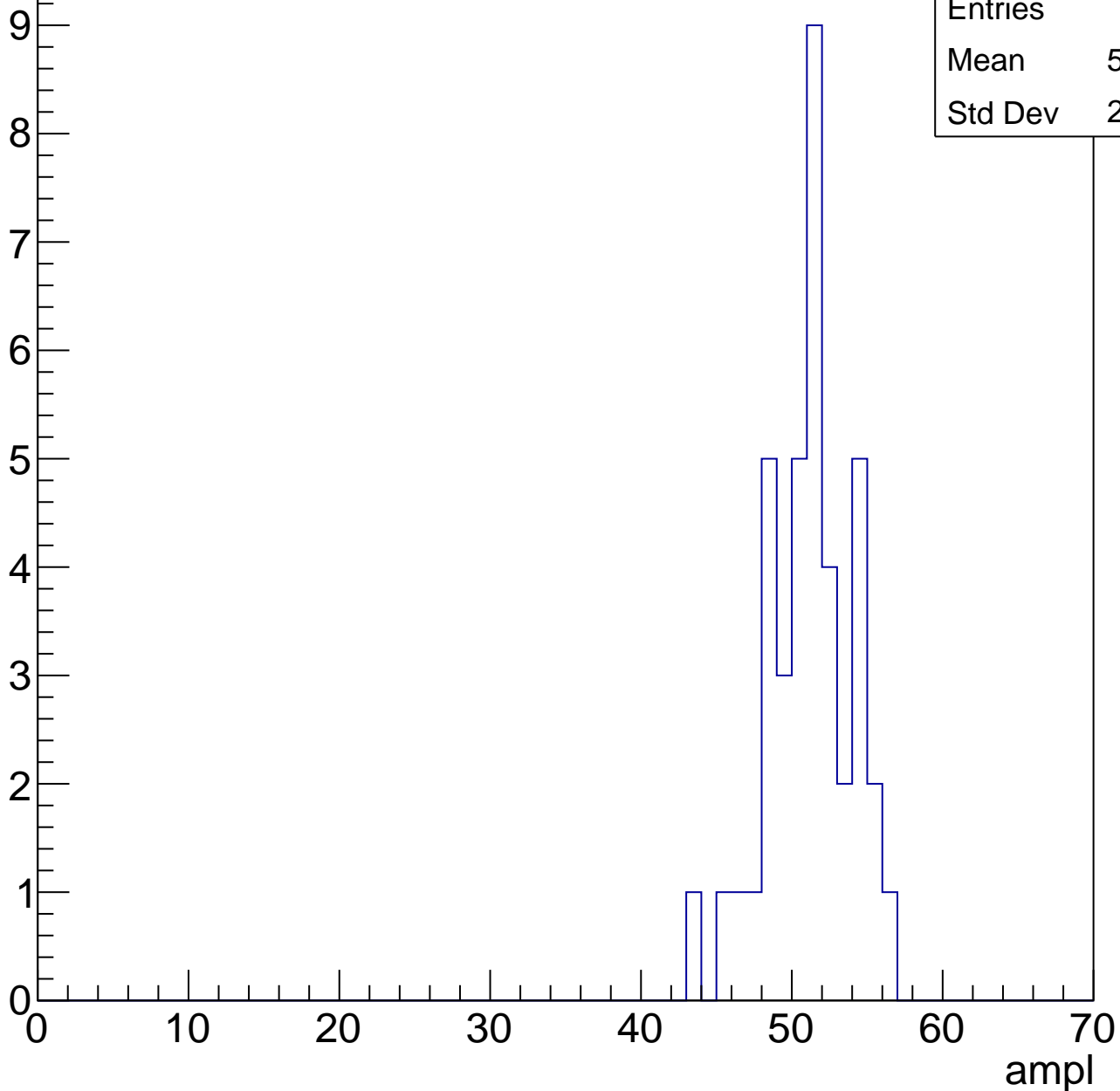


# B1L002S, U26-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	50.67
Std Dev	2.796



# B1L002S, U26-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

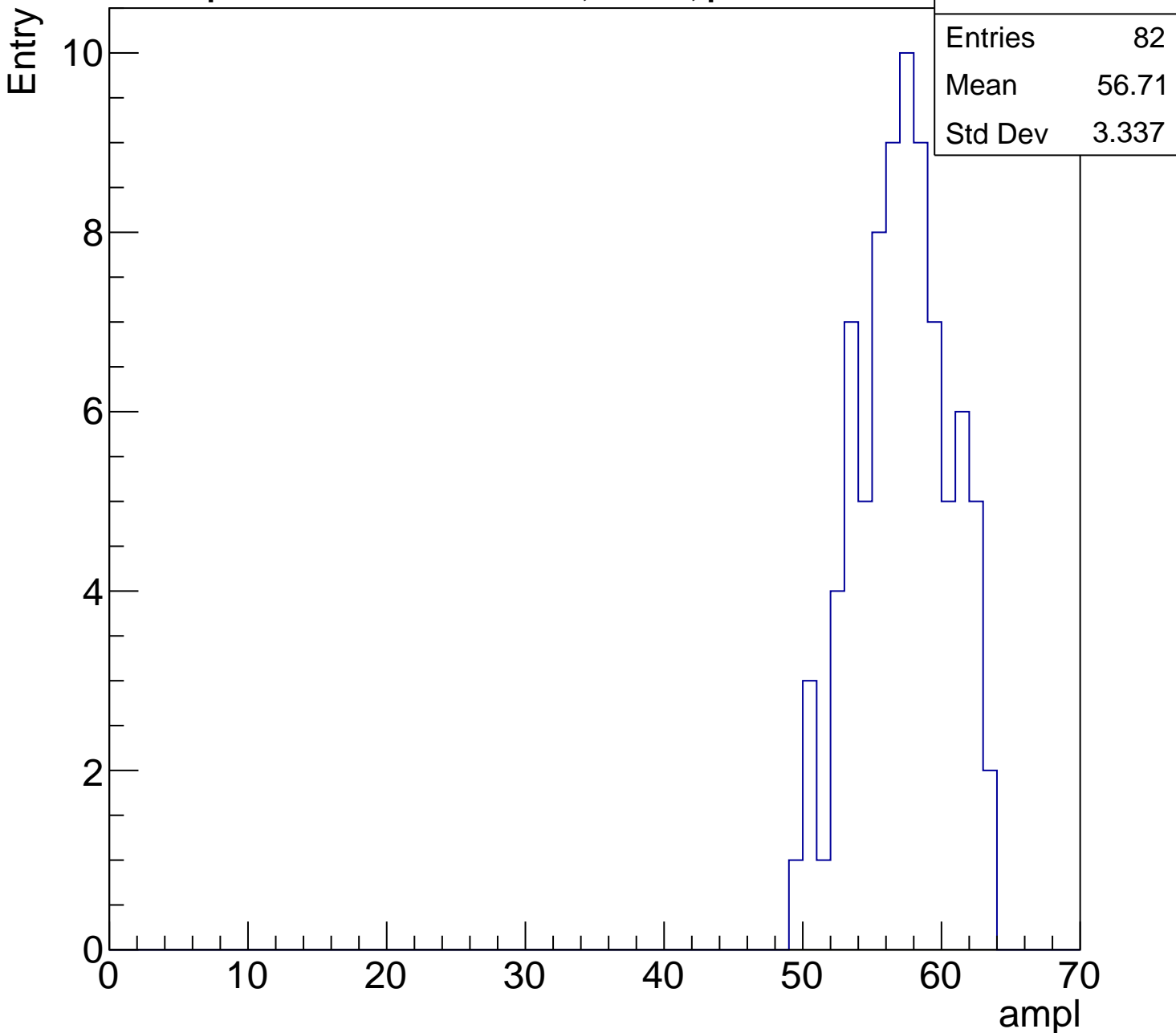
Entries	82
Mean	56.71
Std Dev	3.337

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

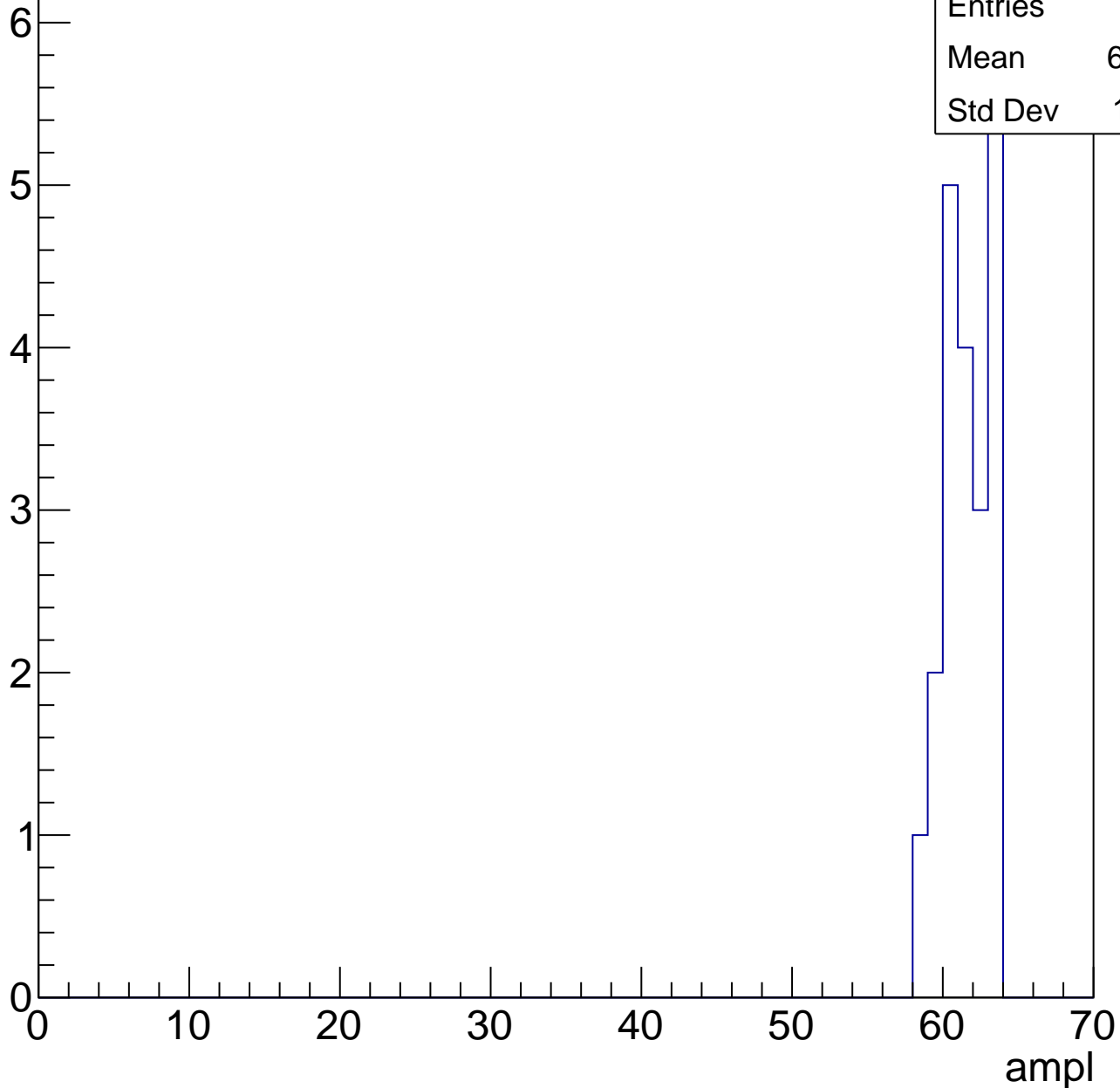


# B1L002S, U26-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.14
Std Dev	1.521





# B1L002S, U26-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

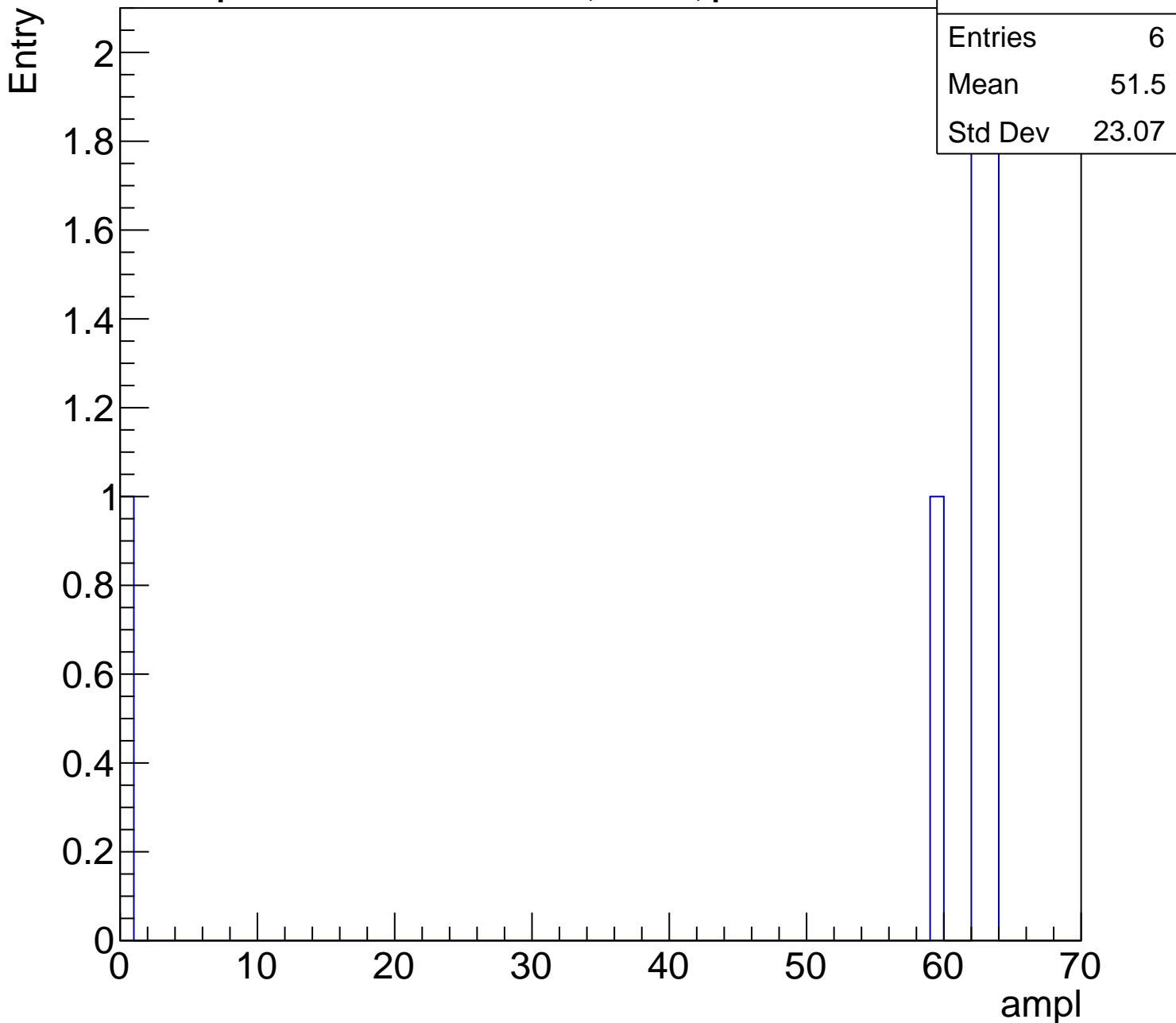
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.5
Std Dev	23.07

0 10 20 30 40 50 60 70

ampl



# B1L002S, U26-ch67, adc0

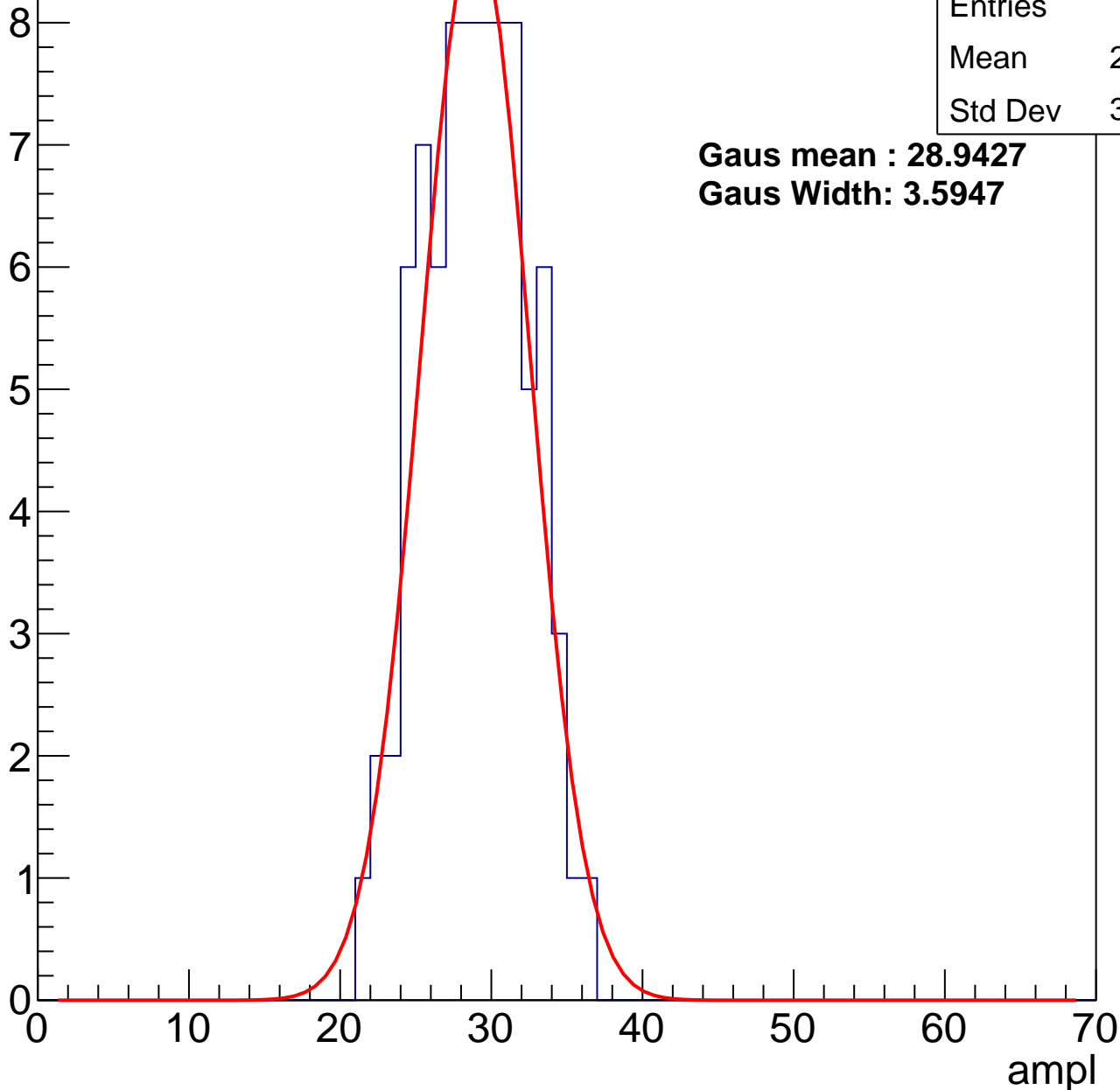
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	28.46
Std Dev	3.369

**Gaus mean : 28.9427**

**Gaus Width: 3.5947**



# B1L002S, U26-ch67, adc1

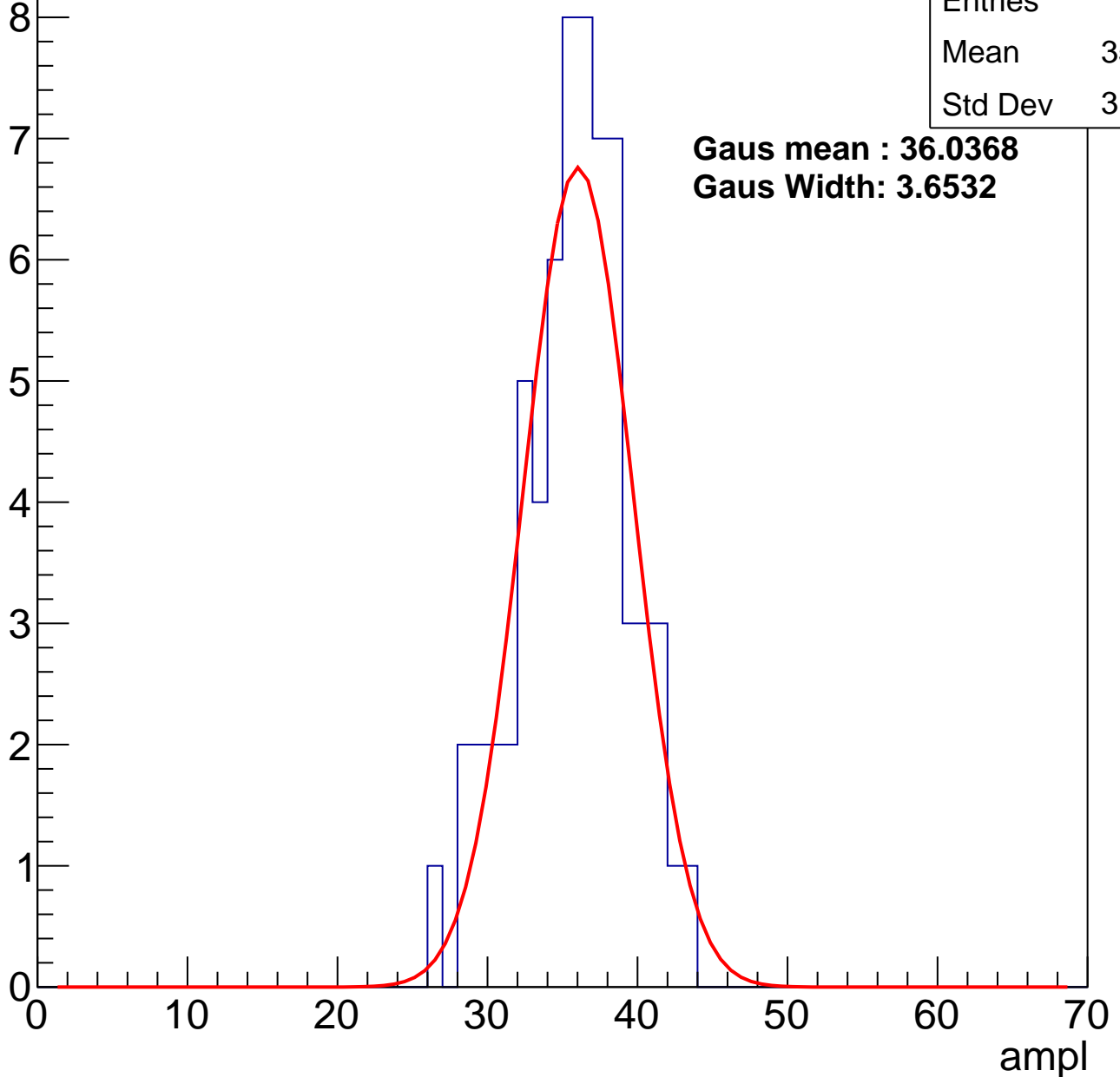
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35.32
Std Dev	3.587

**Gaus mean : 36.0368**

**Gaus Width: 3.6532**



# B1L002S, U26-ch67, adc2

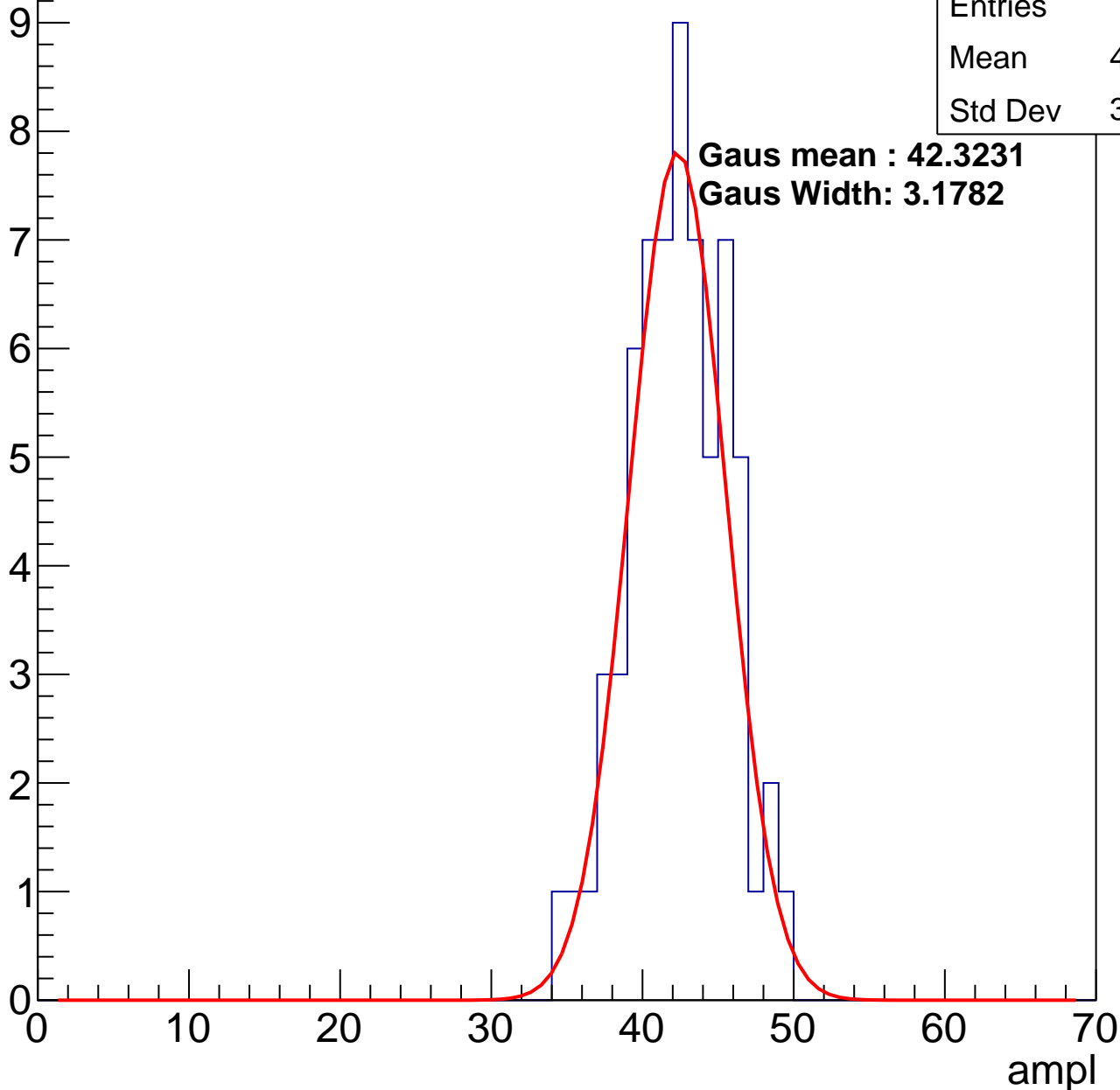
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.92
Std Dev	3.202

**Gaus mean : 42.3231**

**Gaus Width: 3.1782**

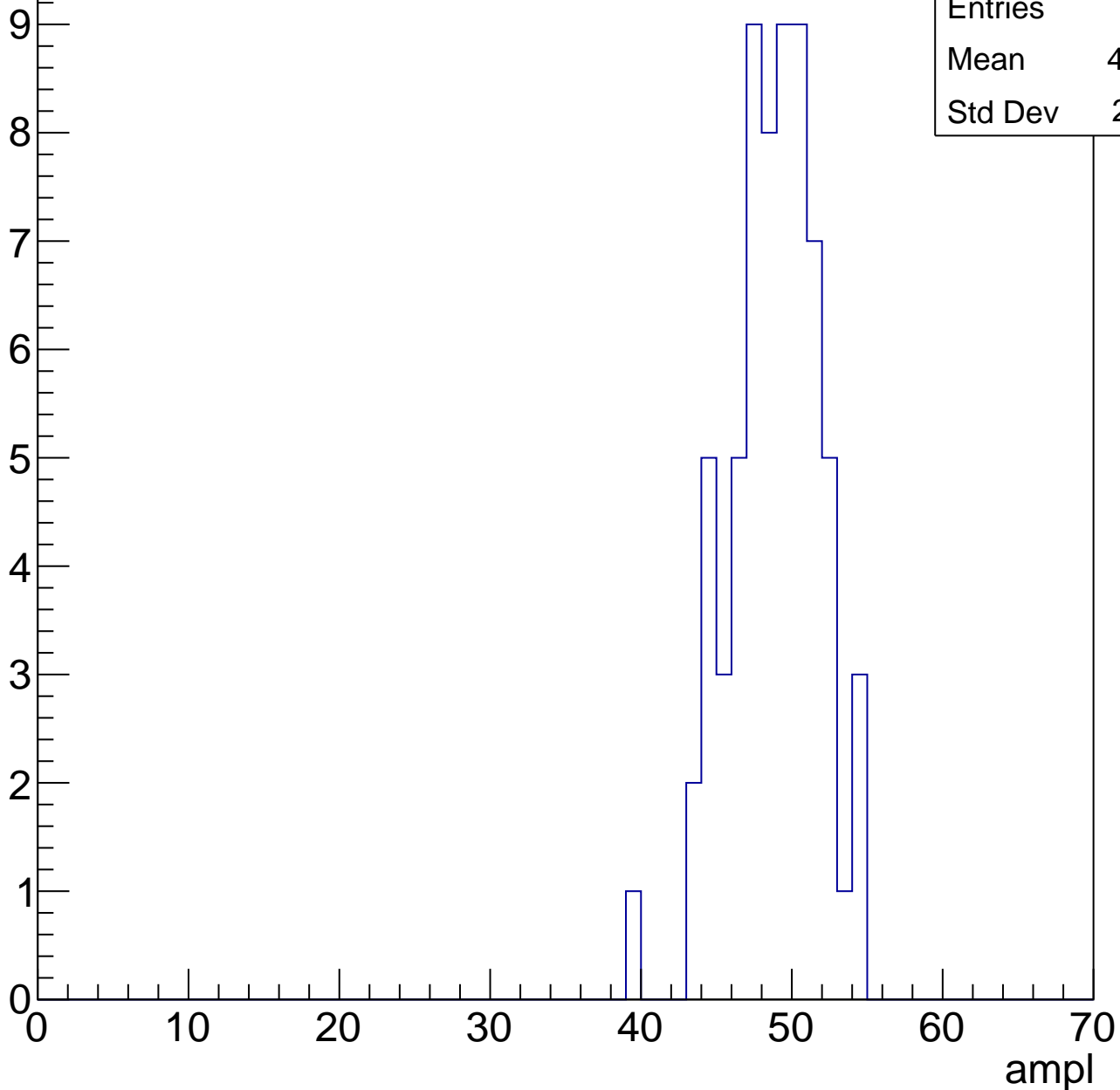


# B1L002S, U26-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.36
Std Dev	2.941

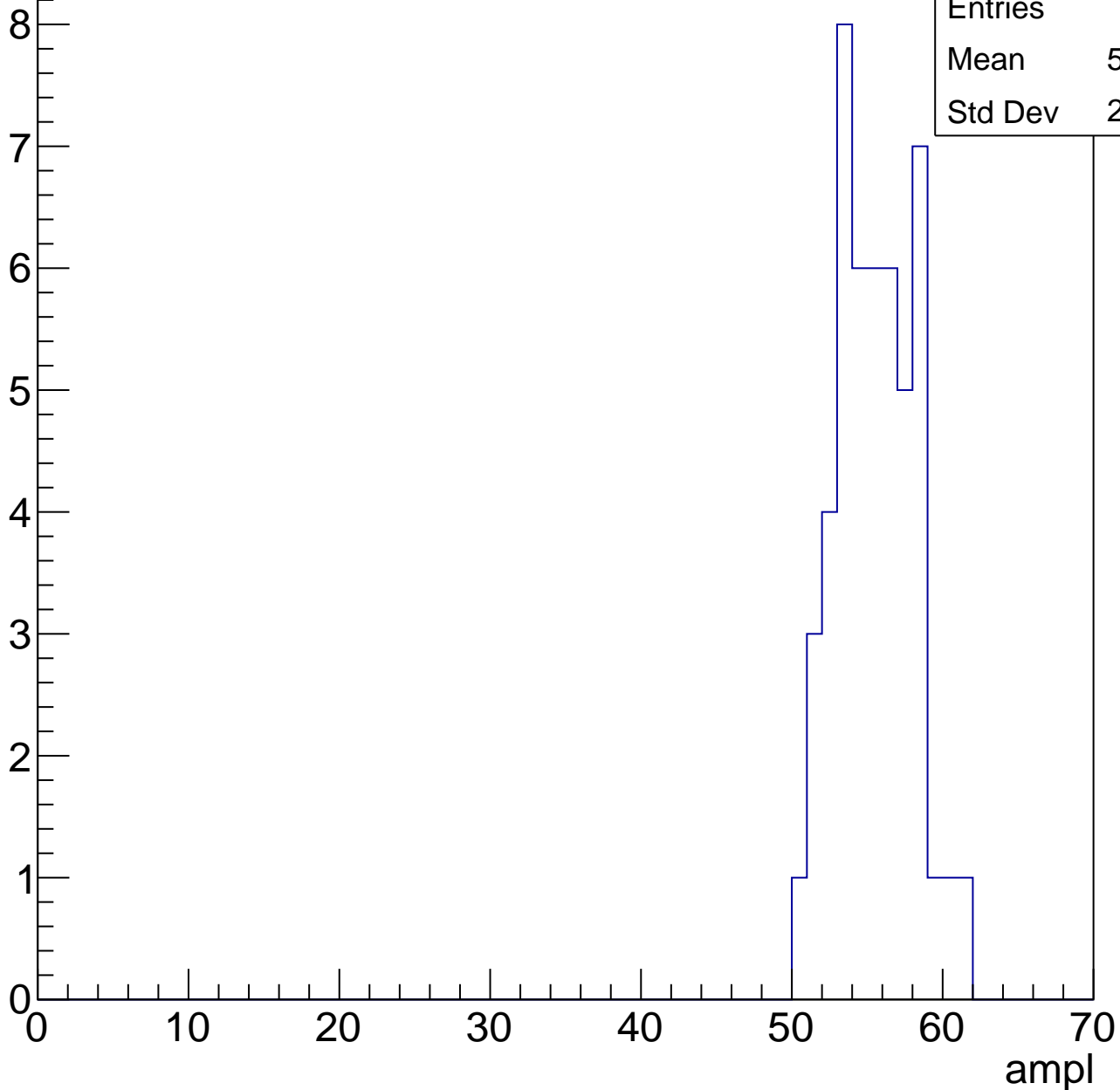


# B1L002S, U26-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

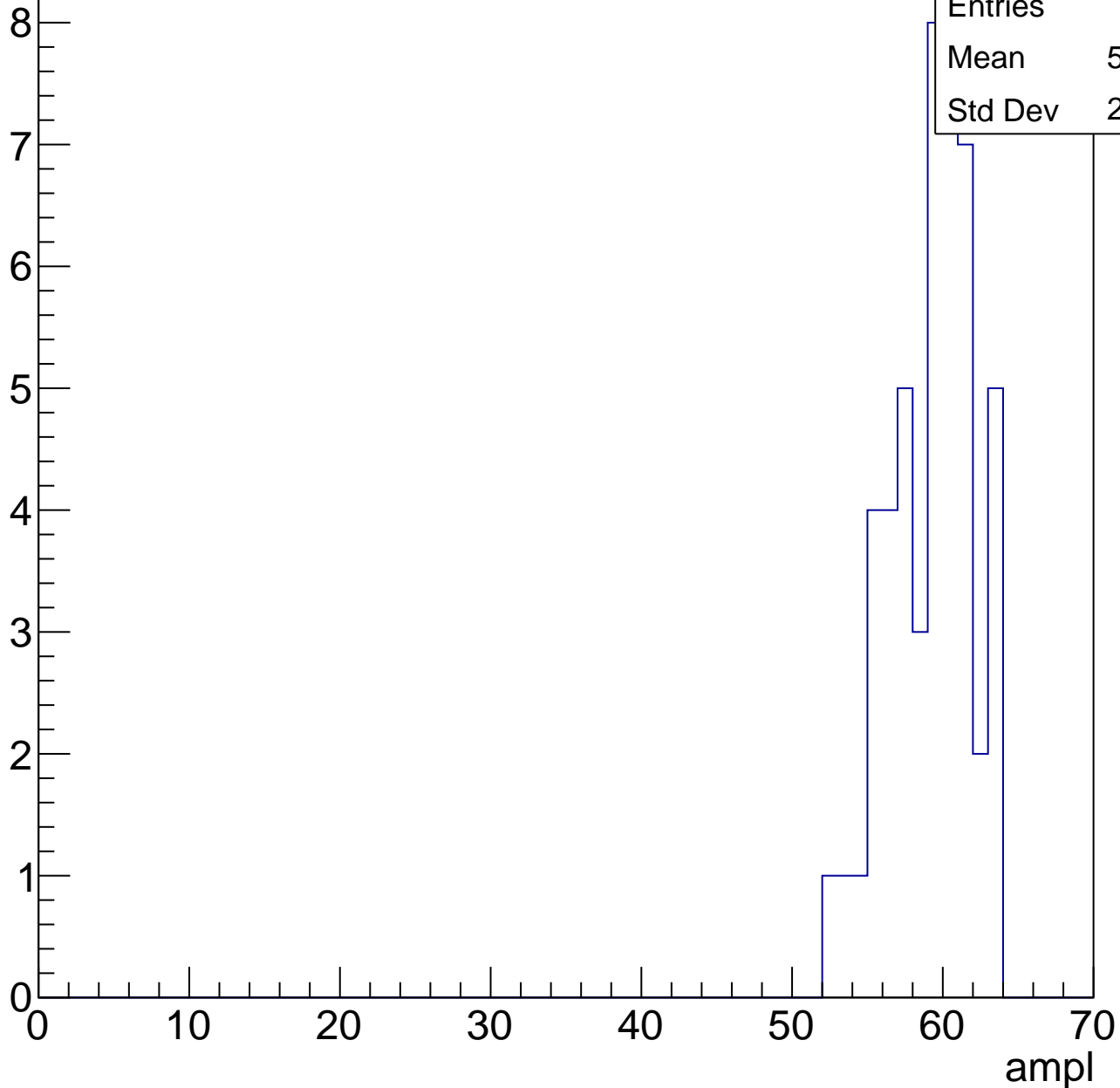
Entries	49
Mean	55.02
Std Dev	2.527



# B1L002S, U26-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

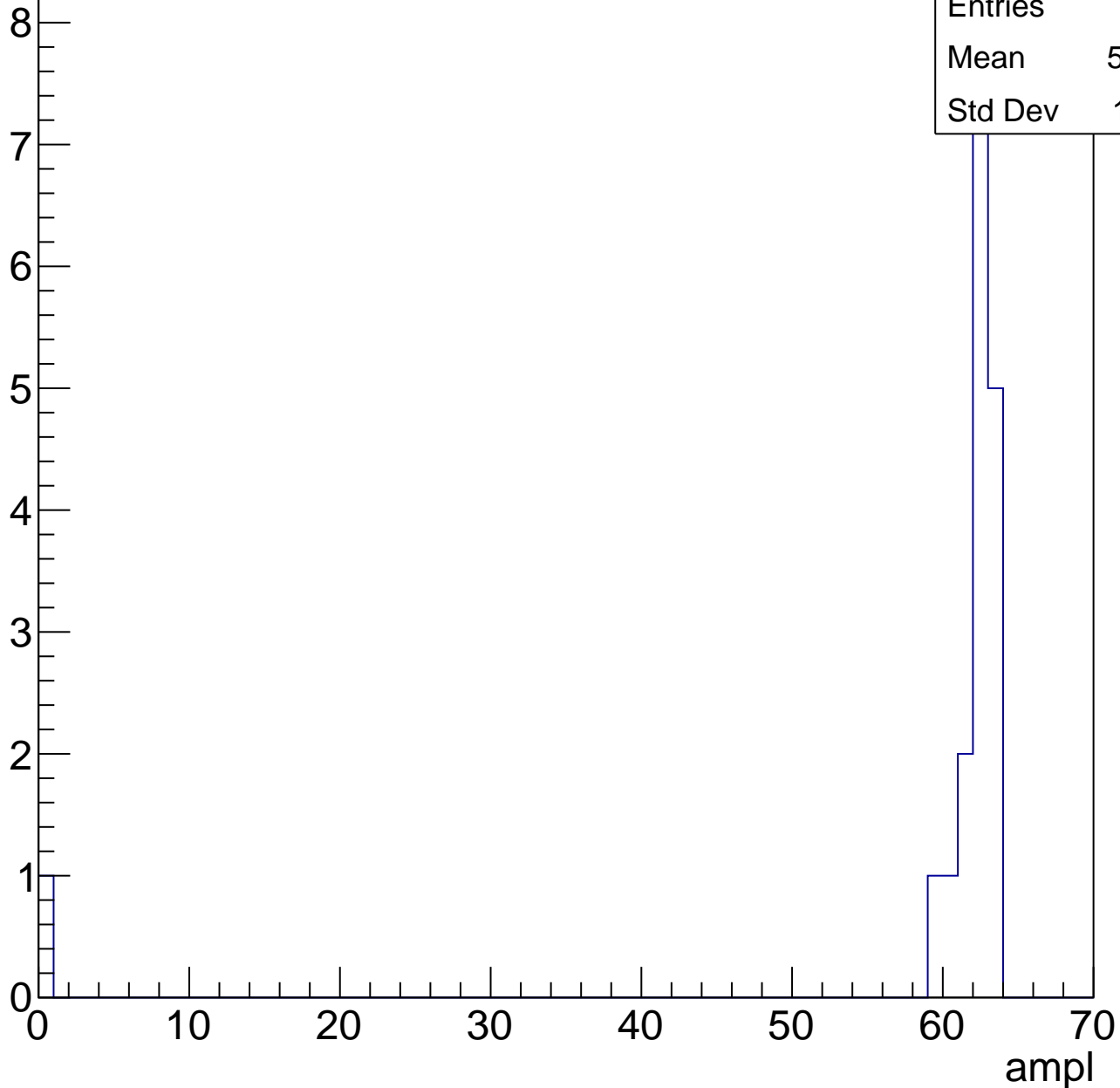


# B1L002S, U26-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.44
Std Dev	14.21

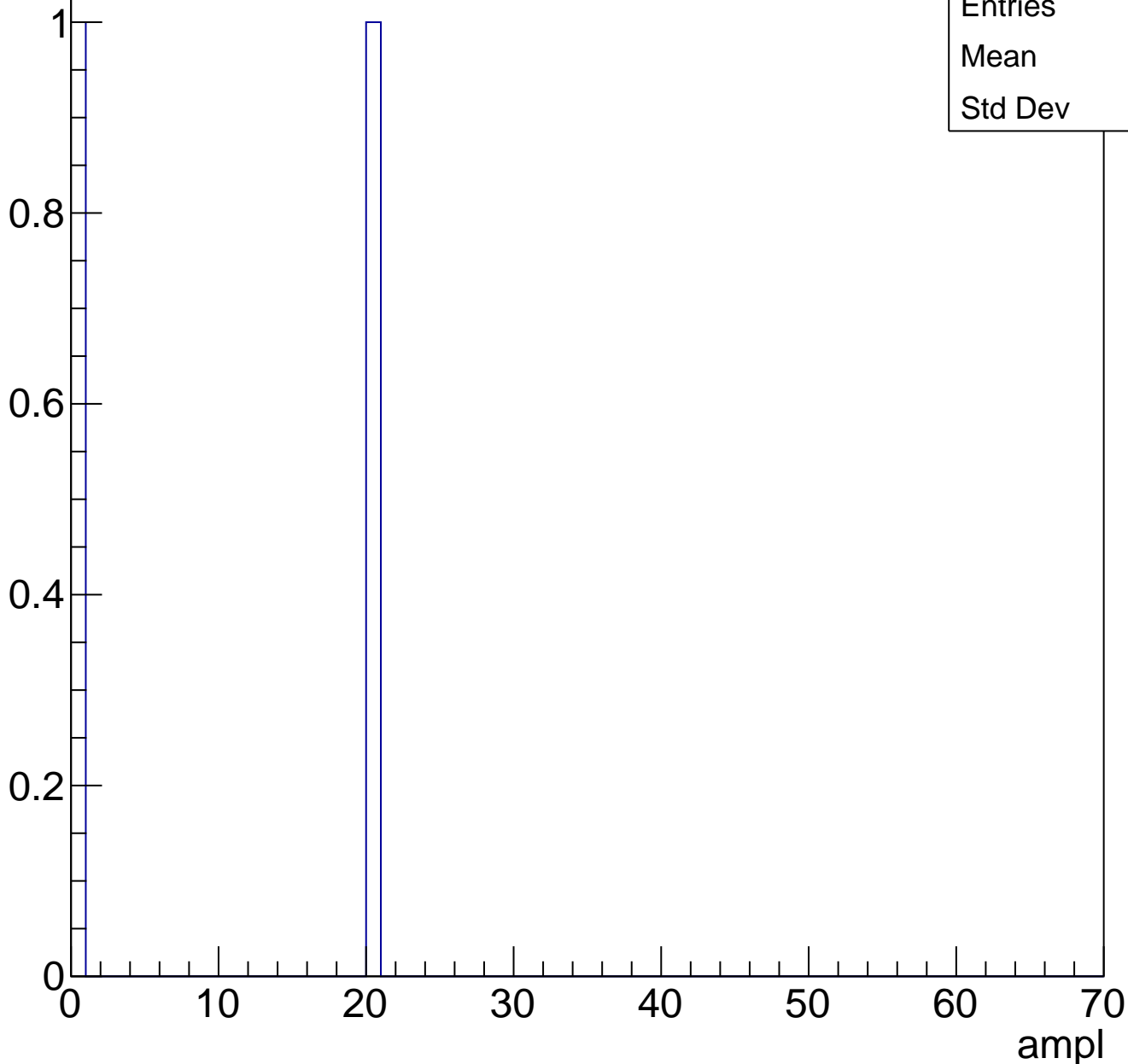




# B1L002S, U26-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L002S, U26-ch68, adc0

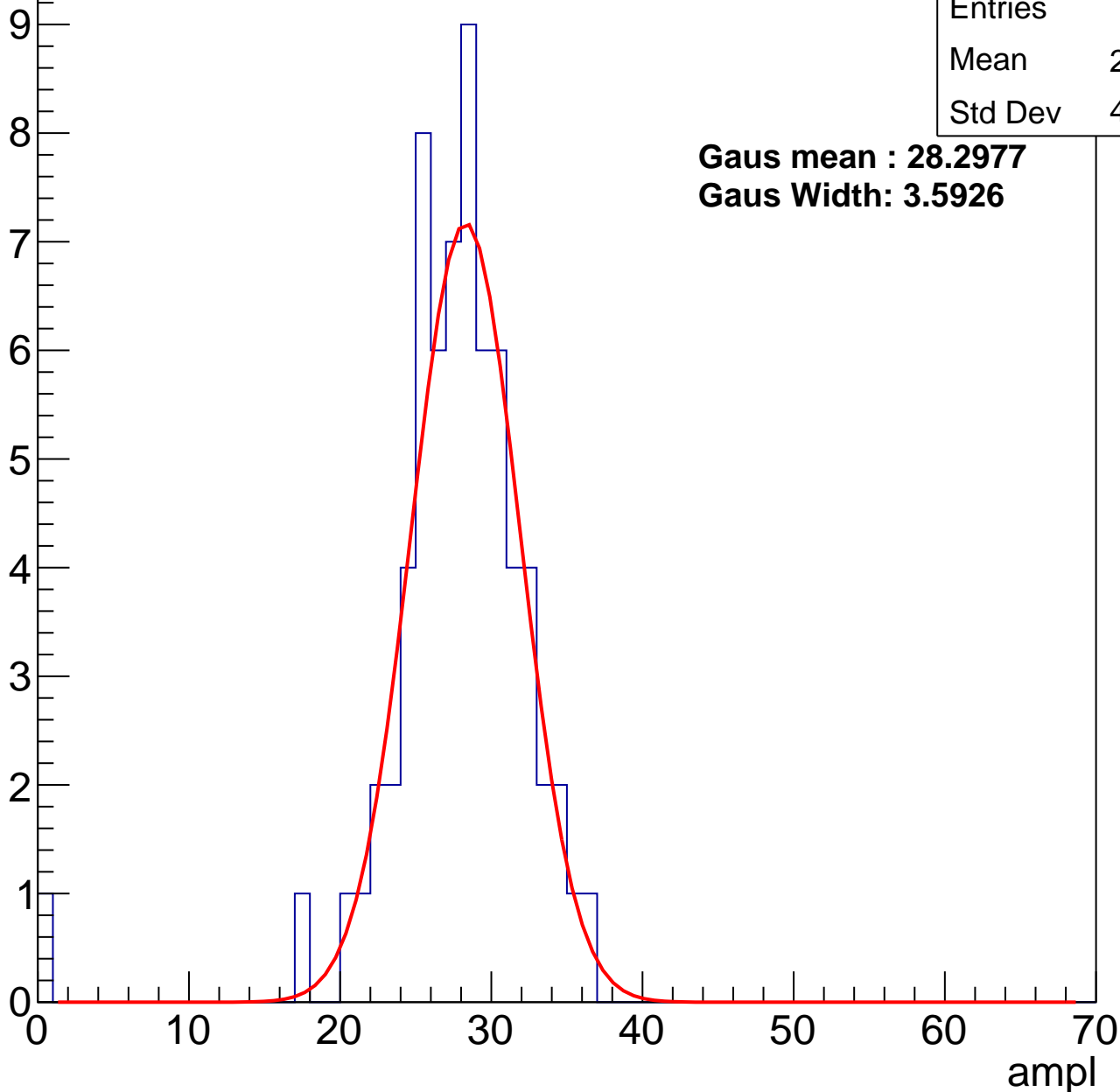
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.24
Std Dev	4.899

**Gaus mean : 28.2977**

**Gaus Width: 3.5926**



# B1L002S, U26-ch68, adc1

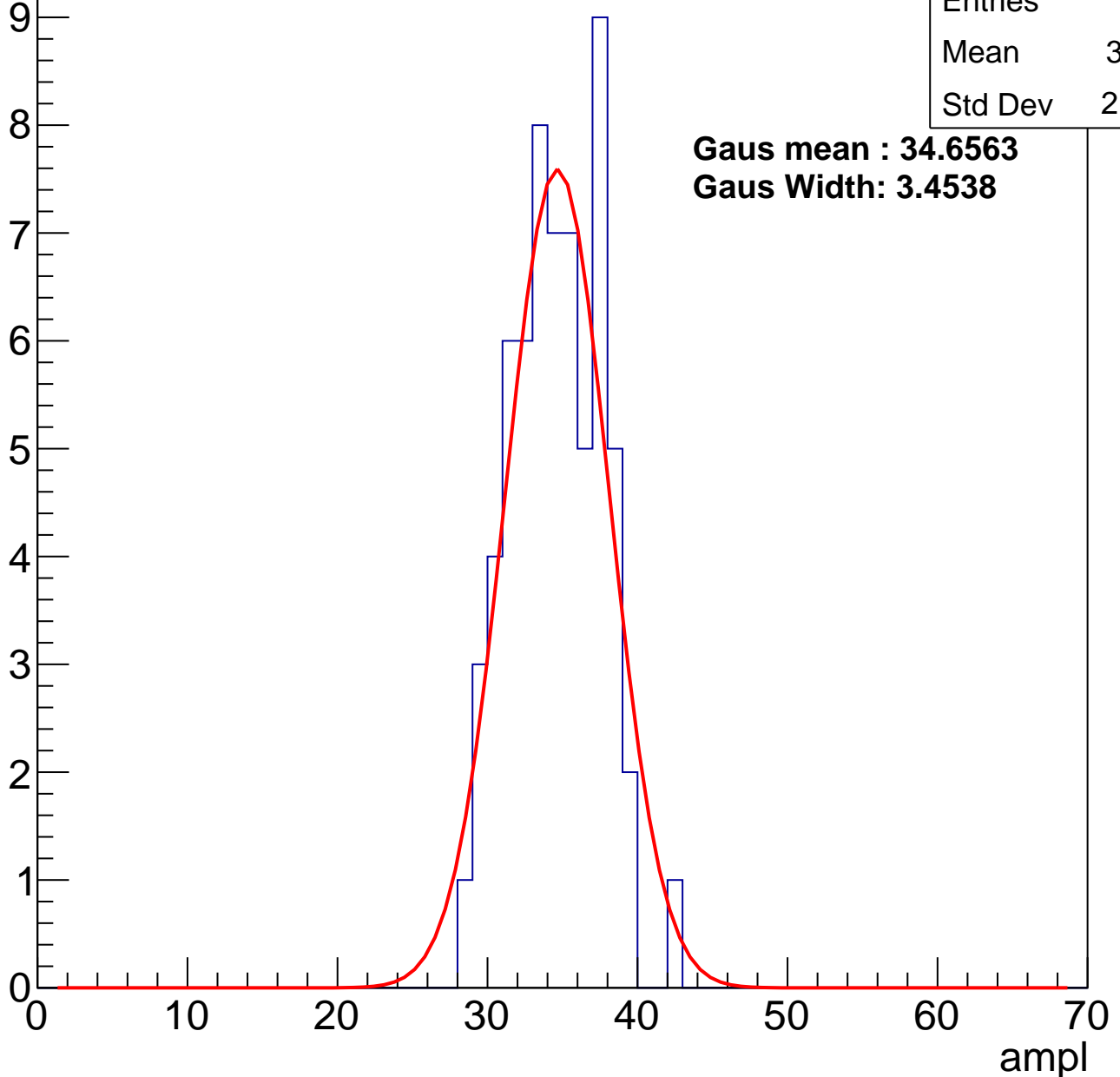
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.11
Std Dev	2.964

**Gaus mean : 34.6563**

**Gaus Width: 3.4538**



# B1L002S, U26-ch68, adc2

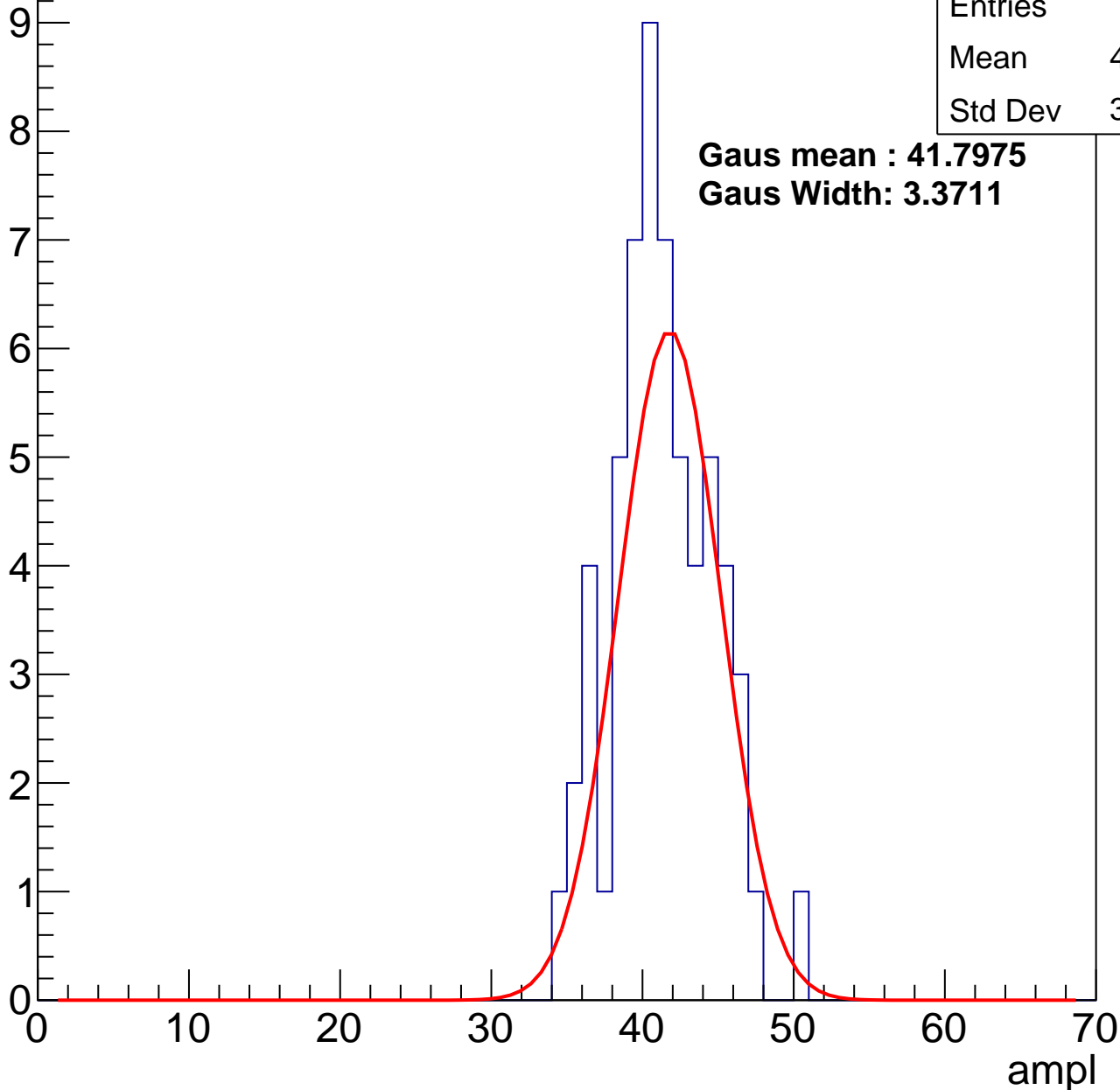
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.88
Std Dev	3.294

**Gaus mean : 41.7975**

**Gaus Width: 3.3711**

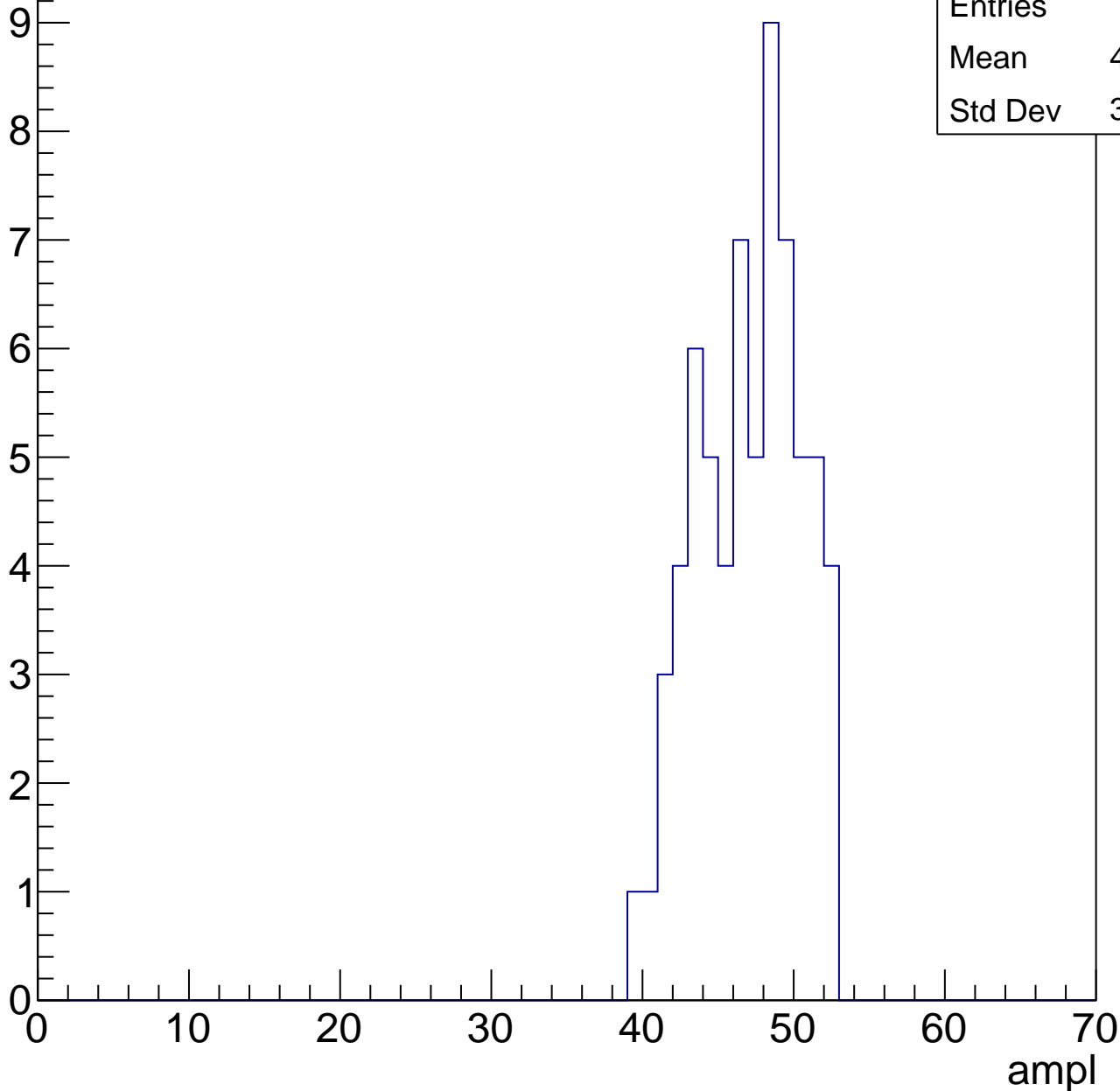


# B1L002S, U26-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.56
Std Dev	3.335

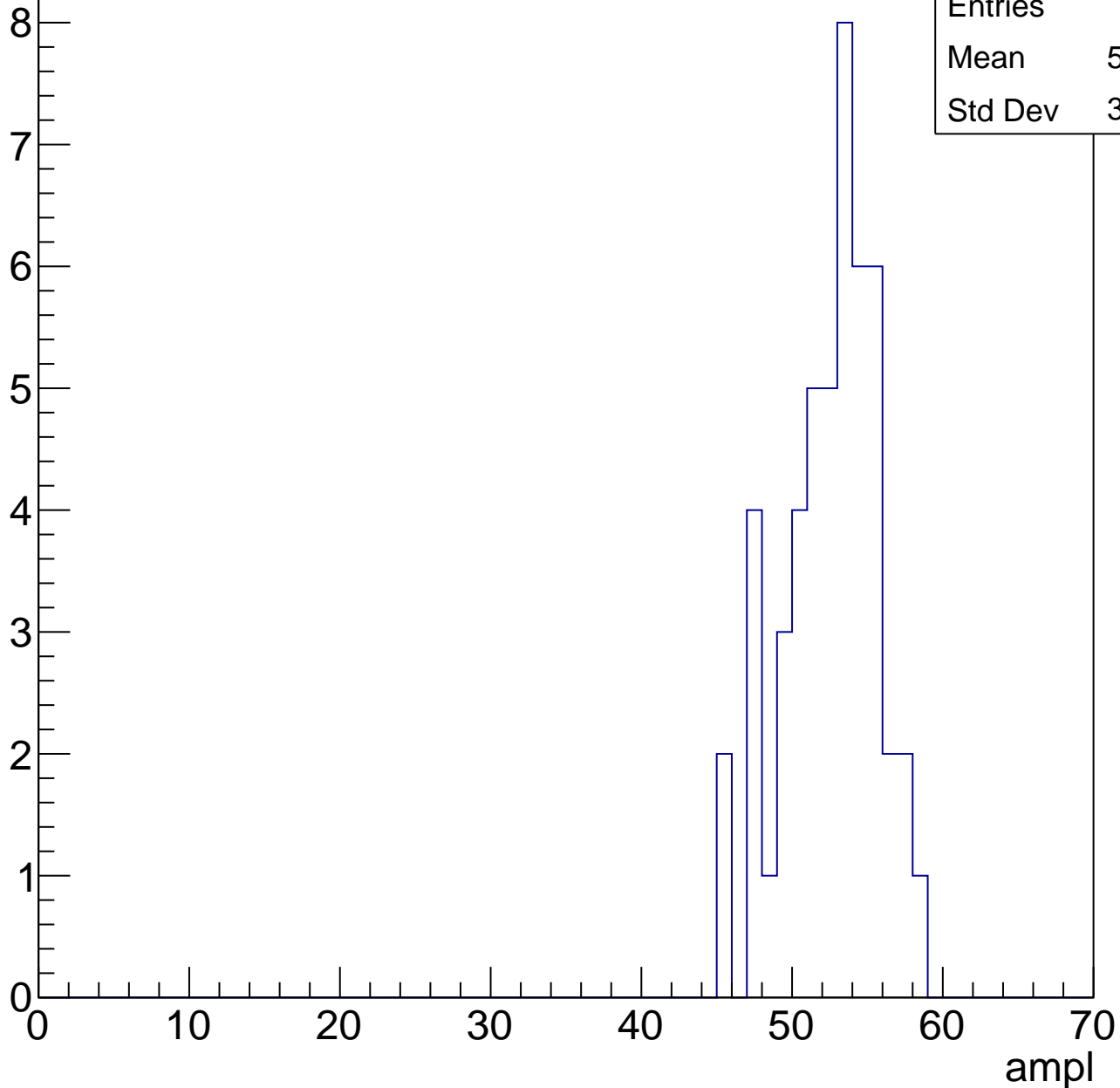


# B1L002S, U26-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.04
Std Dev	3.084

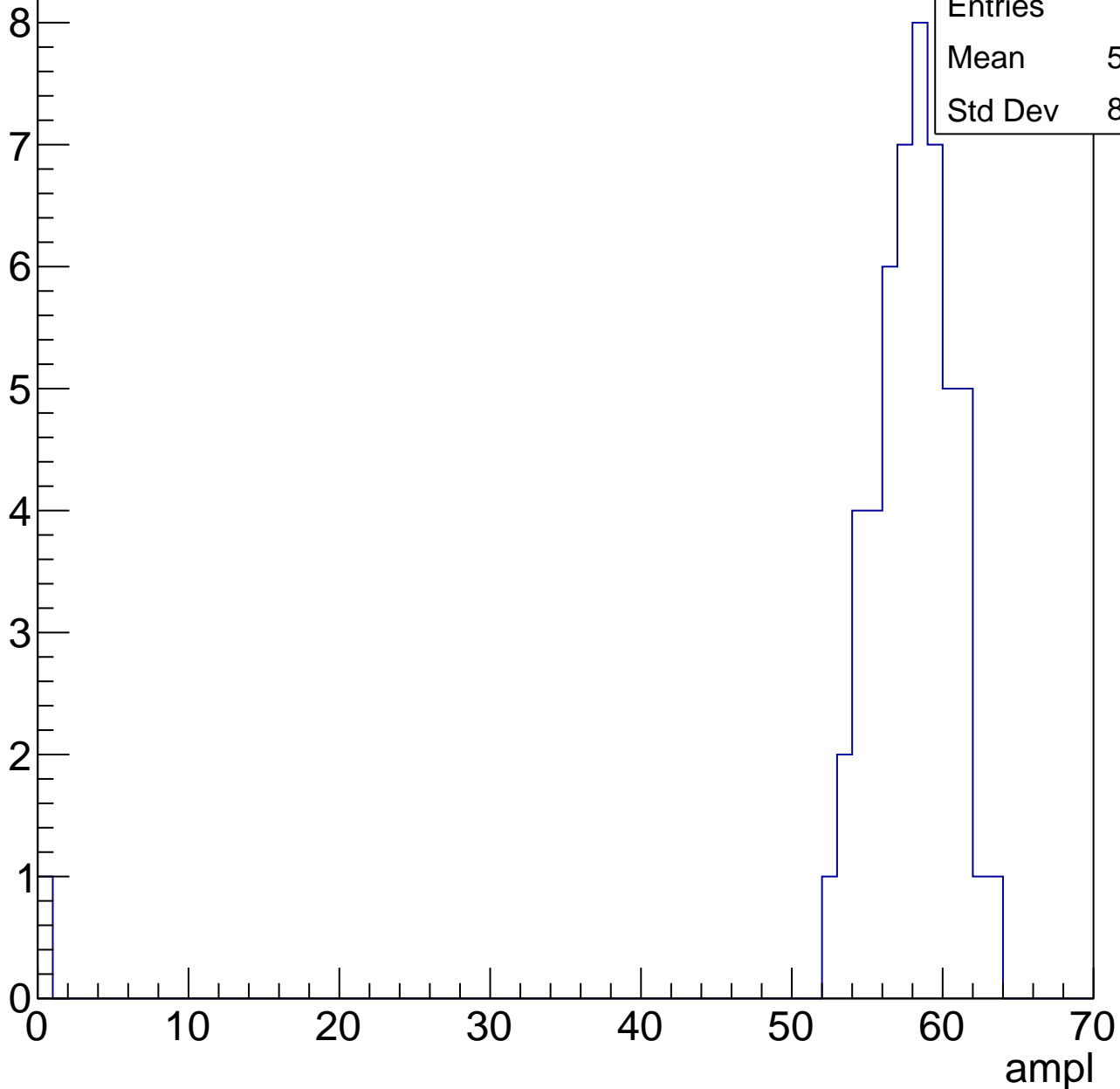


# B1L002S, U26-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	56.46
Std Dev	8.287

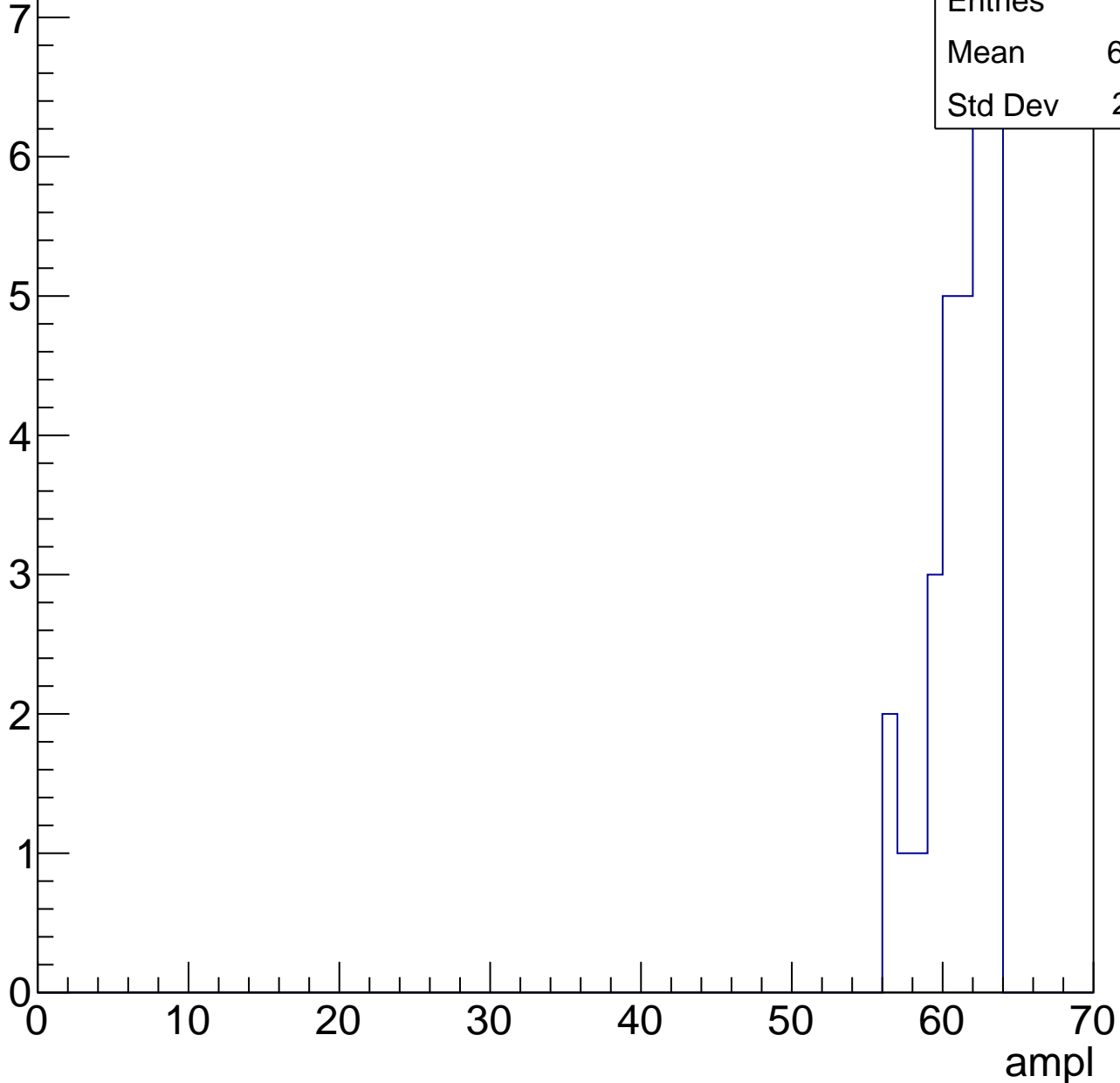


# B1L002S, U26-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	60.77
Std Dev	2.011

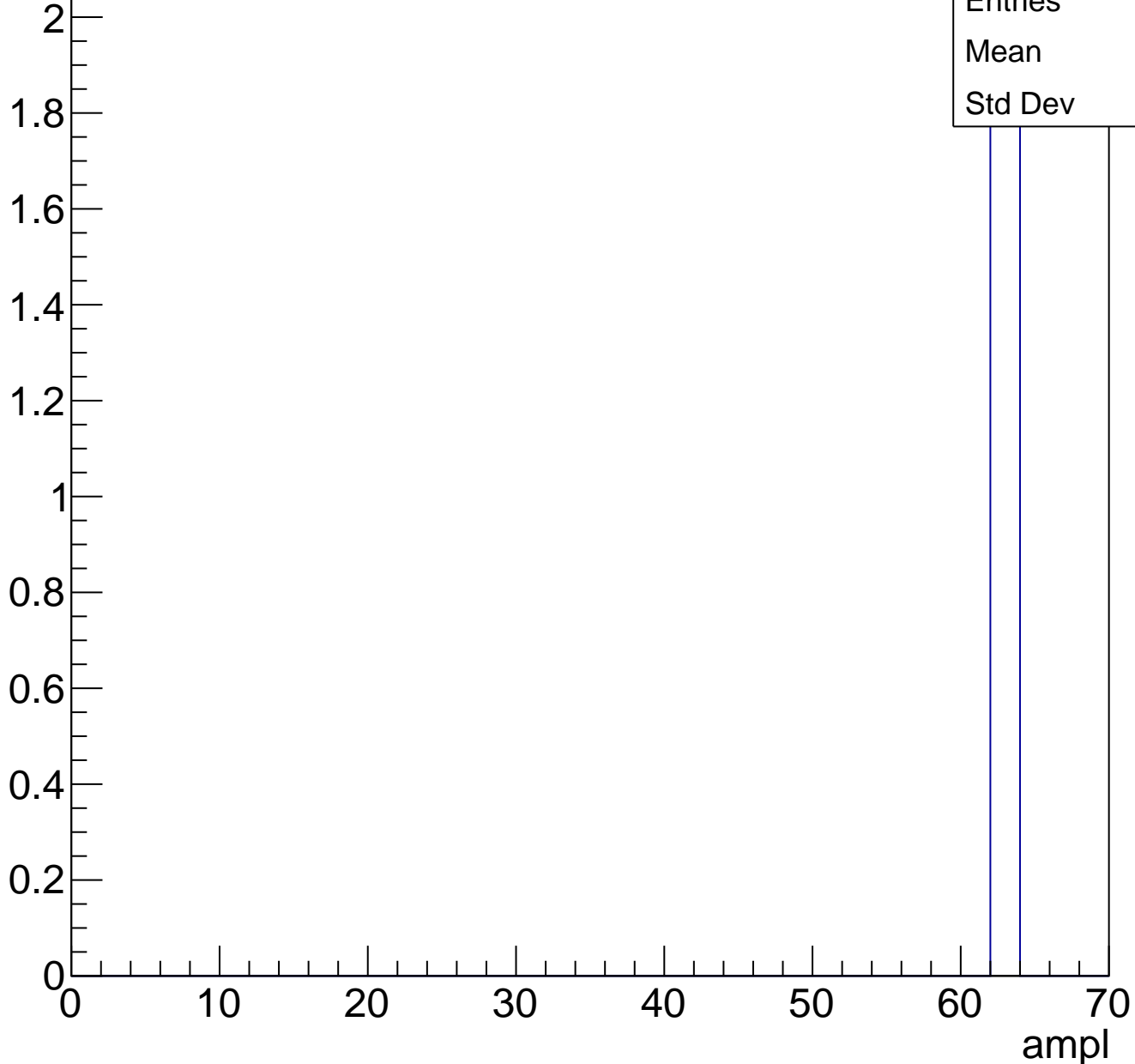




# B1L002S, U26-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch69, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	28.23
Std Dev	2.849

**Gaus mean : 28.7592**

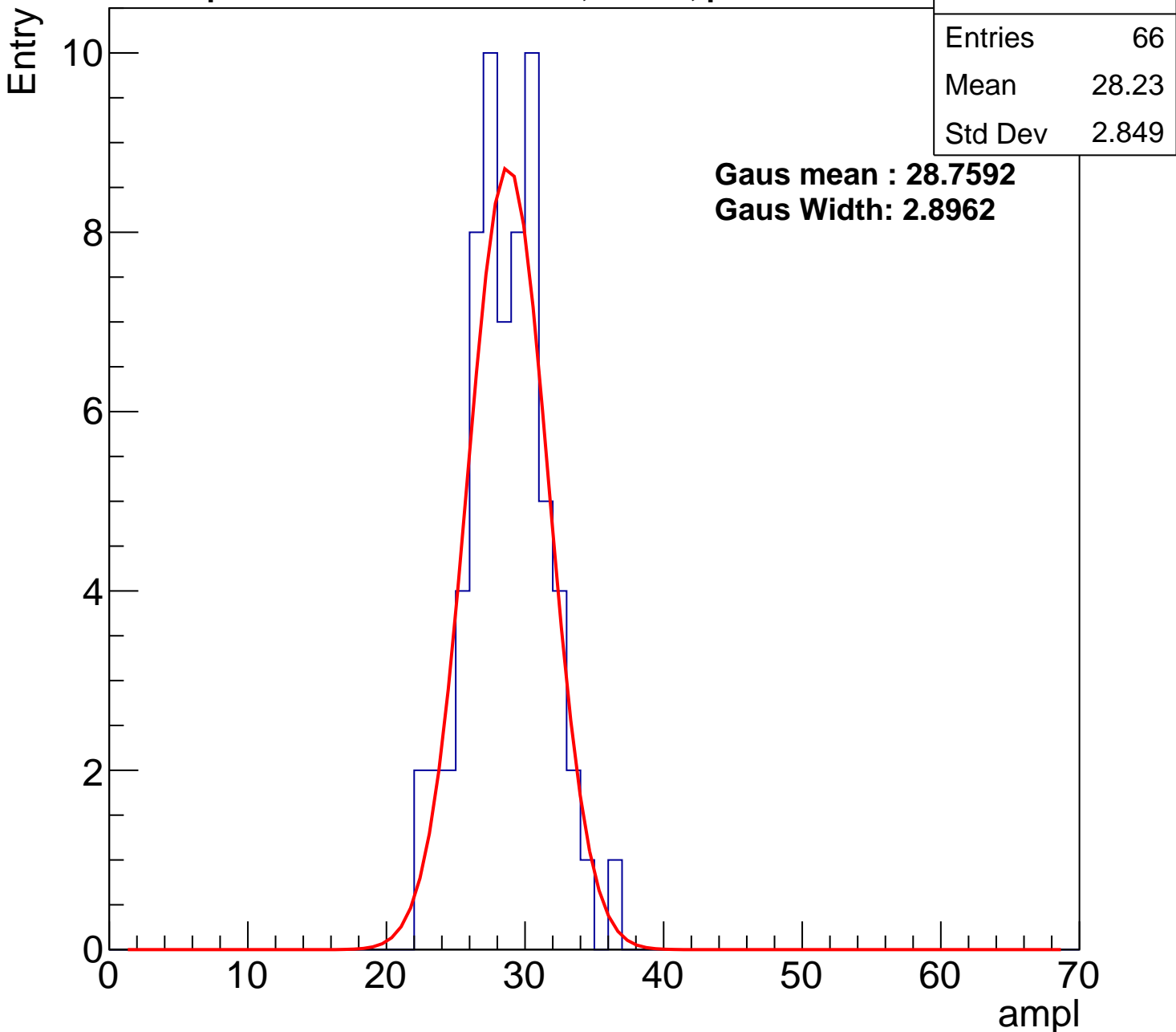
**Gaus Width: 2.8962**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch69, adc1

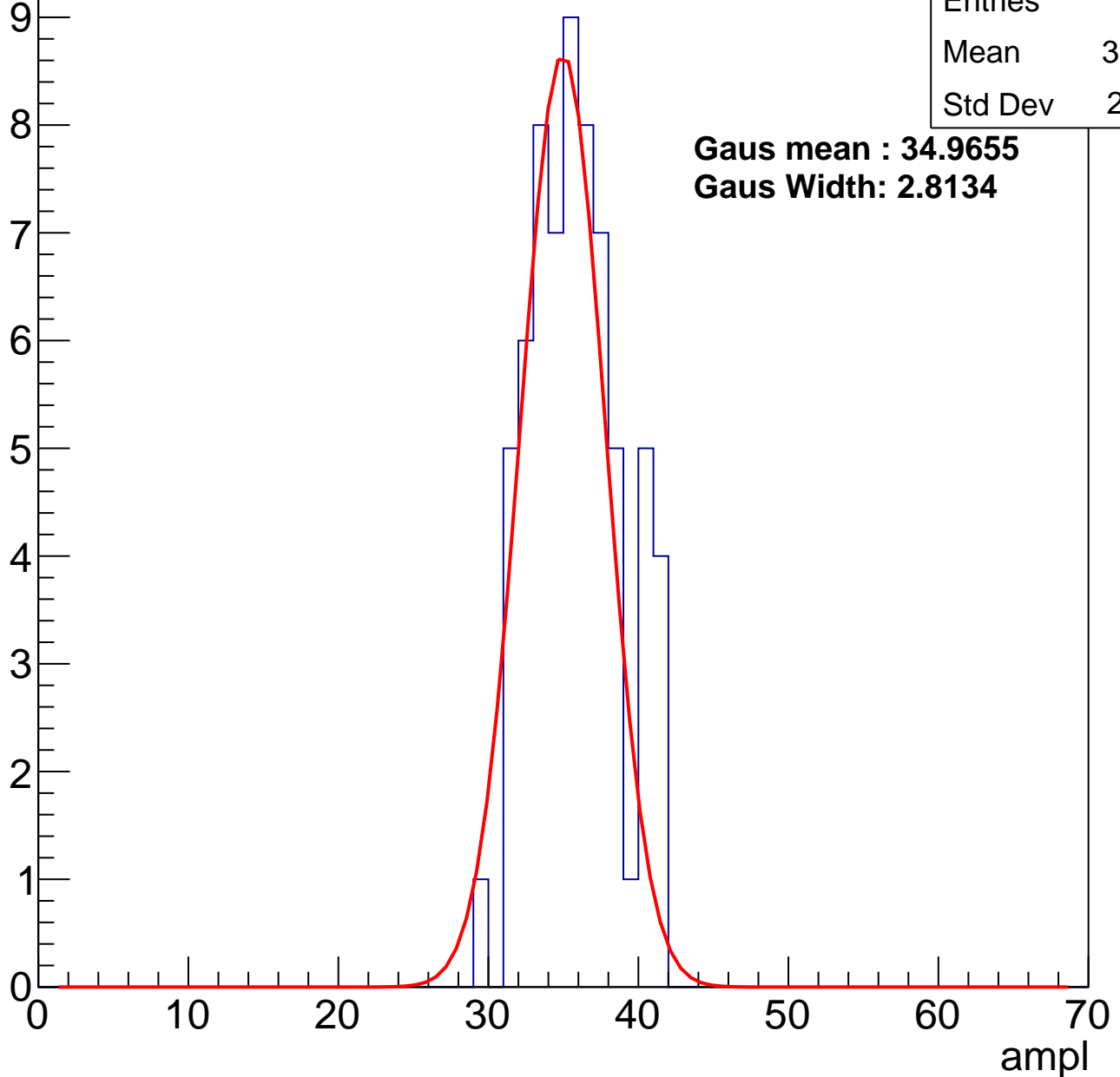
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	35.35
Std Dev	2.931

**Gaus mean : 34.9655**

**Gaus Width: 2.8134**



# B1L002S, U26-ch69, adc2

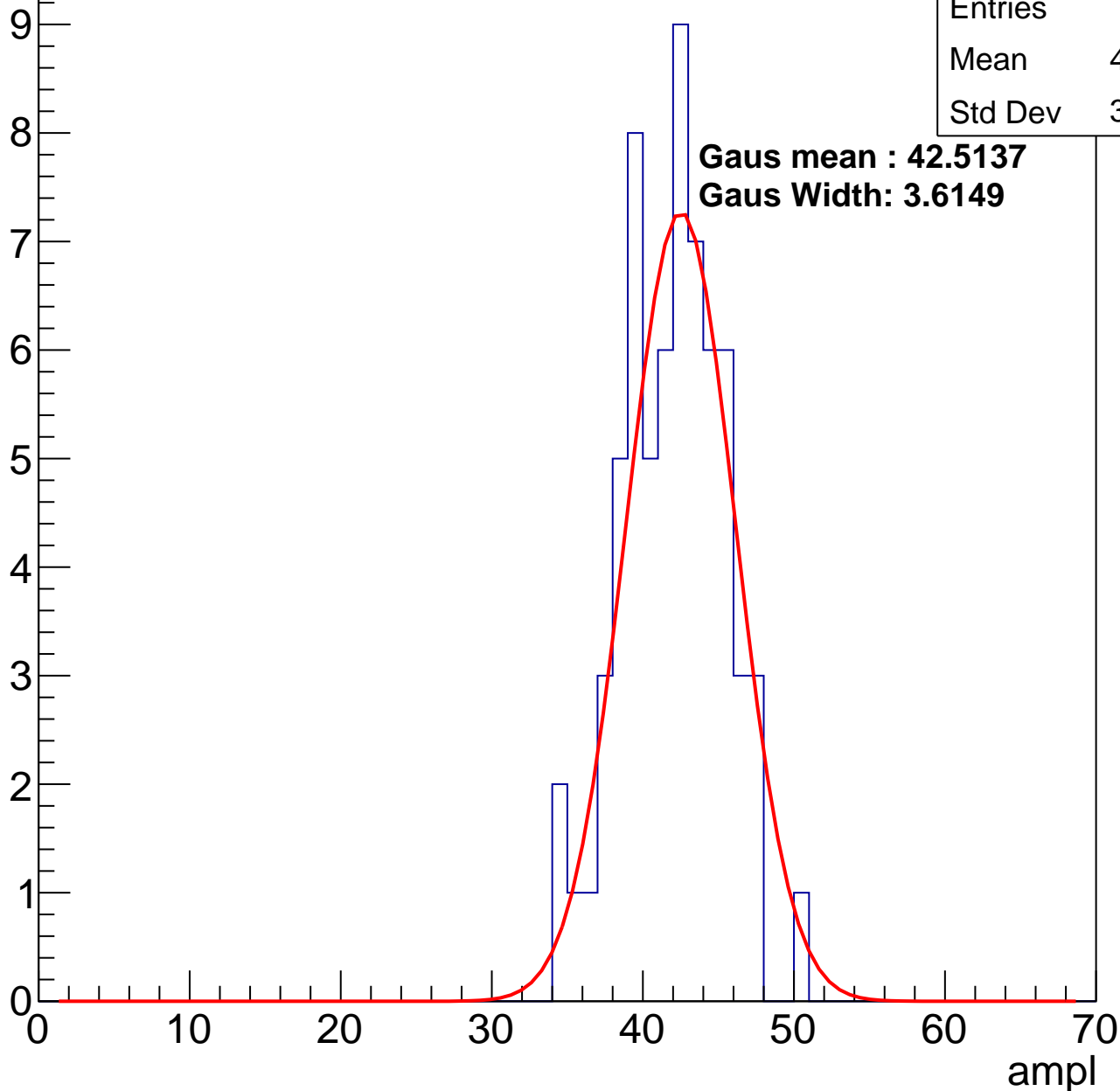
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	41.52
Std Dev	3.309

**Gaus mean : 42.5137**

**Gaus Width: 3.6149**

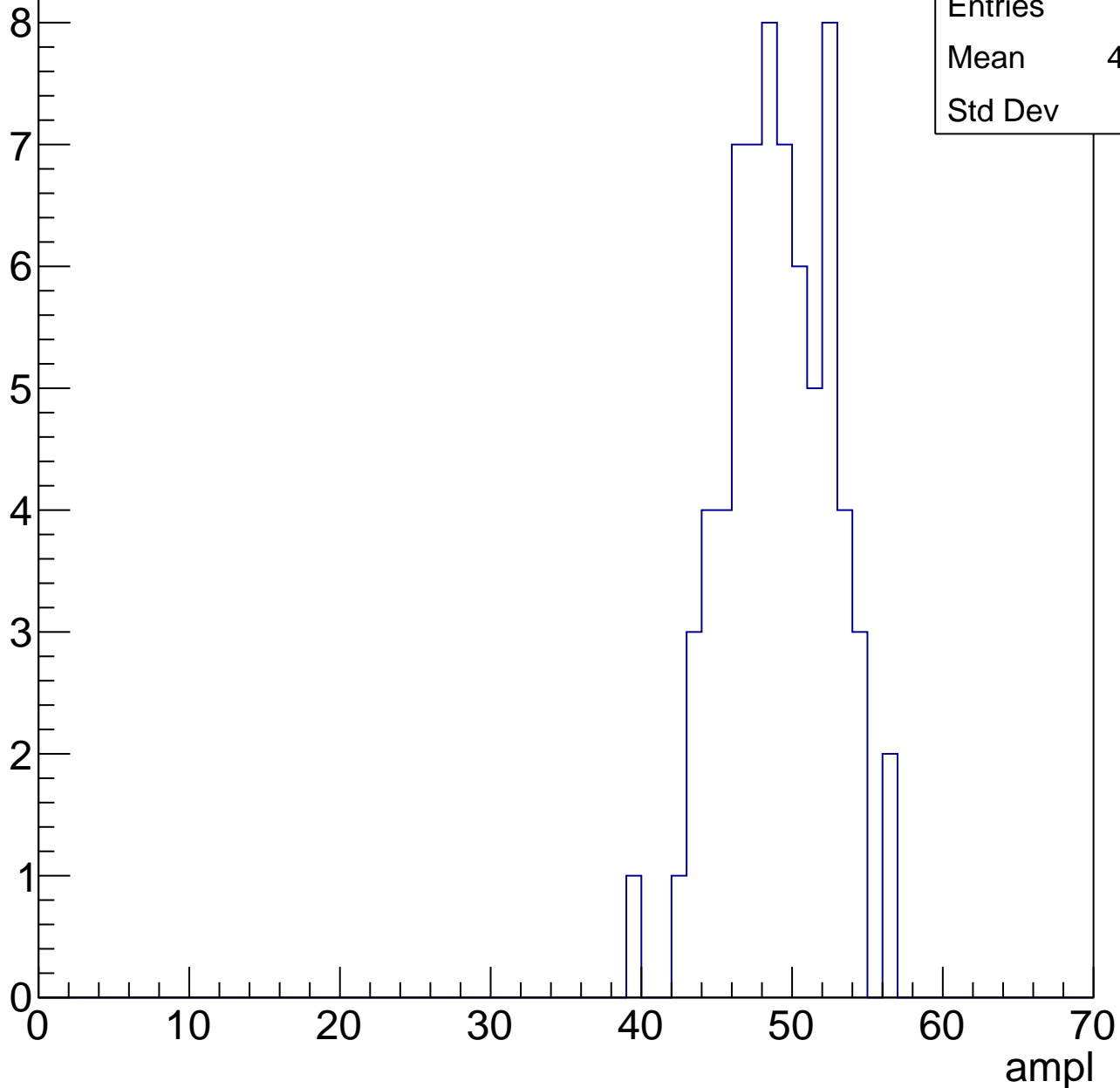


# B1L002S, U26-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

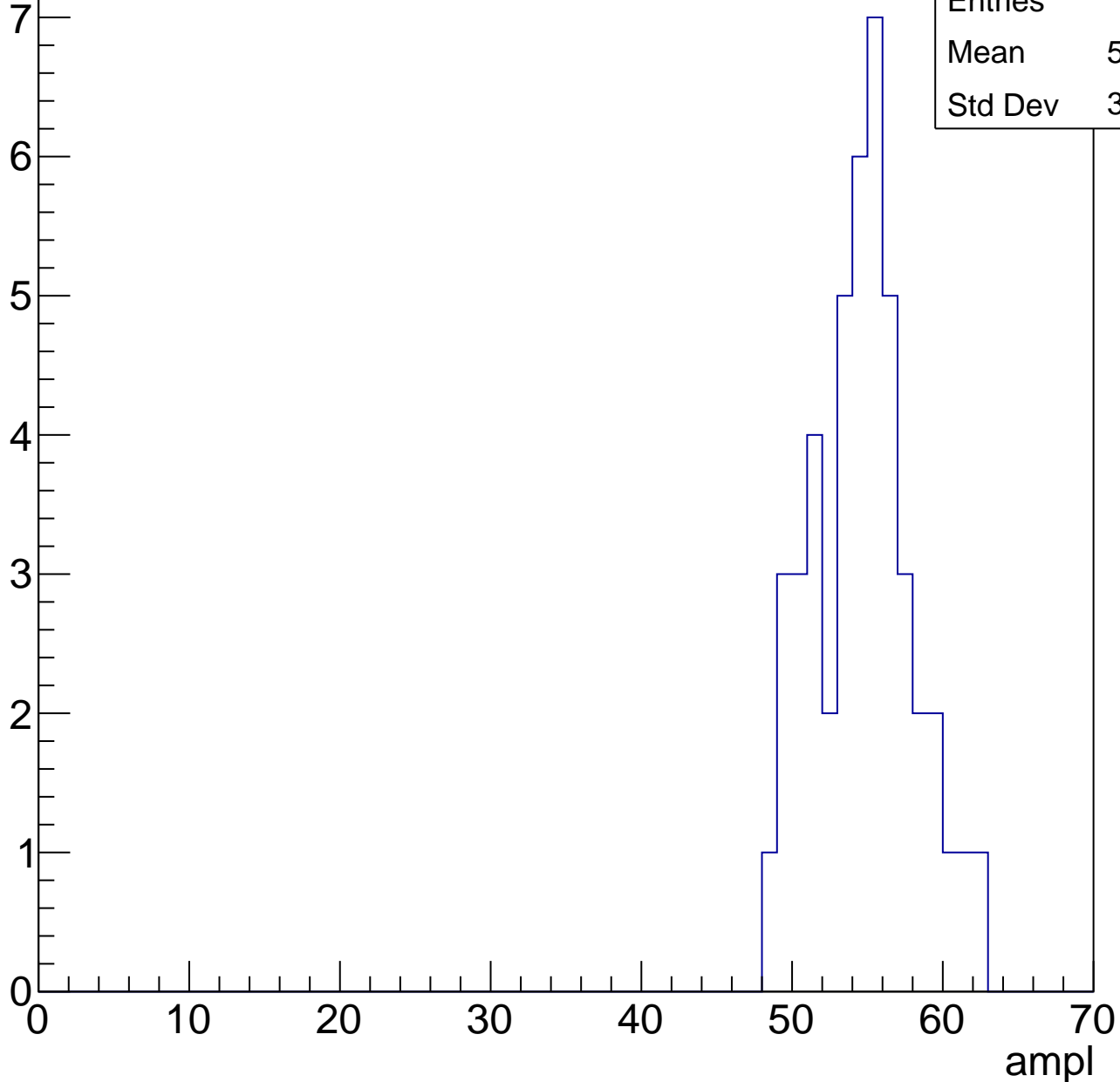
Entries	70
Mean	48.59
Std Dev	3.47



# B1L002S, U26-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



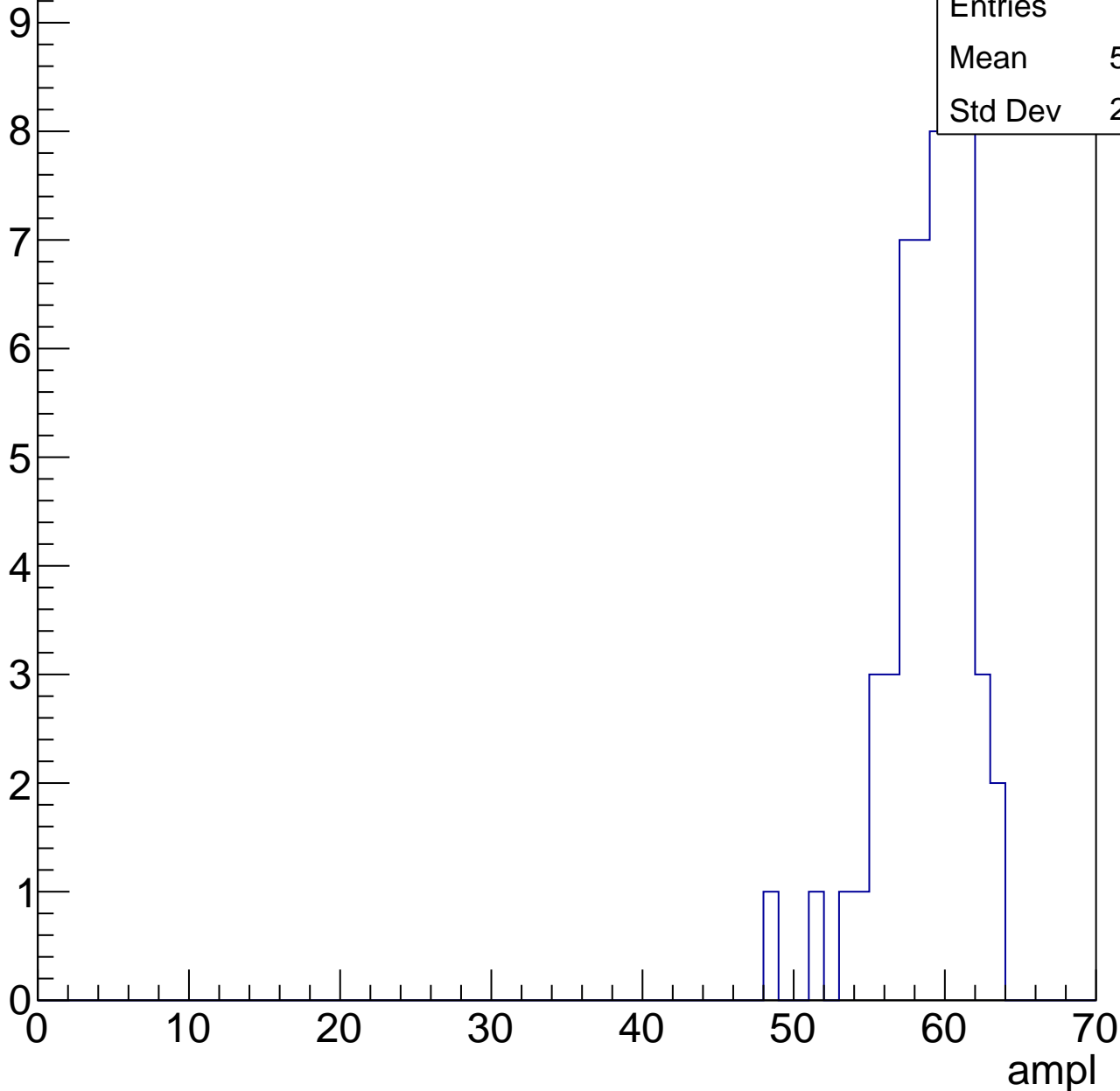
Entries	46
Mean	54.24
Std Dev	3.265

# B1L002S, U26-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	58.44
Std Dev	2.852

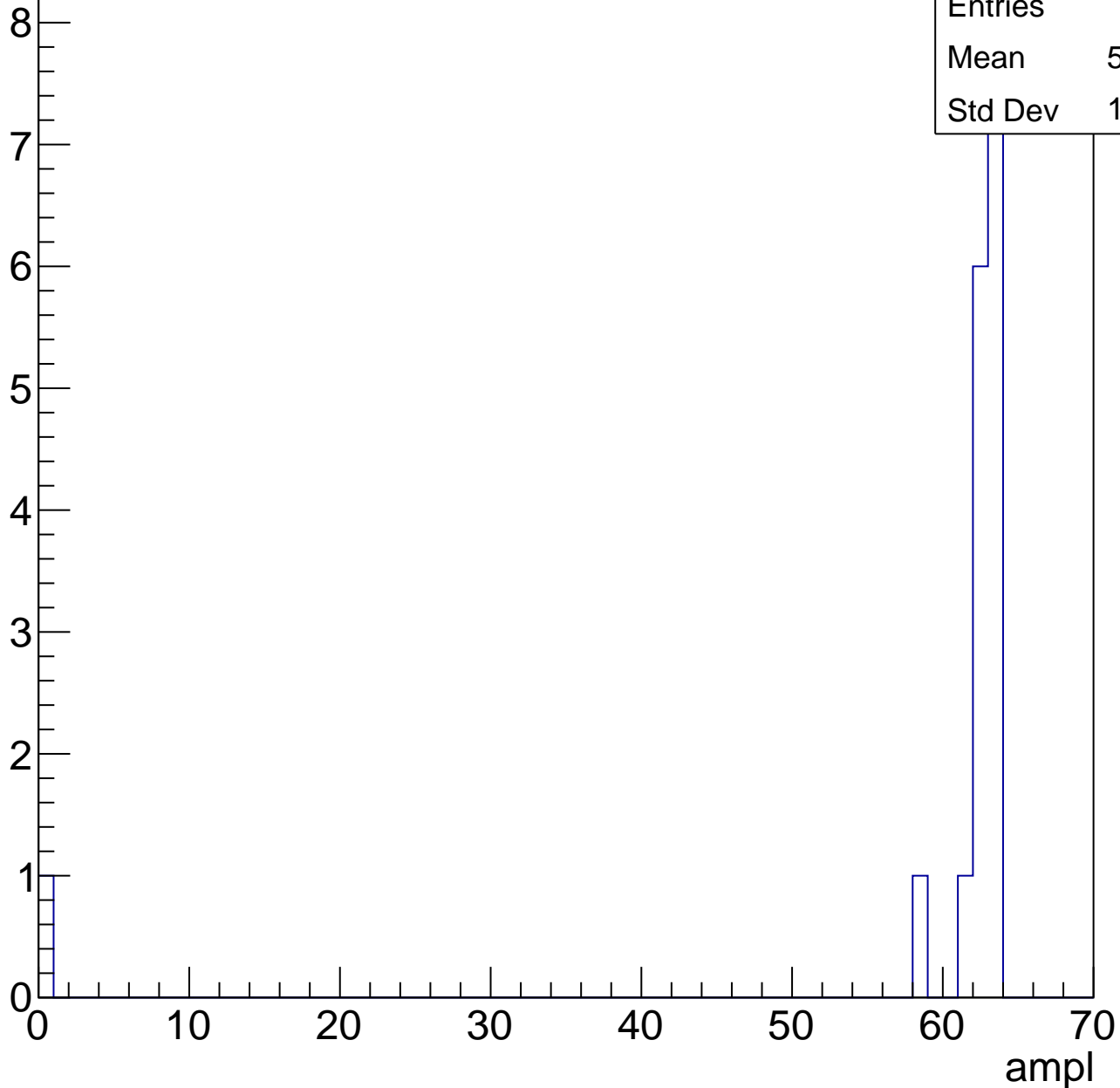


# B1L002S, U26-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58.53
Std Dev	14.68

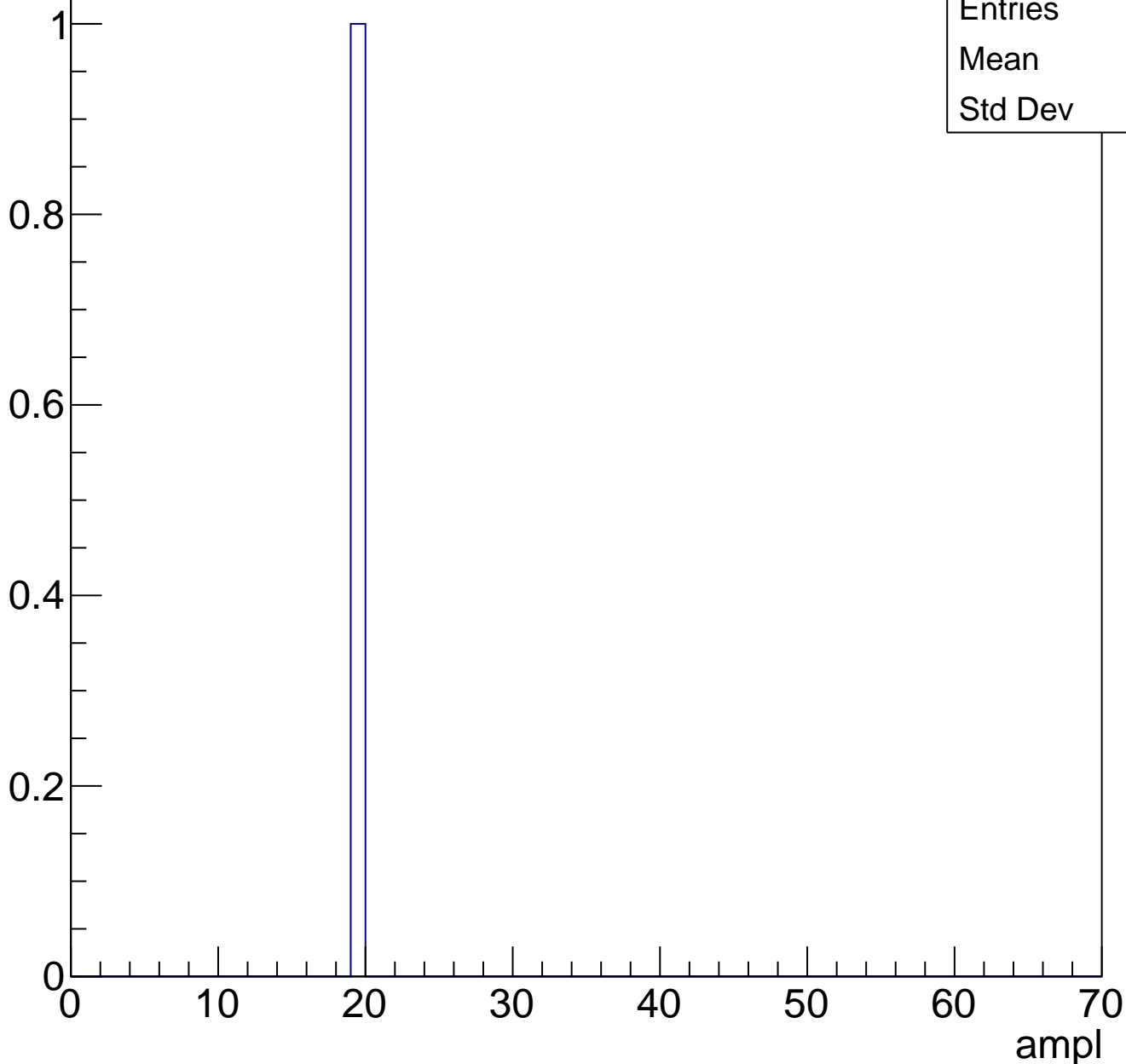




# B1L002S, U26-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	27.23
Std Dev	3.309

**Gaus mean : 28.0647**

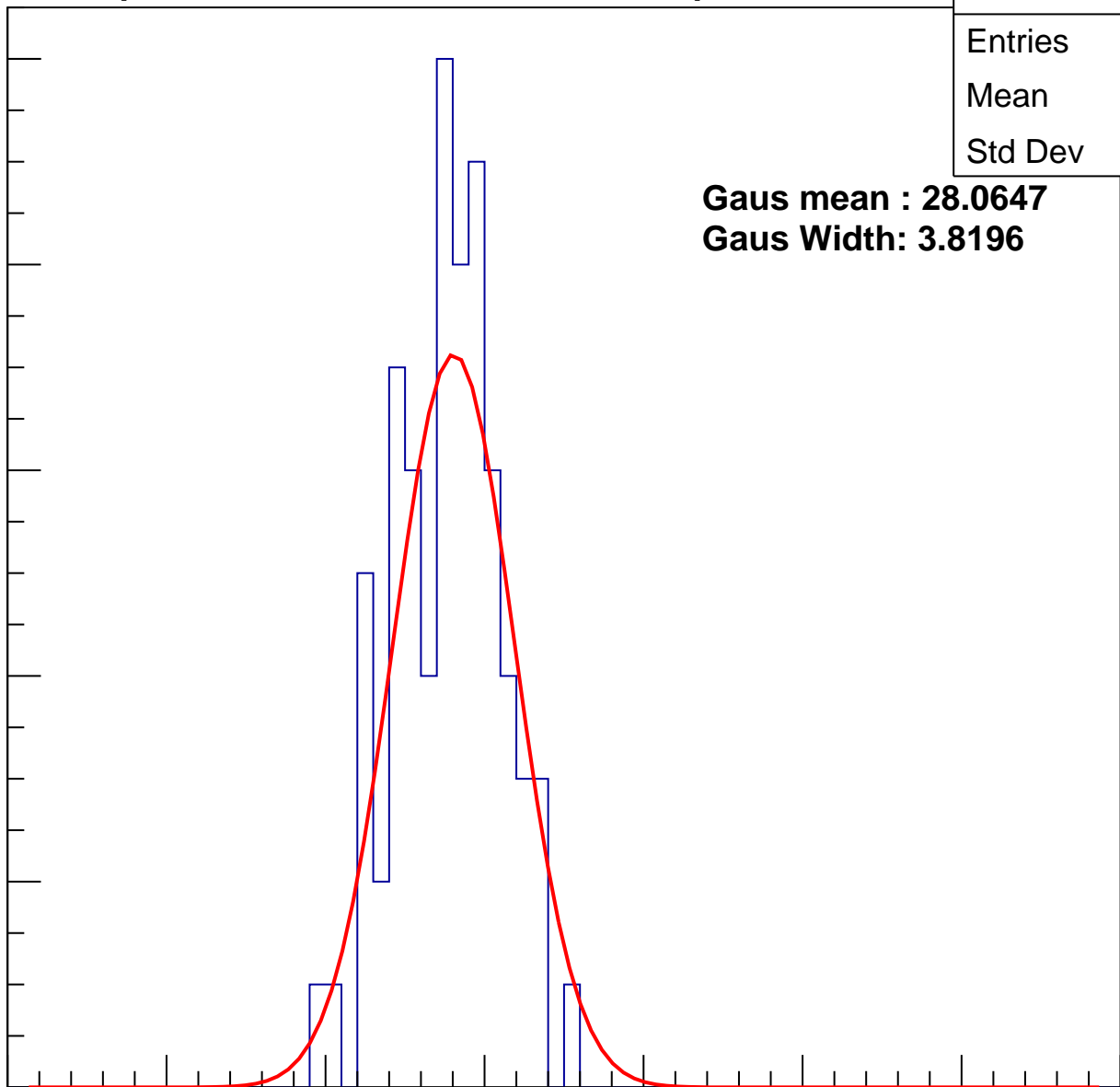
**Gaus Width: 3.8196**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch70, adc1

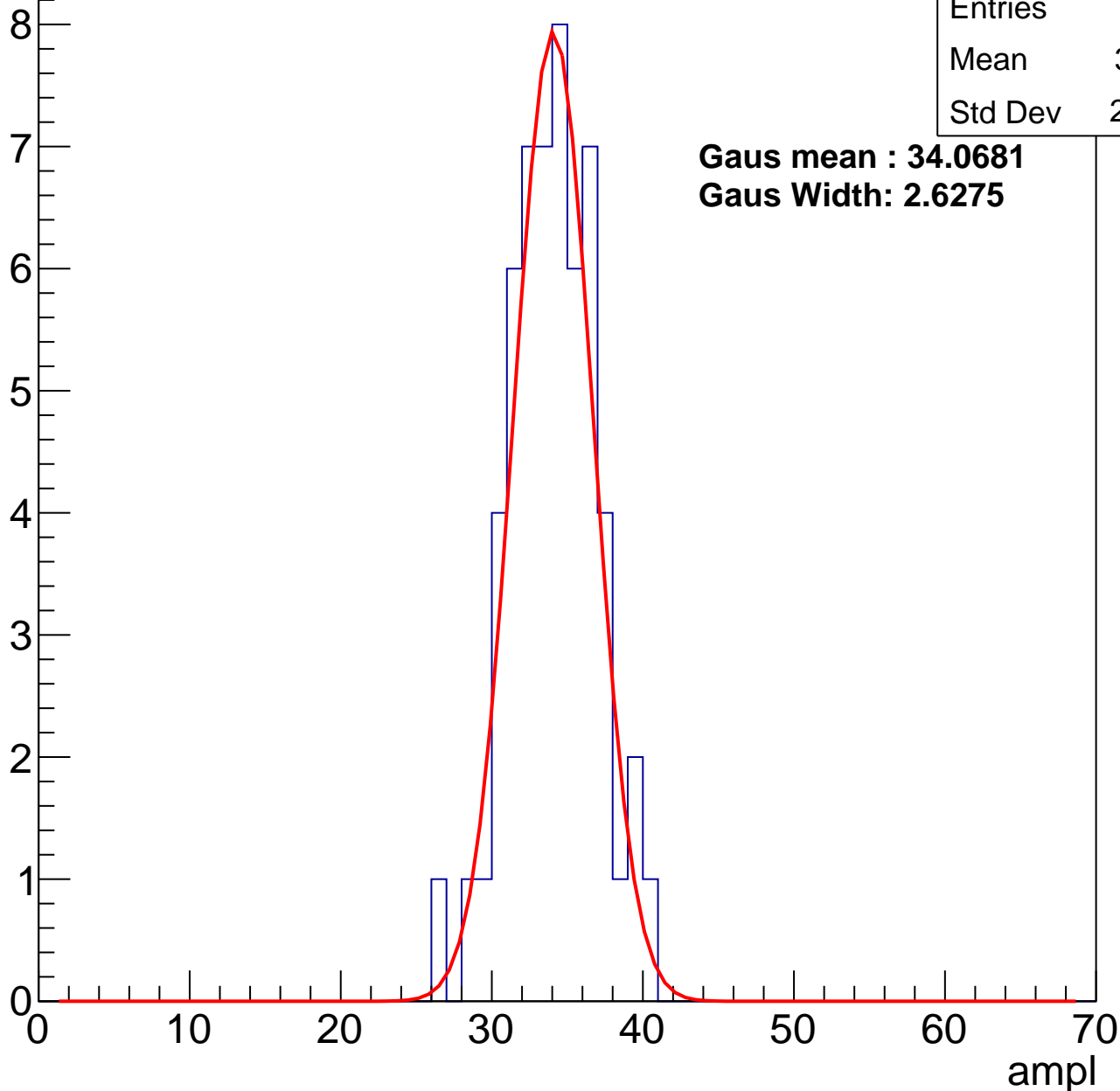
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	33.61
Std Dev	2.807

**Gaus mean : 34.0681**

**Gaus Width: 2.6275**



# B1L002S, U26-ch70, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	39.59
Std Dev	2.847

**Gaus mean : 39.9383**

**Gaus Width: 3.2497**

10

8

6

4

2

0

0

10

20

30

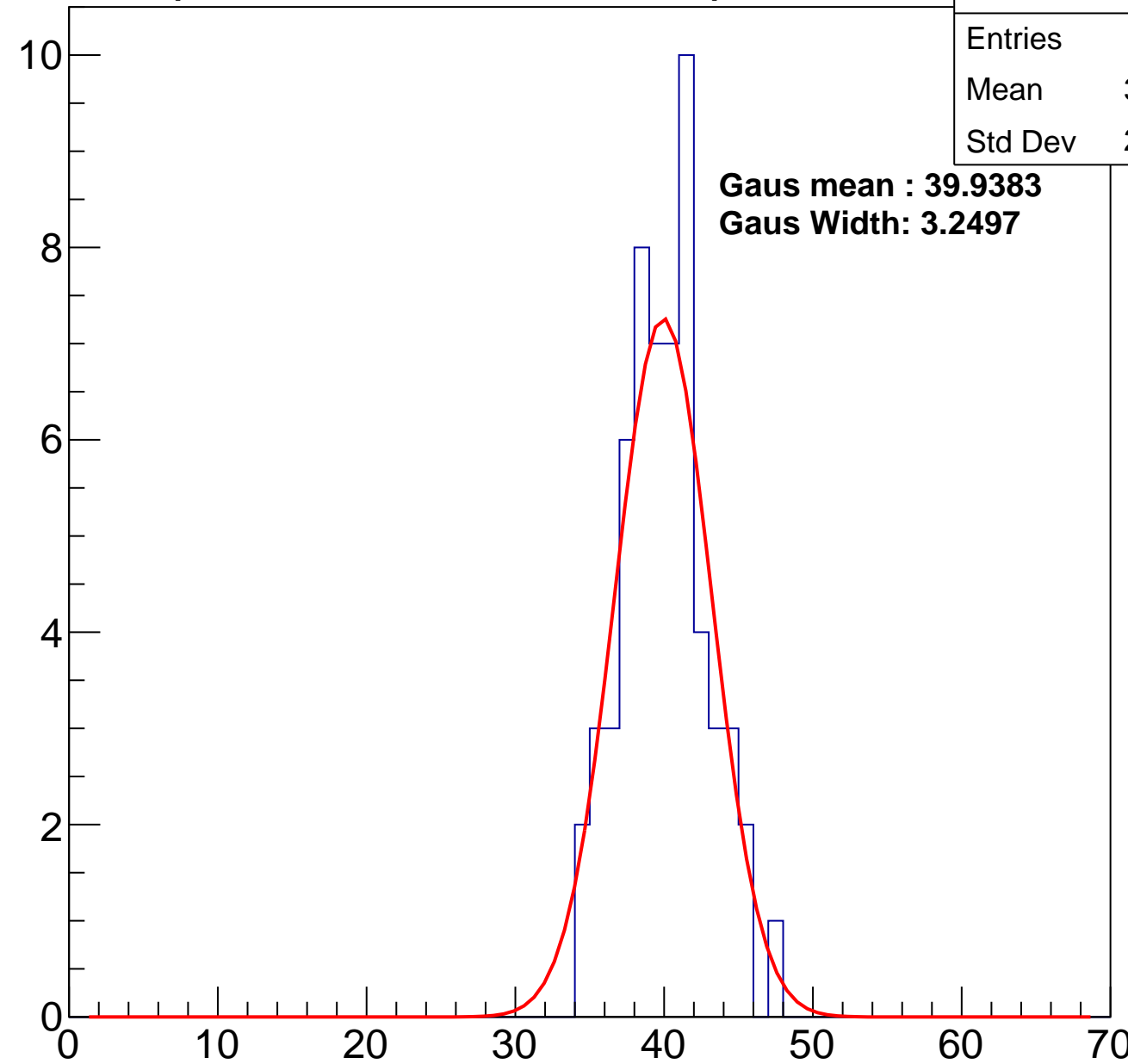
40

50

60

70

ampl

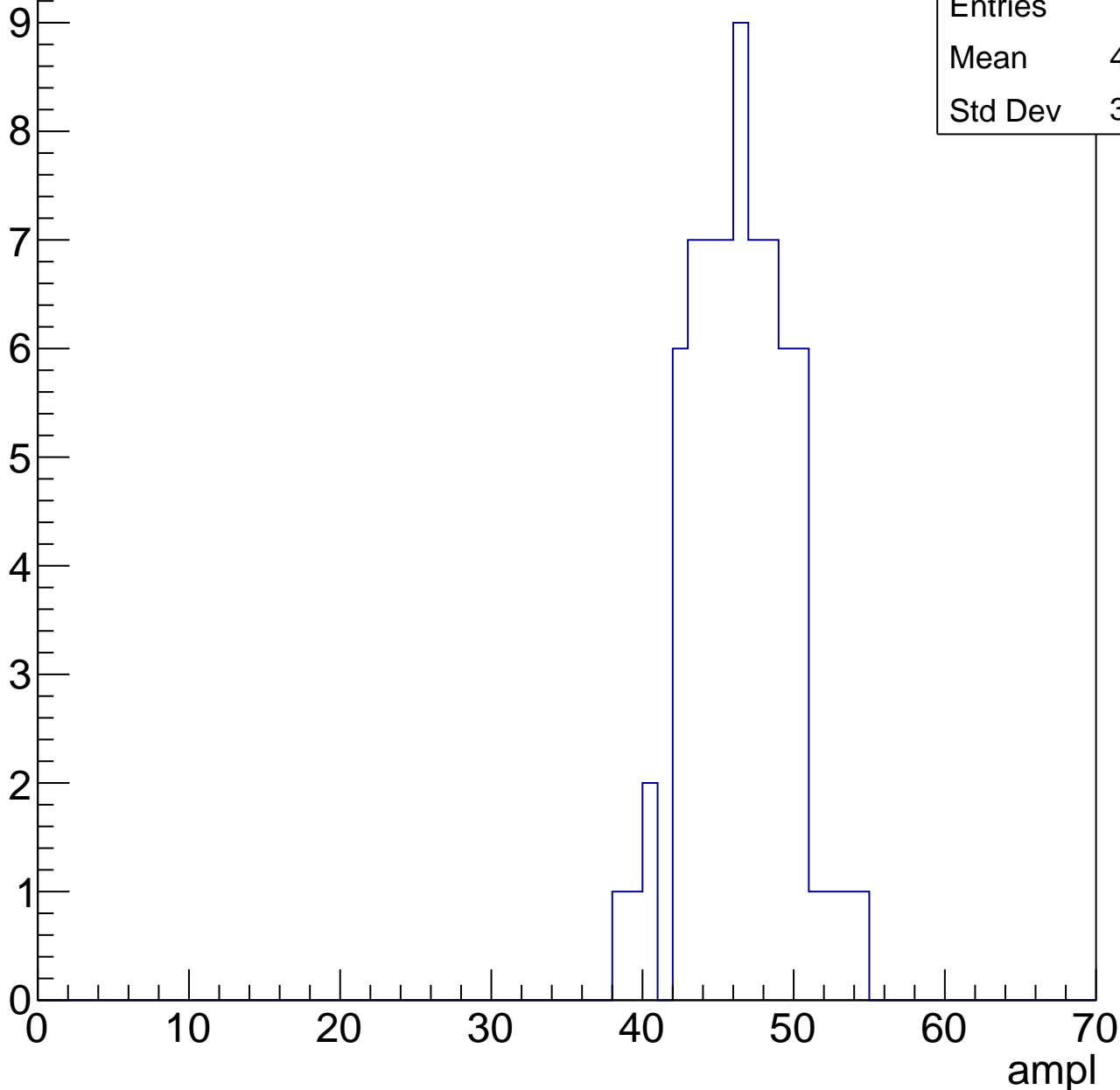


# B1L002S, U26-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	45.94
Std Dev	3.246

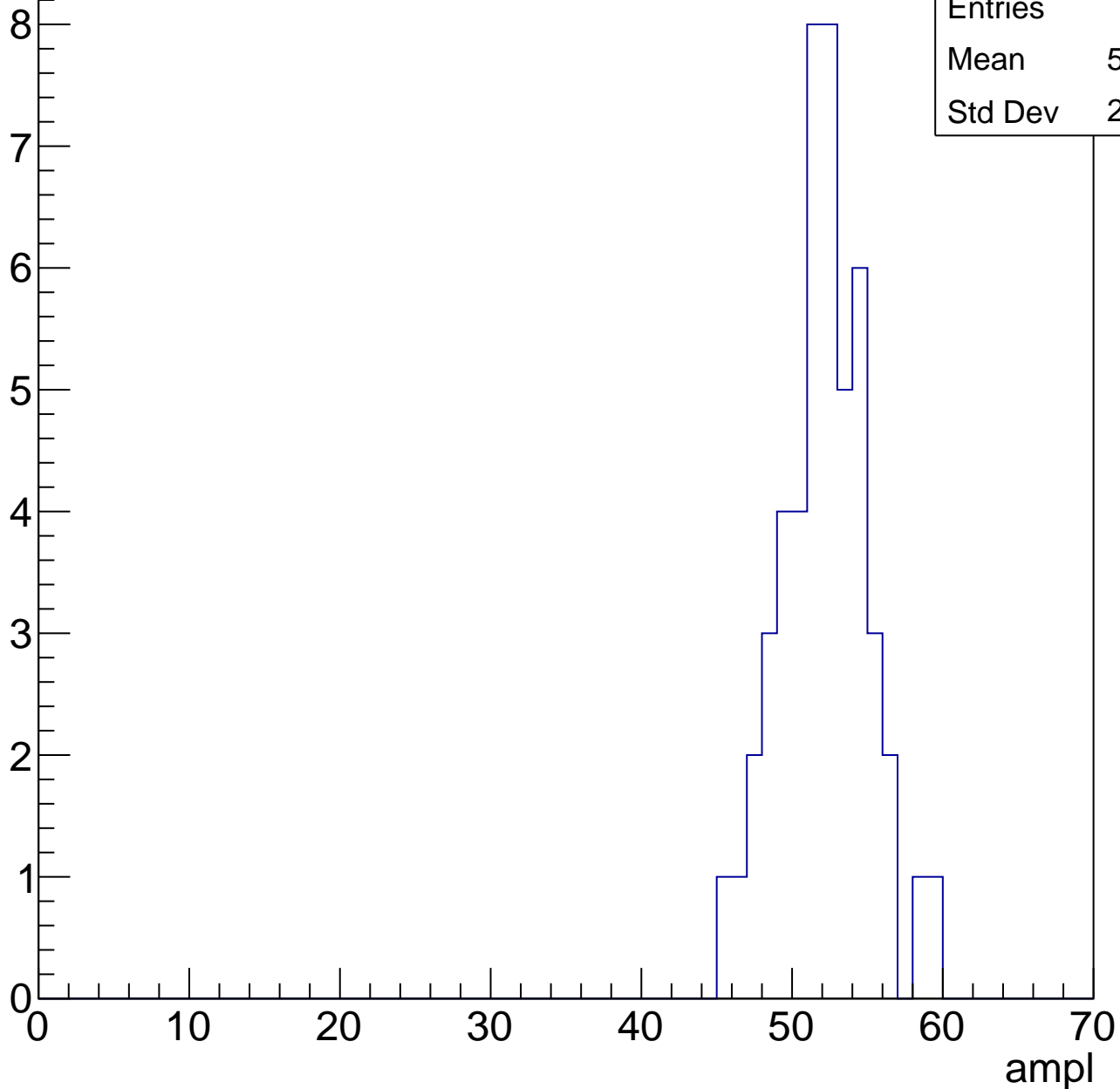


# B1L002S, U26-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

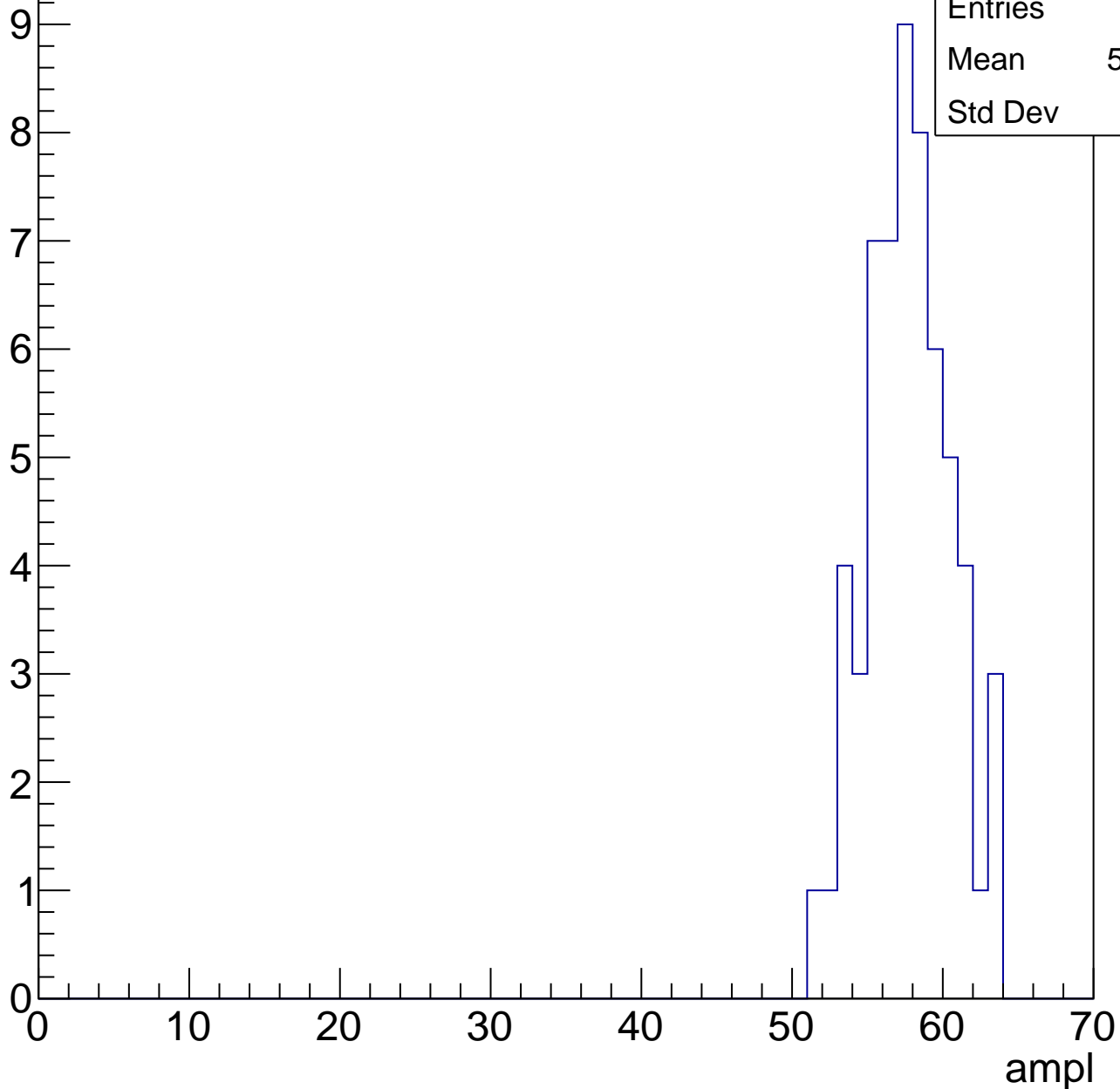
Entries	49
Mean	51.67
Std Dev	2.895



# B1L002S, U26-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



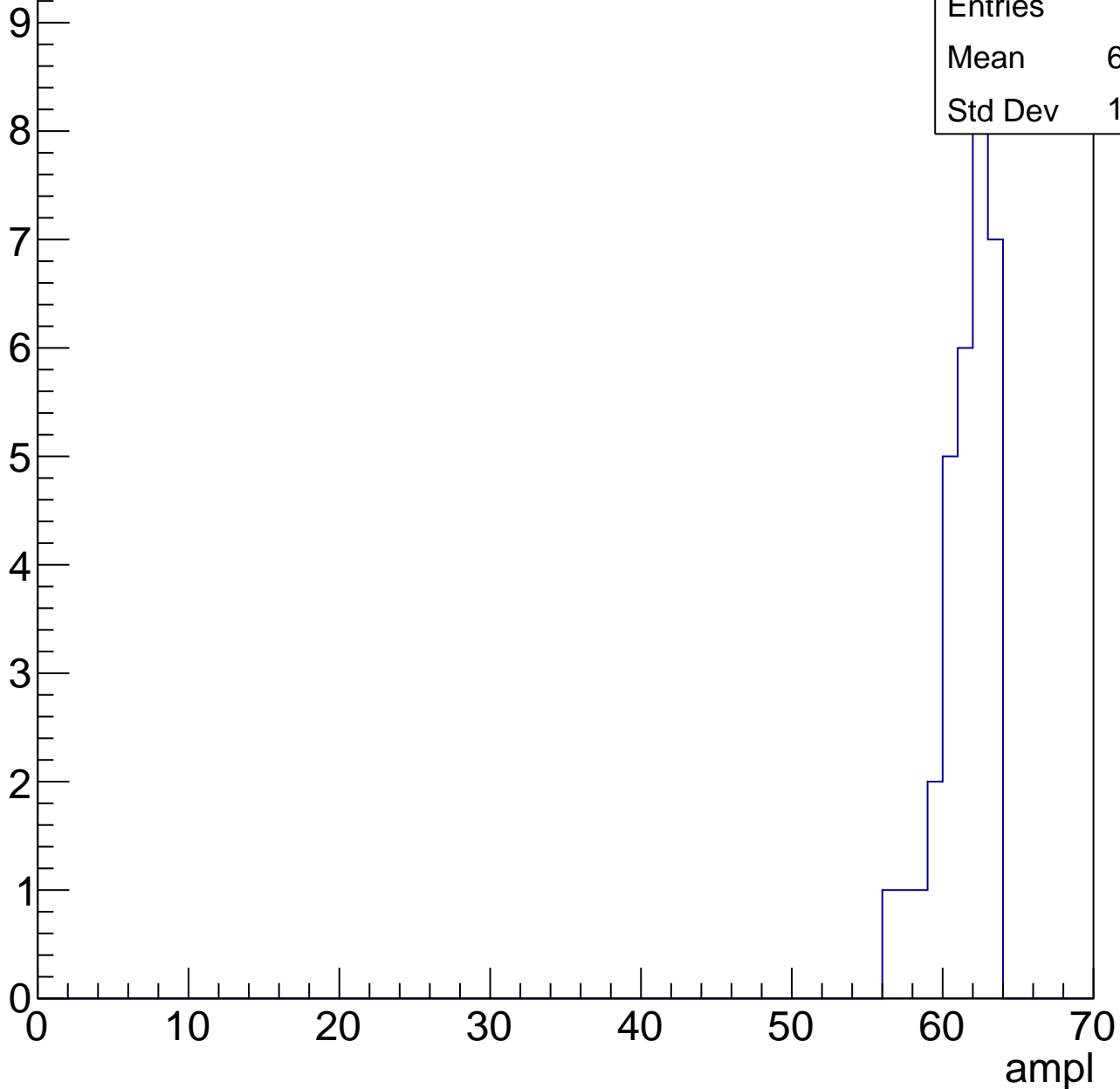
Entries	59
Mean	57.29
Std Dev	2.78

# B1L002S, U26-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	61.06
Std Dev	1.767

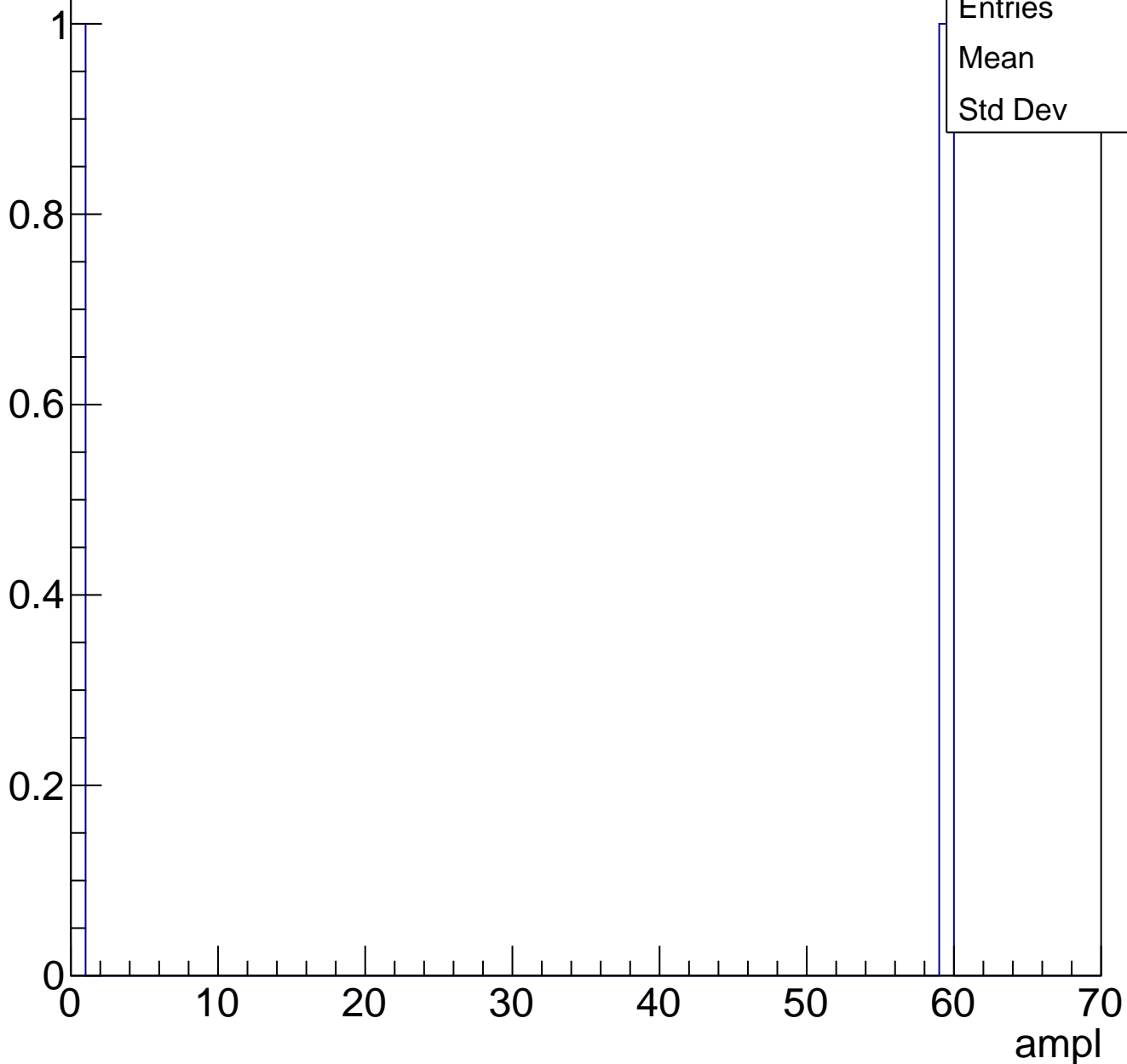




# B1L002S, U26-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch71, adc0

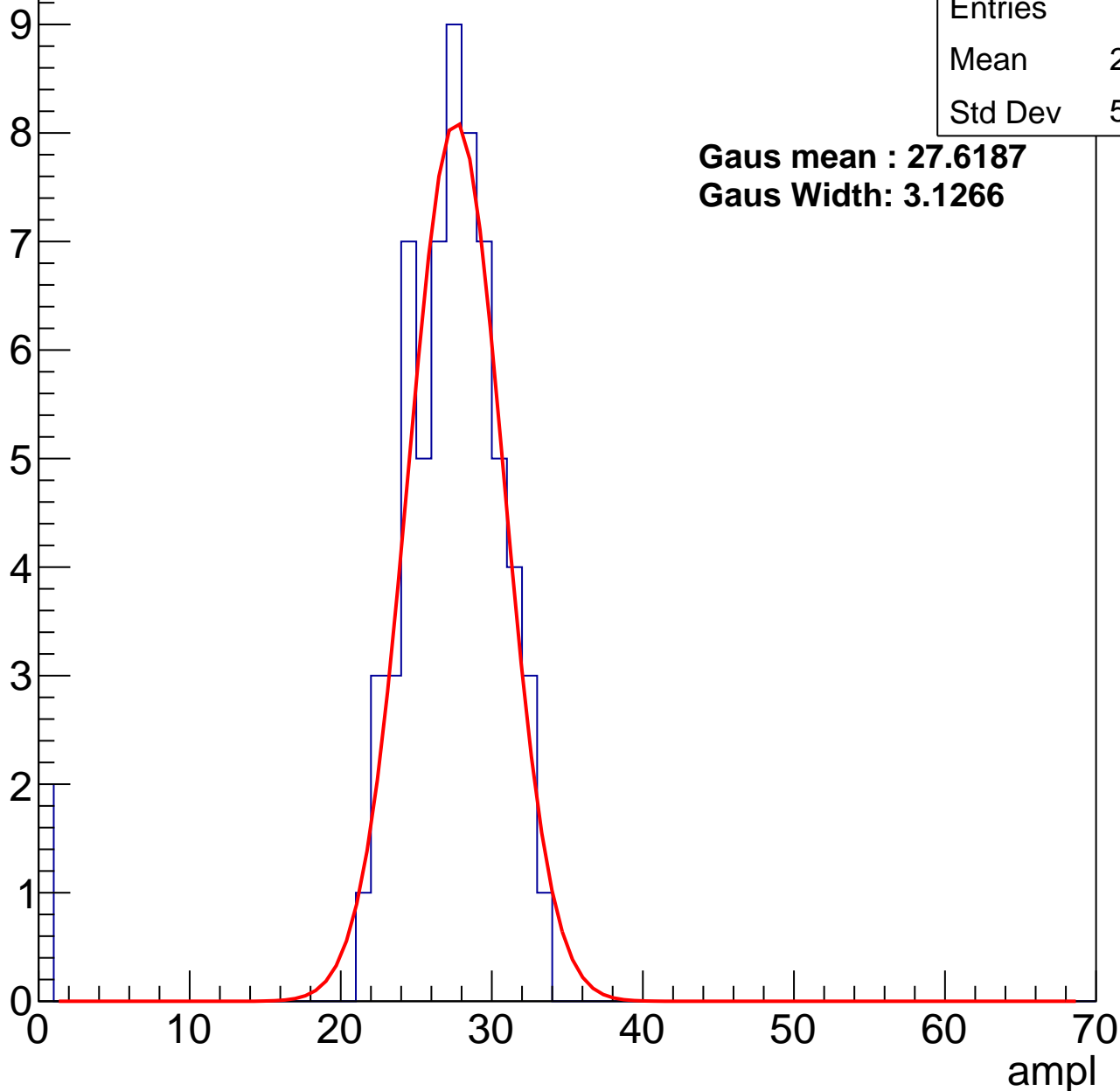
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	26.22
Std Dev	5.439

**Gaus mean : 27.6187**

**Gaus Width: 3.1266**



# B1L002S, U26-ch71, adc1

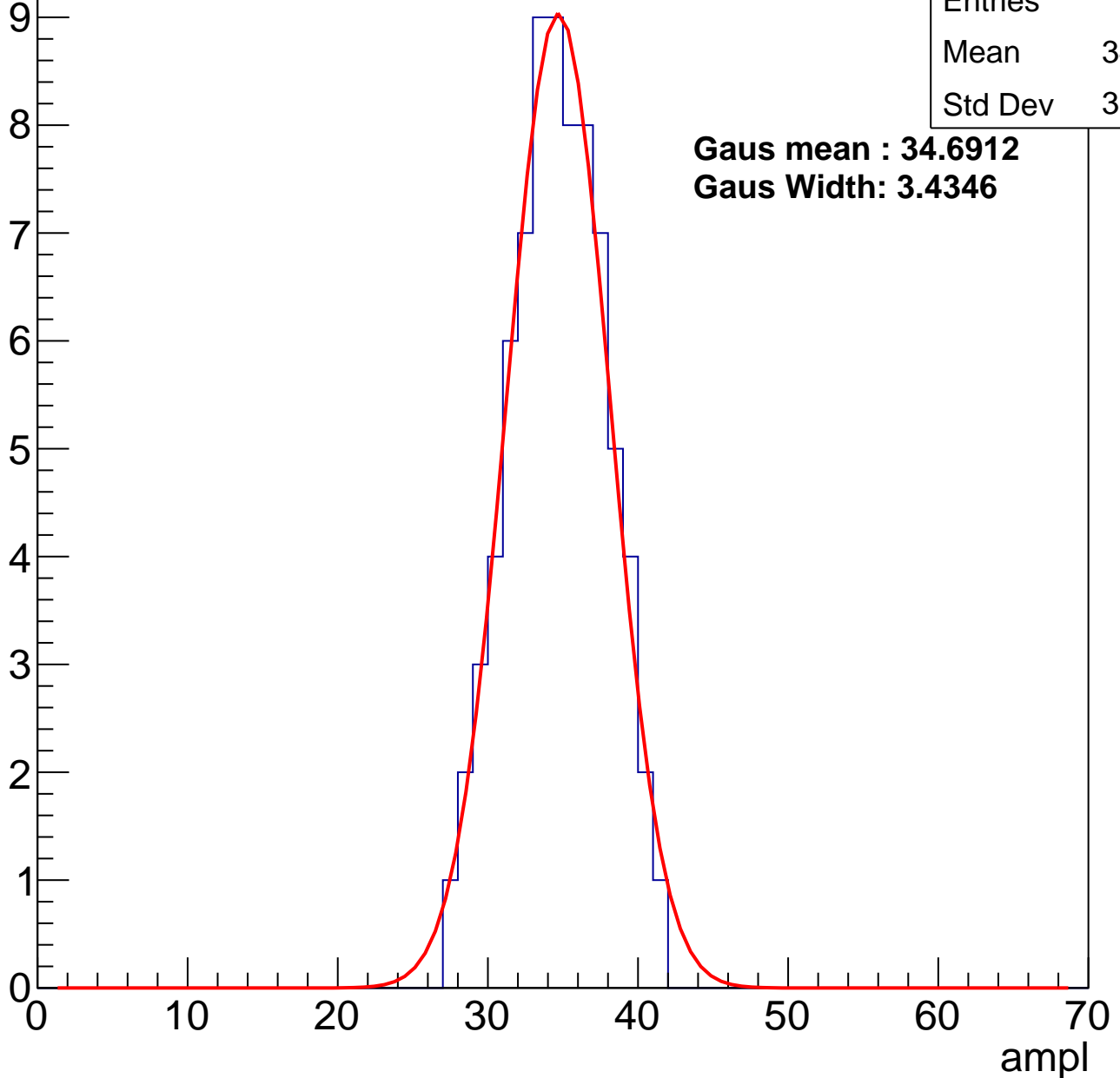
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	34.17
Std Dev	3.147

**Gaus mean : 34.6912**

**Gaus Width: 3.4346**



# B1L002S, U26-ch71, adc2

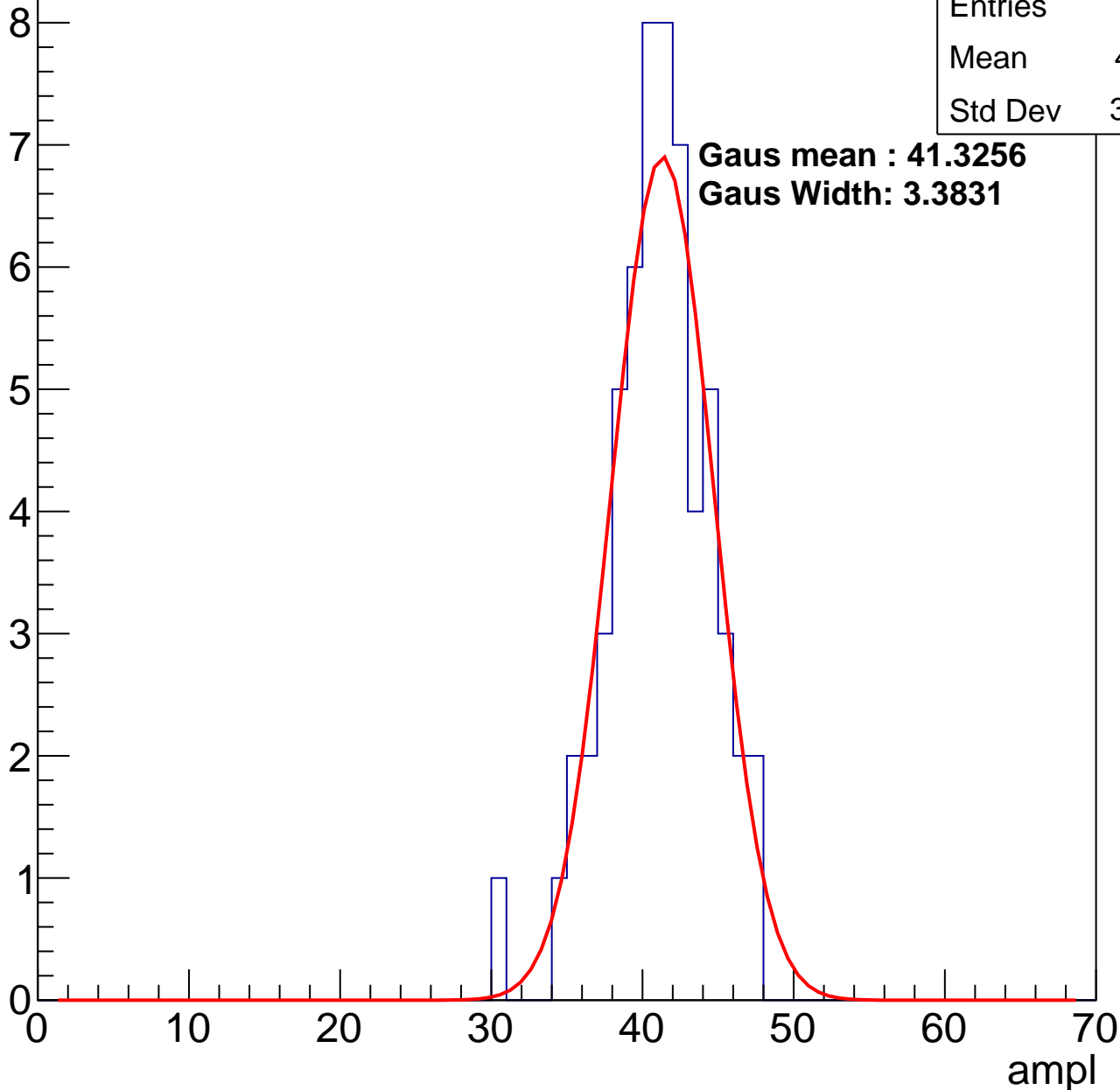
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.61
Std Dev	3.309

**Gaus mean : 41.3256**

**Gaus Width: 3.3831**

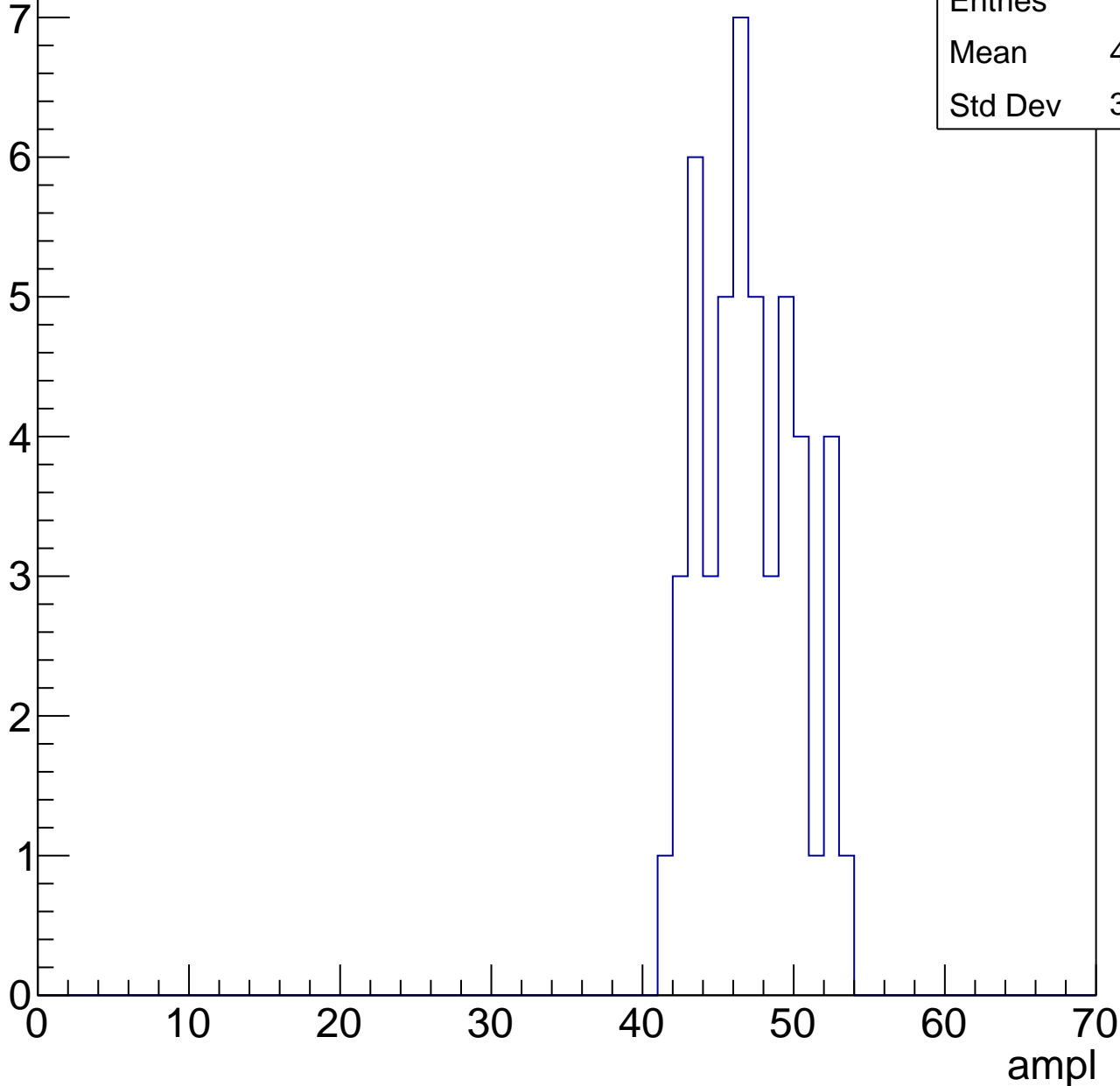


# B1L002S, U26-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	46.67
Std Dev	3.118

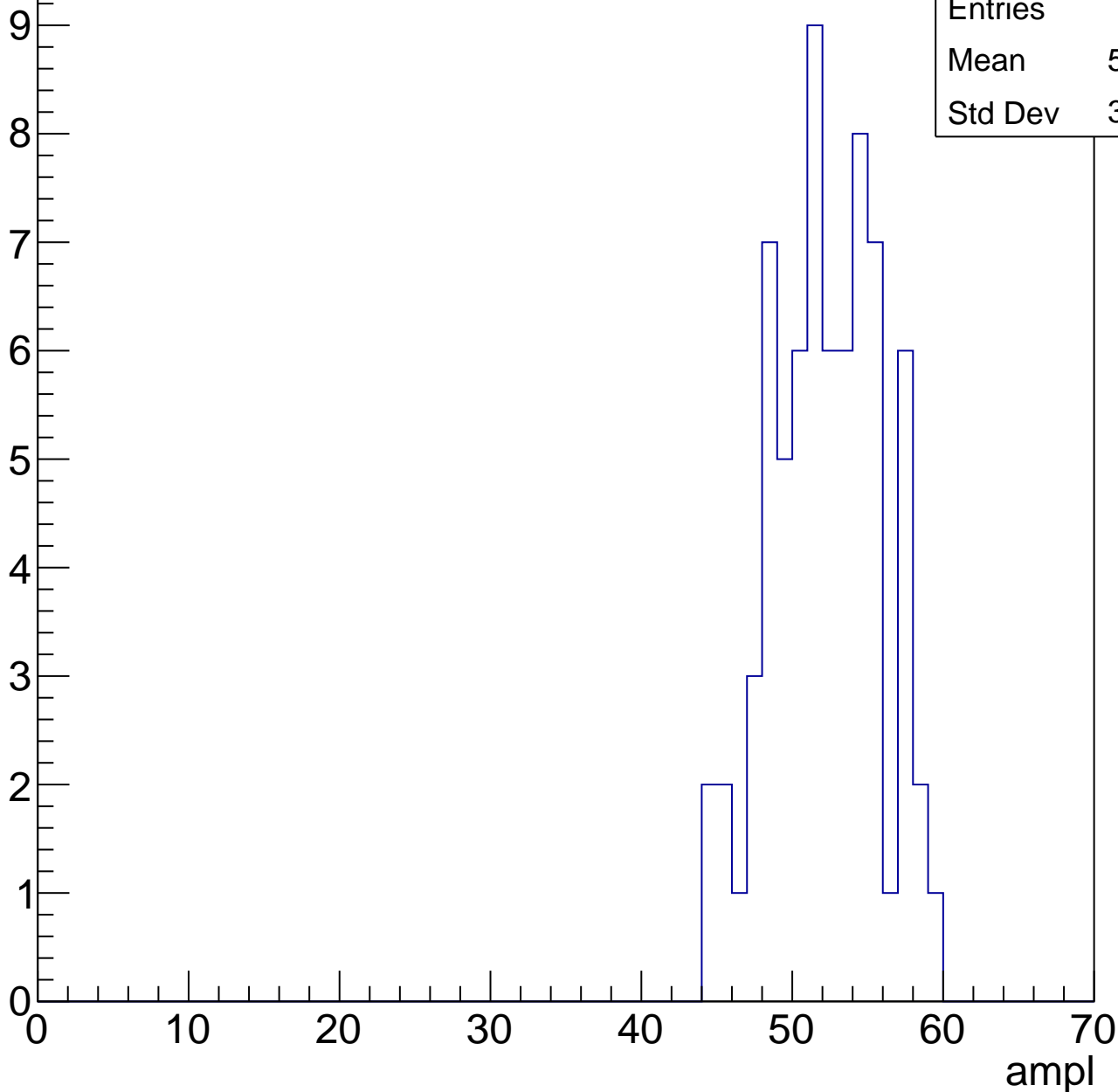


# B1L002S, U26-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	51.74
Std Dev	3.555



# B1L002S, U26-ch71, adc5

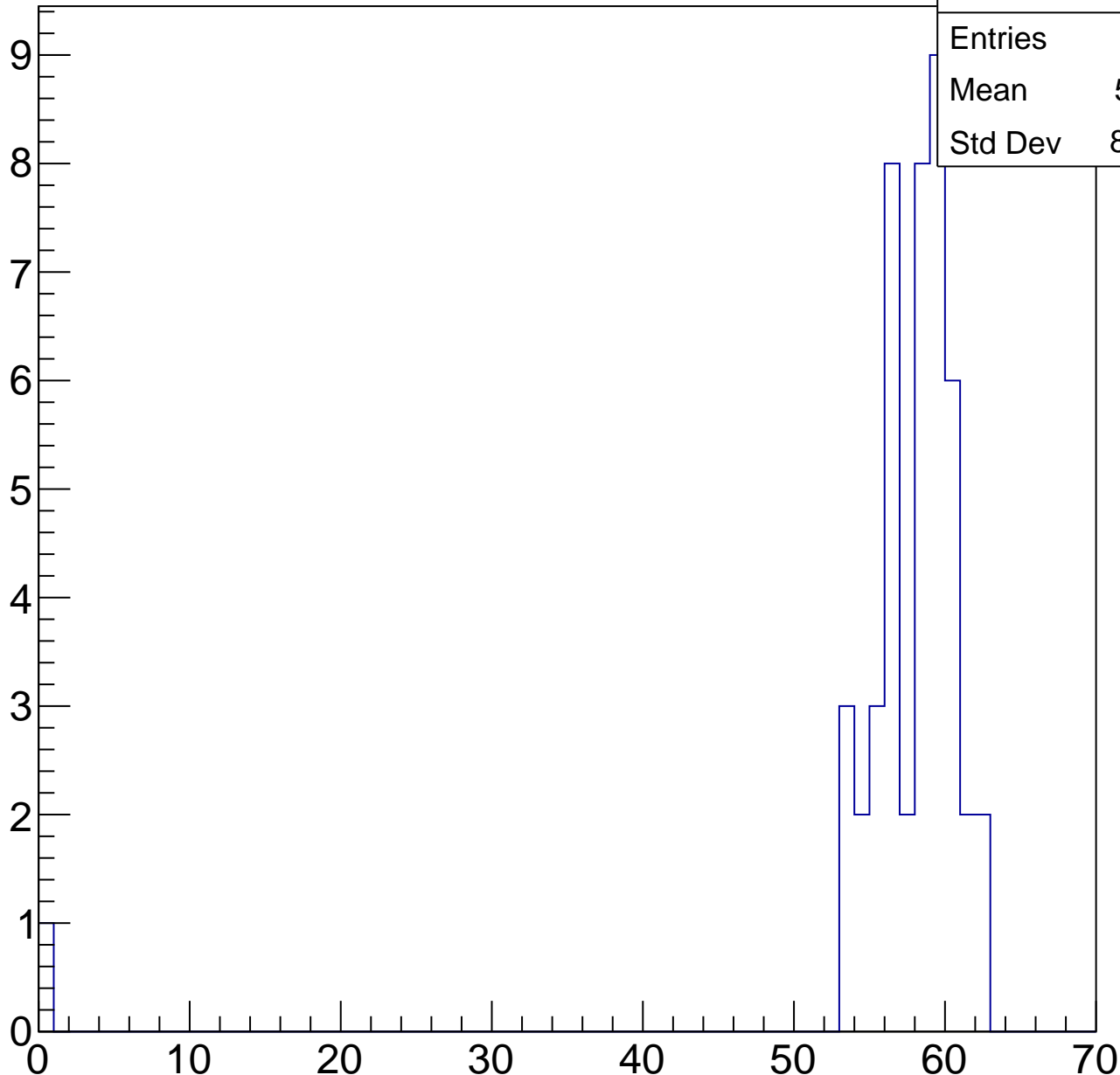
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	46
Mean	56.41
Std Dev	8.722

ampl

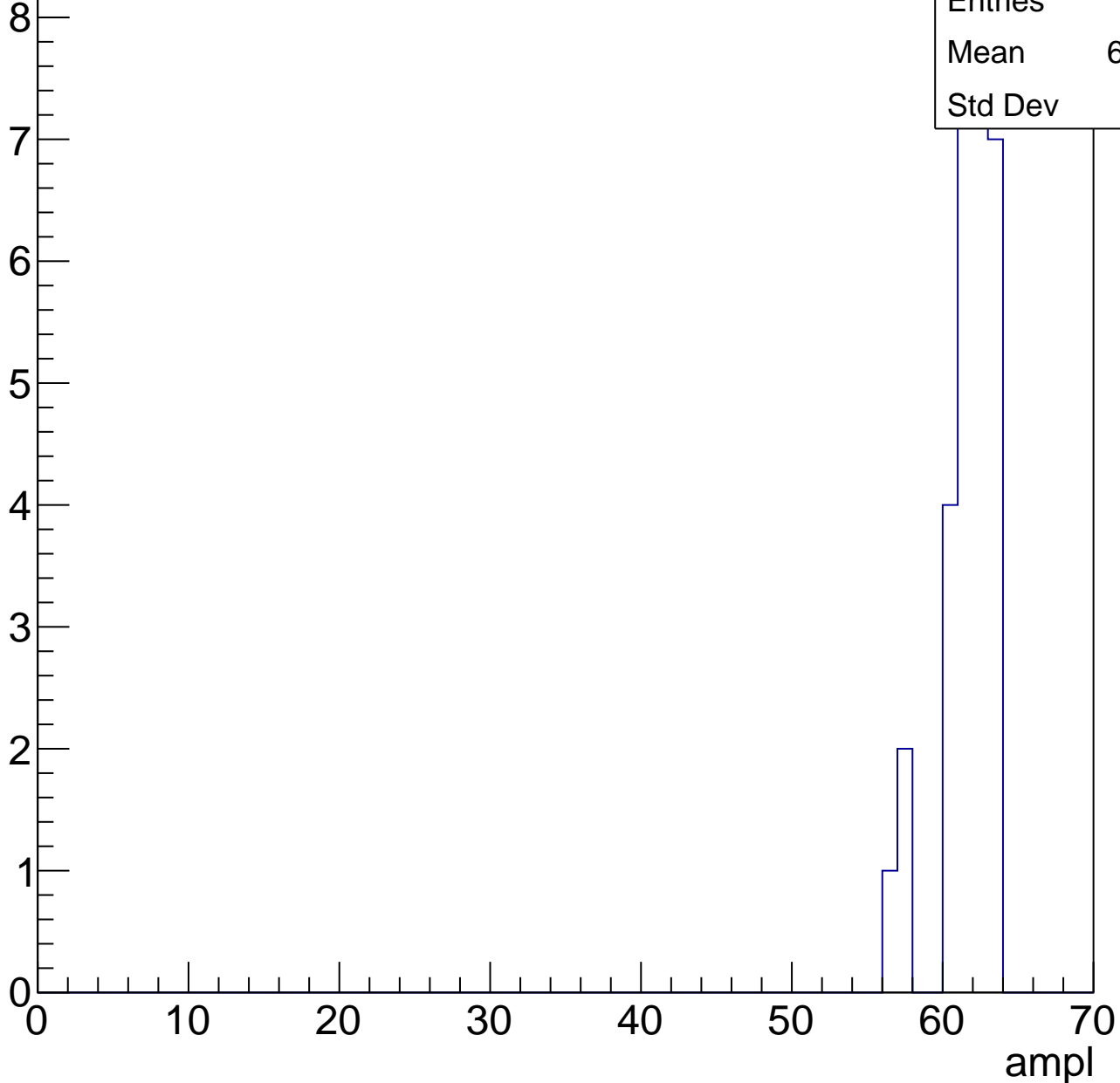


# B1L002S, U26-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	61.17
Std Dev	1.79

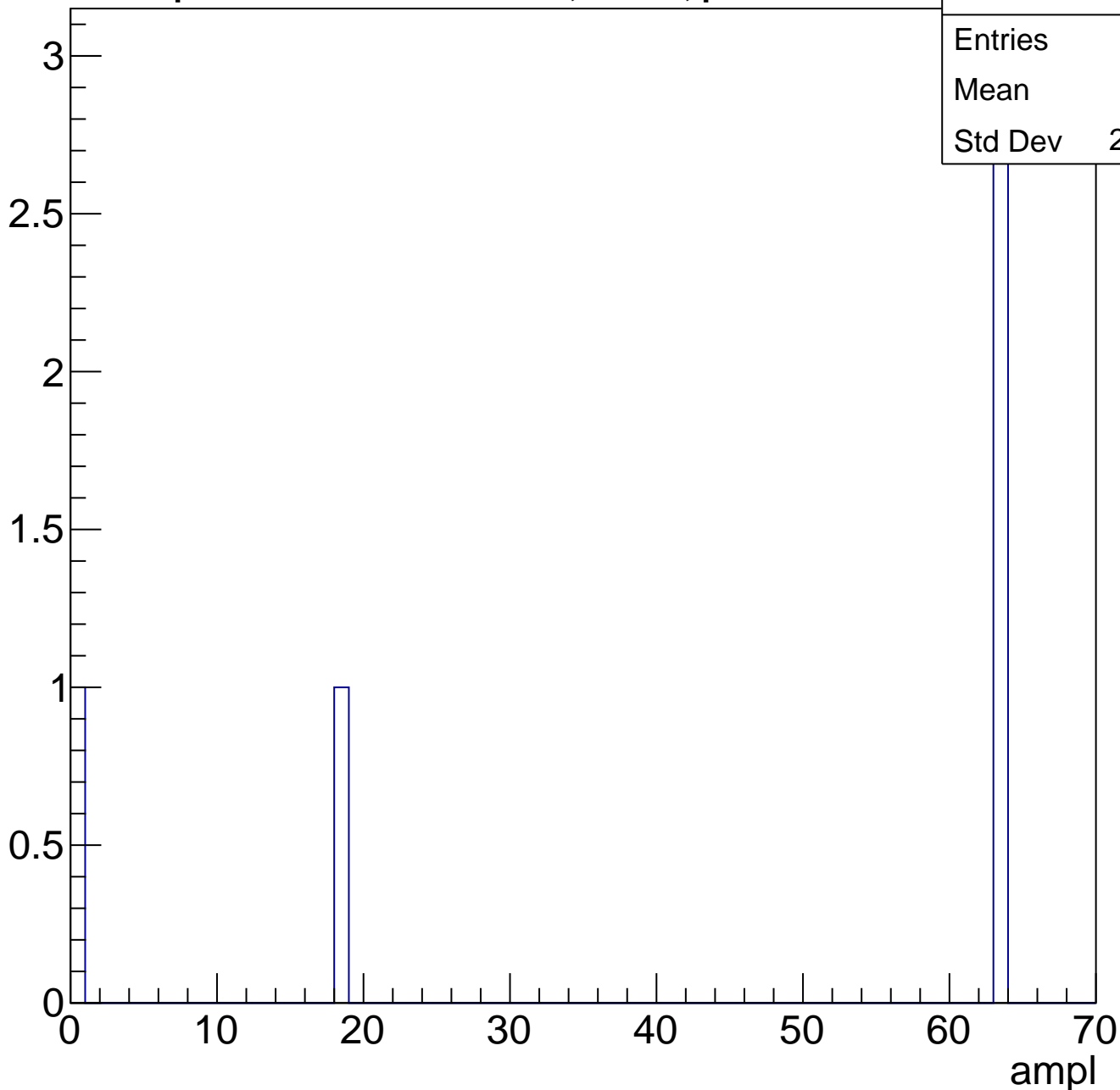




# B1L002S, U26-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	41.4
Std Dev	27.06

# B1L002S, U26-ch72, adc0

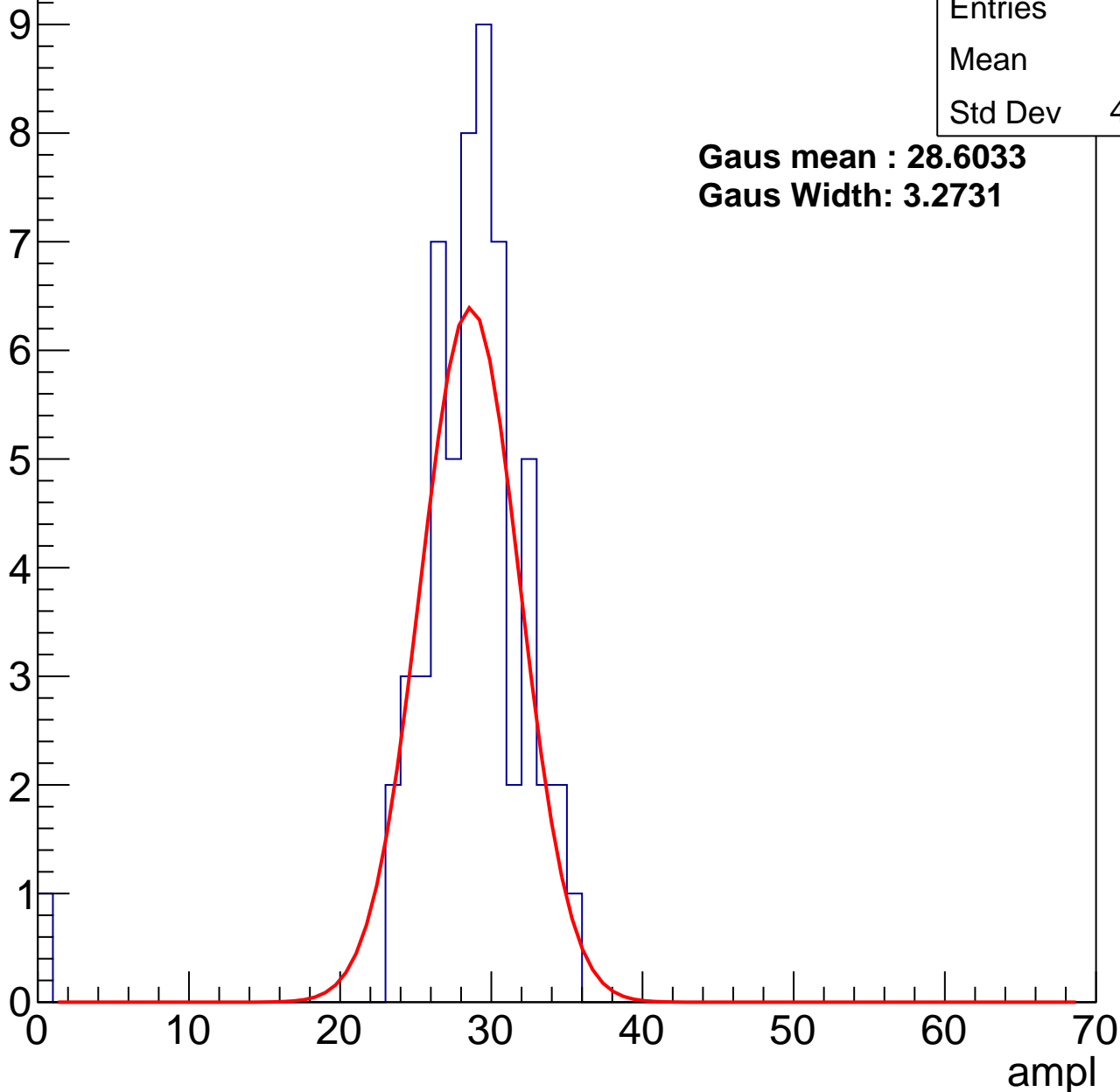
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	28
Std Dev	4.679

**Gaus mean : 28.6033**

**Gaus Width: 3.2731**



# B1L002S, U26-ch72, adc1

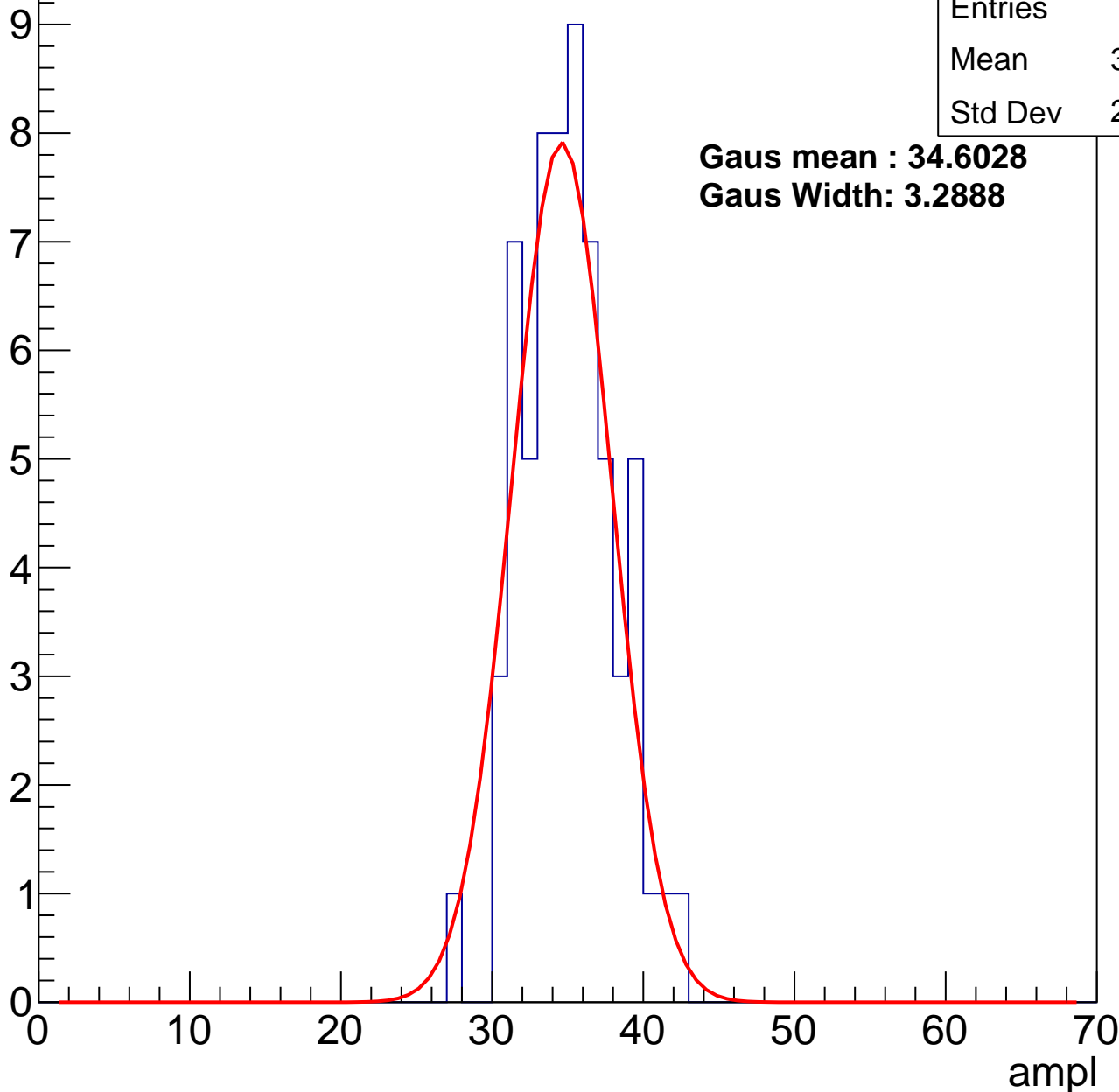
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.59
Std Dev	2.983

**Gaus mean : 34.6028**

**Gaus Width: 3.2888**



# B1L002S, U26-ch72, adc2

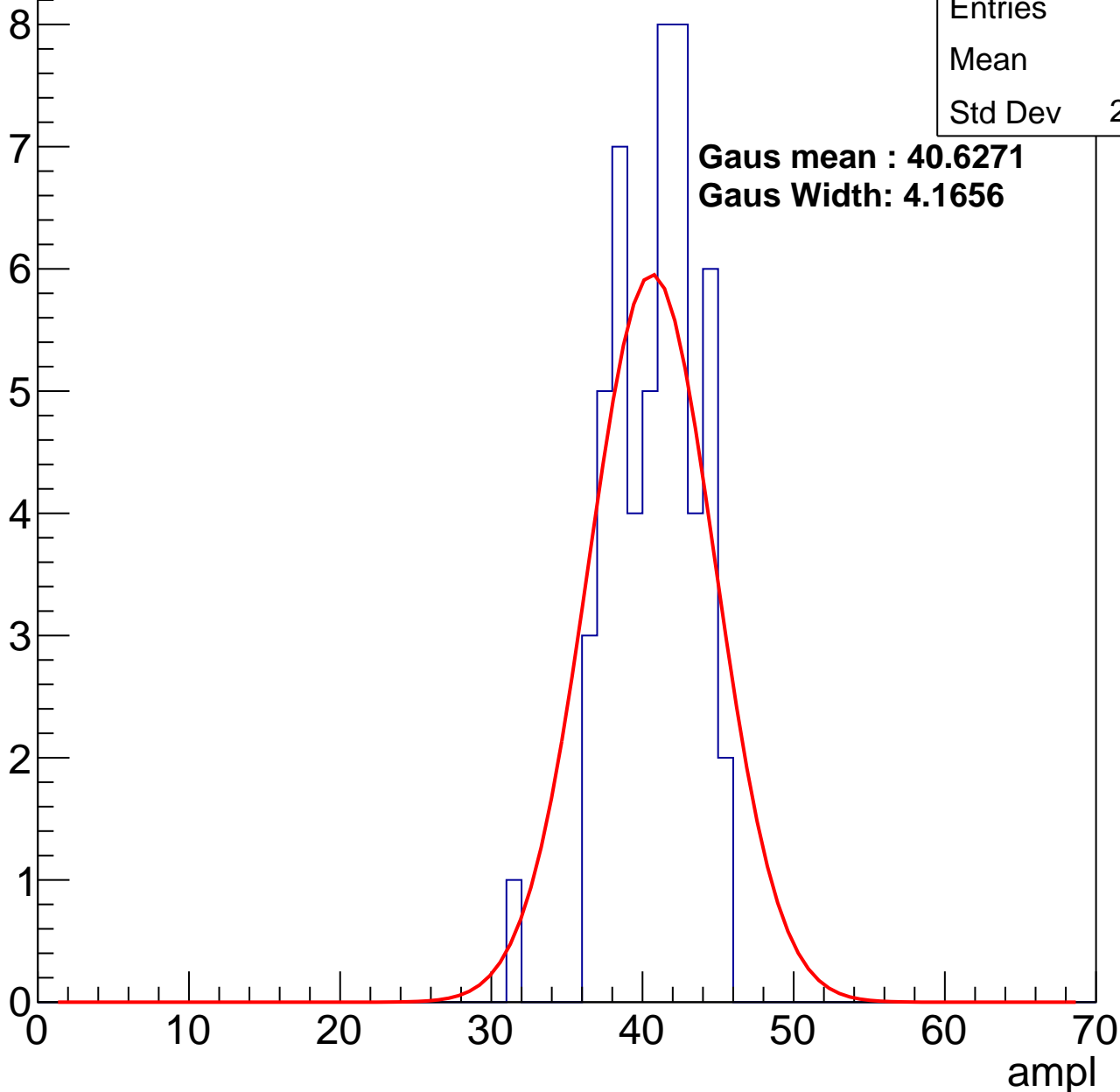
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	40.3
Std Dev	2.826

**Gaus mean : 40.6271**

**Gaus Width: 4.1656**



# B1L002S, U26-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

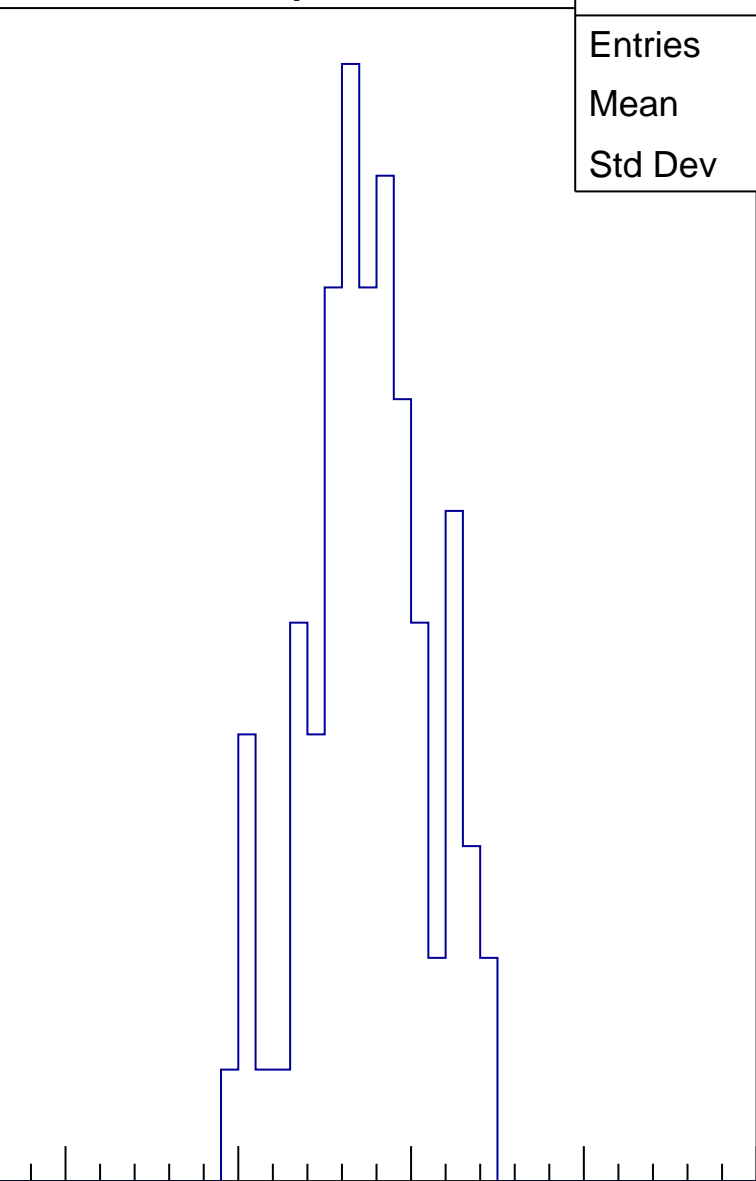
Entries	76
Mean	47.04
Std Dev	3.533

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

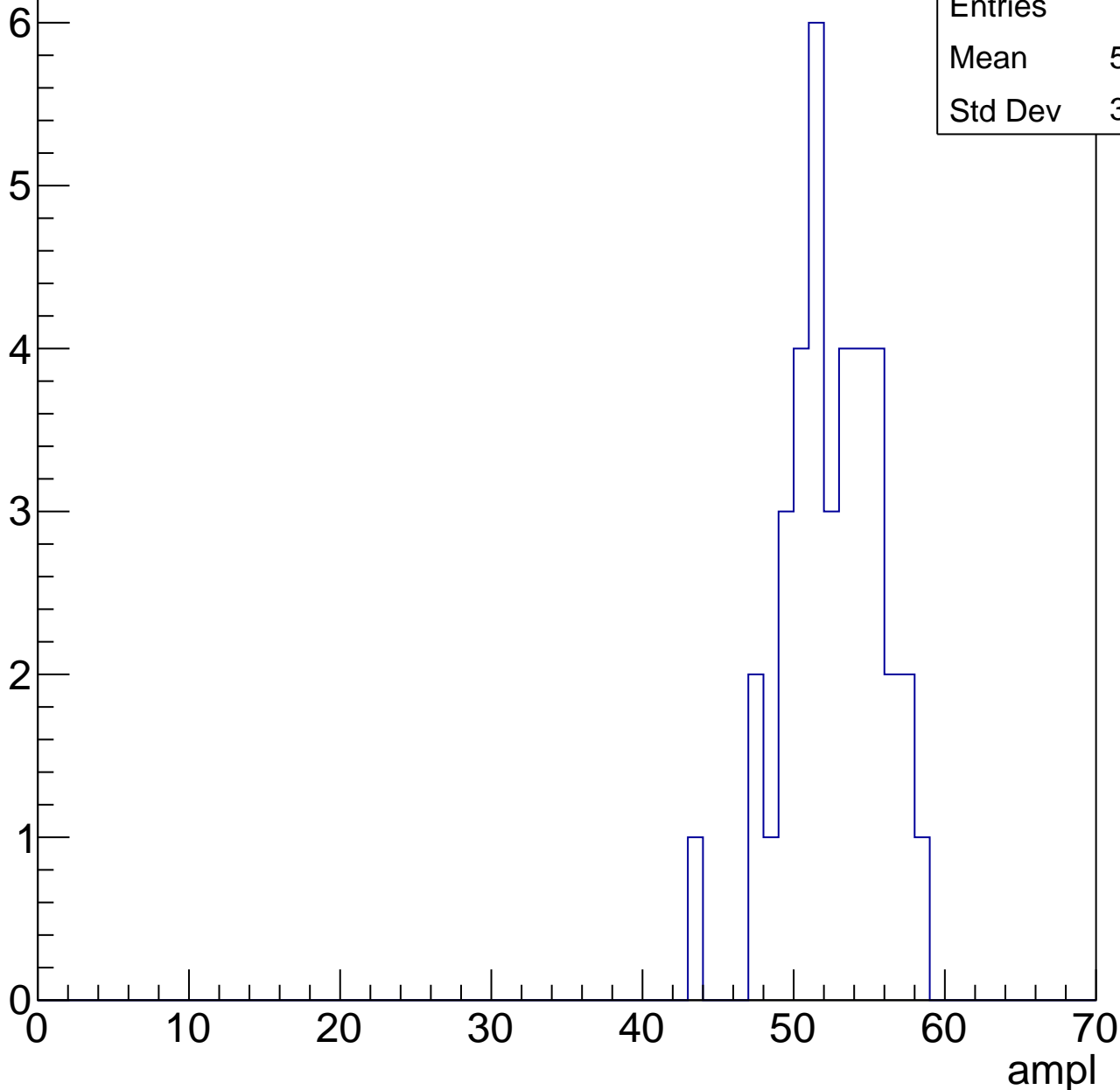


# B1L002S, U26-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	52.05
Std Dev	3.162

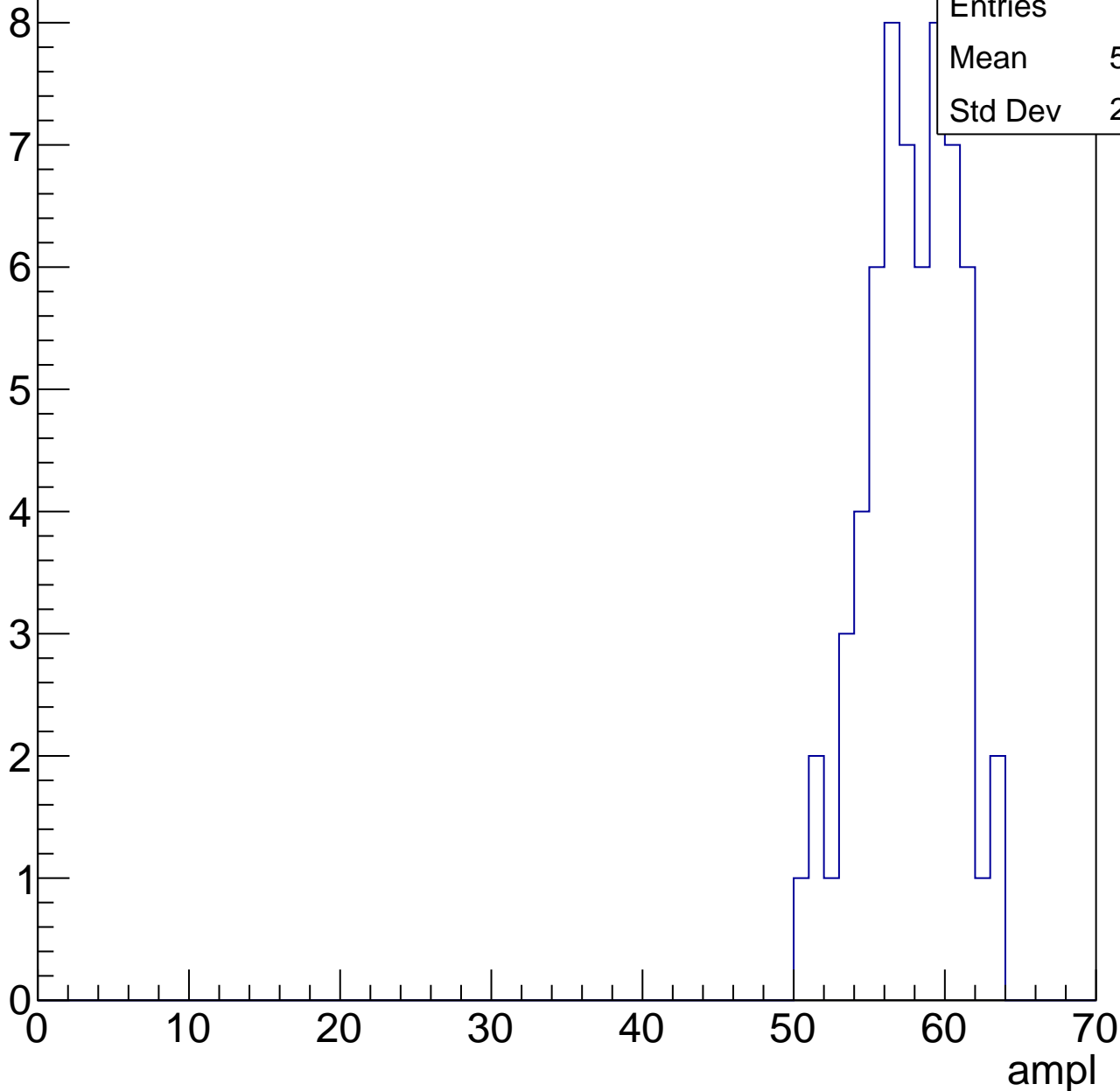


# B1L002S, U26-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	57.26
Std Dev	2.983

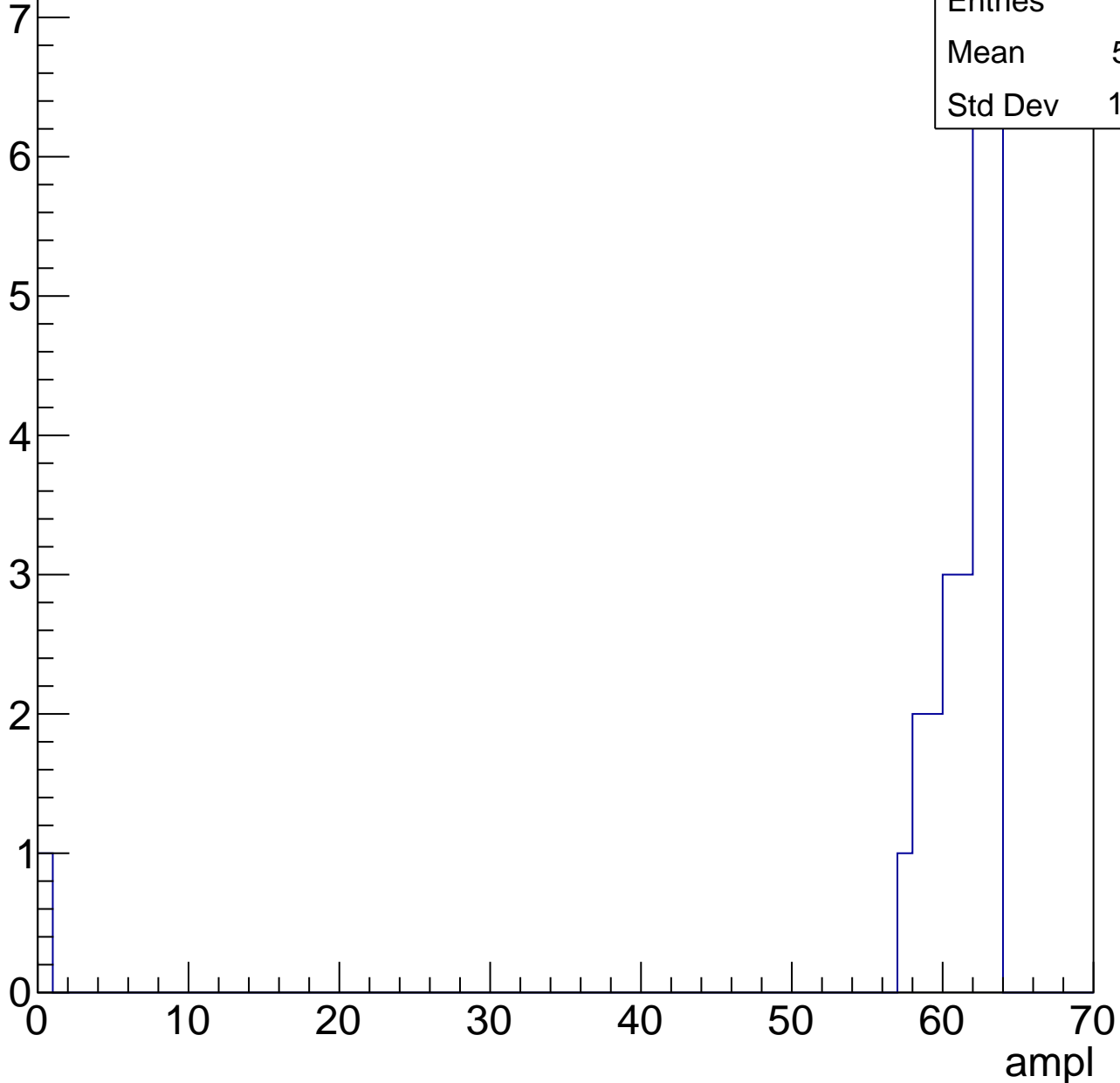


# B1L002S, U26-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.81
Std Dev	11.89

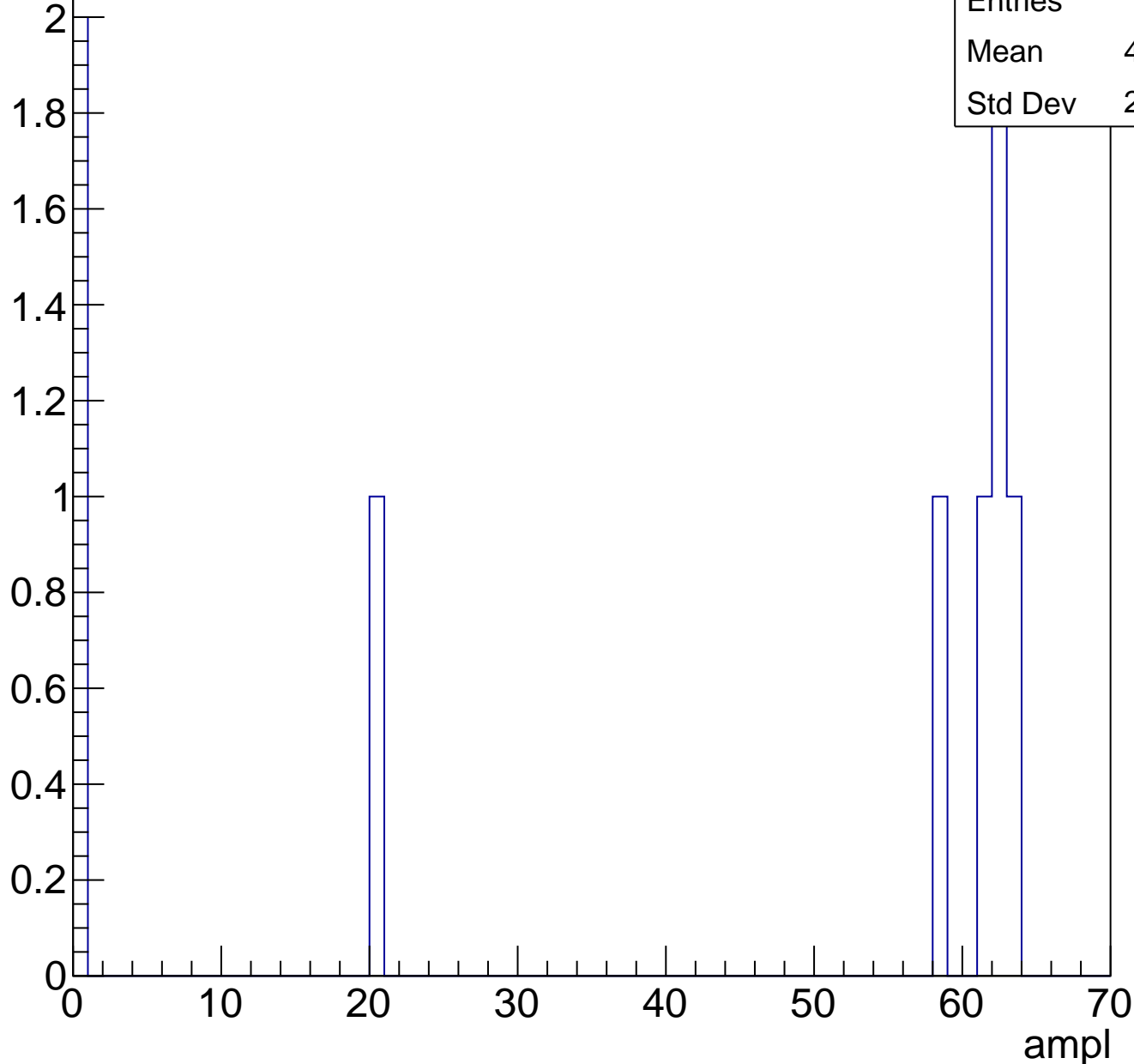




# B1L002S, U26-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch73, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	27.23
Std Dev	4.395

**Gaus mean : 28.3504**

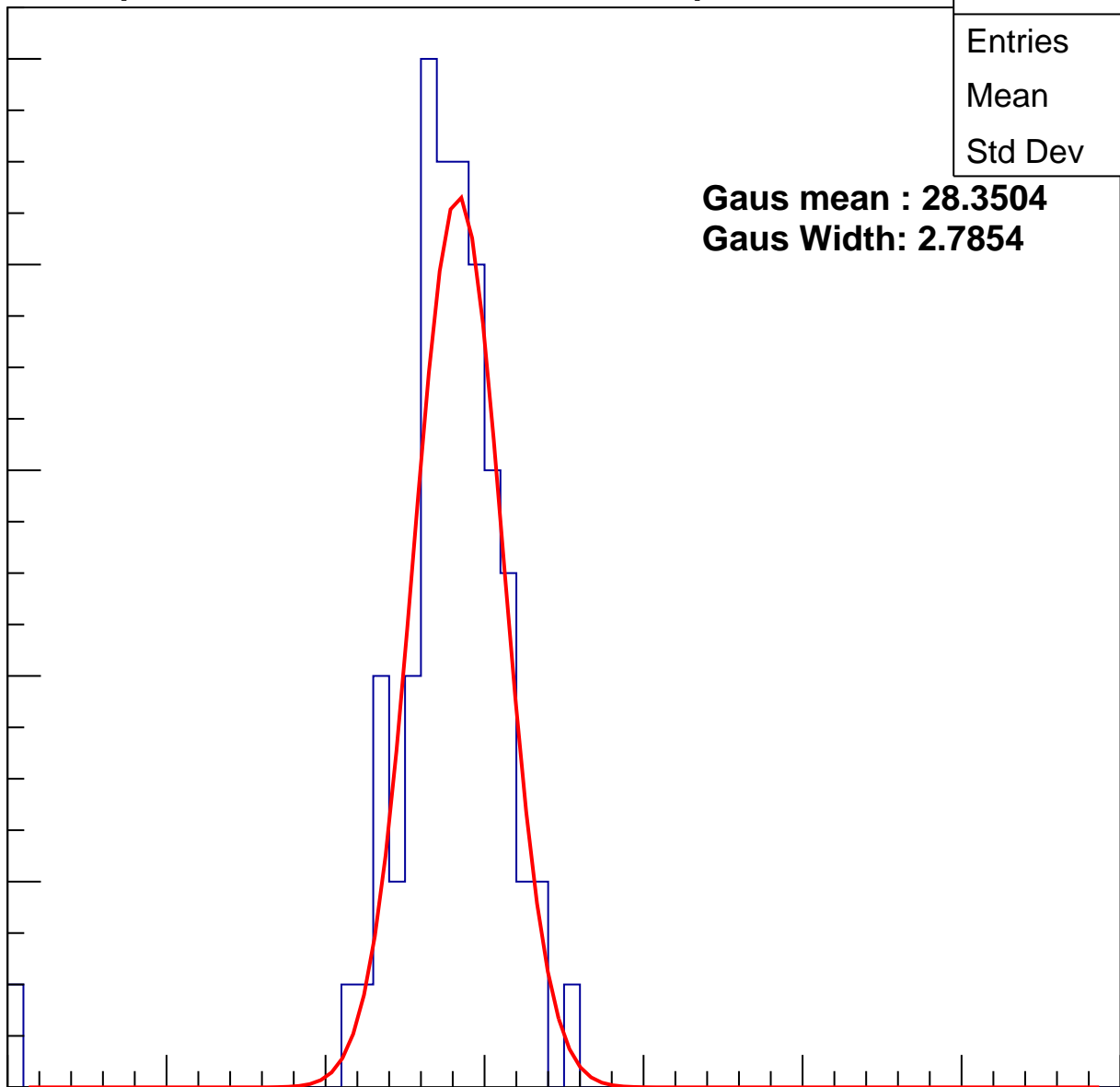
**Gaus Width: 2.7854**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch73, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	34.48
Std Dev	2.971

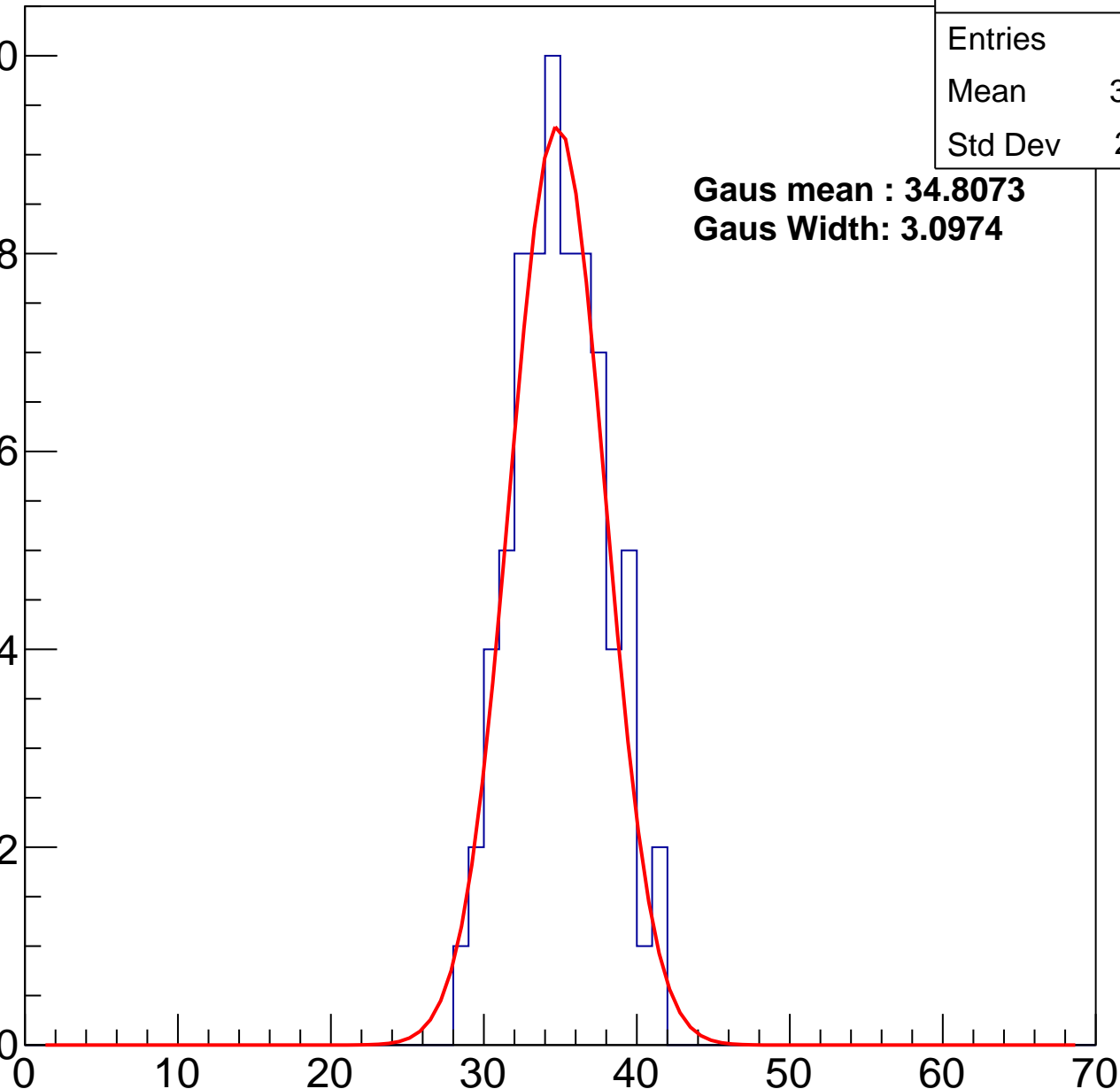
**Gaus mean : 34.8073**

**Gaus Width: 3.0974**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U26-ch73, adc2

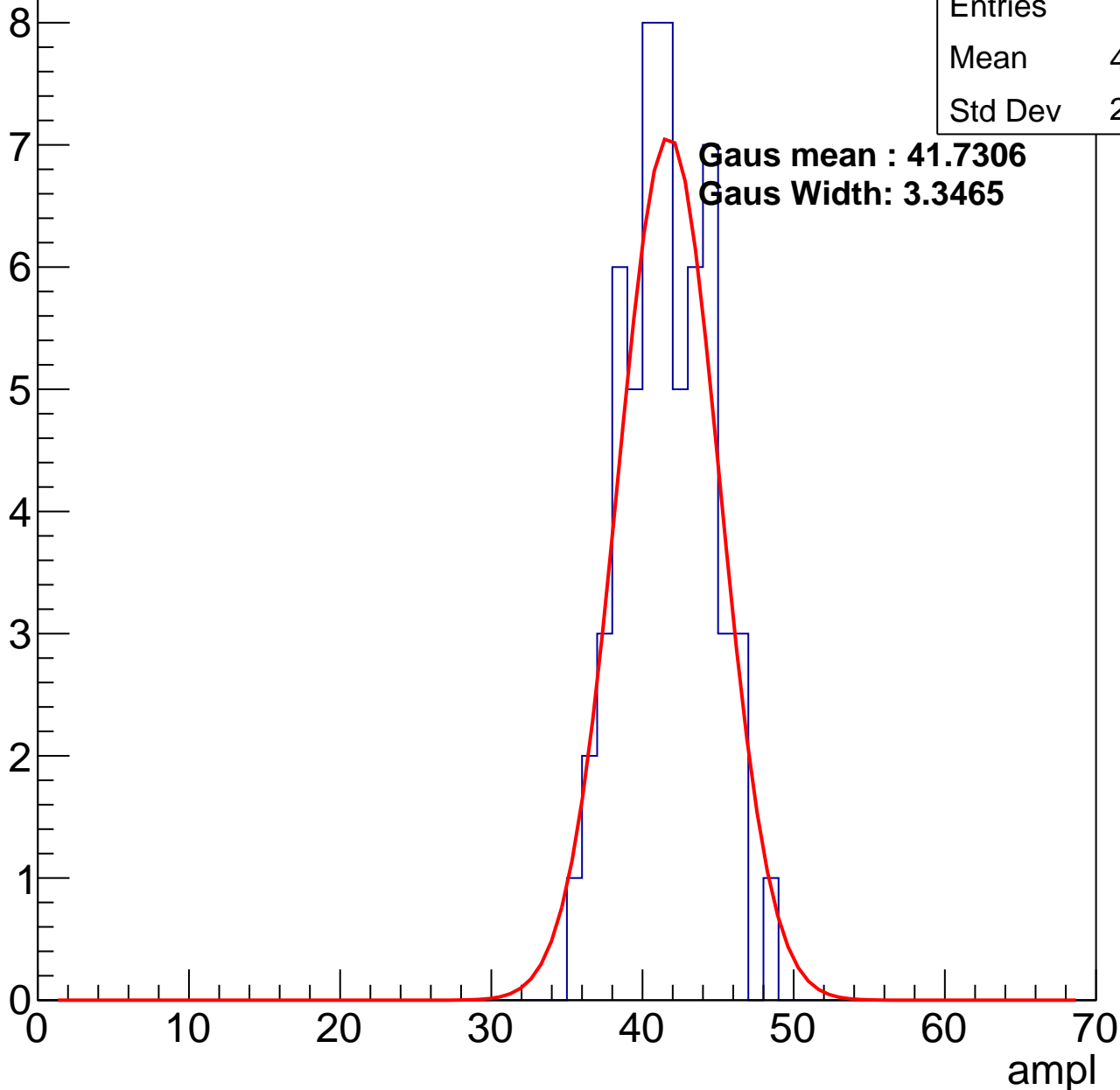
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.14
Std Dev	2.873

**Gaus mean : 41.7306**

**Gaus Width: 3.3465**

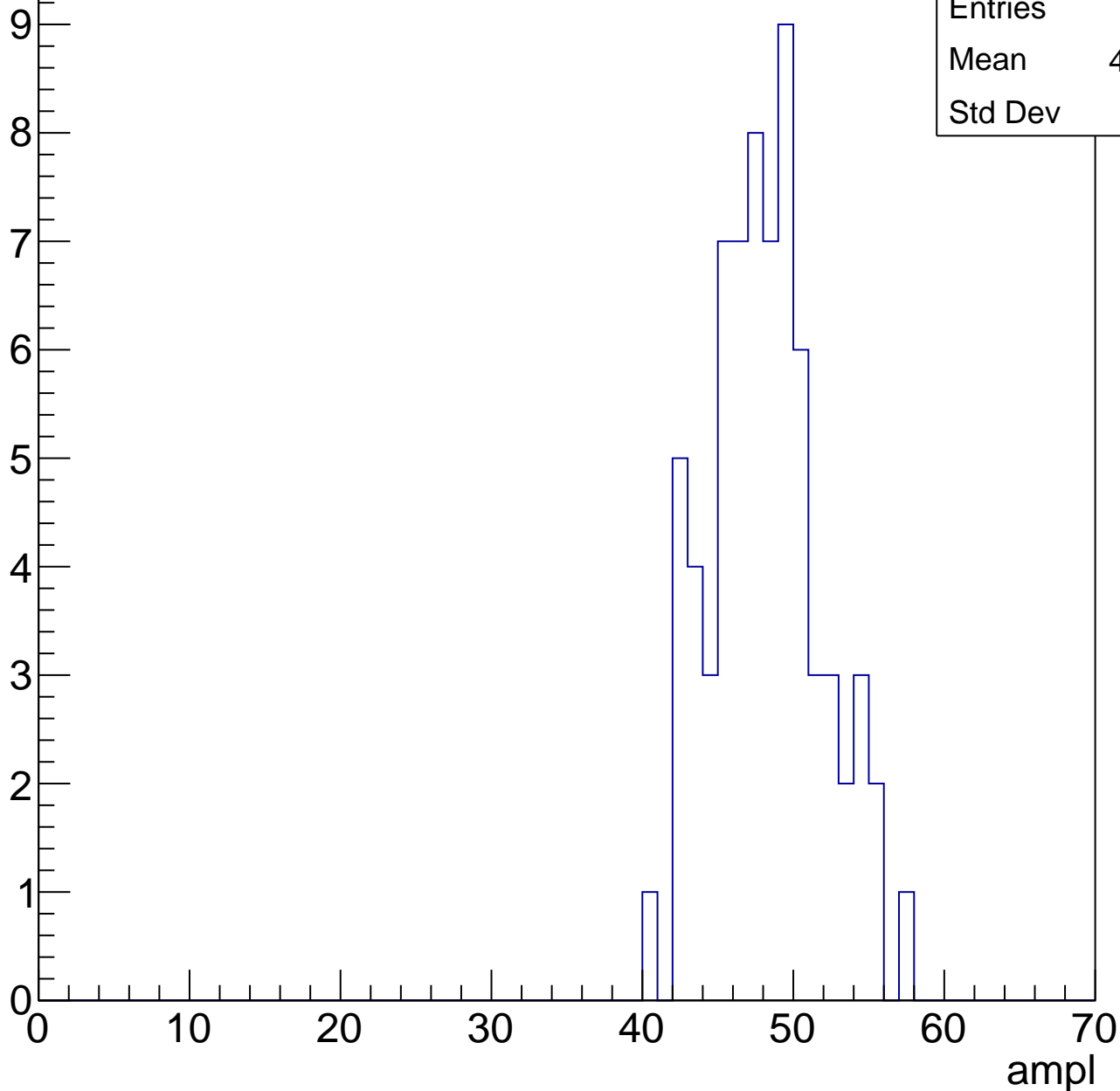


# B1L002S, U26-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

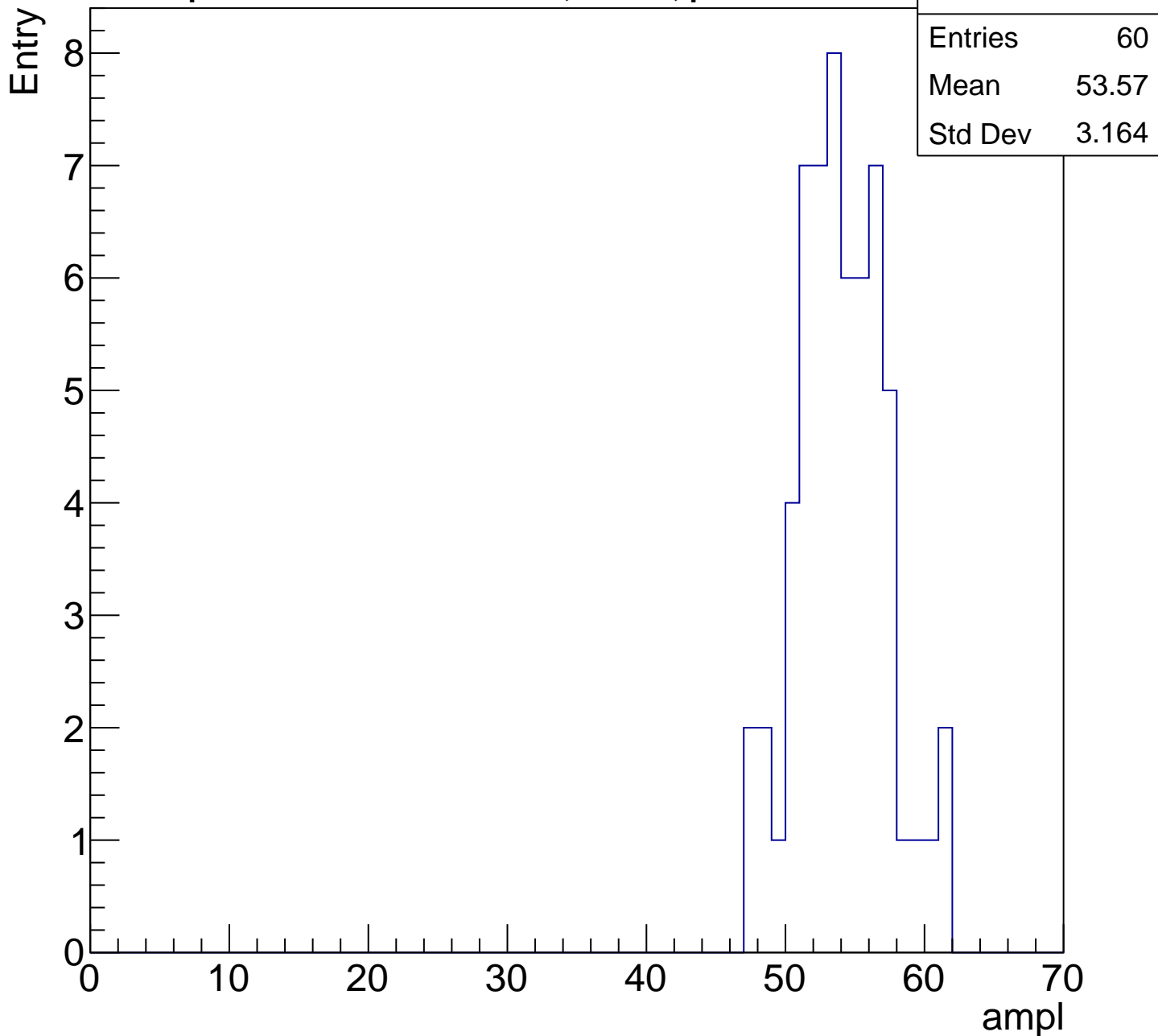
Entry

Entries	71
Mean	47.72
Std Dev	3.62



# B1L002S, U26-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

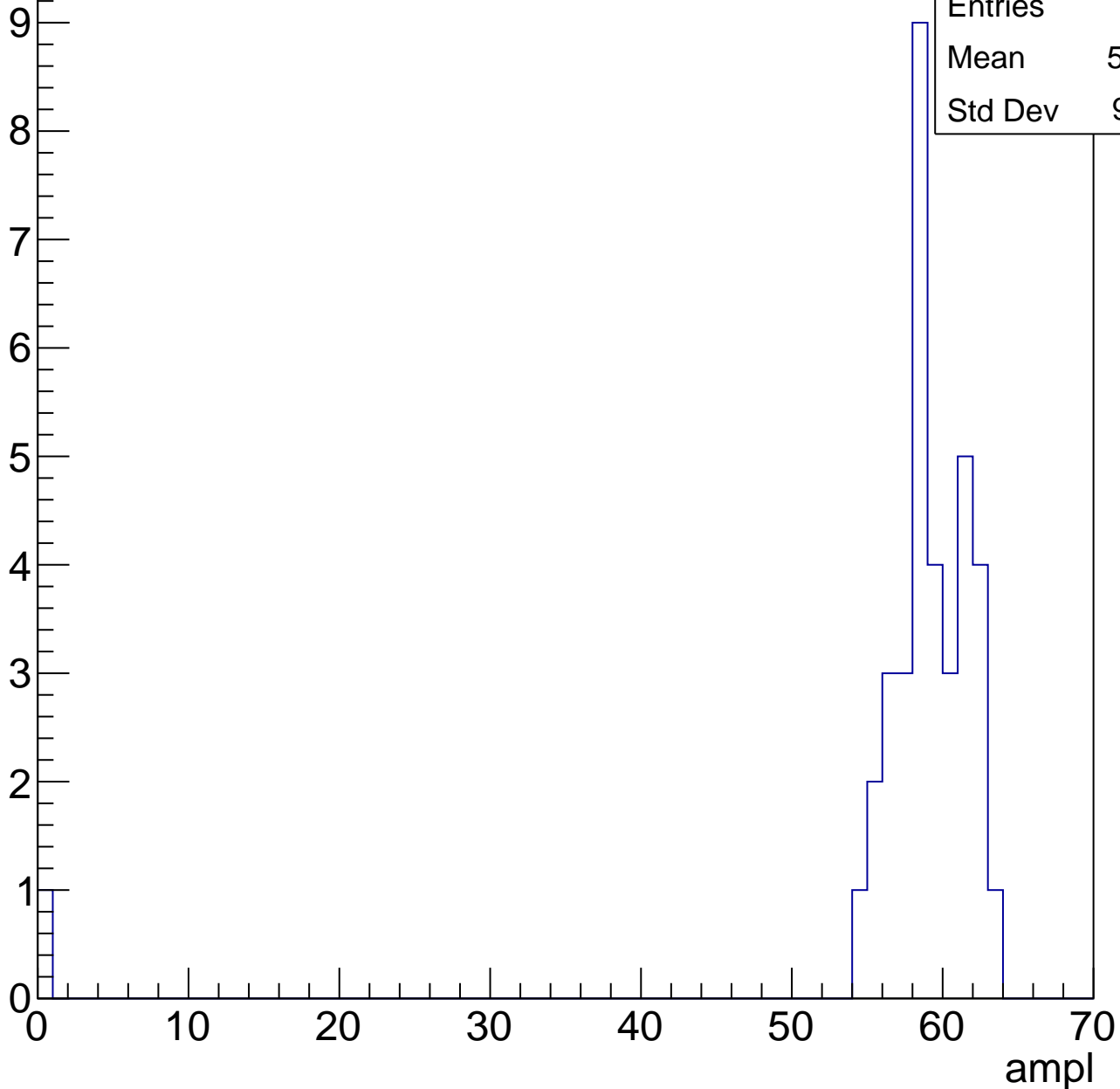


# B1L002S, U26-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	57.14
Std Dev	9.911

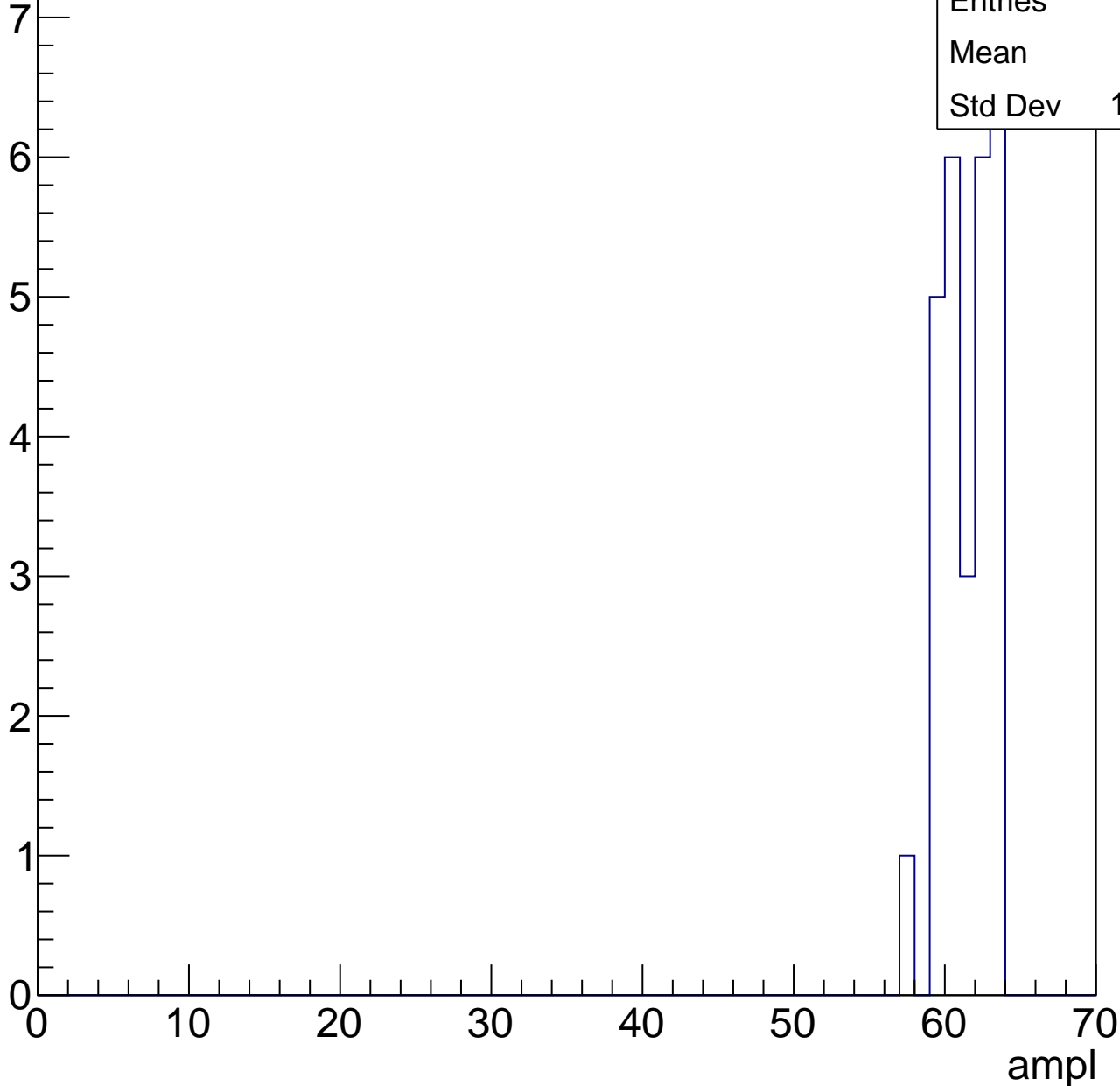


# B1L002S, U26-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	61
Std Dev	1.648





# B1L002S, U26-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch74, adc0

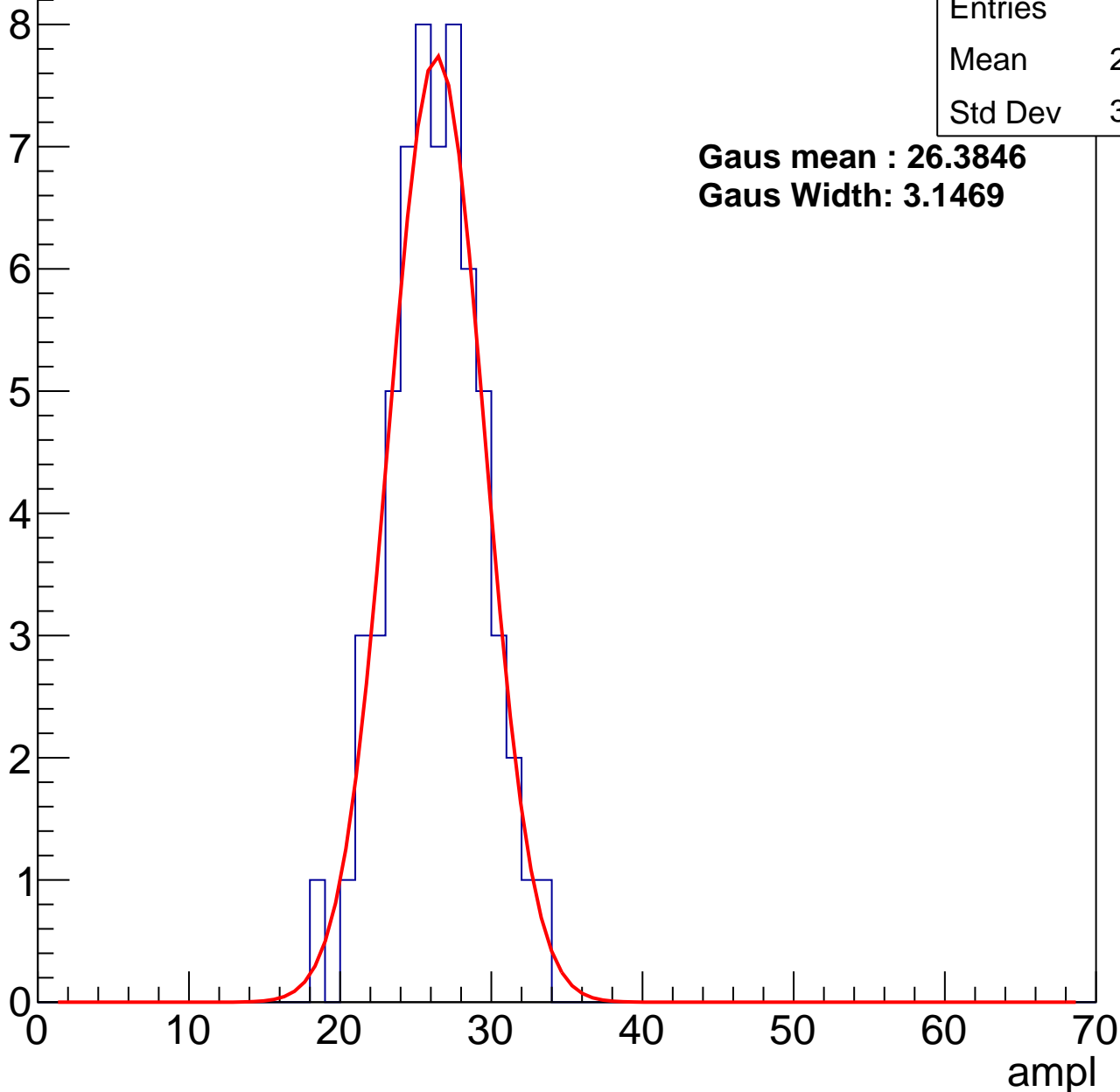
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	25.87
Std Dev	3.038

**Gaus mean : 26.3846**

**Gaus Width: 3.1469**



# B1L002S, U26-ch74, adc1

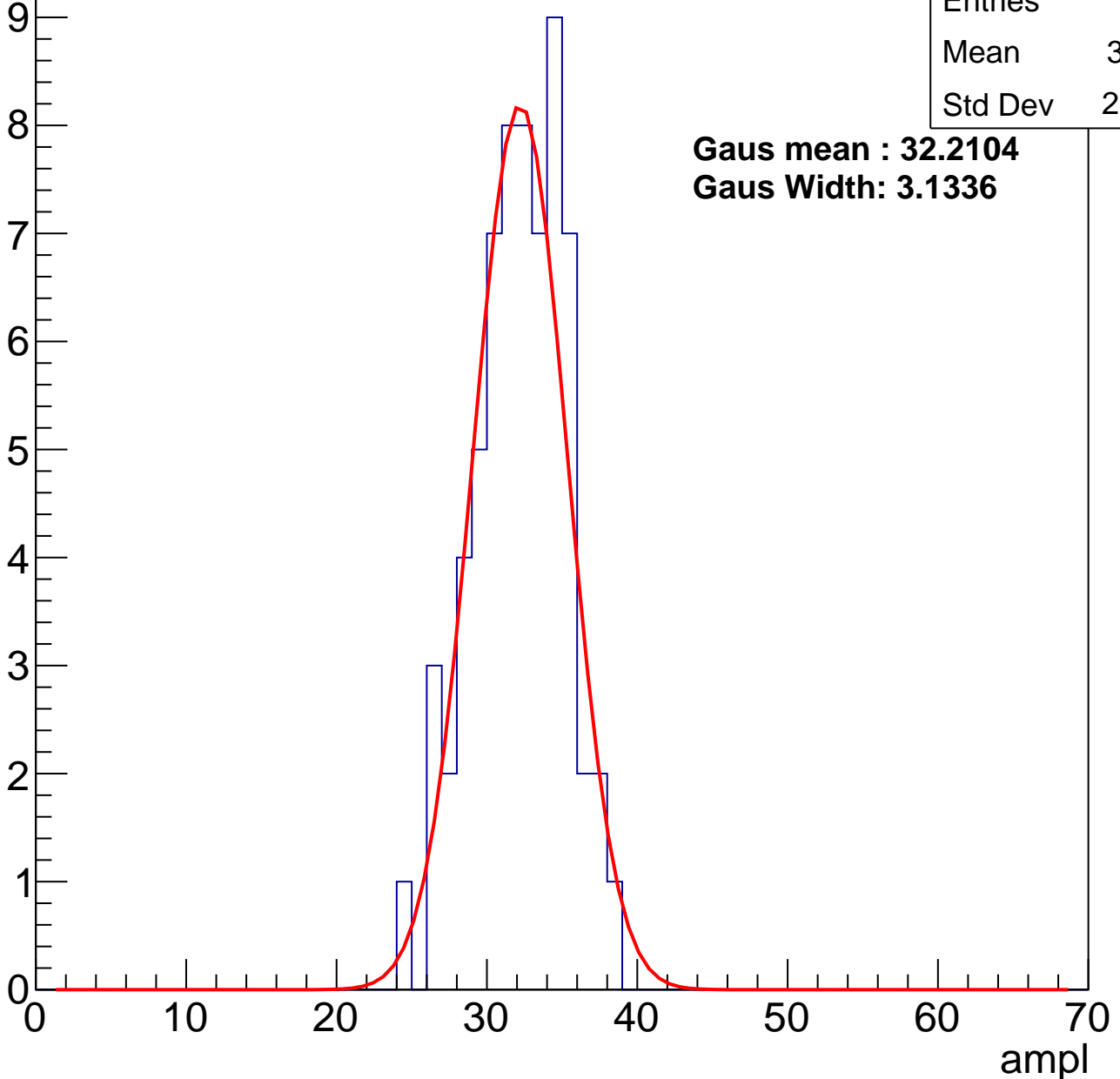
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	31.71
Std Dev	2.979

**Gaus mean : 32.2104**

**Gaus Width: 3.1336**



# B1L002S, U26-ch74, adc2

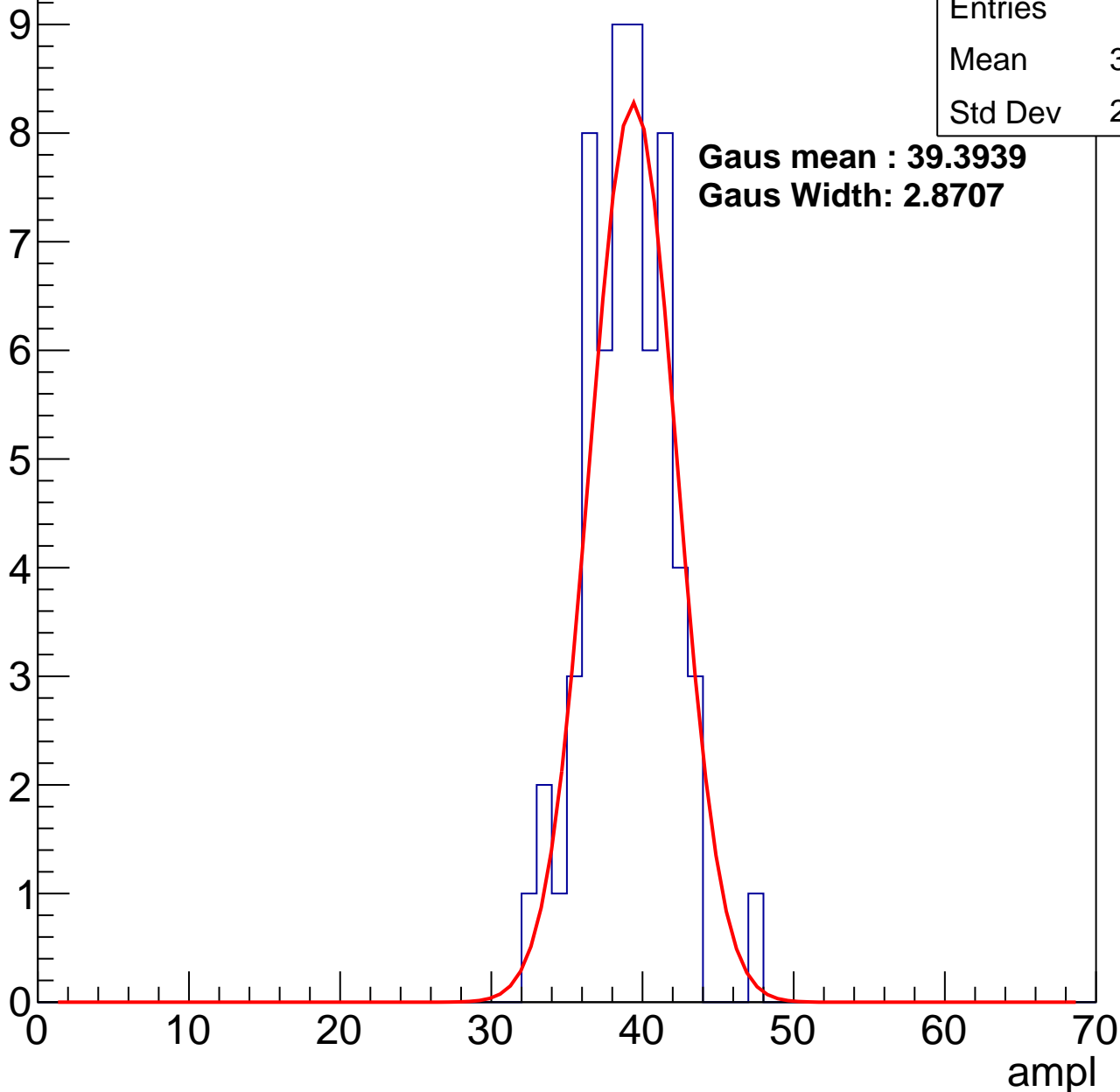
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	38.56
Std Dev	2.785

**Gaus mean : 39.3939**

**Gaus Width: 2.8707**

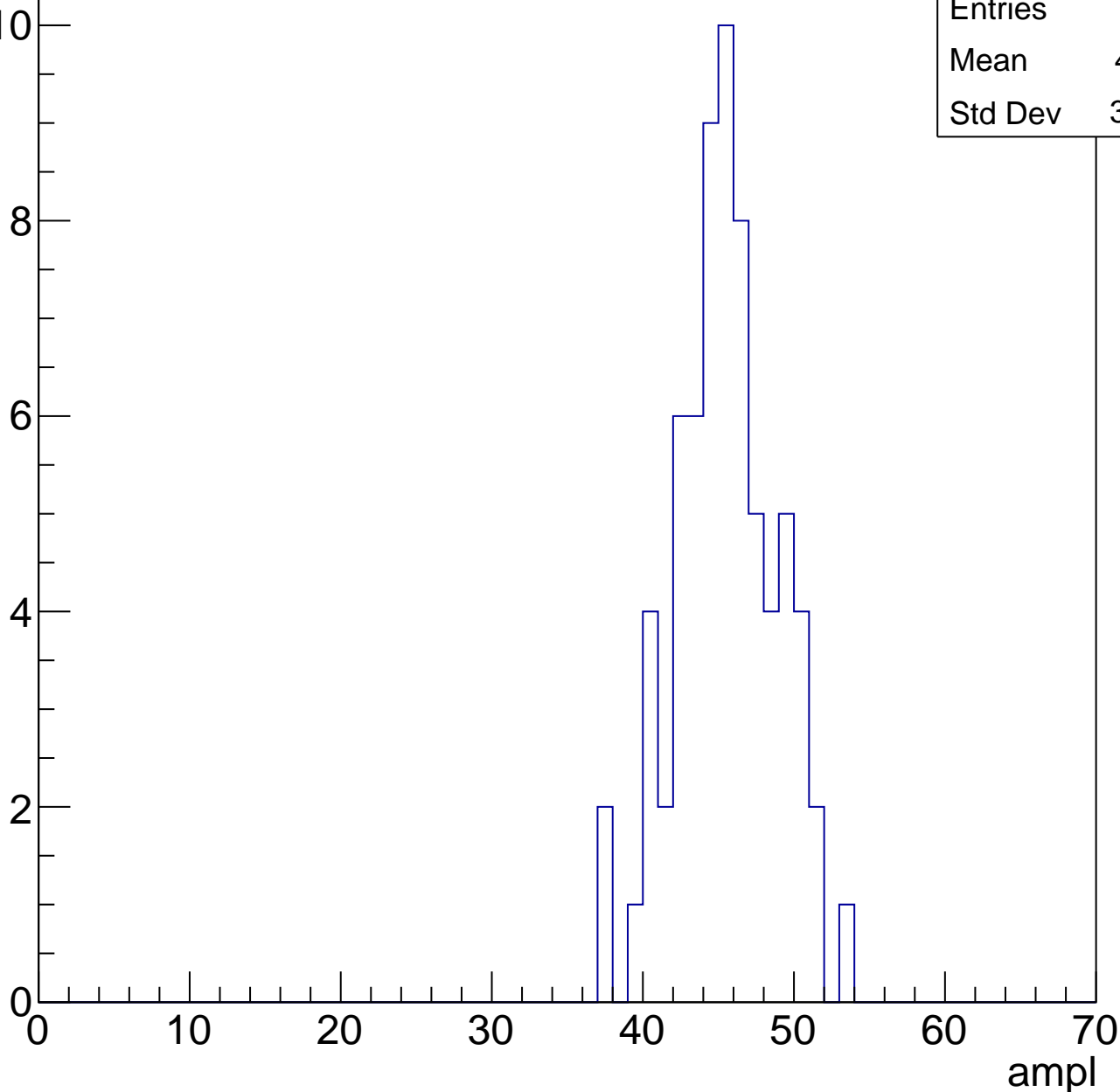


# B1L002S, U26-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.01
Std Dev	3.325

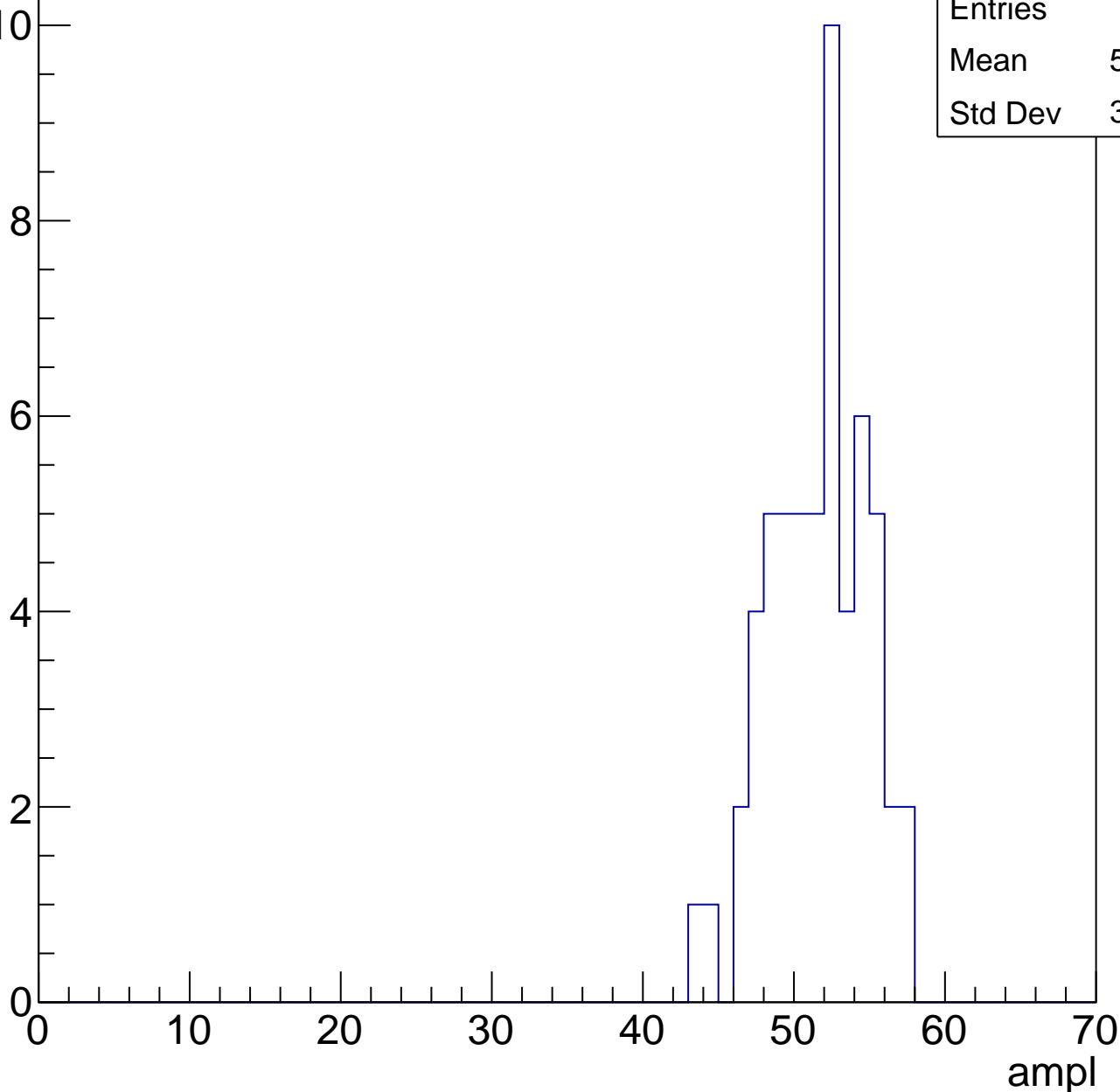


# B1L002S, U26-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	51.12
Std Dev	3.185

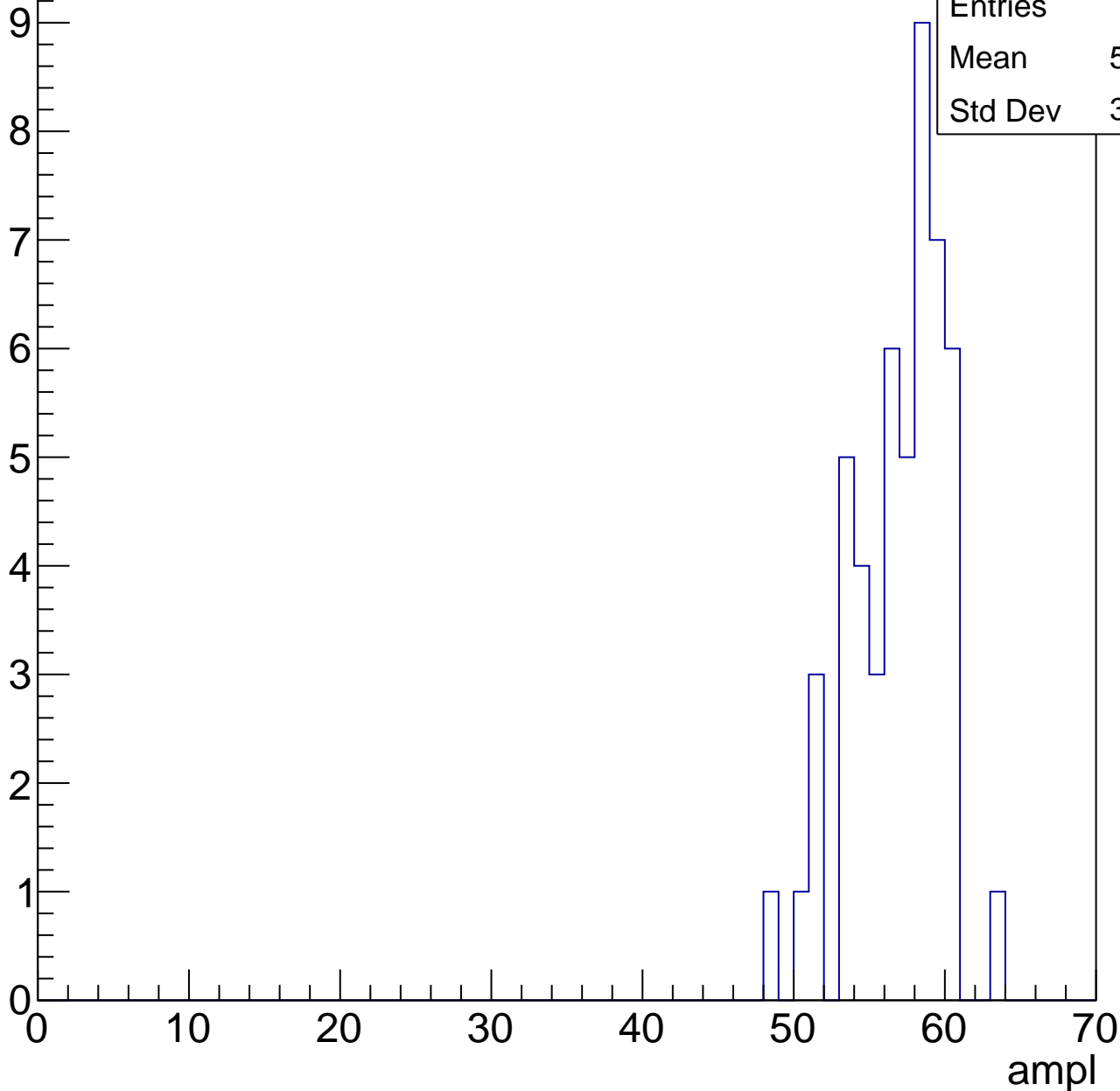


# B1L002S, U26-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	56.39
Std Dev	3.068

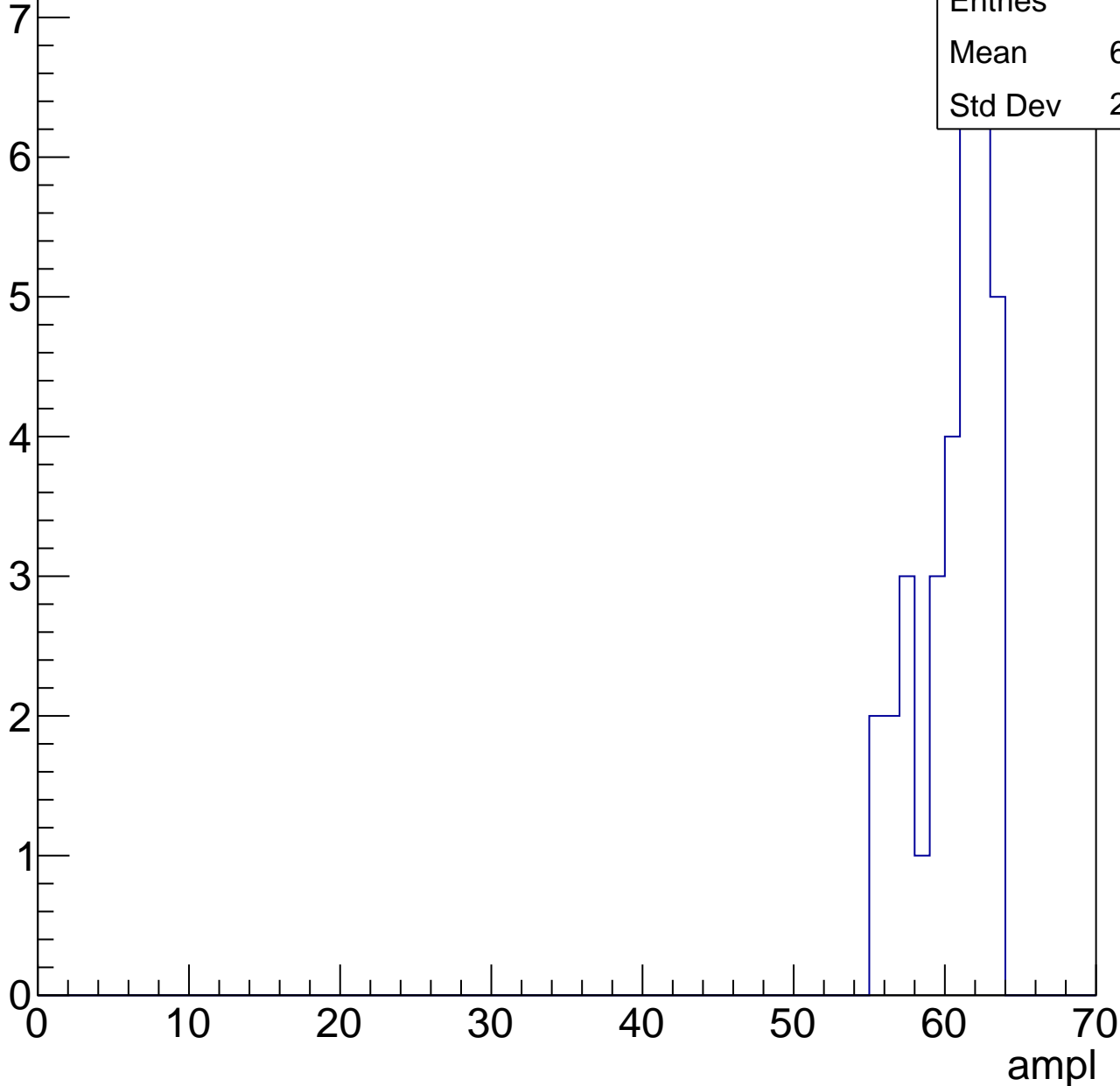


# B1L002S, U26-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	60.12
Std Dev	2.398

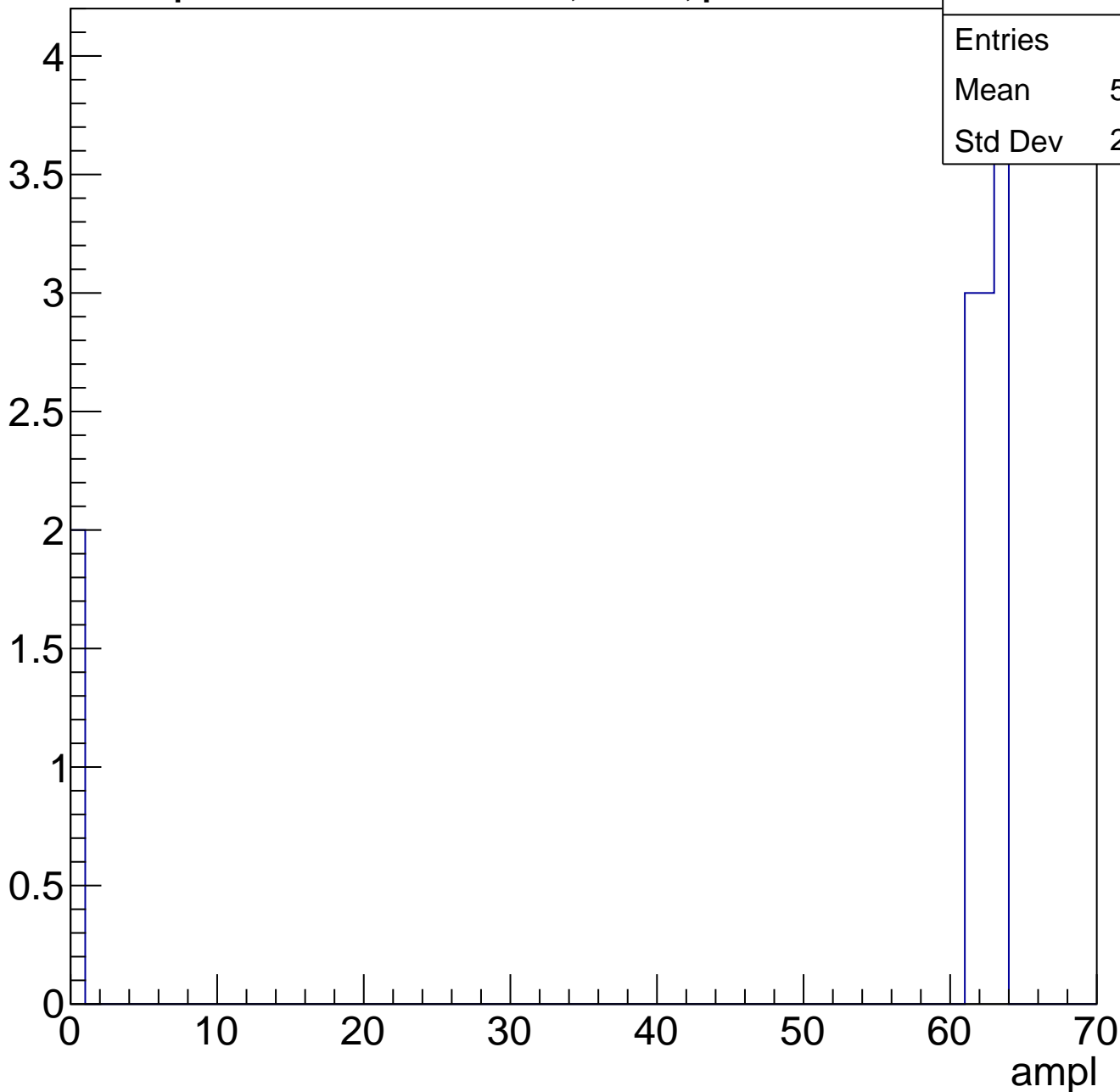




# B1L002S, U26-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch75, adc0

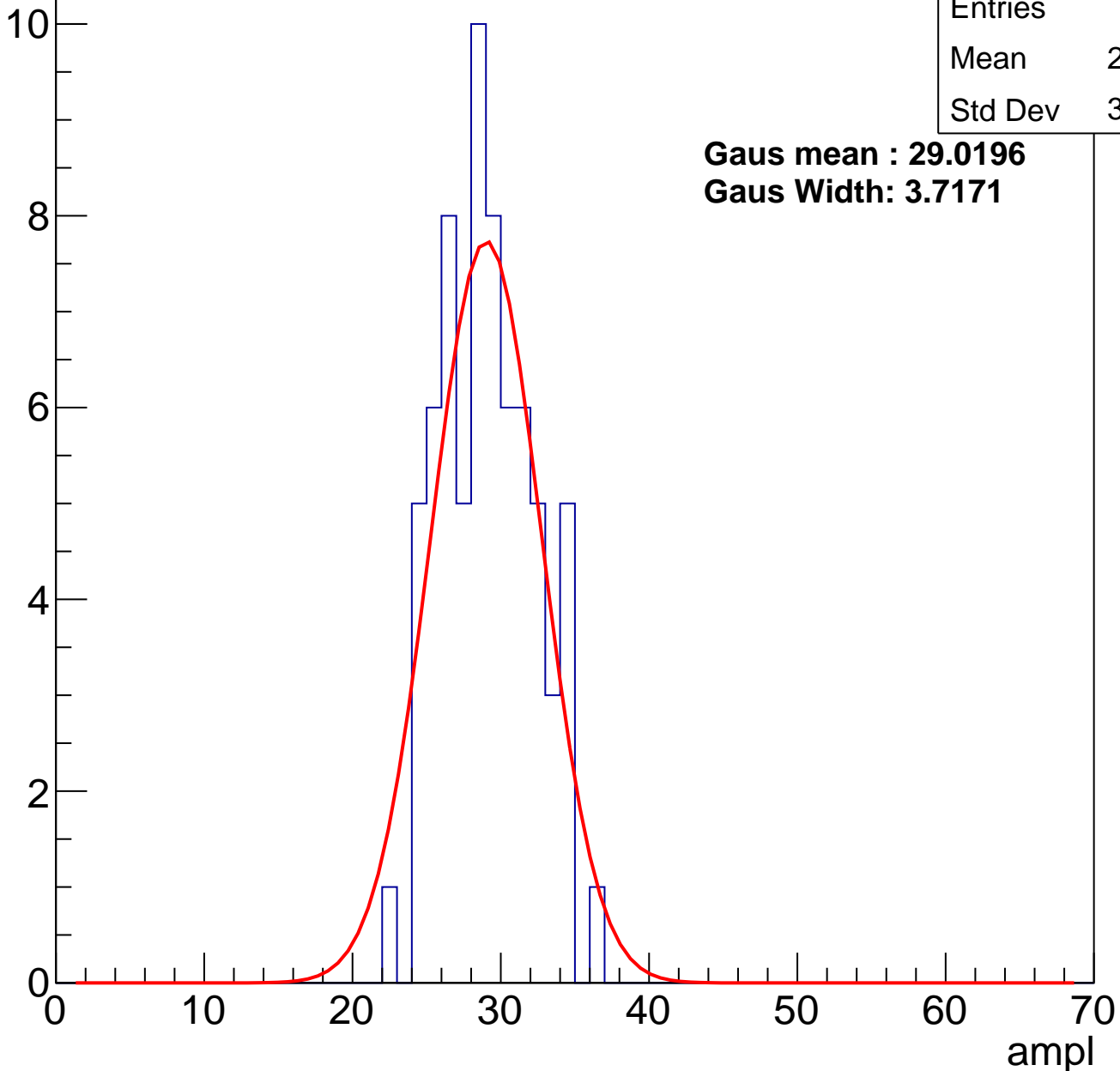
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	28.67
Std Dev	3.096

**Gaus mean : 29.0196**

**Gaus Width: 3.7171**

Entry



# B1L002S, U26-ch75, adc1

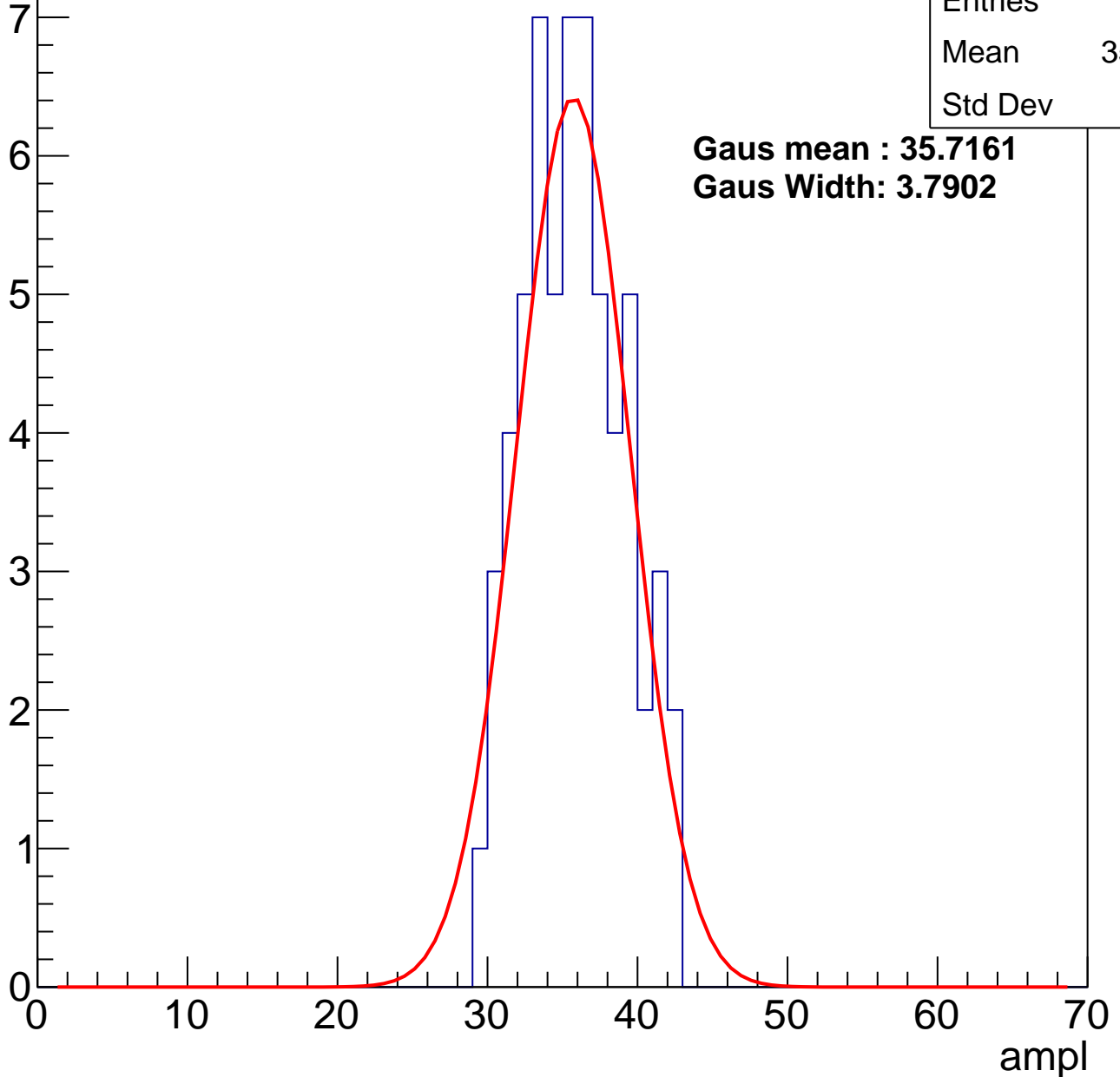
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.33
Std Dev	3.28

**Gaus mean : 35.7161**

**Gaus Width: 3.7902**



# B1L002S, U26-ch75, adc2

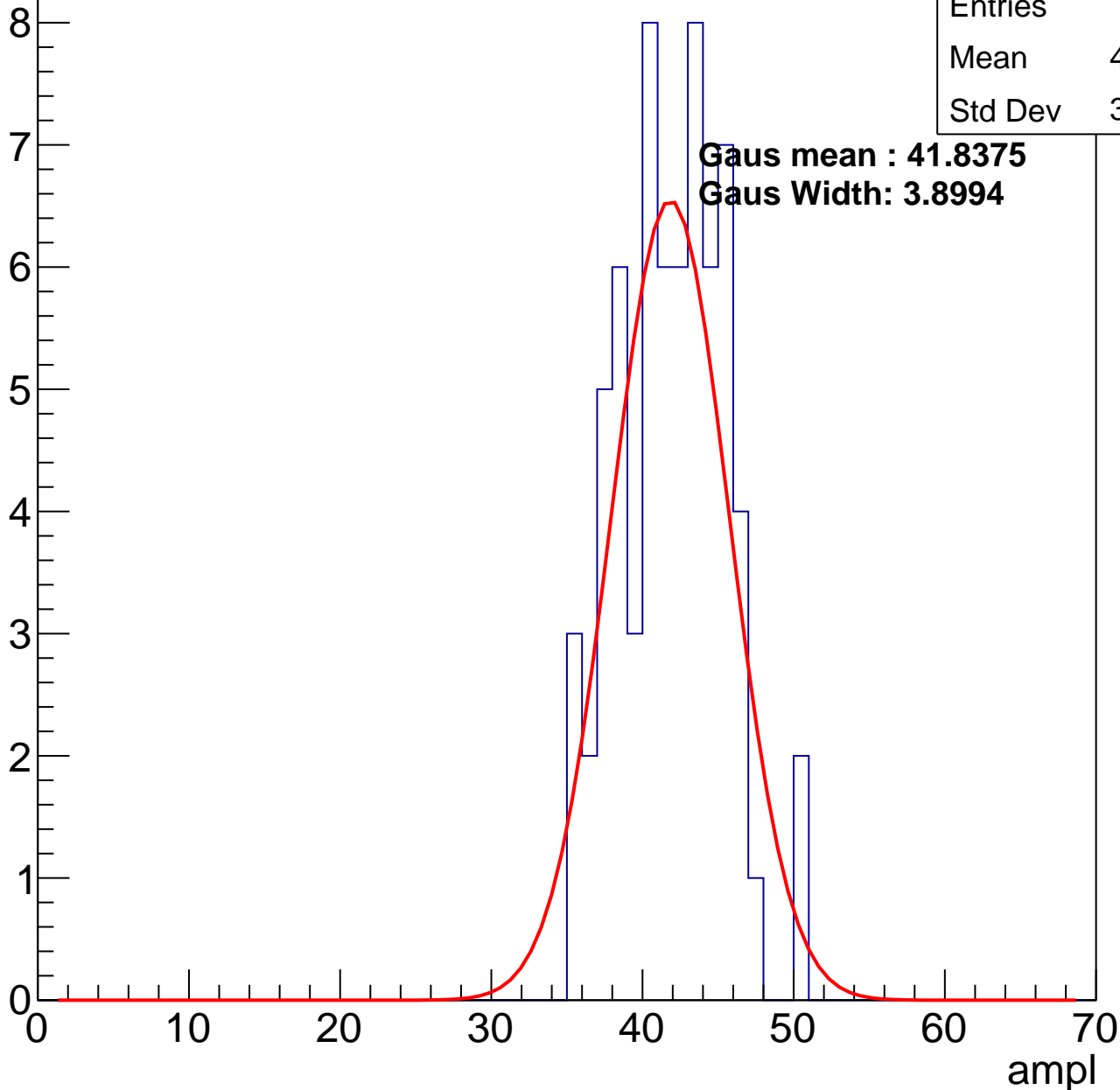
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	41.48
Std Dev	3.453

**Gaus mean : 41.8375**

**Gaus Width: 3.8994**



# B1L002S, U26-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

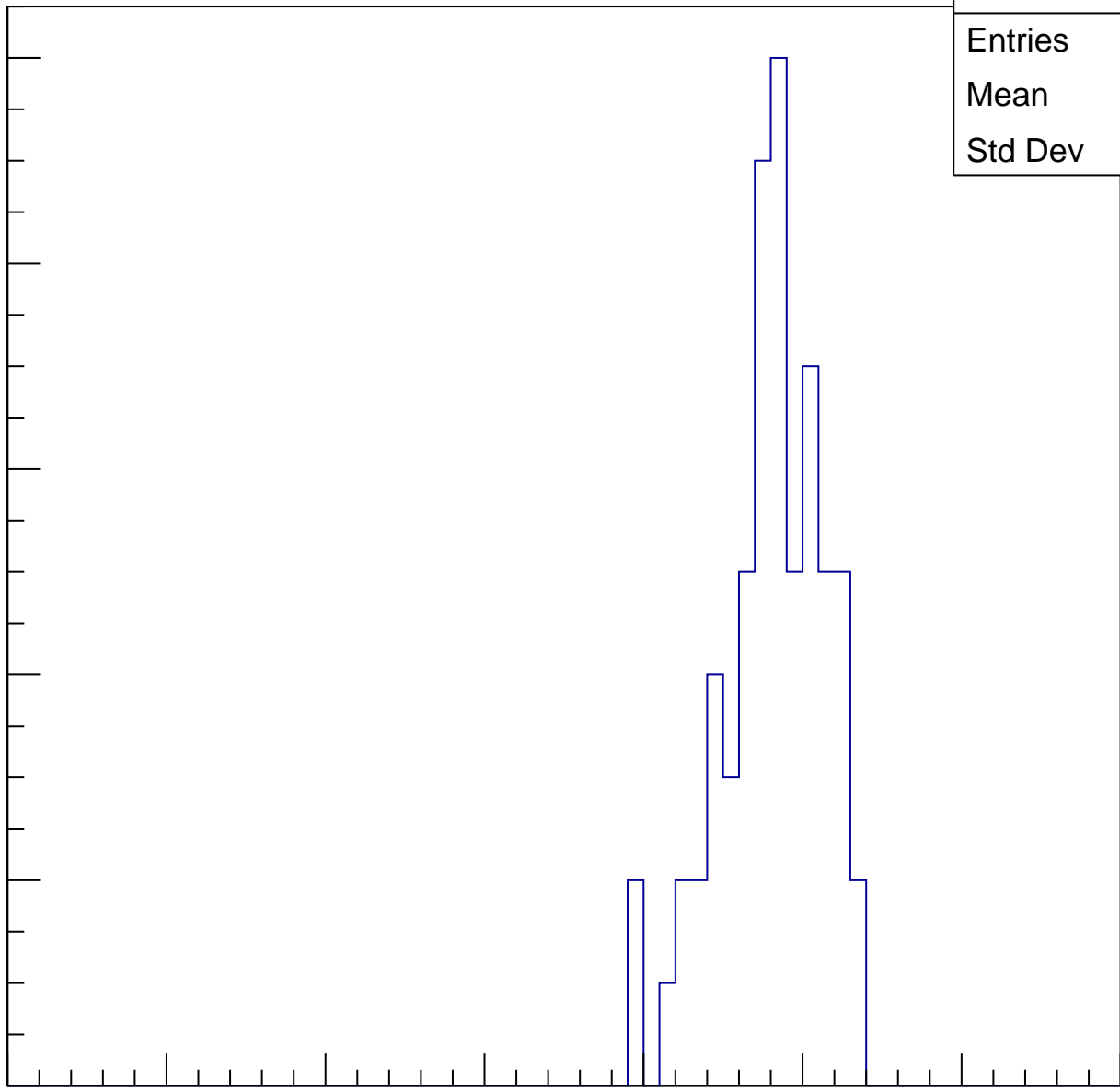
Entries	62
Mean	47.56
Std Dev	3.236

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

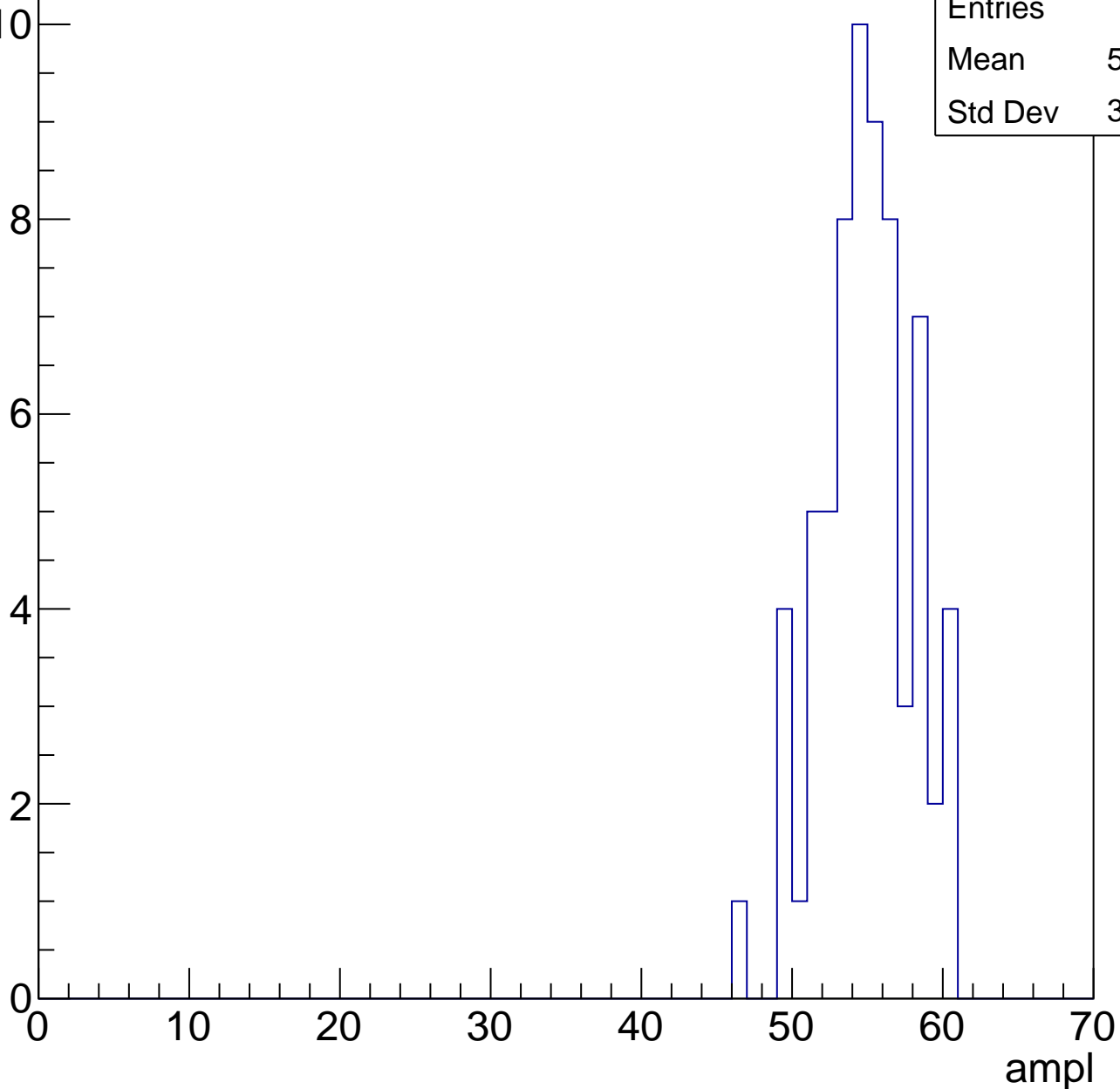


# B1L002S, U26-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

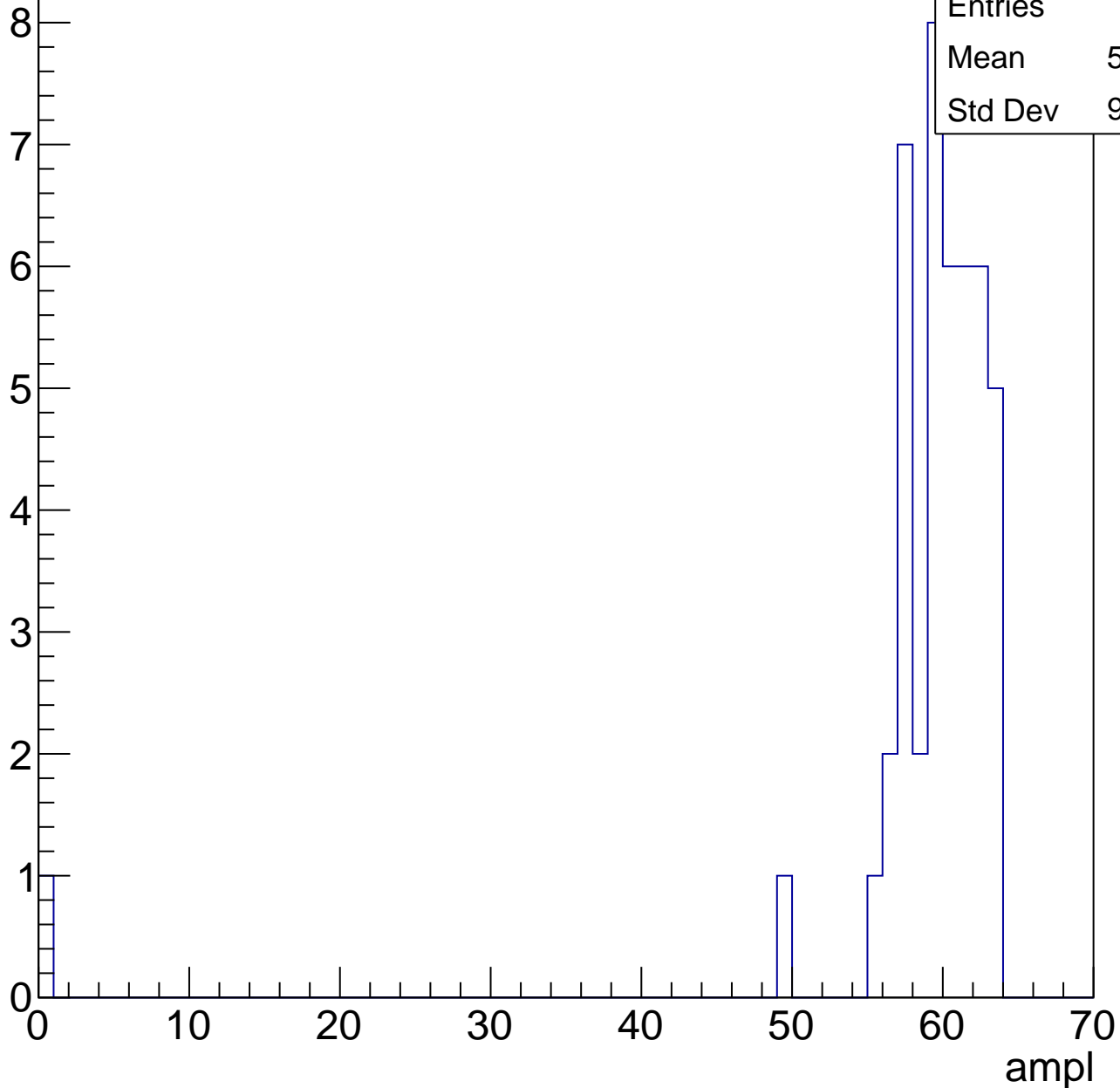
Entries	67
Mean	54.46
Std Dev	3.024



# B1L002S, U26-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

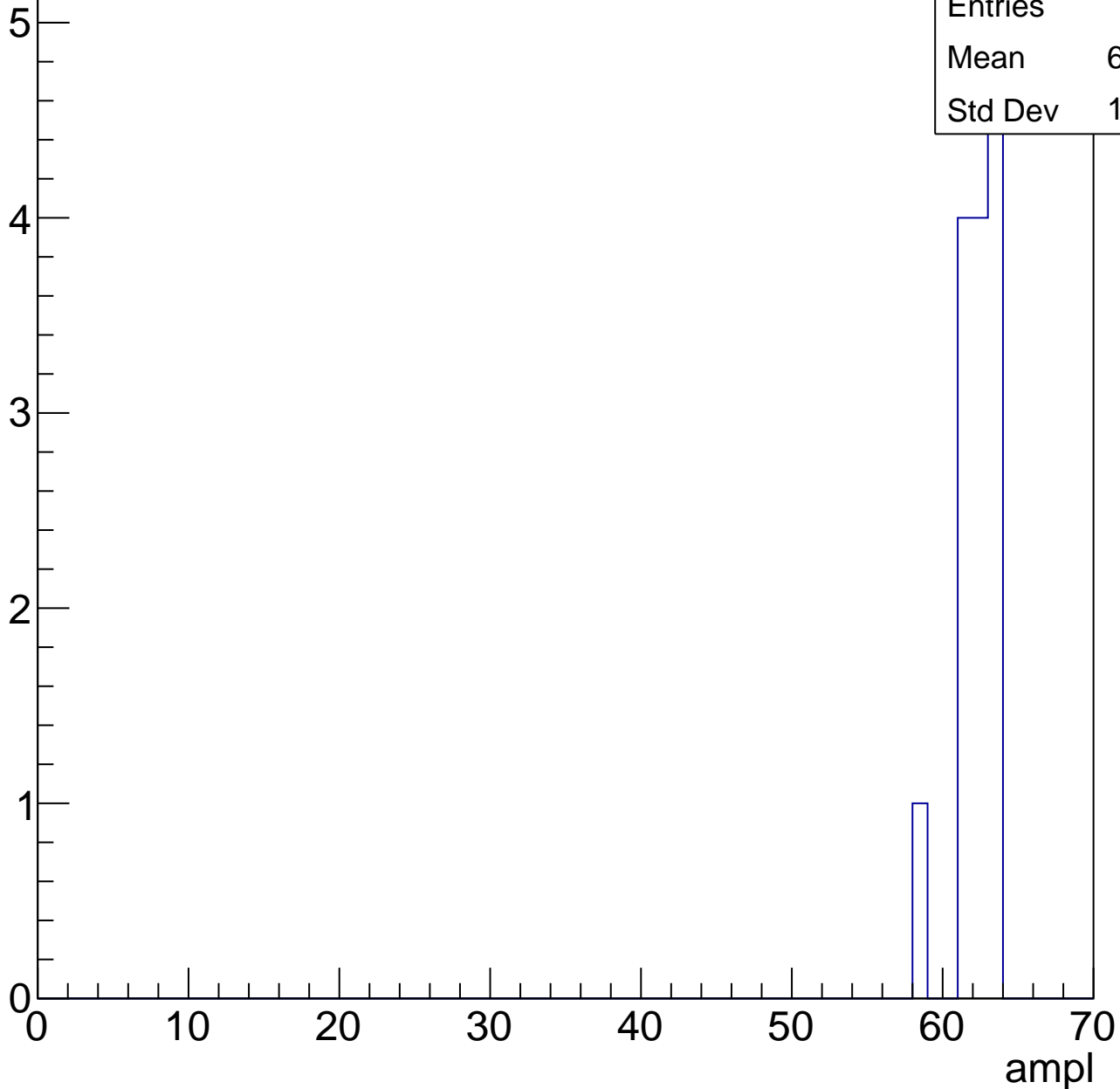


# B1L002S, U26-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.79
Std Dev	1.319





# B1L002S, U26-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B1L002S, U26-ch76, adc0

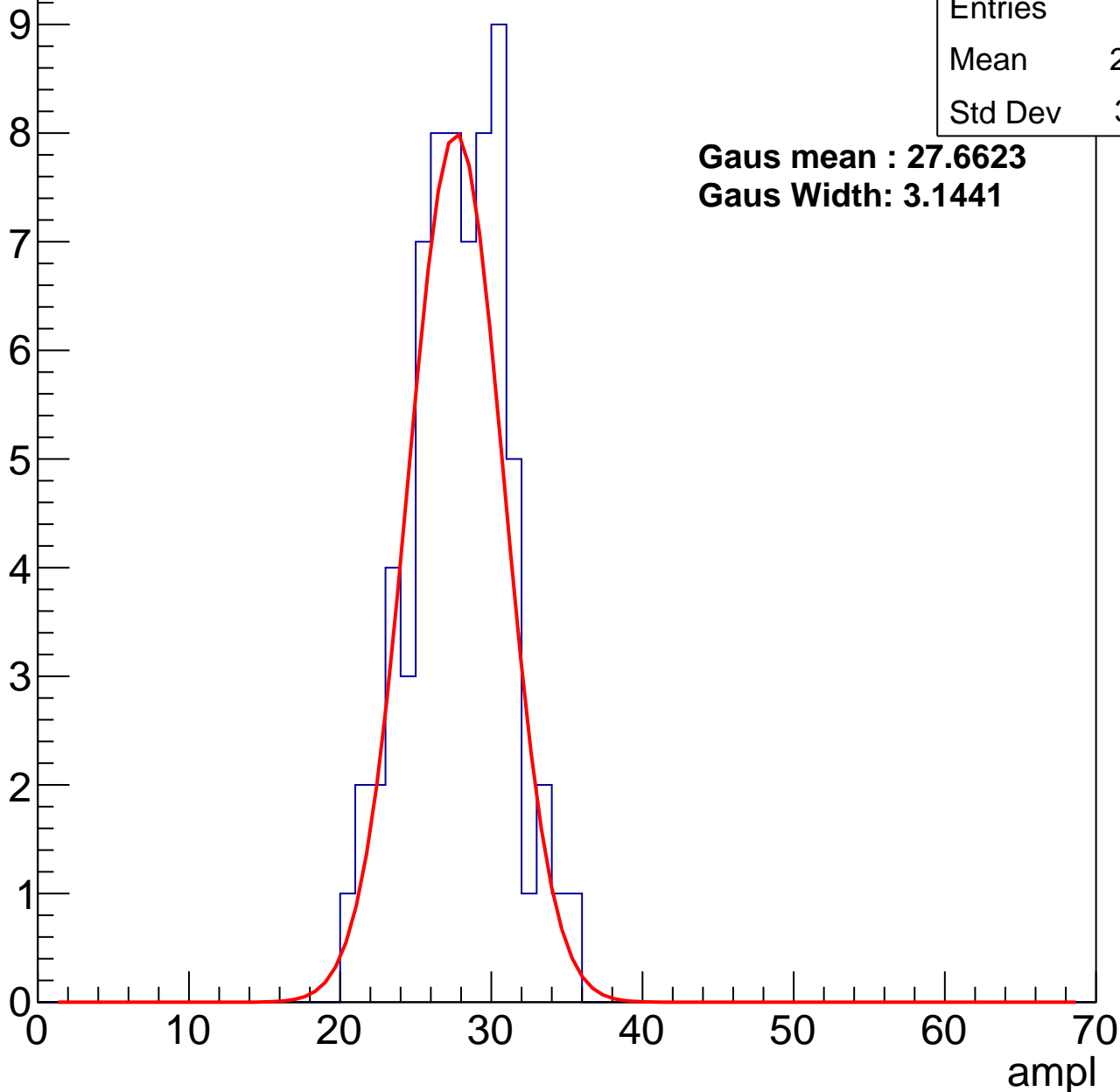
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.38
Std Dev	3.181

**Gaus mean : 27.6623**

**Gaus Width: 3.1441**



# B1L002S, U26-ch76, adc1

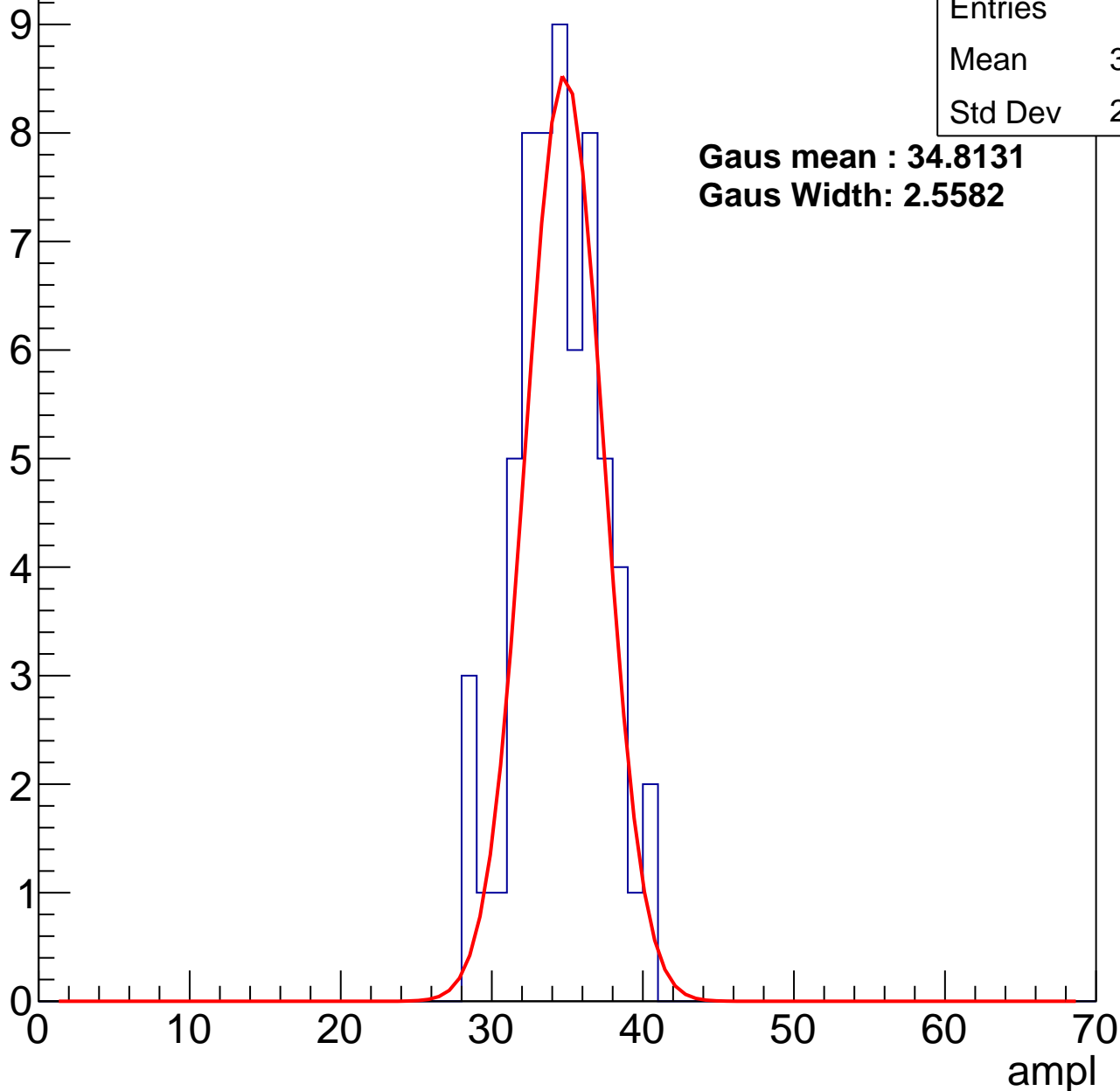
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.07
Std Dev	2.799

**Gaus mean : 34.8131**

**Gaus Width: 2.5582**



# B1L002S, U26-ch76, adc2

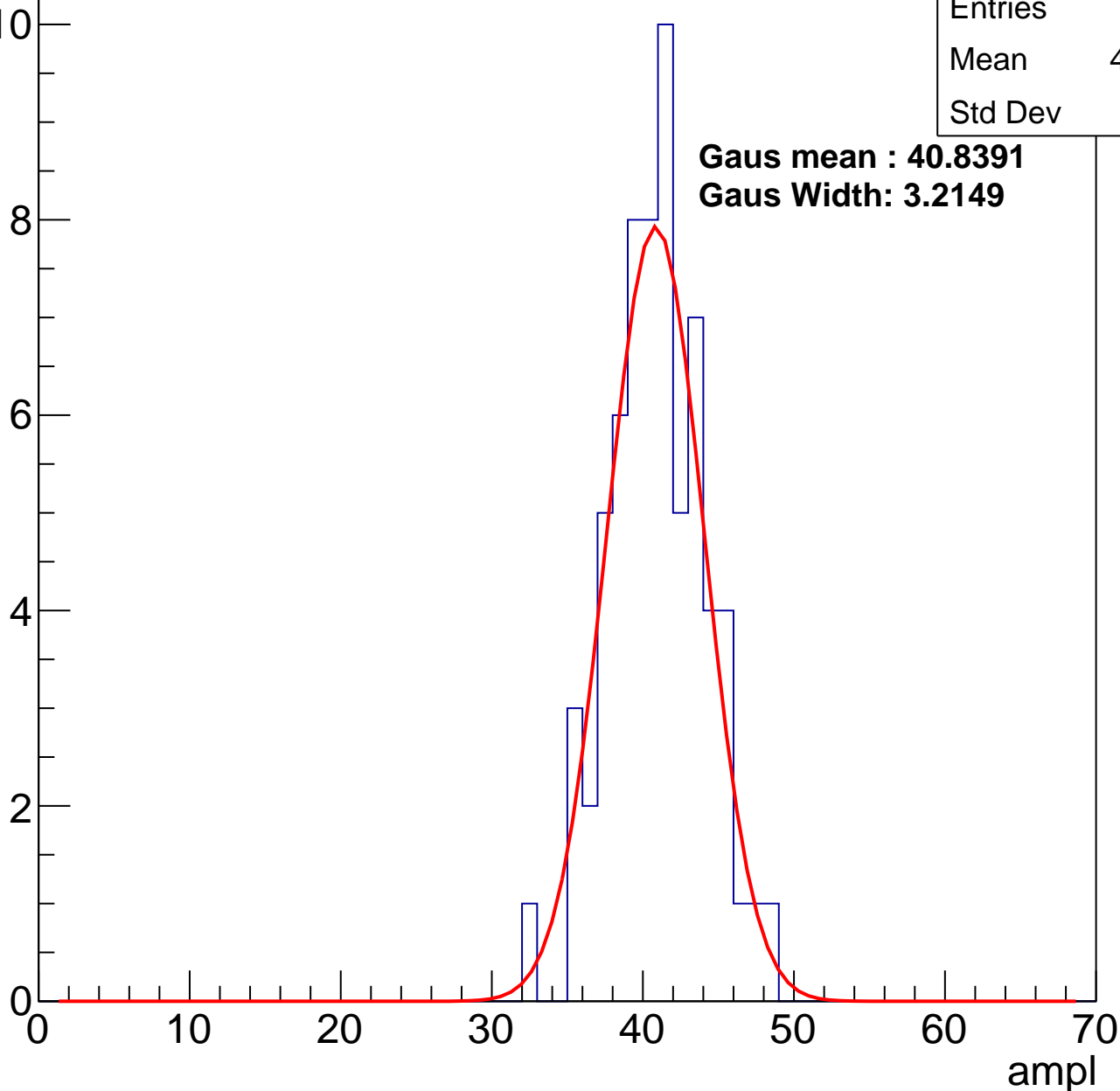
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.48
Std Dev	3.11

**Gaus mean : 40.8391**

**Gaus Width: 3.2149**

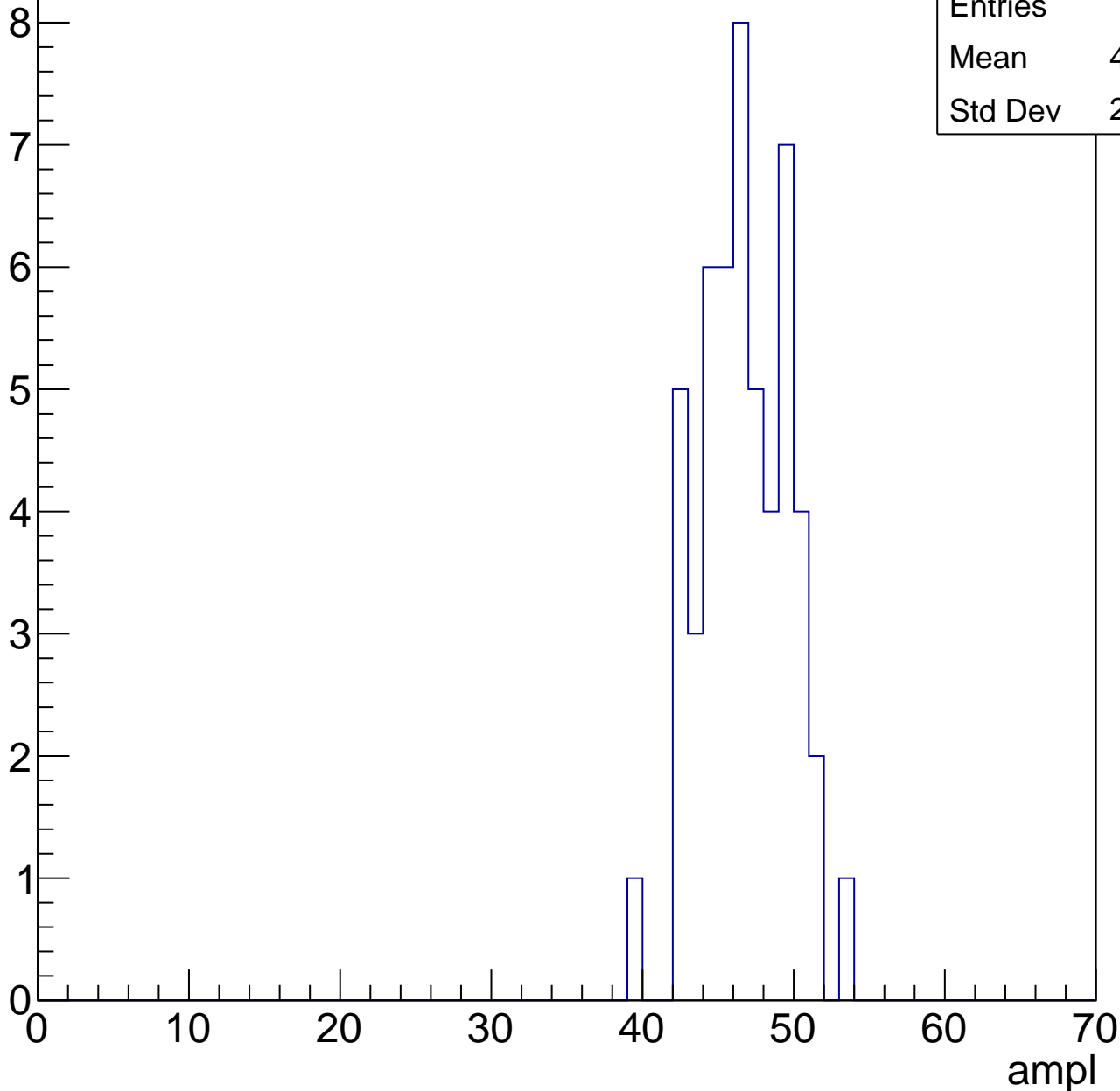


# B1L002S, U26-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	46.25
Std Dev	2.875

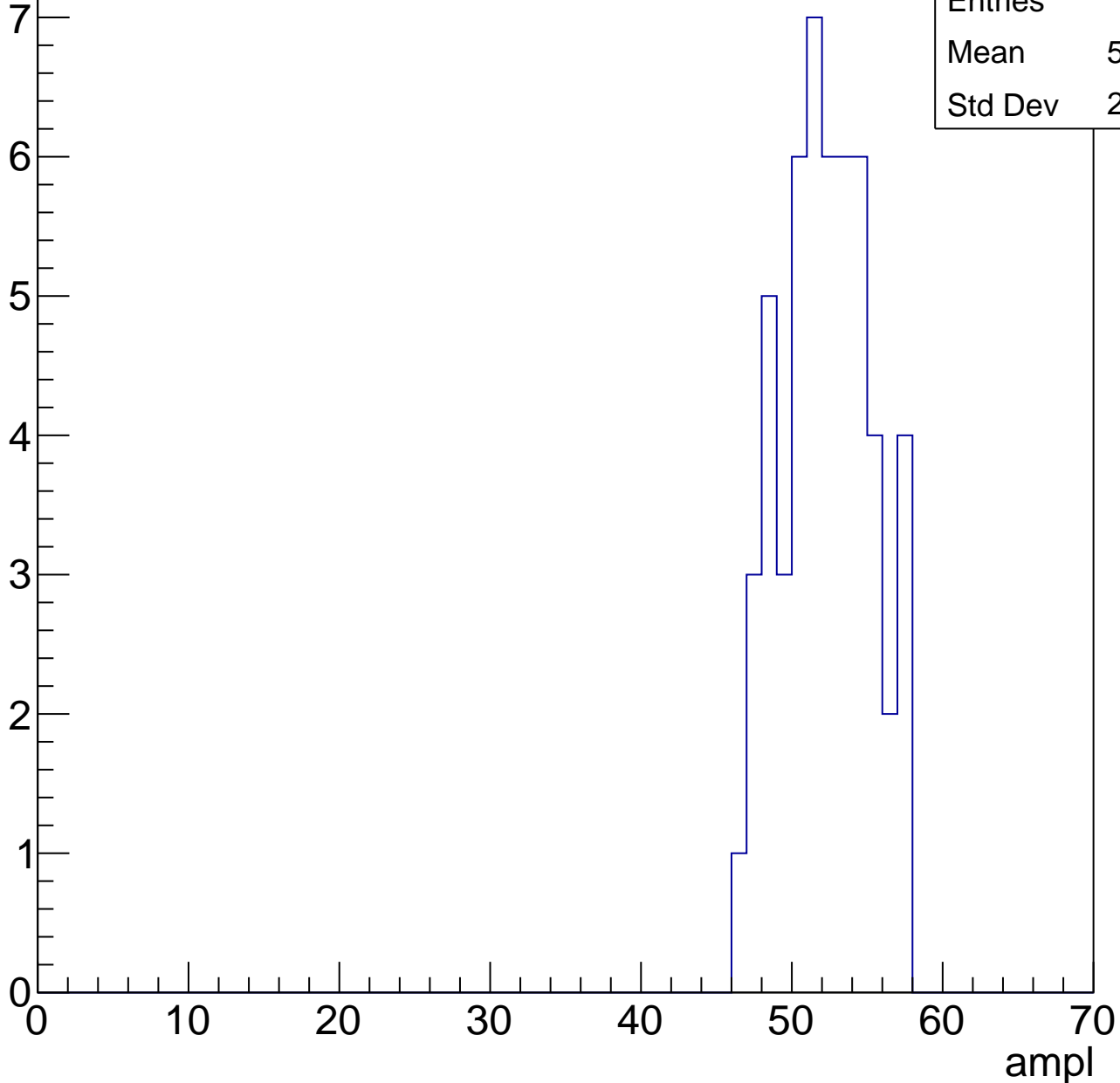


# B1L002S, U26-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	51.79
Std Dev	2.897

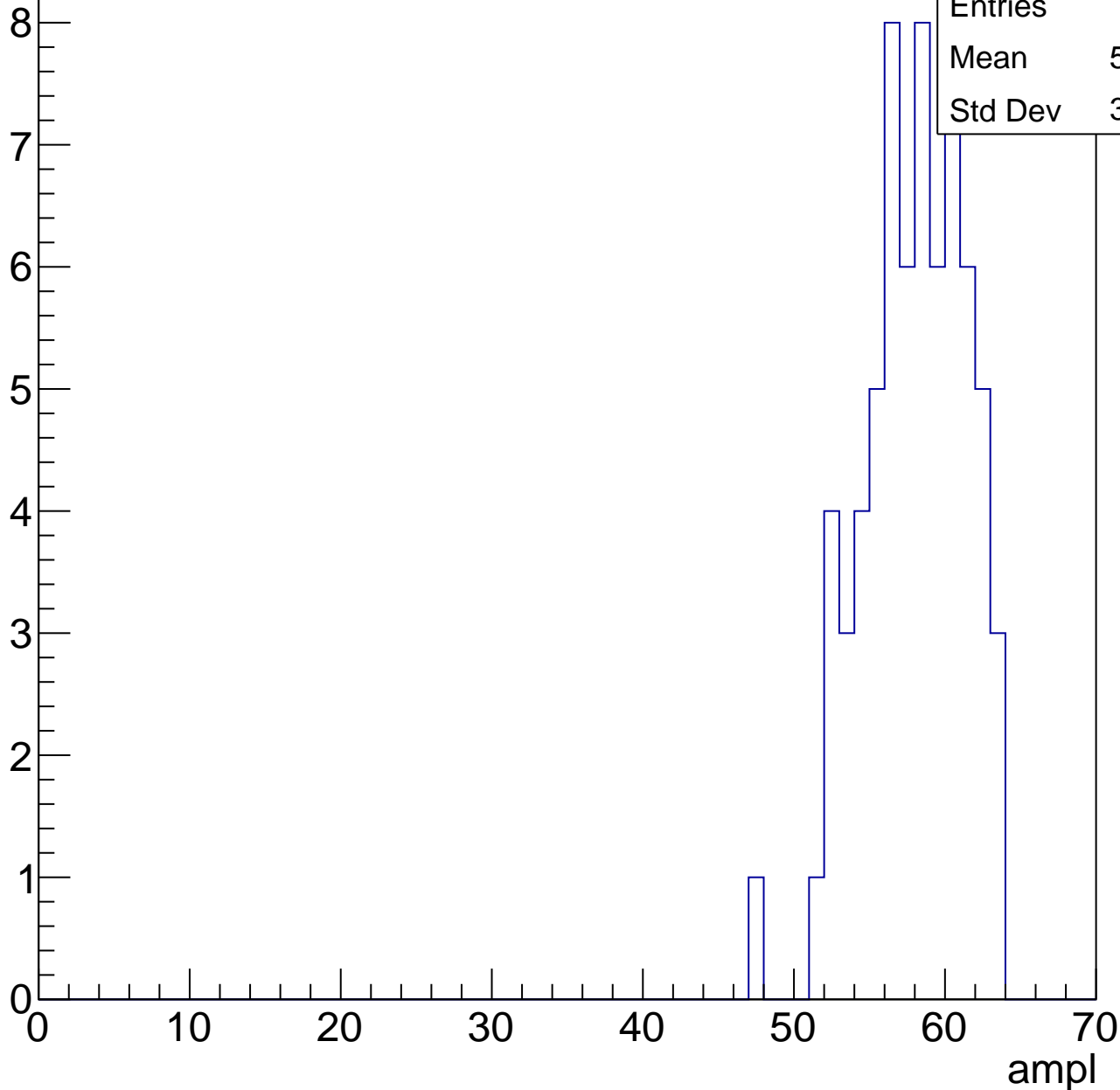


# B1L002S, U26-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	57.49
Std Dev	3.354

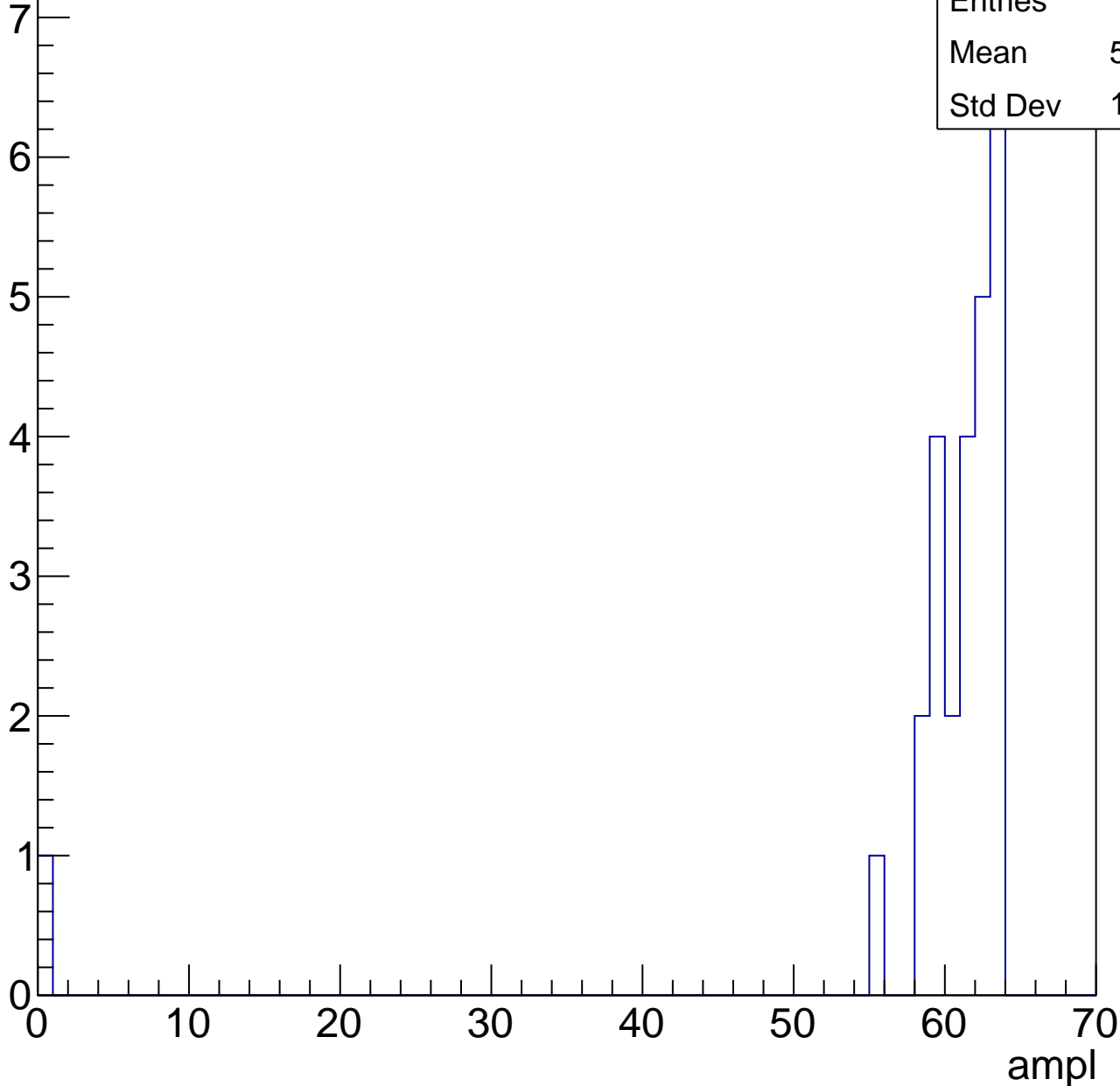


# B1L002S, U26-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.54
Std Dev	11.88

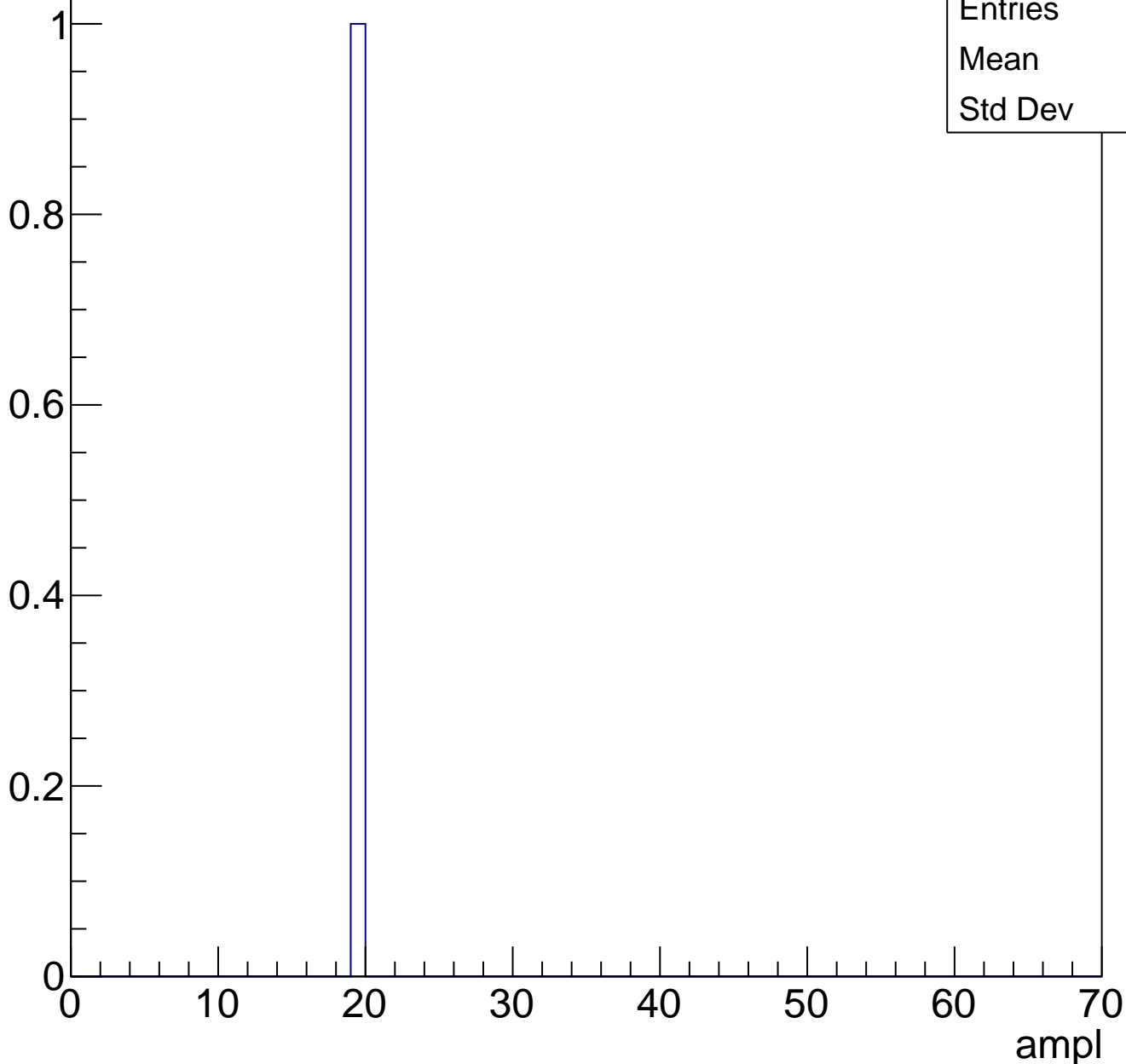




# B1L002S, U26-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch77, adc0

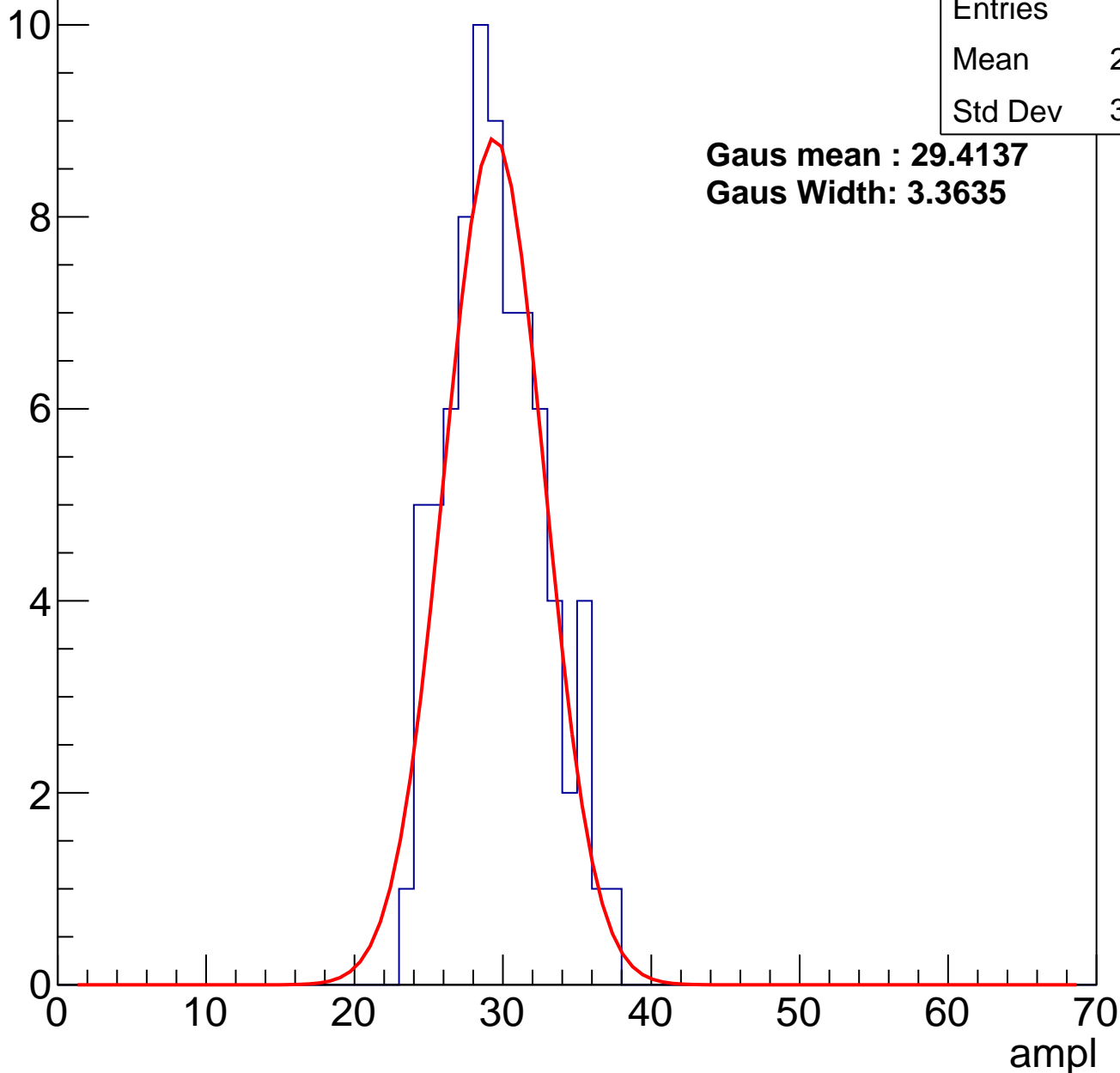
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	29.12
Std Dev	3.236

**Gaus mean : 29.4137**

**Gaus Width: 3.3635**

Entry



# B1L002S, U26-ch77, adc1

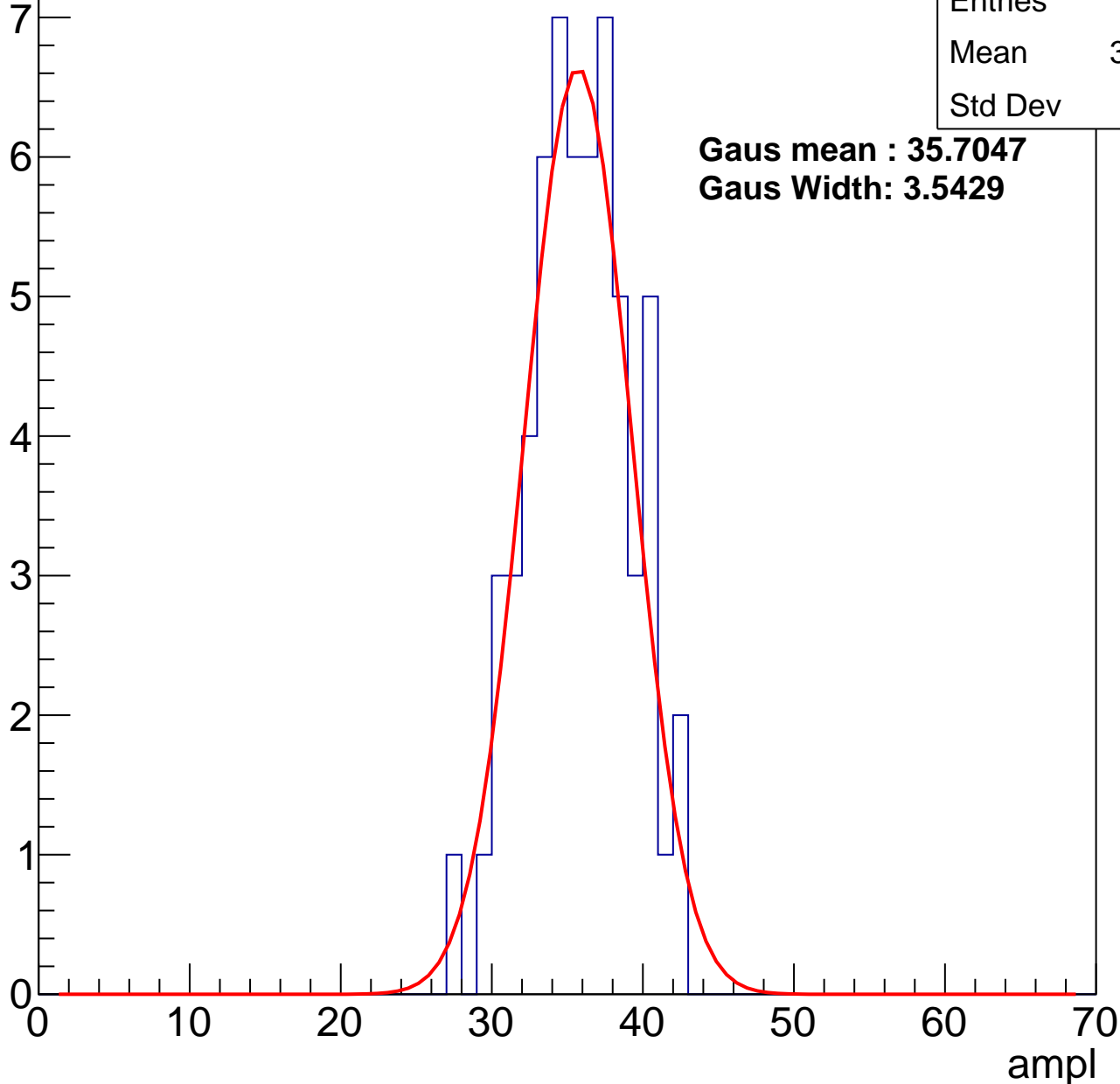
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	35.33
Std Dev	3.34

**Gaus mean : 35.7047**

**Gaus Width: 3.5429**



# B1L002S, U26-ch77, adc2

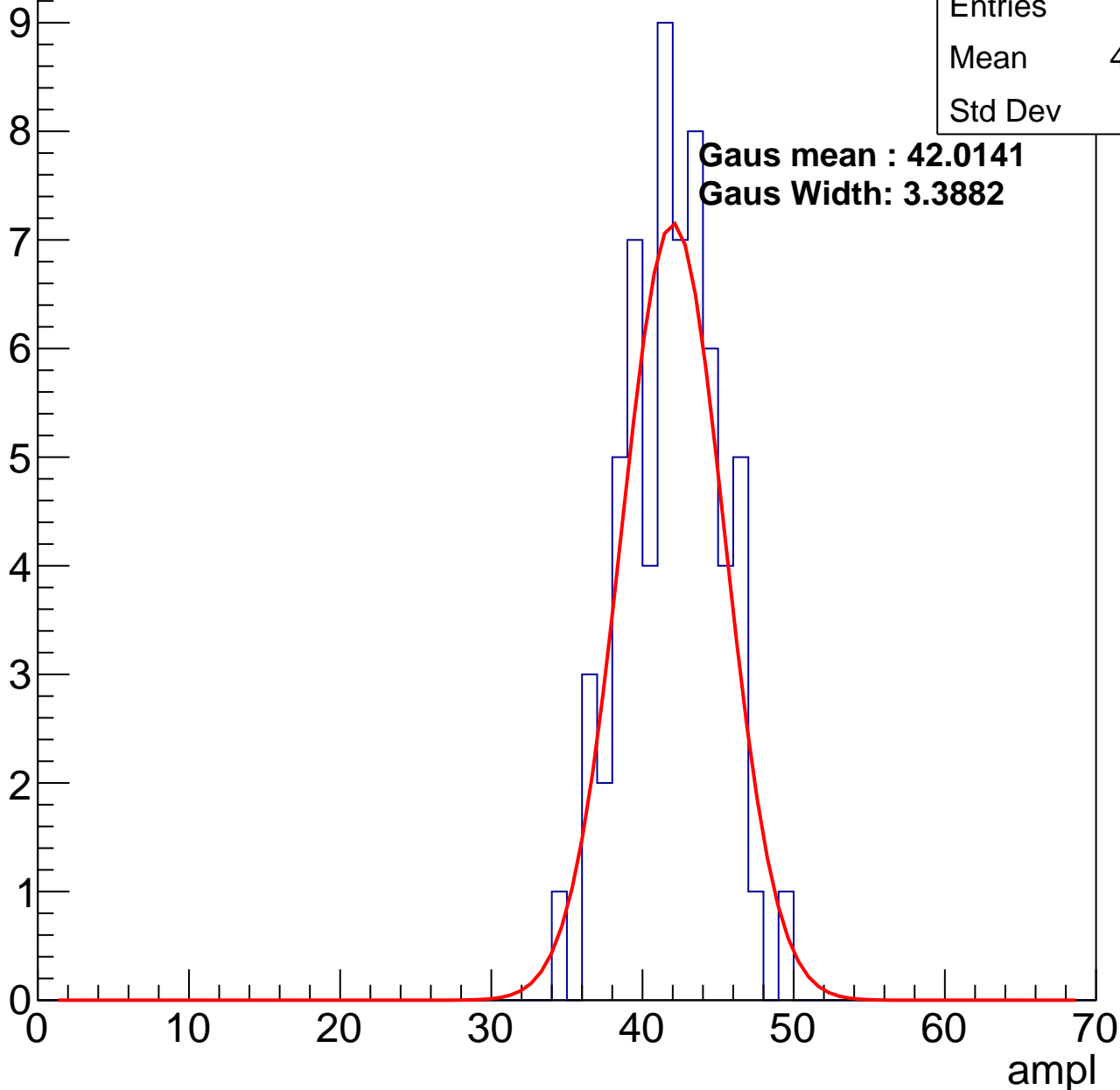
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.52
Std Dev	3.08

**Gaus mean : 42.0141**

**Gaus Width: 3.3882**

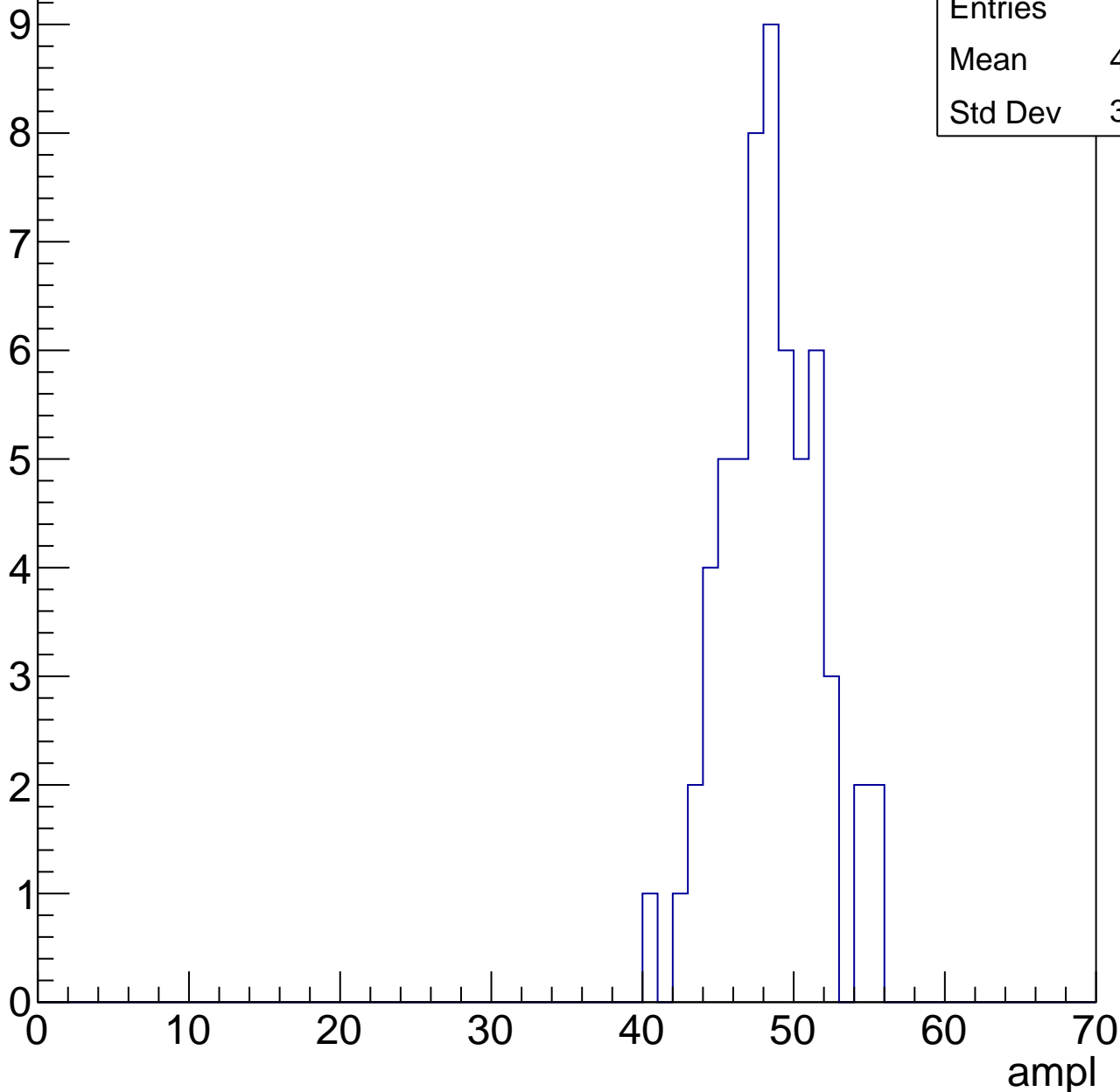


# B1L002S, U26-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.98
Std Dev	3.149

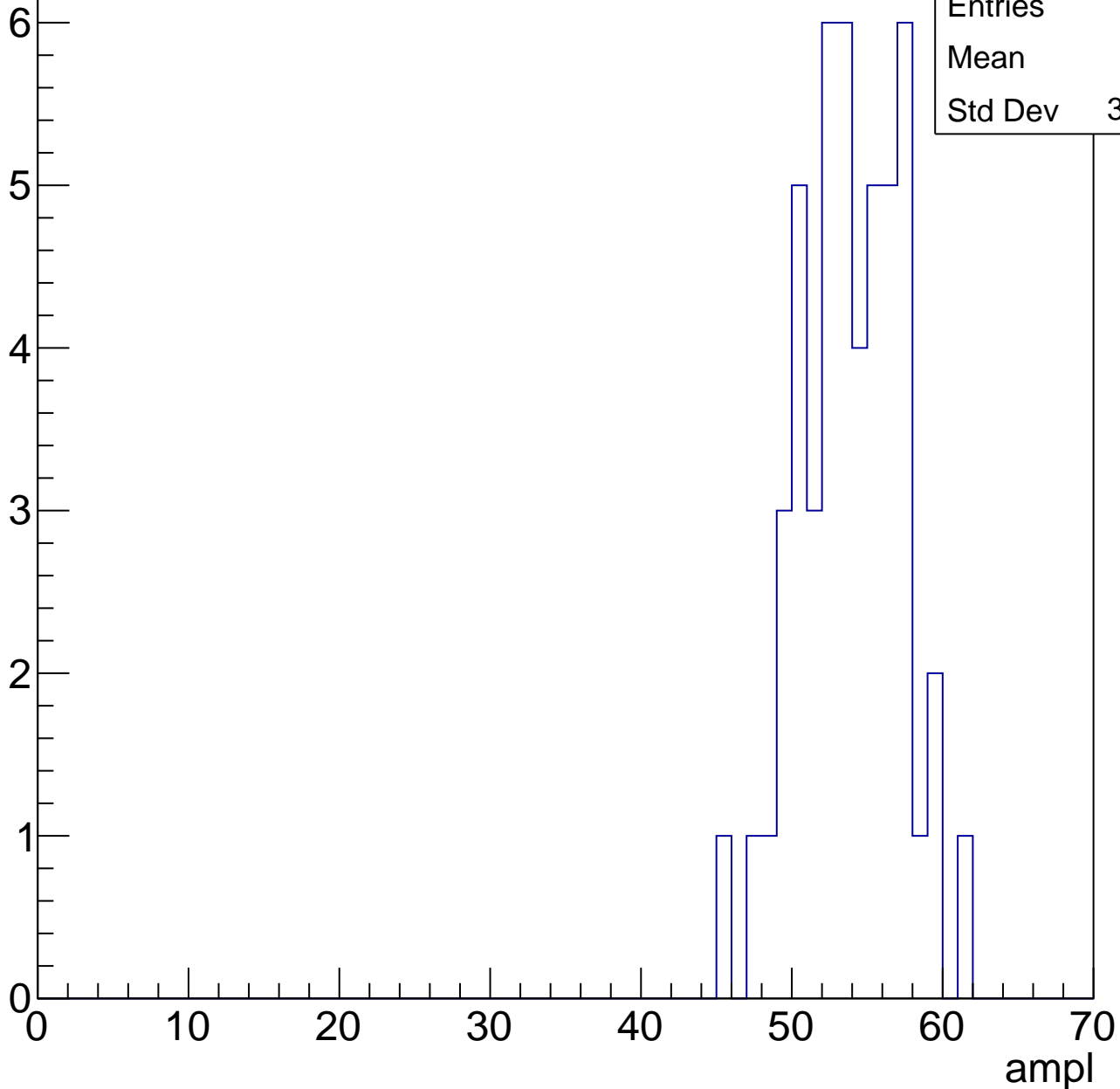


# B1L002S, U26-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	53.4
Std Dev	3.329

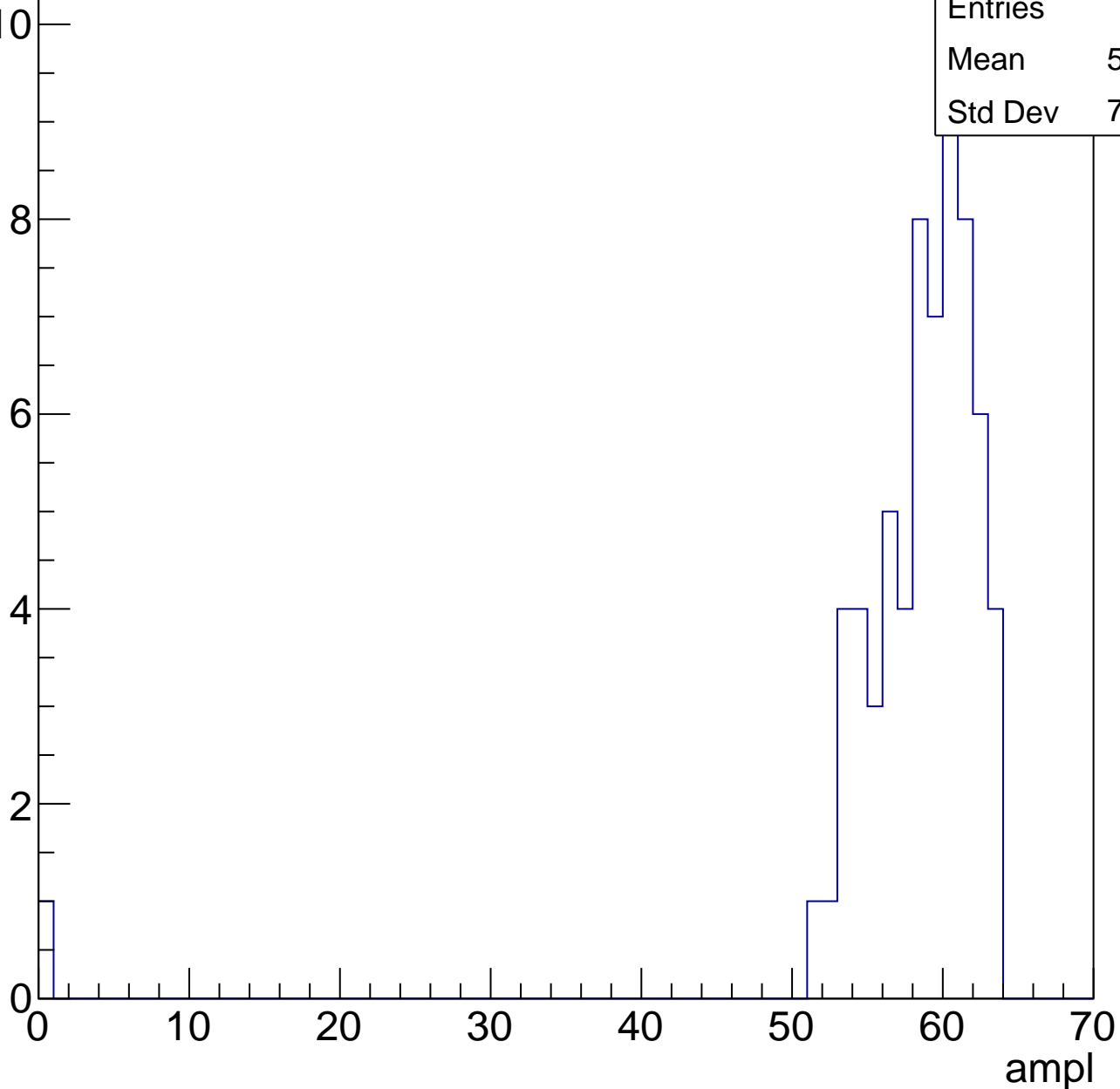


# B1L002S, U26-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.47
Std Dev	7.746

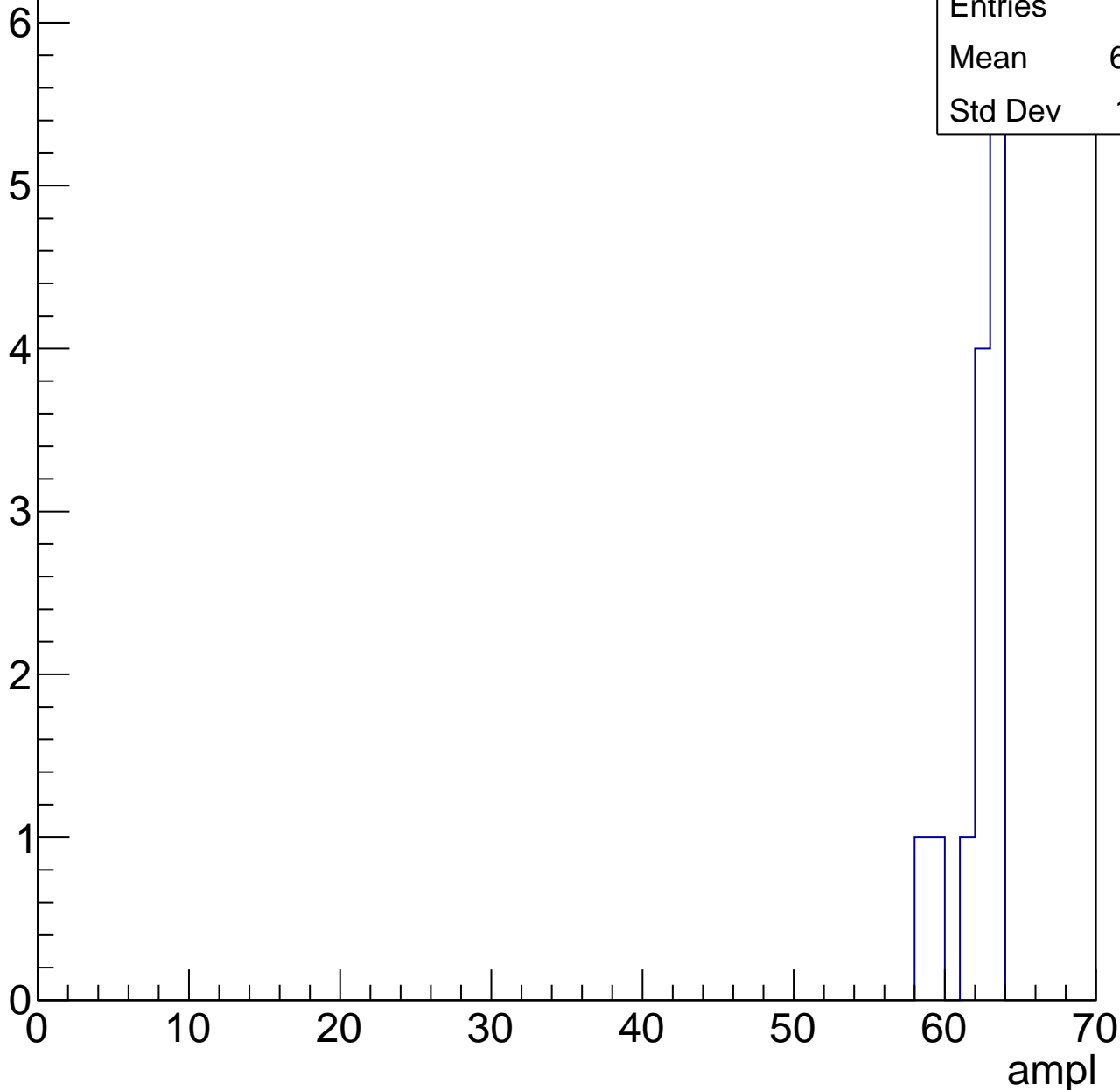


# B1L002S, U26-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.85
Std Dev	1.561





# B1L002S, U26-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	20
Std Dev	0

ampl

# B1L002S, U26-ch78, adc0

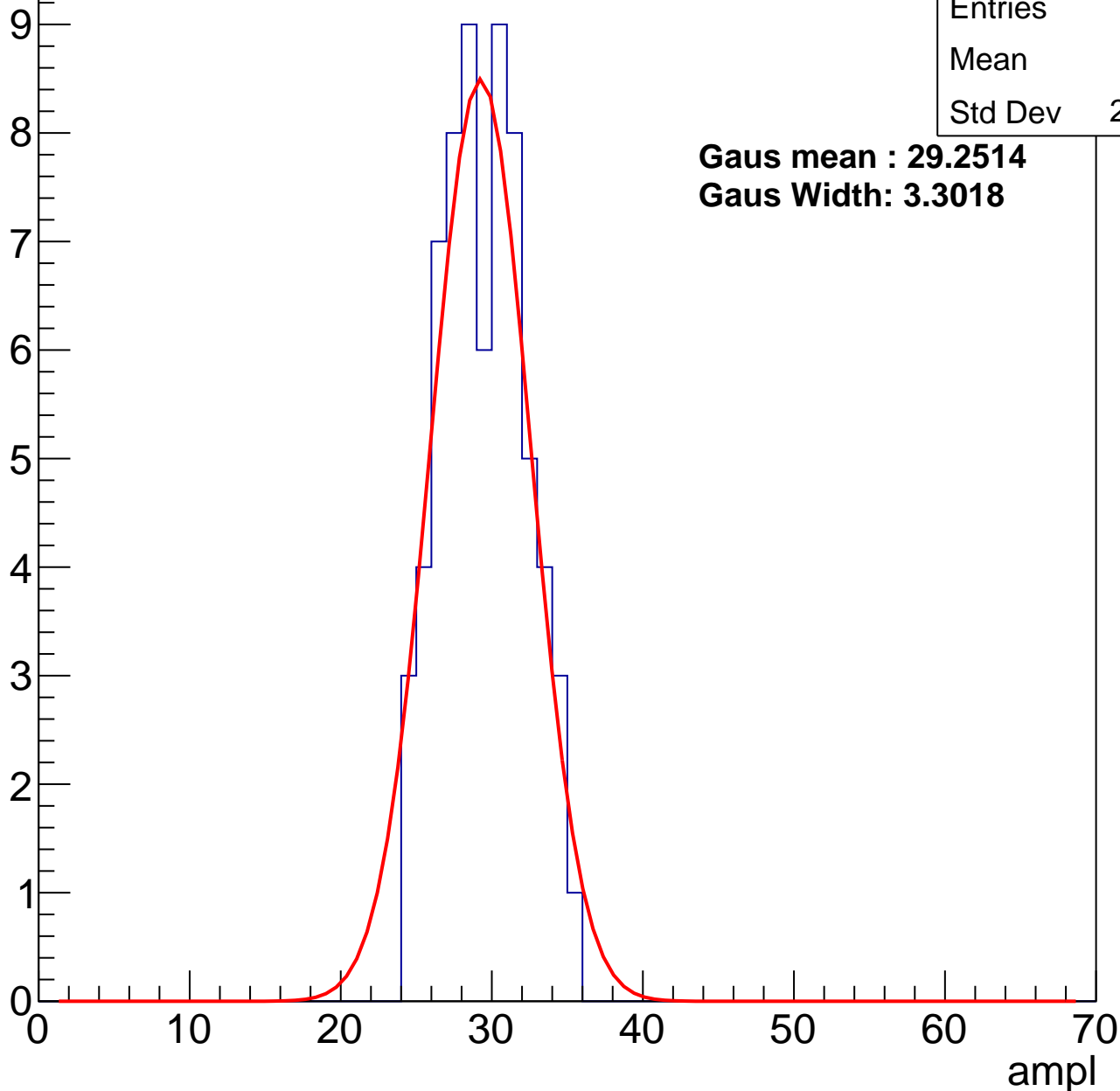
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29
Std Dev	2.743

**Gaus mean : 29.2514**

**Gaus Width: 3.3018**



# B1L002S, U26-ch78, adc1

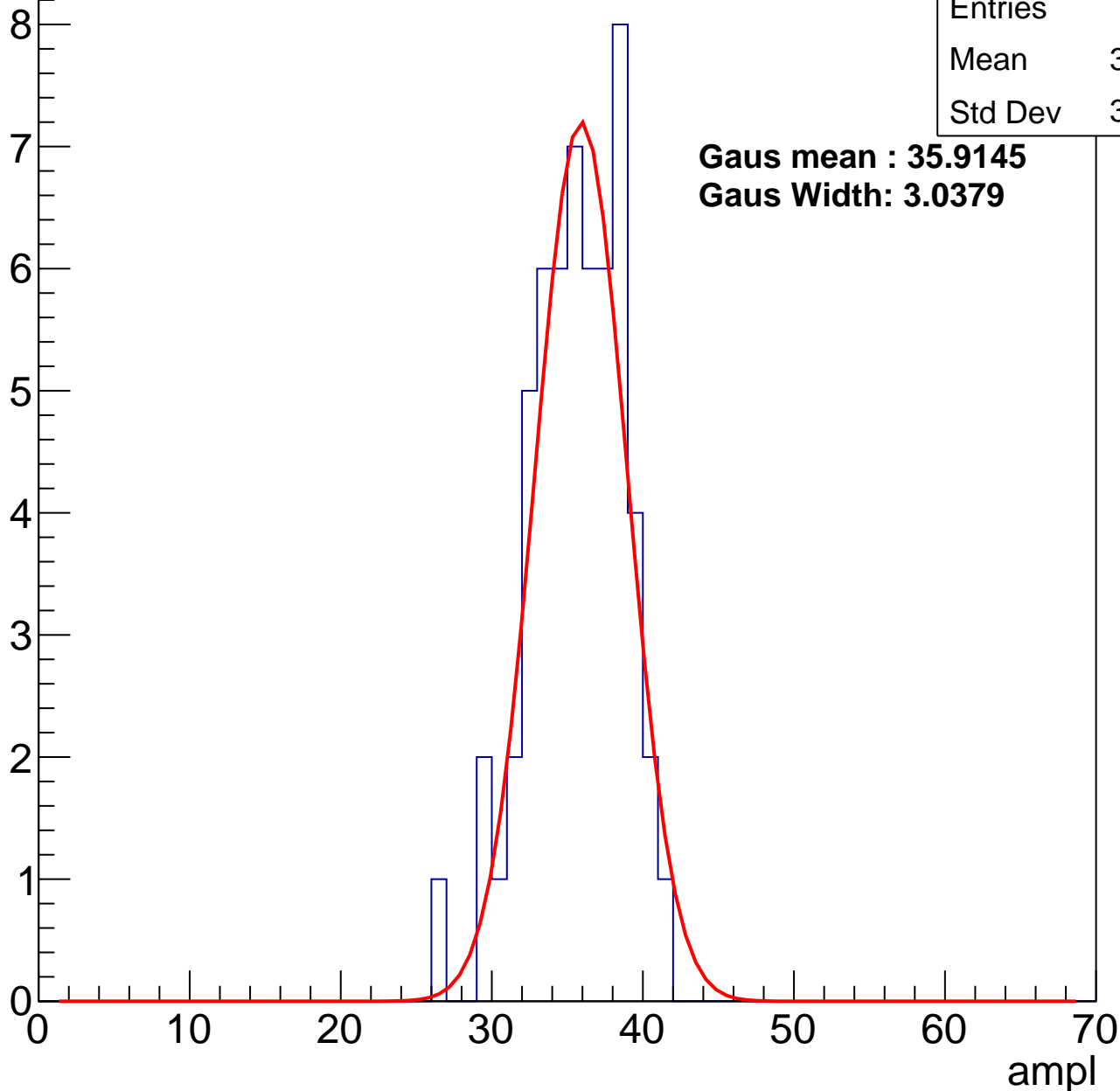
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	35.12
Std Dev	3.067

**Gaus mean : 35.9145**

**Gaus Width: 3.0379**



# B1L002S, U26-ch78, adc2

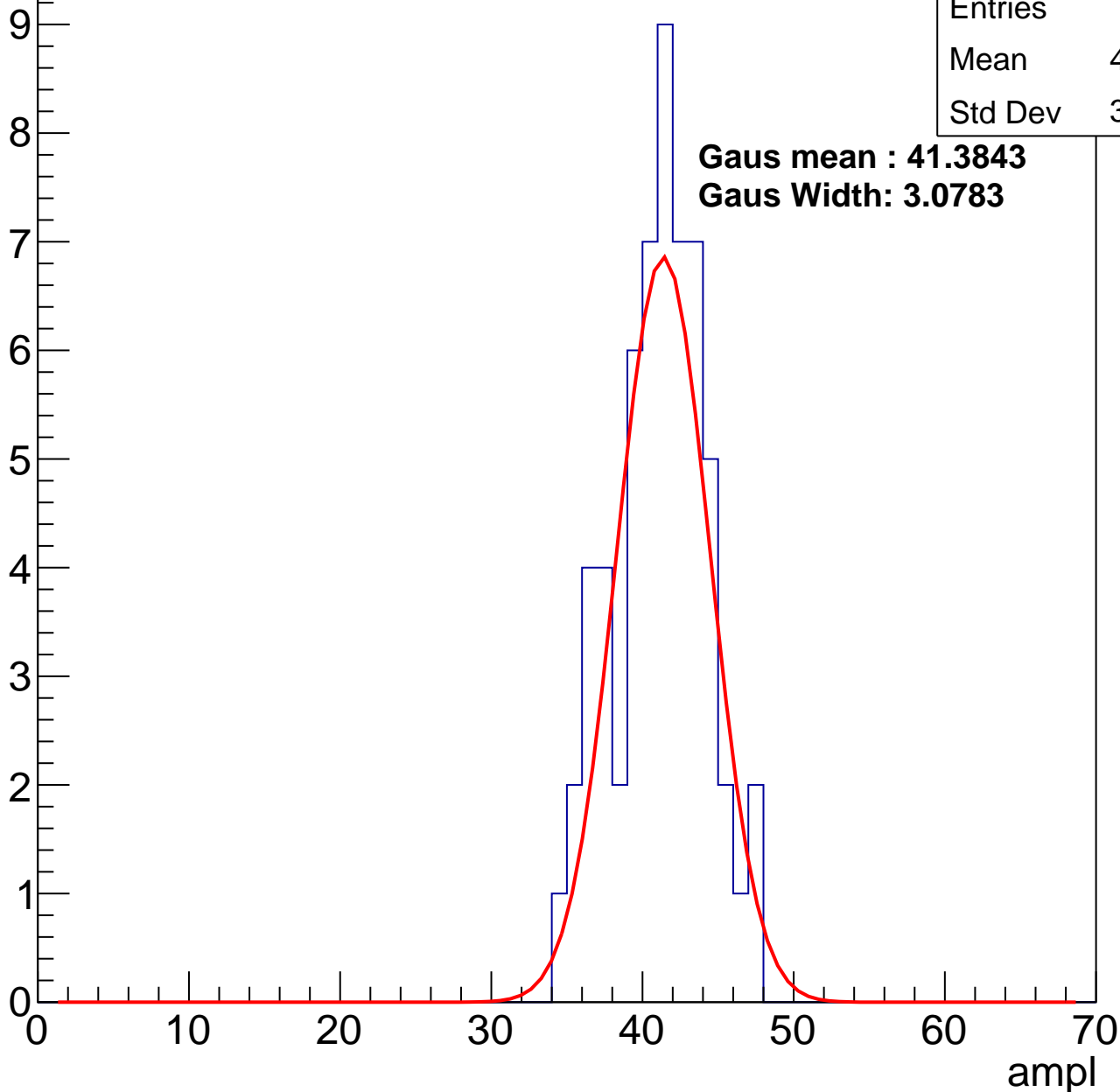
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.68
Std Dev	3.017

**Gaus mean : 41.3843**

**Gaus Width: 3.0783**

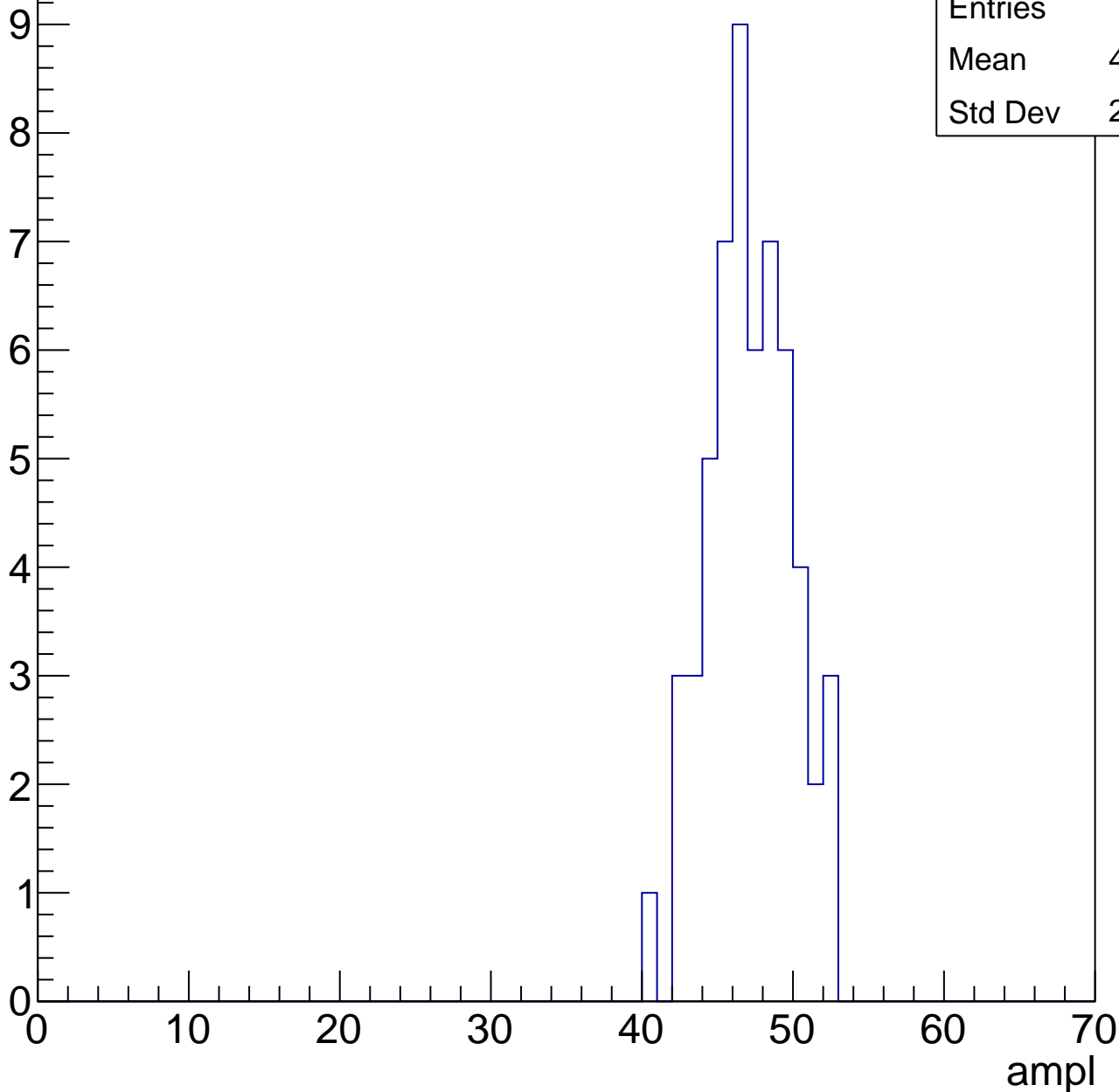


# B1L002S, U26-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

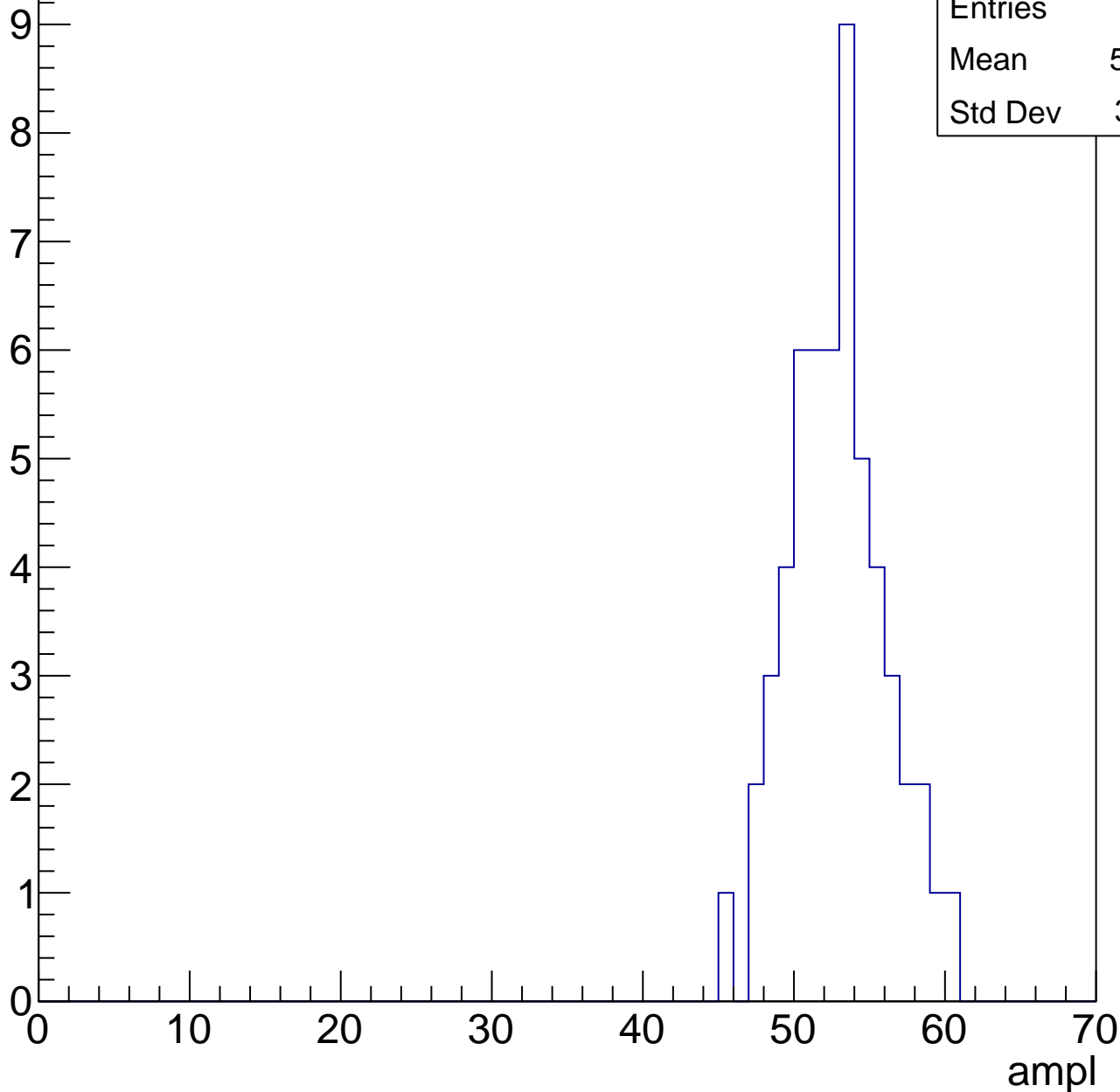
Entries	56
Mean	46.68
Std Dev	2.746



# B1L002S, U26-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



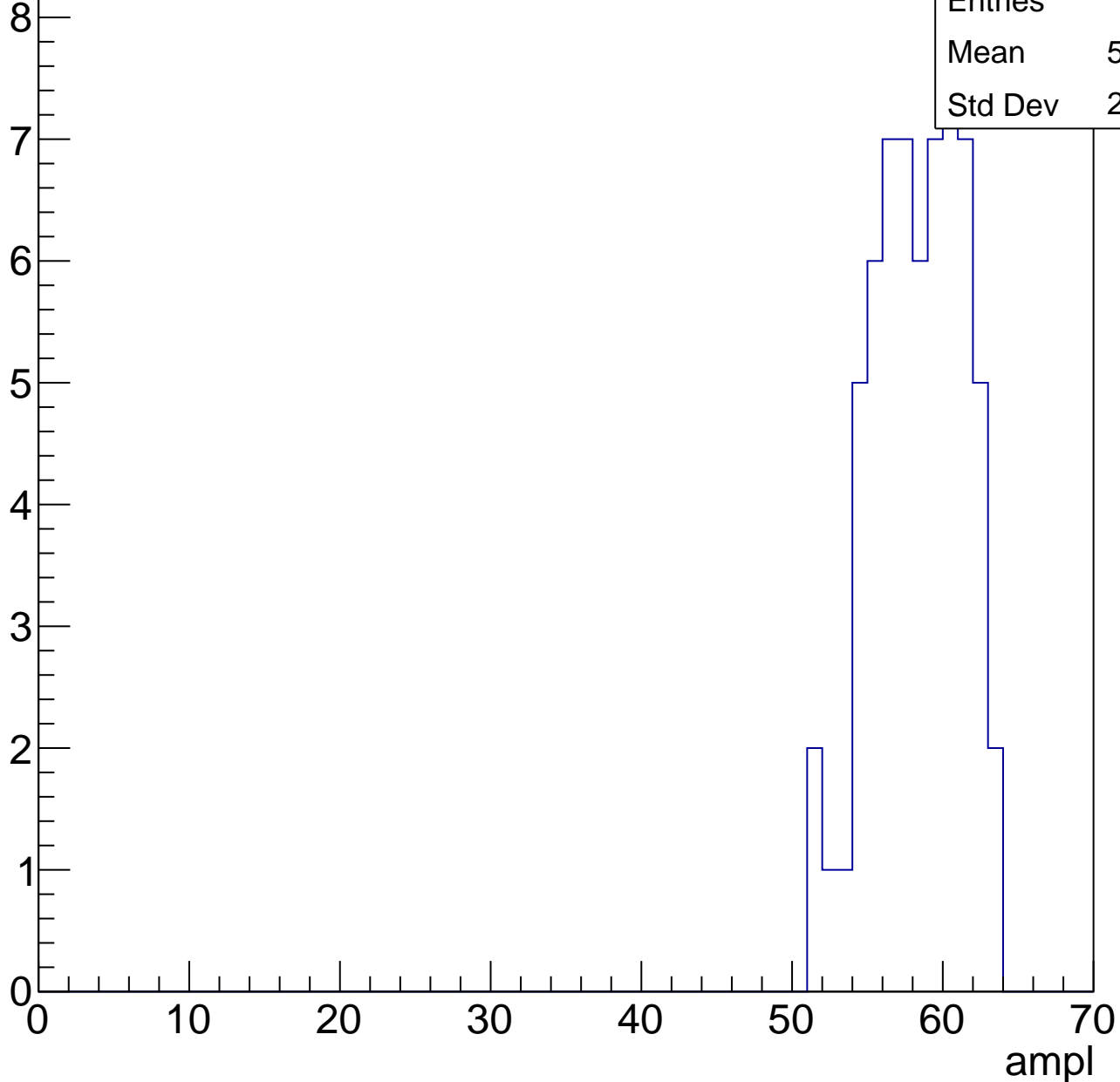
Entries	55
Mean	52.38
Std Dev	3.171

# B1L002S, U26-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.84
Std Dev	2.954

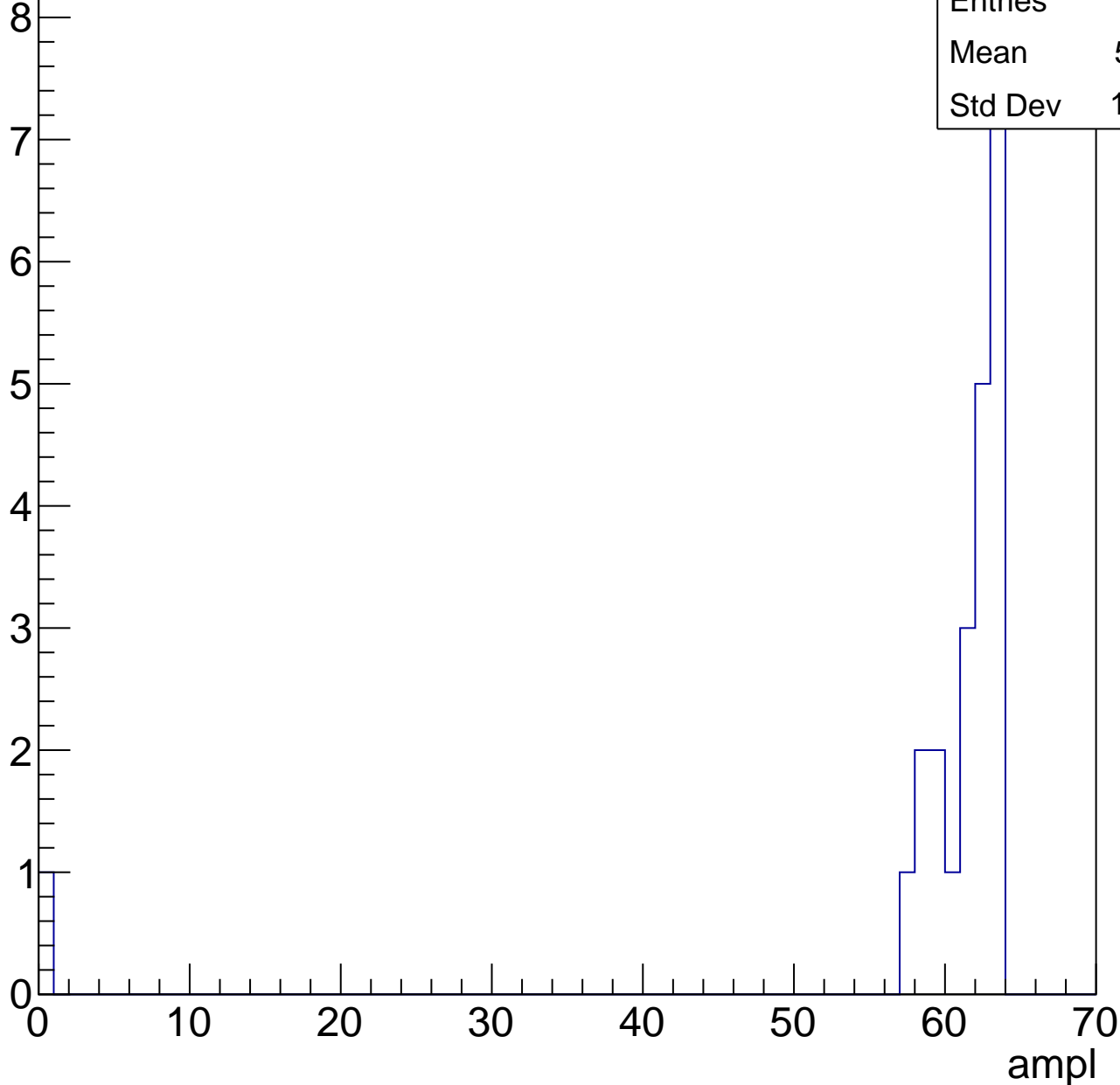


# B1L002S, U26-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.61
Std Dev	12.63





# B1L002S, U26-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B1L002S, U26-ch79, adc0

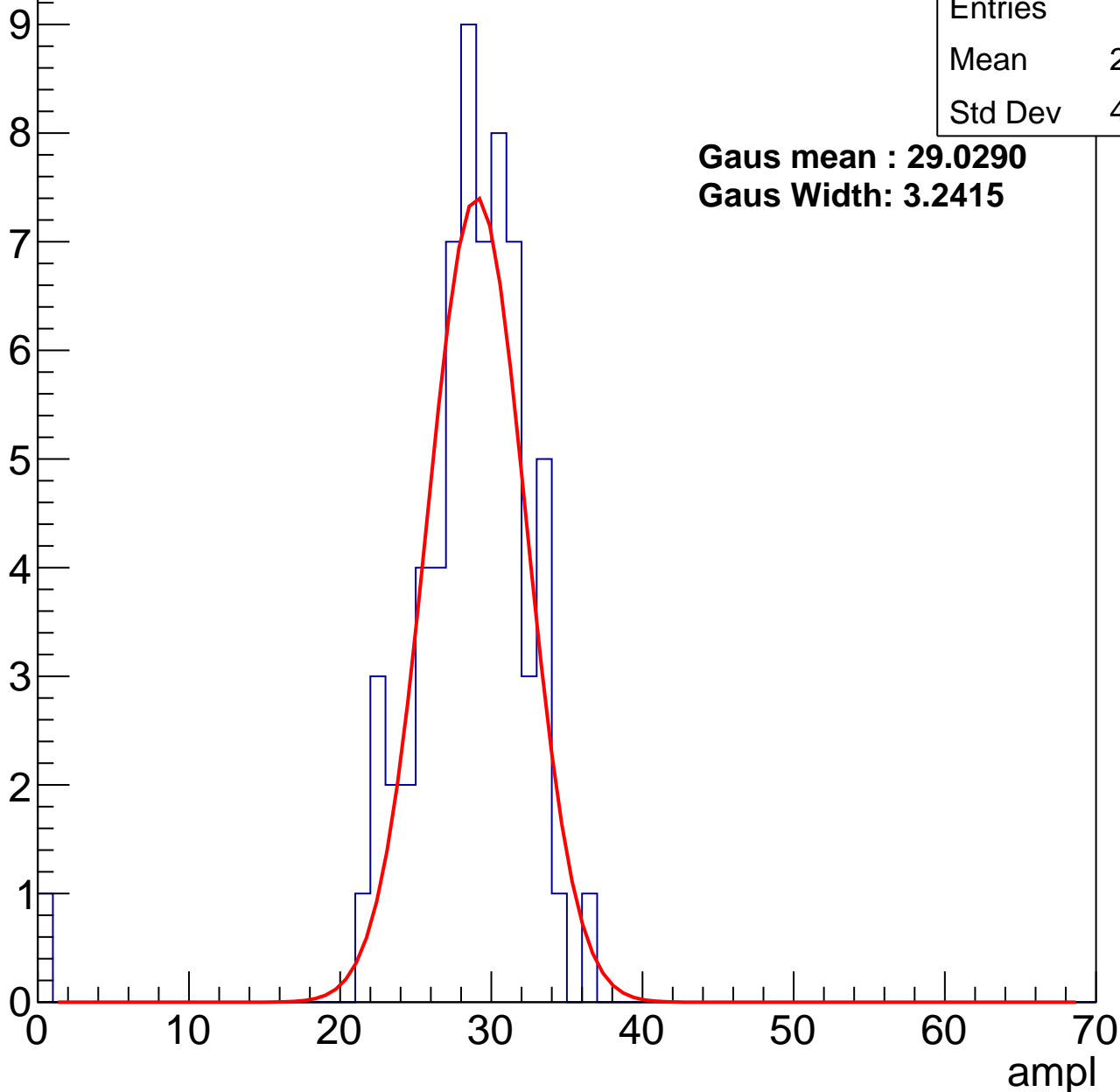
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.95
Std Dev	4.734

**Gaus mean : 29.0290**

**Gaus Width: 3.2415**



# B1L002S, U26-ch79, adc1

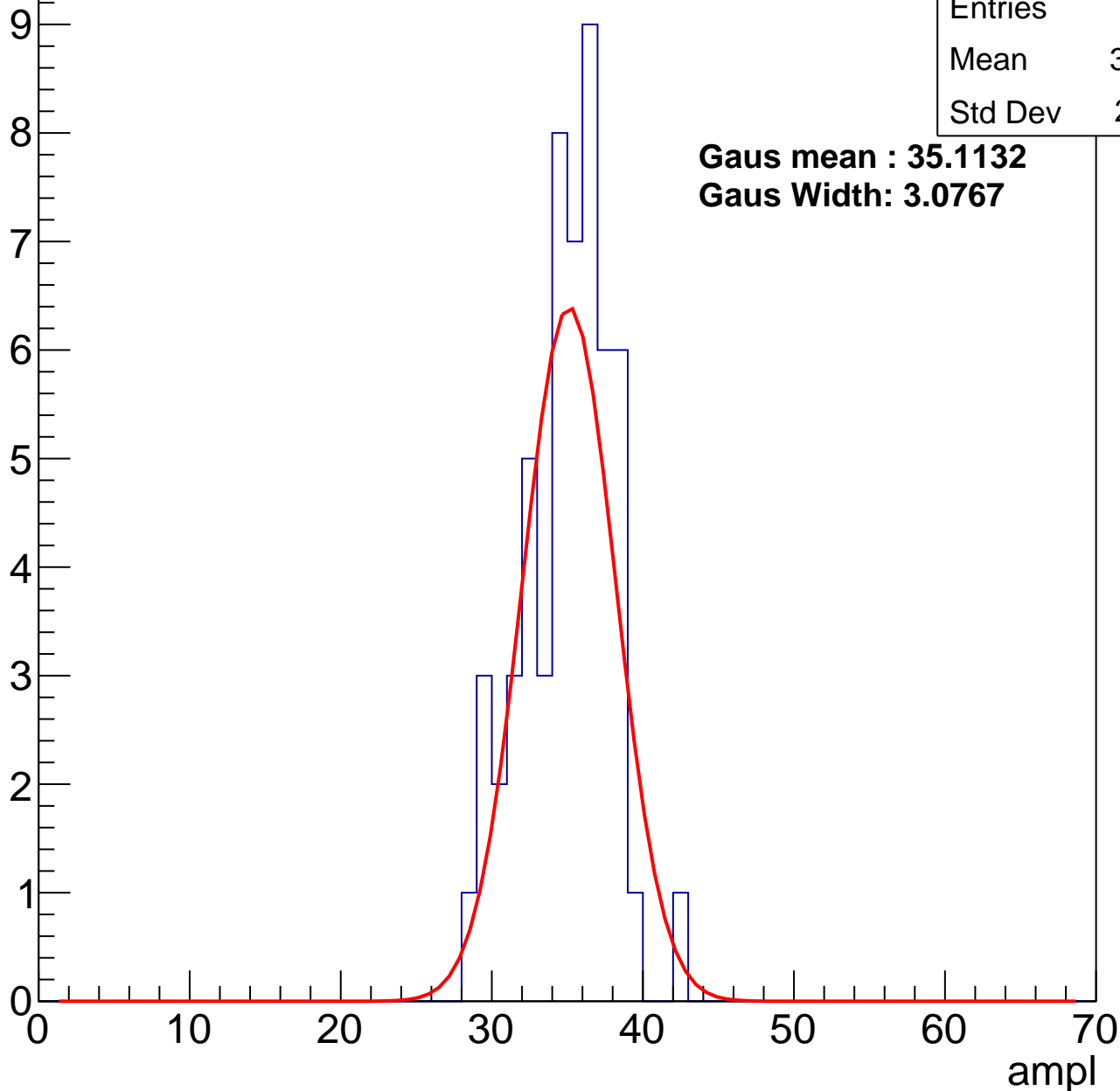
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.53
Std Dev	2.891

**Gaus mean : 35.1132**

**Gaus Width: 3.0767**



# B1L002S, U26-ch79, adc2

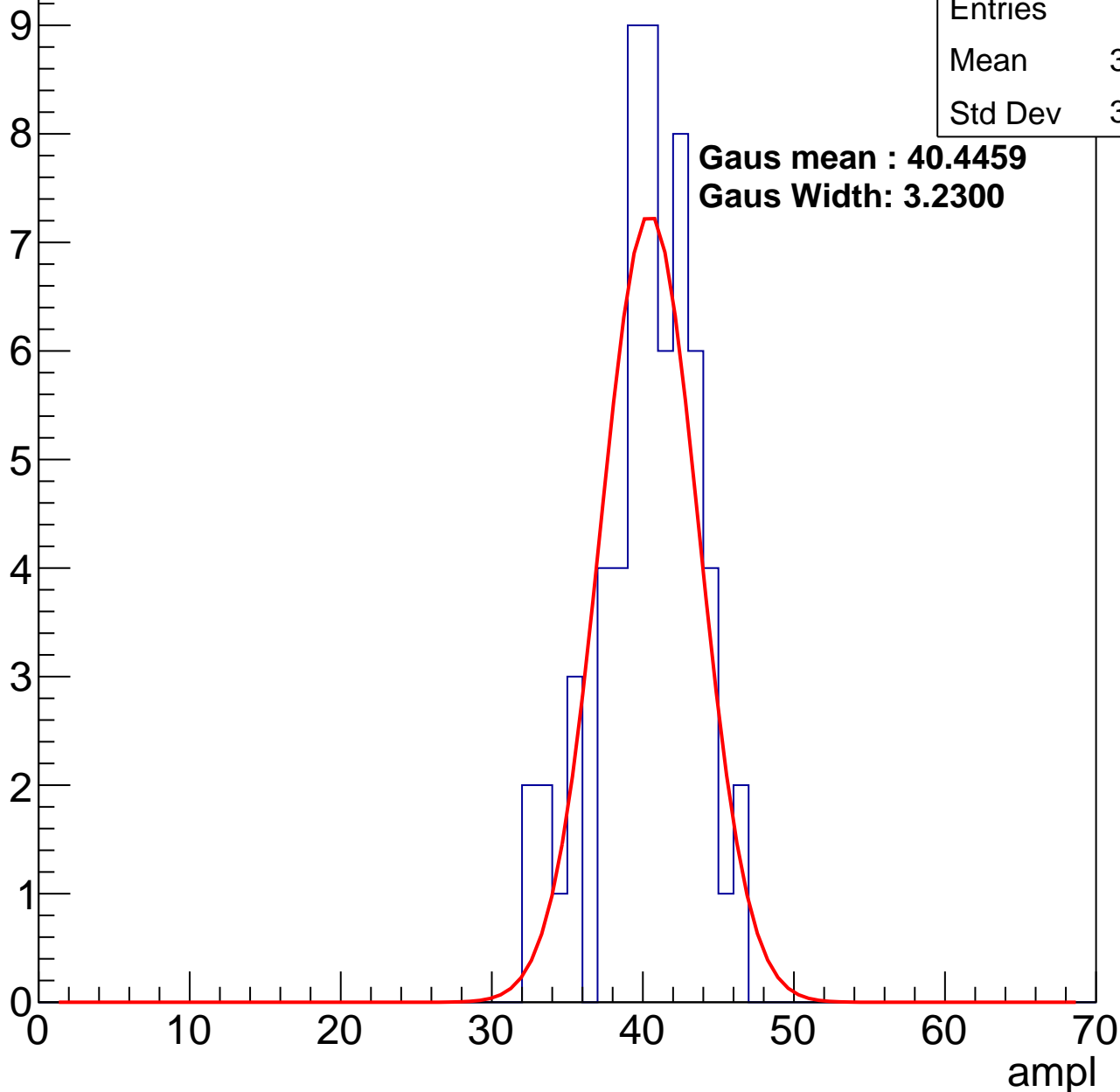
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	39.89
Std Dev	3.265

**Gaus mean : 40.4459**

**Gaus Width: 3.2300**

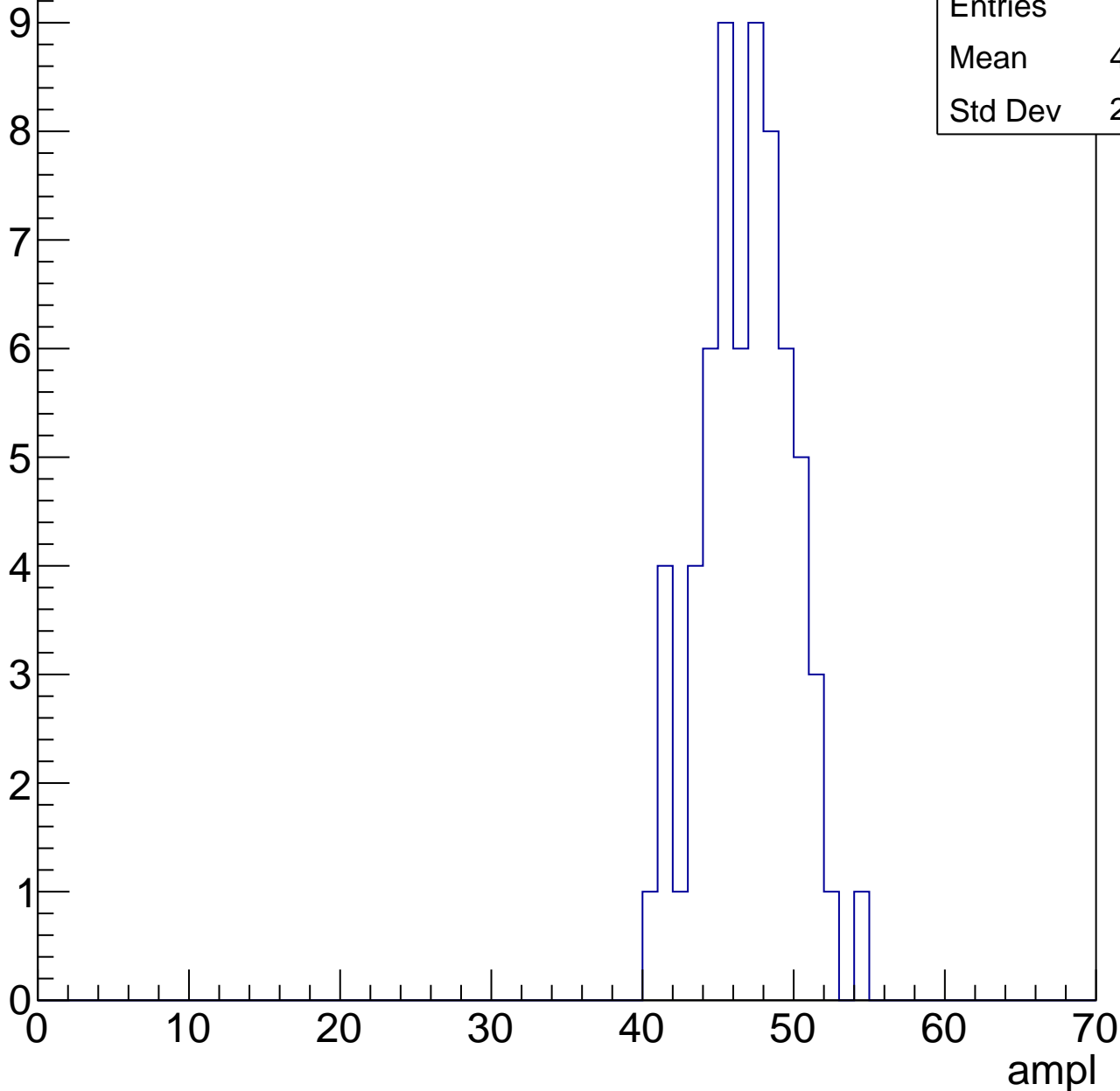


# B1L002S, U26-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.45
Std Dev	2.952

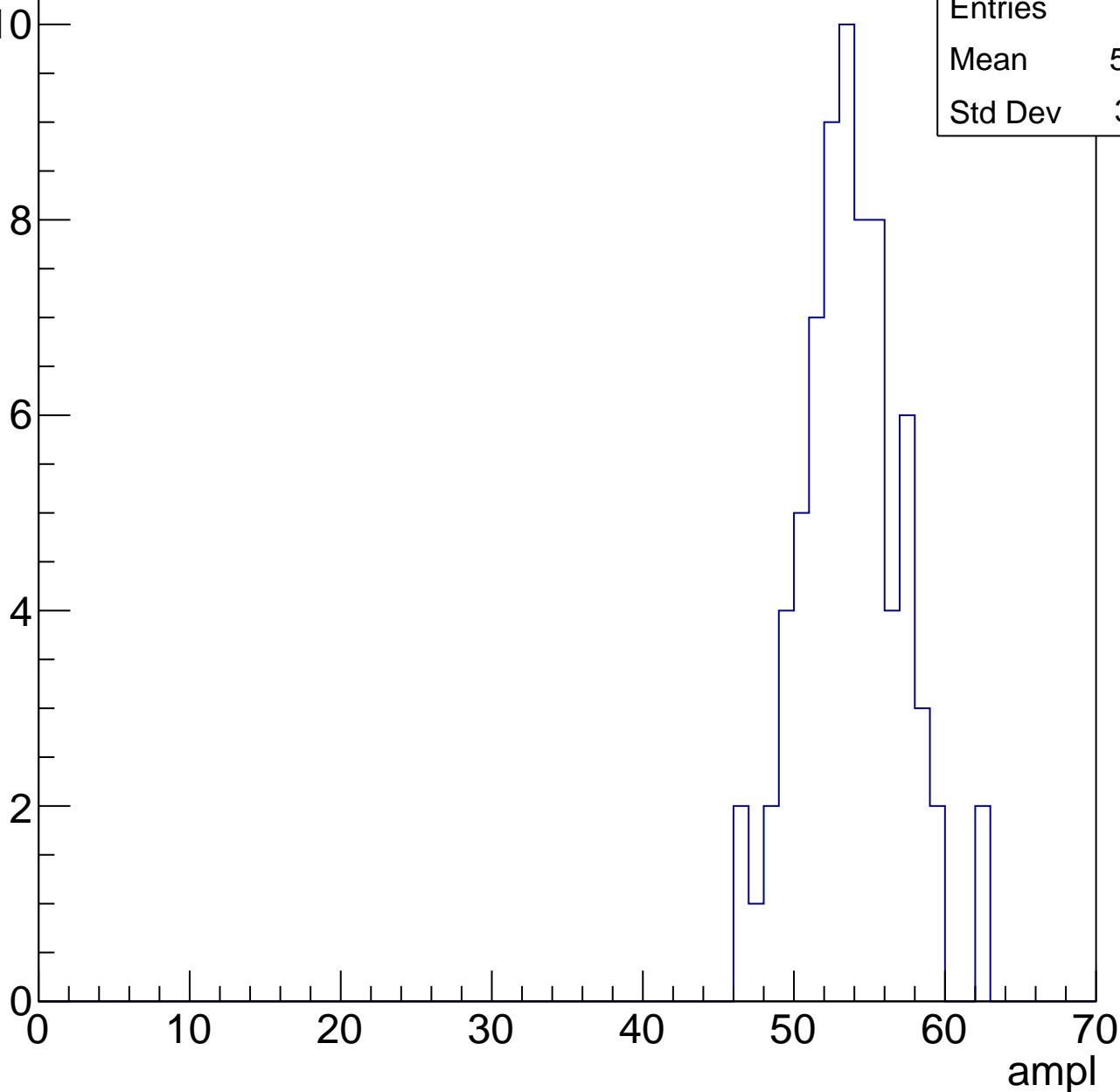


# B1L002S, U26-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	53.29
Std Dev	3.321

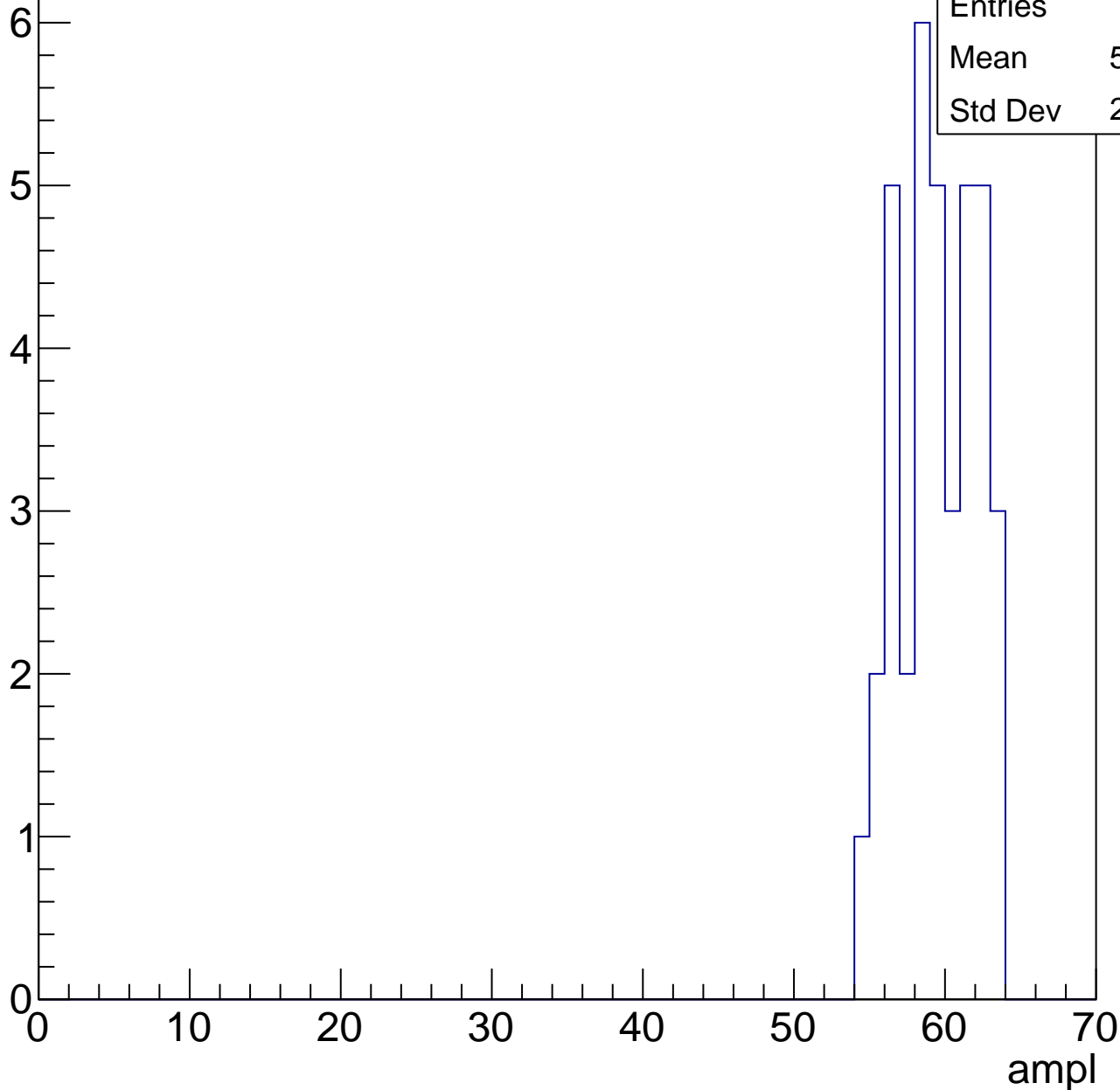


# B1L002S, U26-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

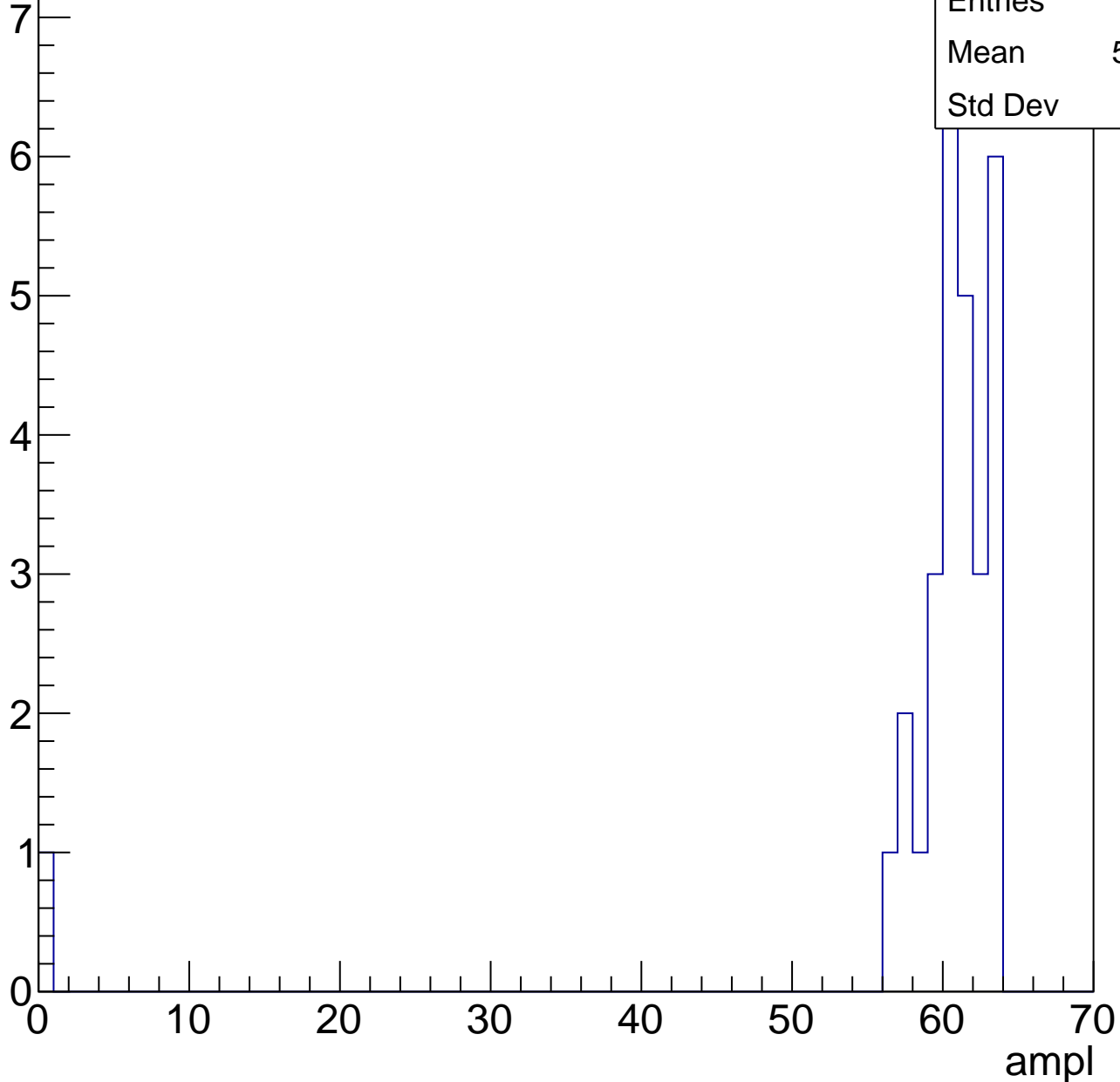
Entries	37
Mean	59.05
Std Dev	2.503



# B1L002S, U26-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch80, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	26.61
Std Dev	5.585

**Gaus mean : 28.1214**

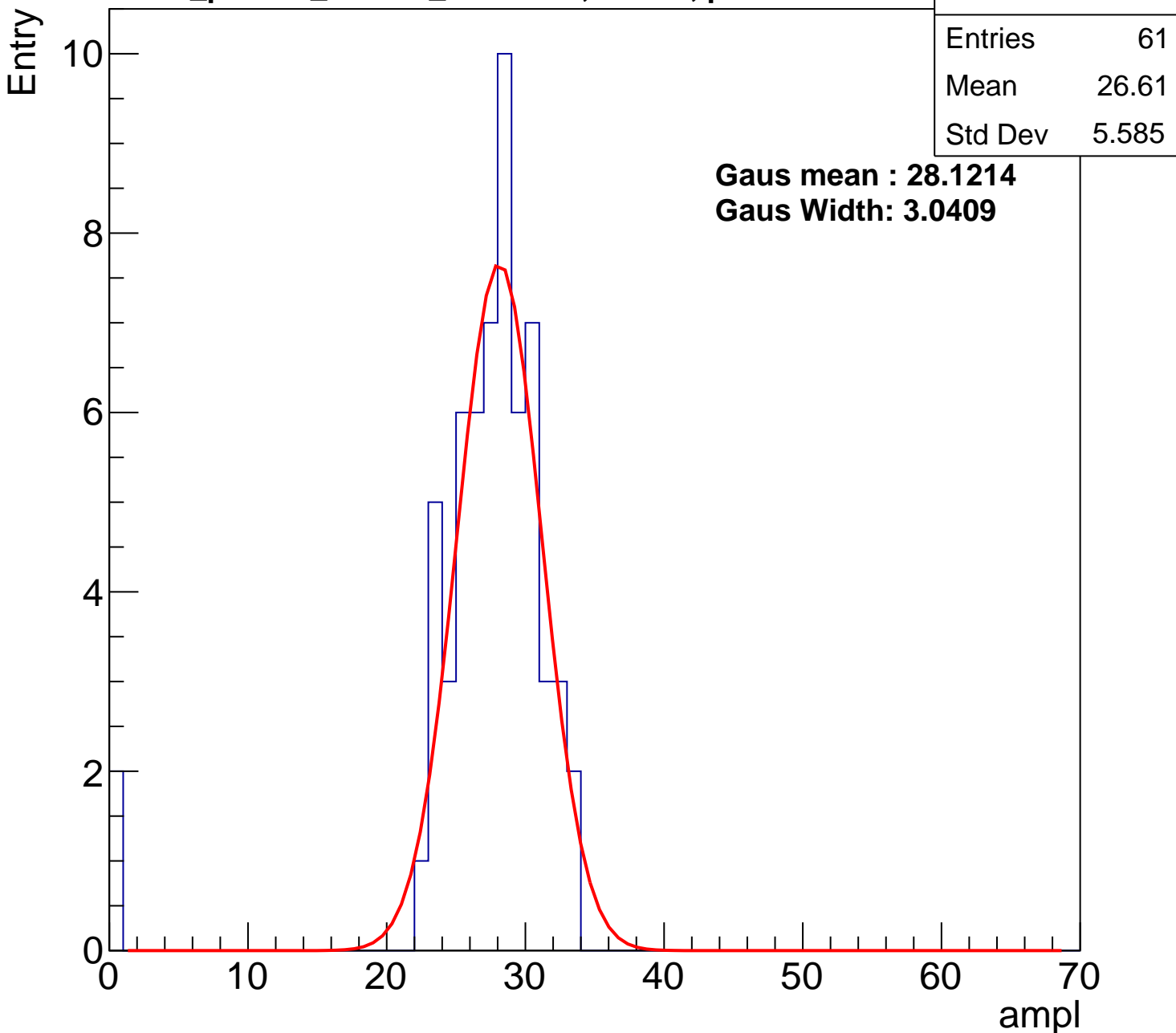
**Gaus Width: 3.0409**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch80, adc1

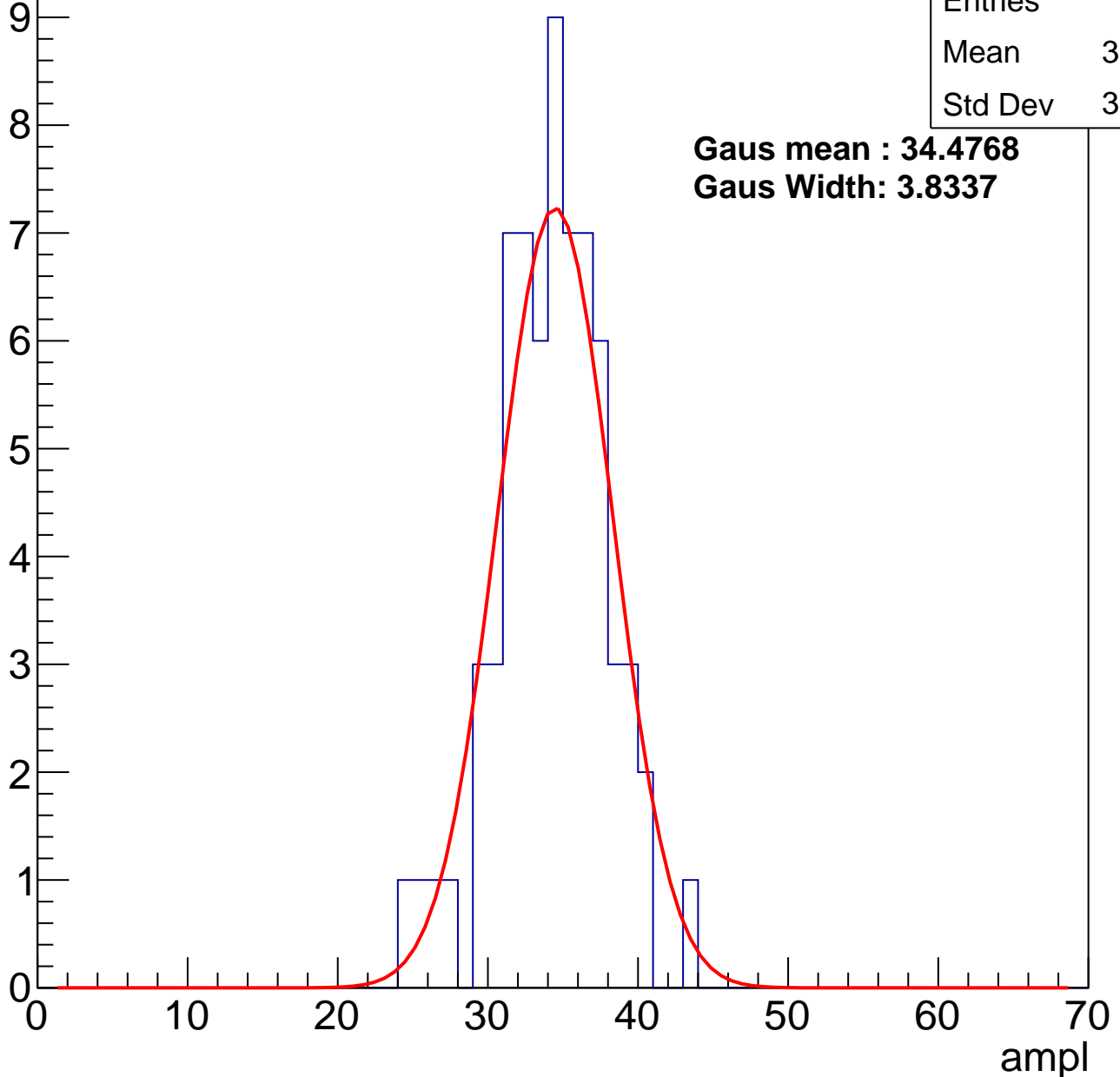
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.78
Std Dev	3.589

**Gaus mean : 34.4768**

**Gaus Width: 3.8337**



# B1L002S, U26-ch80, adc2

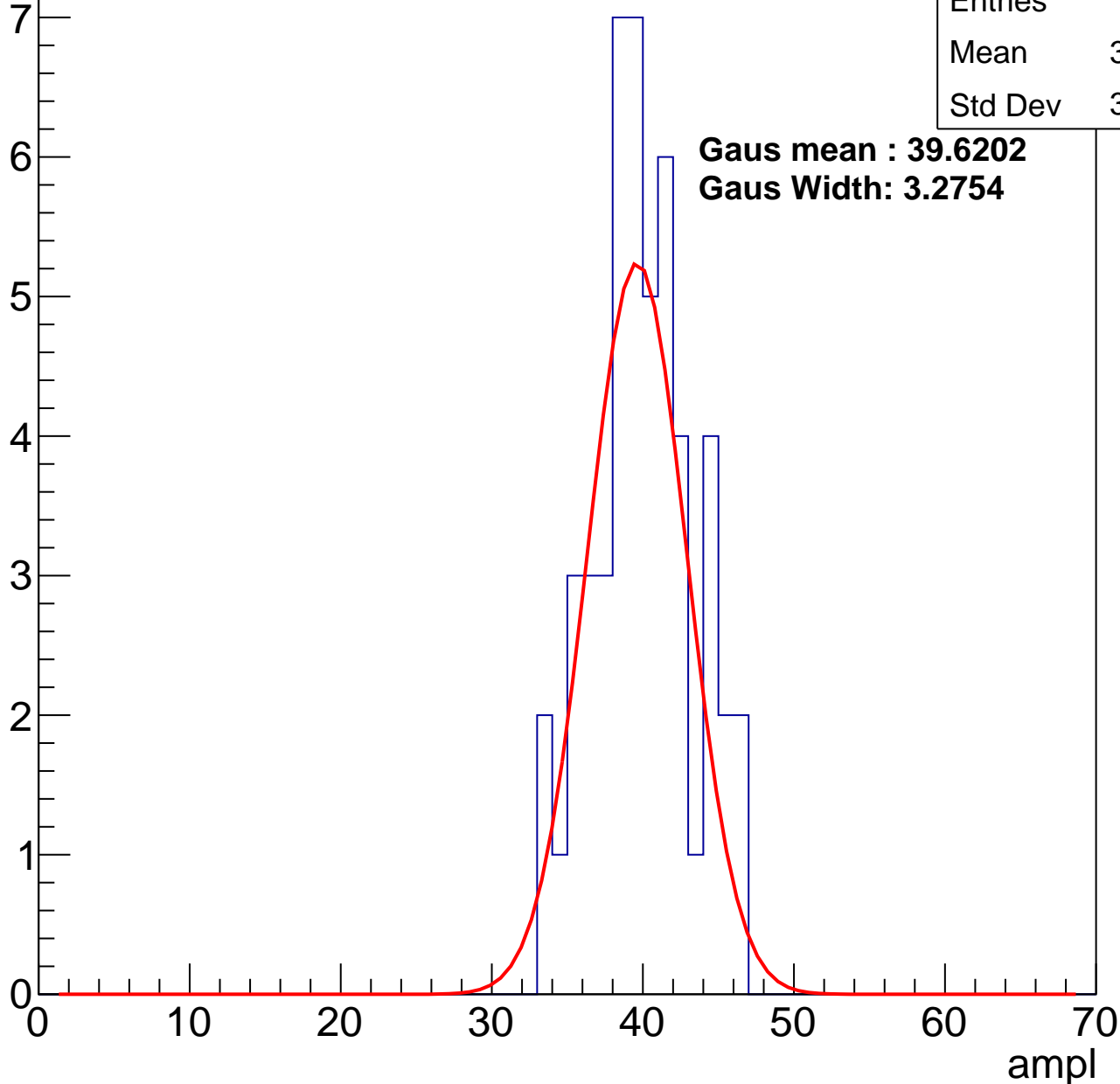
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	39.56
Std Dev	3.244

**Gaus mean : 39.6202**

**Gaus Width: 3.2754**

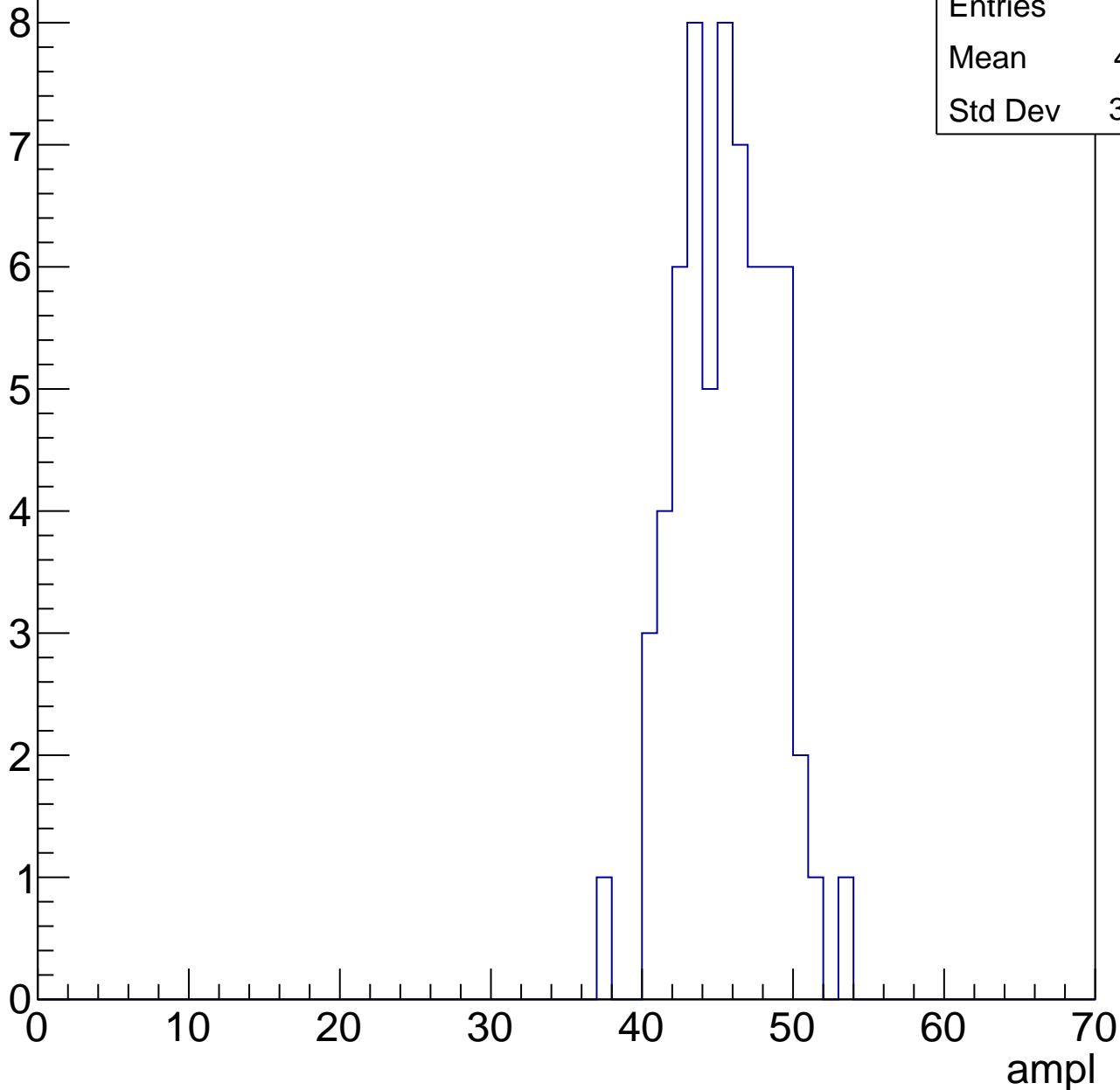


# B1L002S, U26-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	45.11
Std Dev	3.123

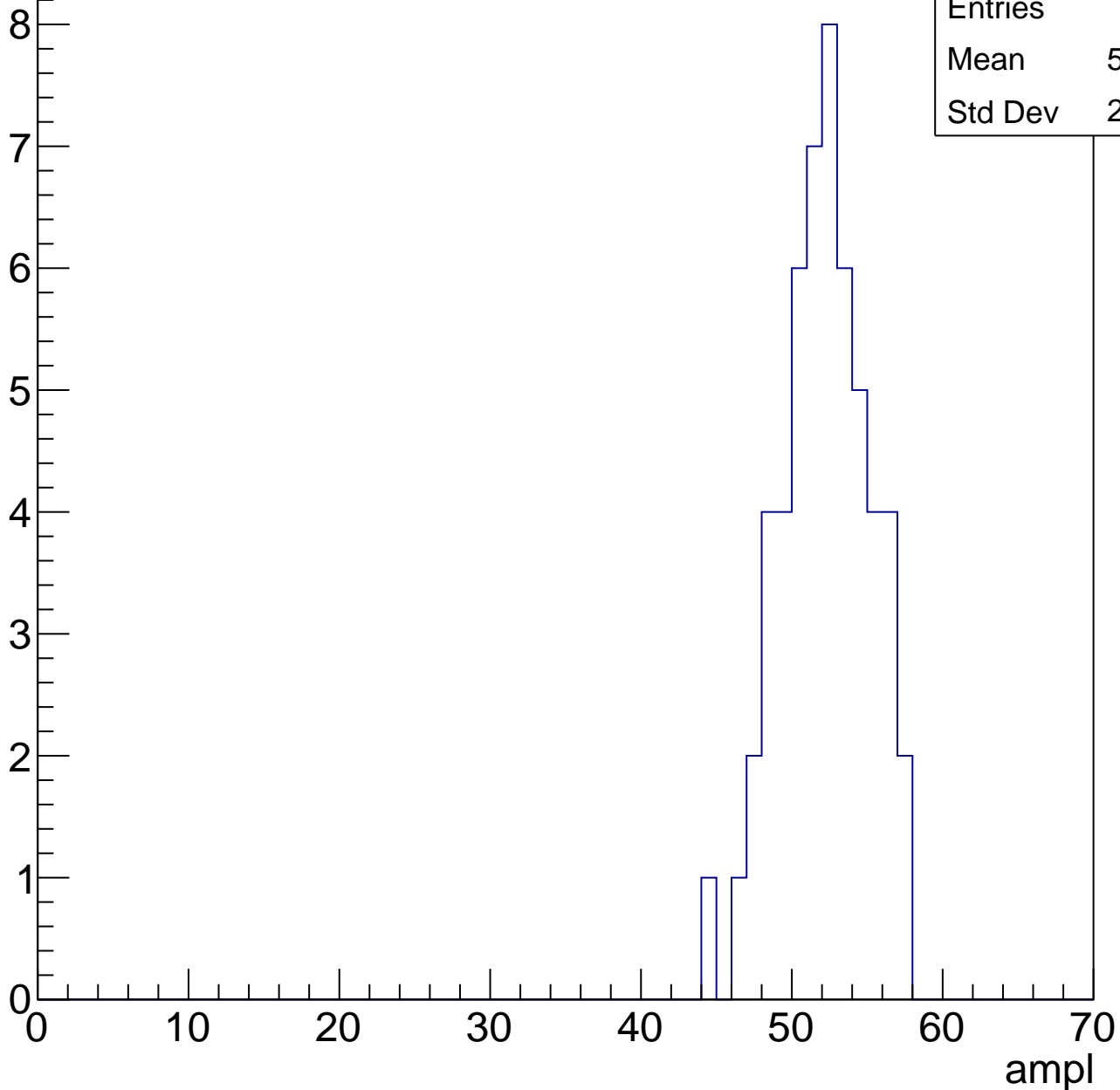


# B1L002S, U26-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

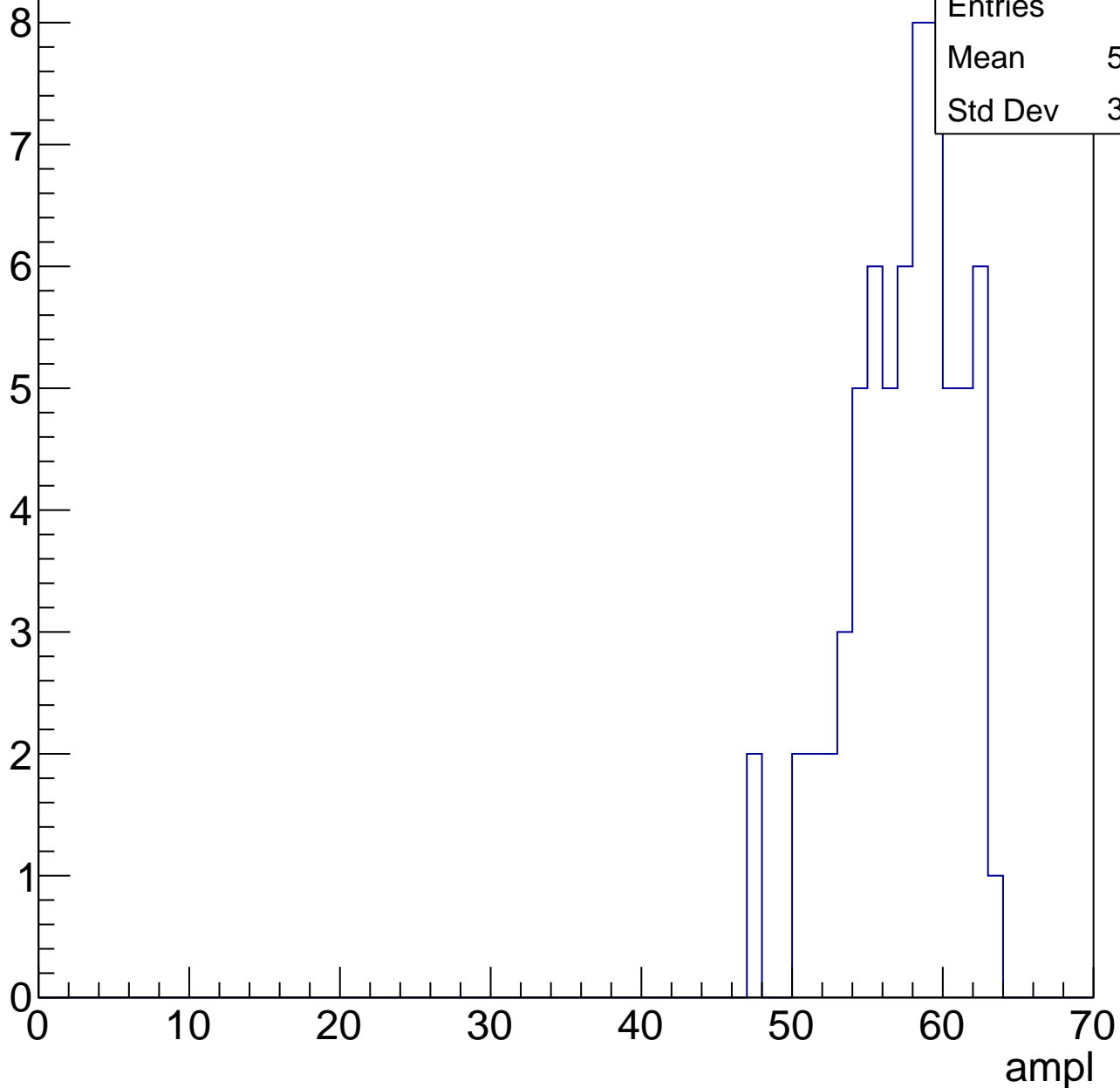
Entries	54
Mean	51.69
Std Dev	2.892



# B1L002S, U26-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

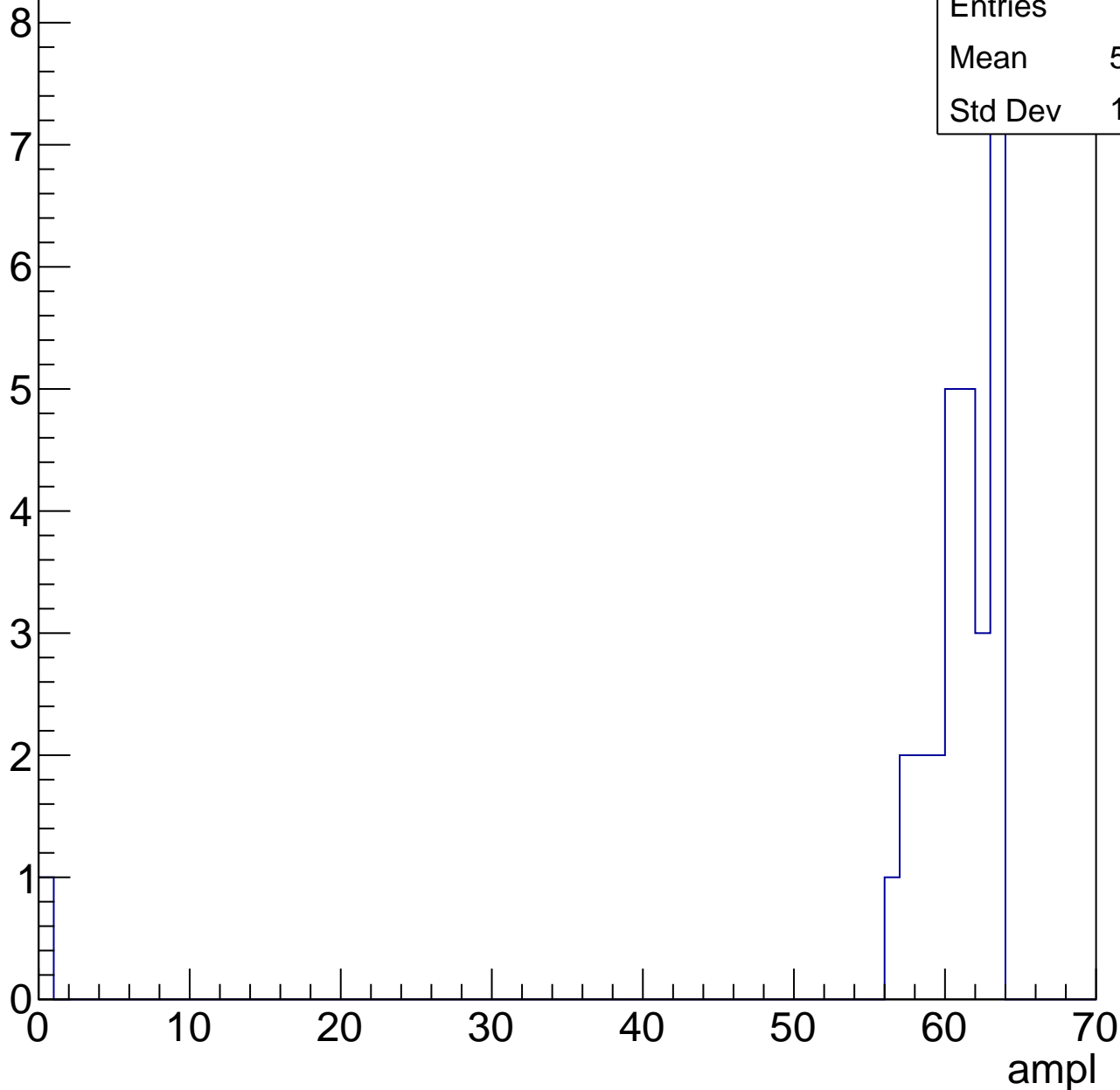


# B1L002S, U26-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.59
Std Dev	11.26

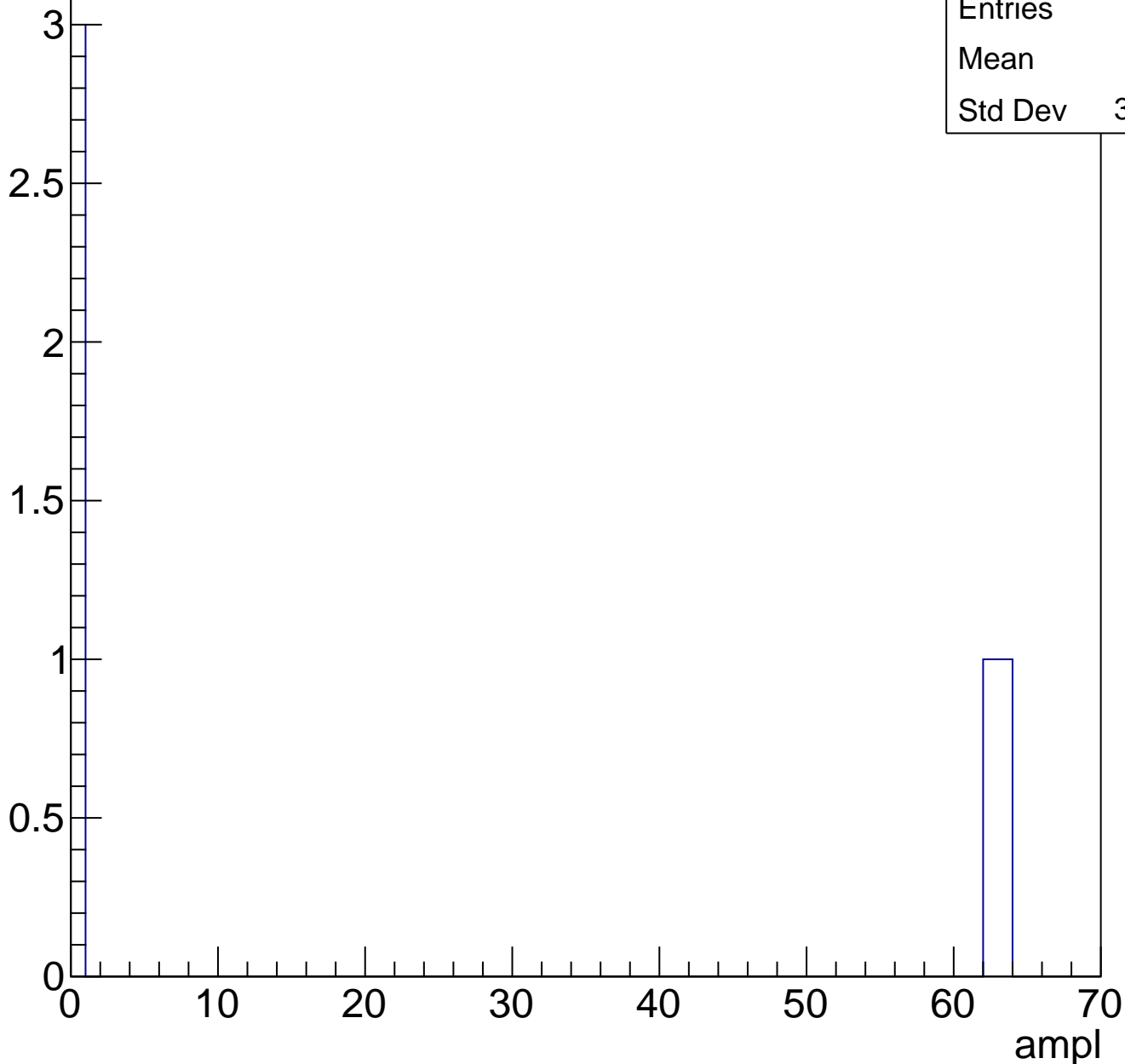




# B1L002S, U26-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch81, adc0

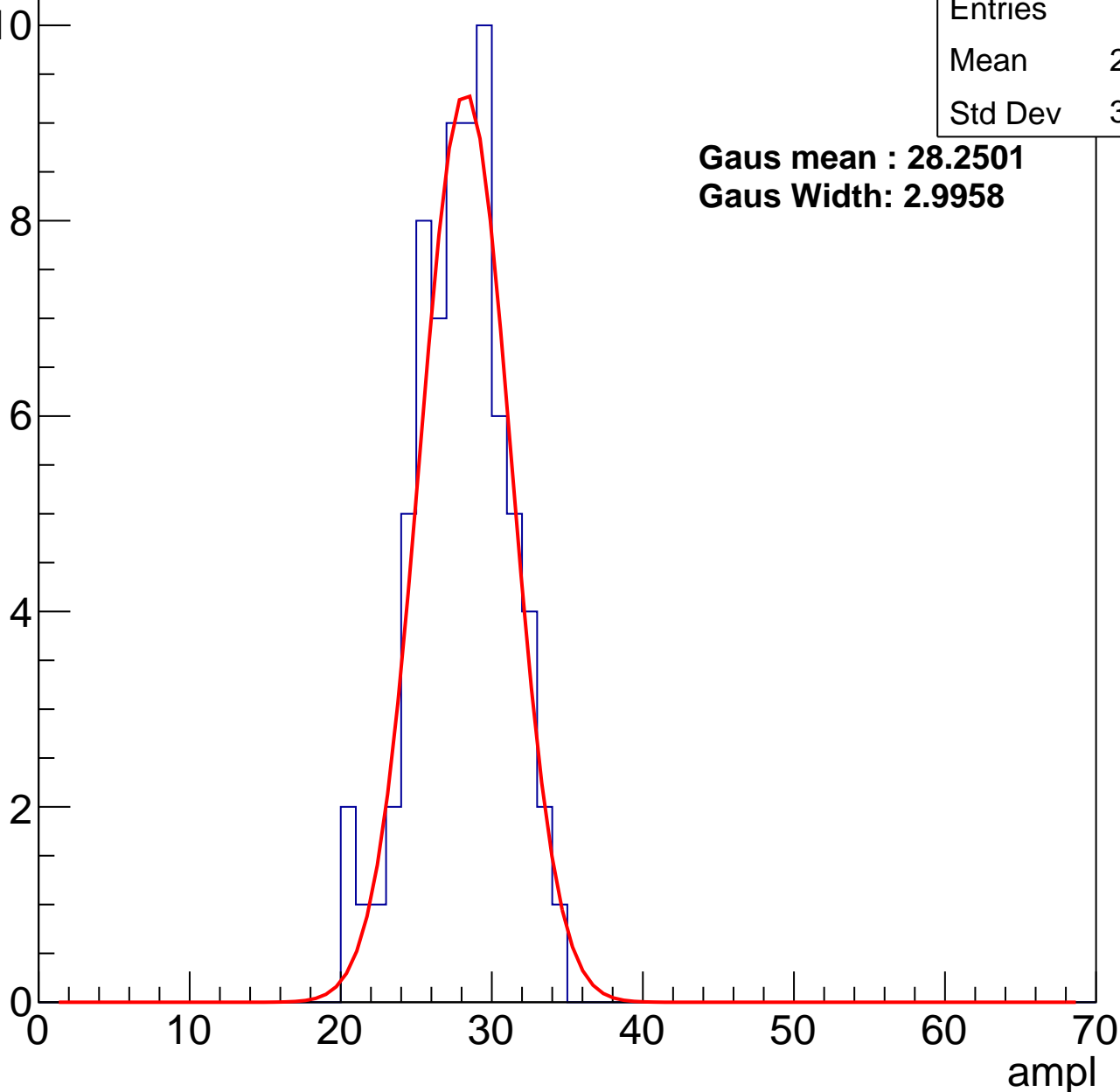
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	27.49
Std Dev	3.032

**Gaus mean : 28.2501**

**Gaus Width: 2.9958**

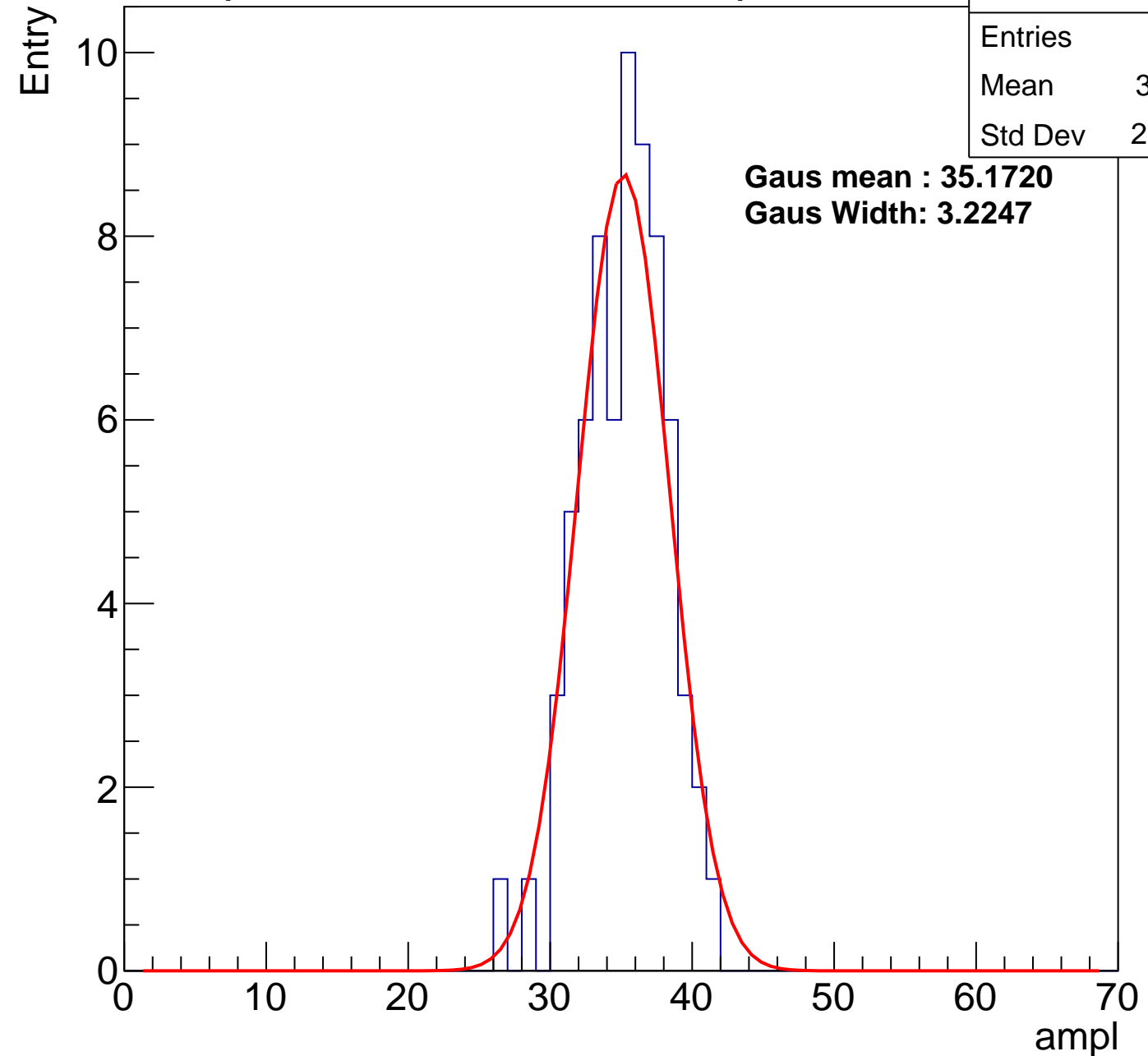


# B1L002S, U26-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	34.71
Std Dev	2.949

**Gaus mean : 35.1720**  
**Gaus Width: 3.2247**



# B1L002S, U26-ch81, adc2

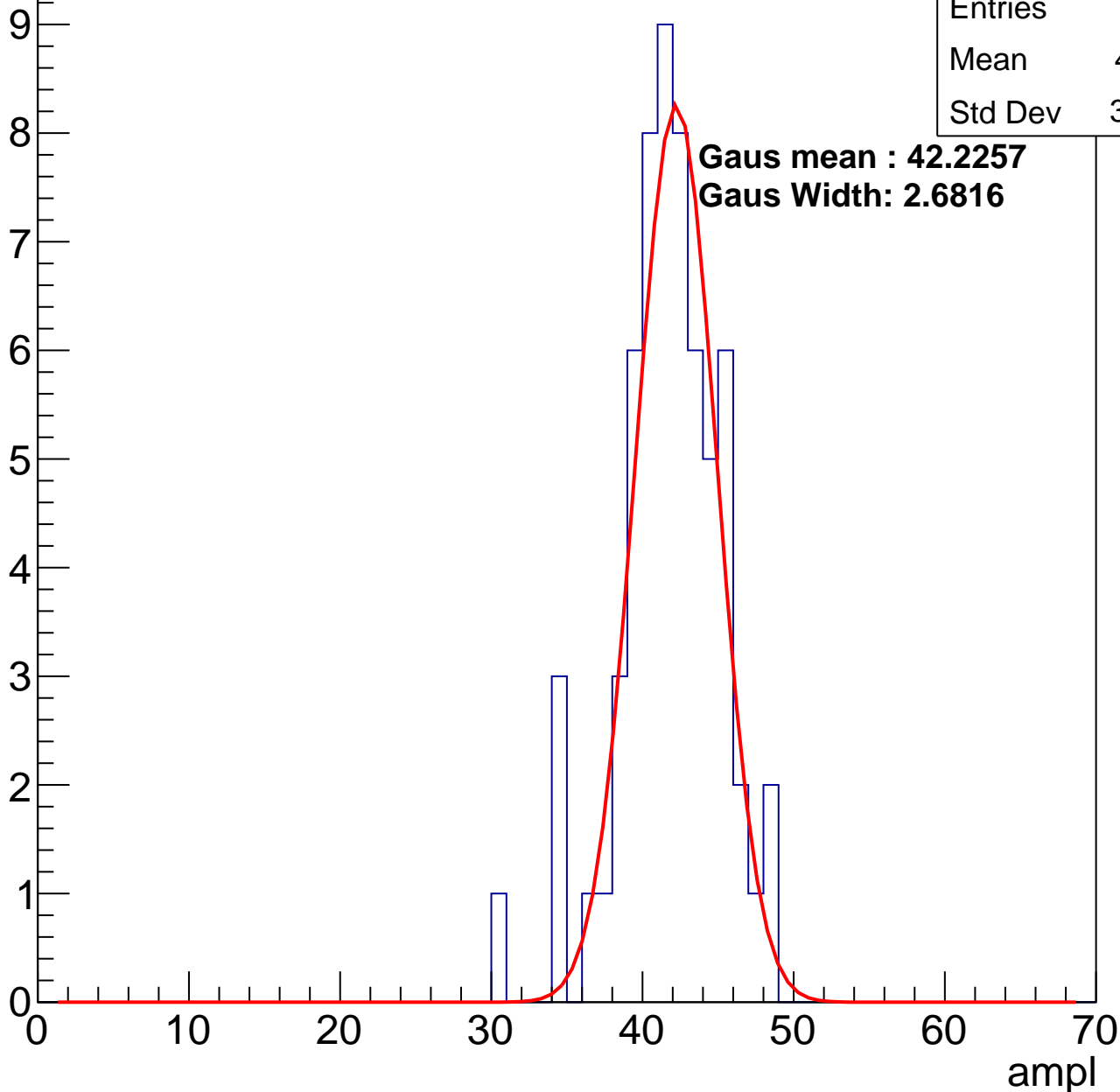
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.31
Std Dev	3.406

**Gaus mean : 42.2257**

**Gaus Width: 2.6816**



# B1L002S, U26-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

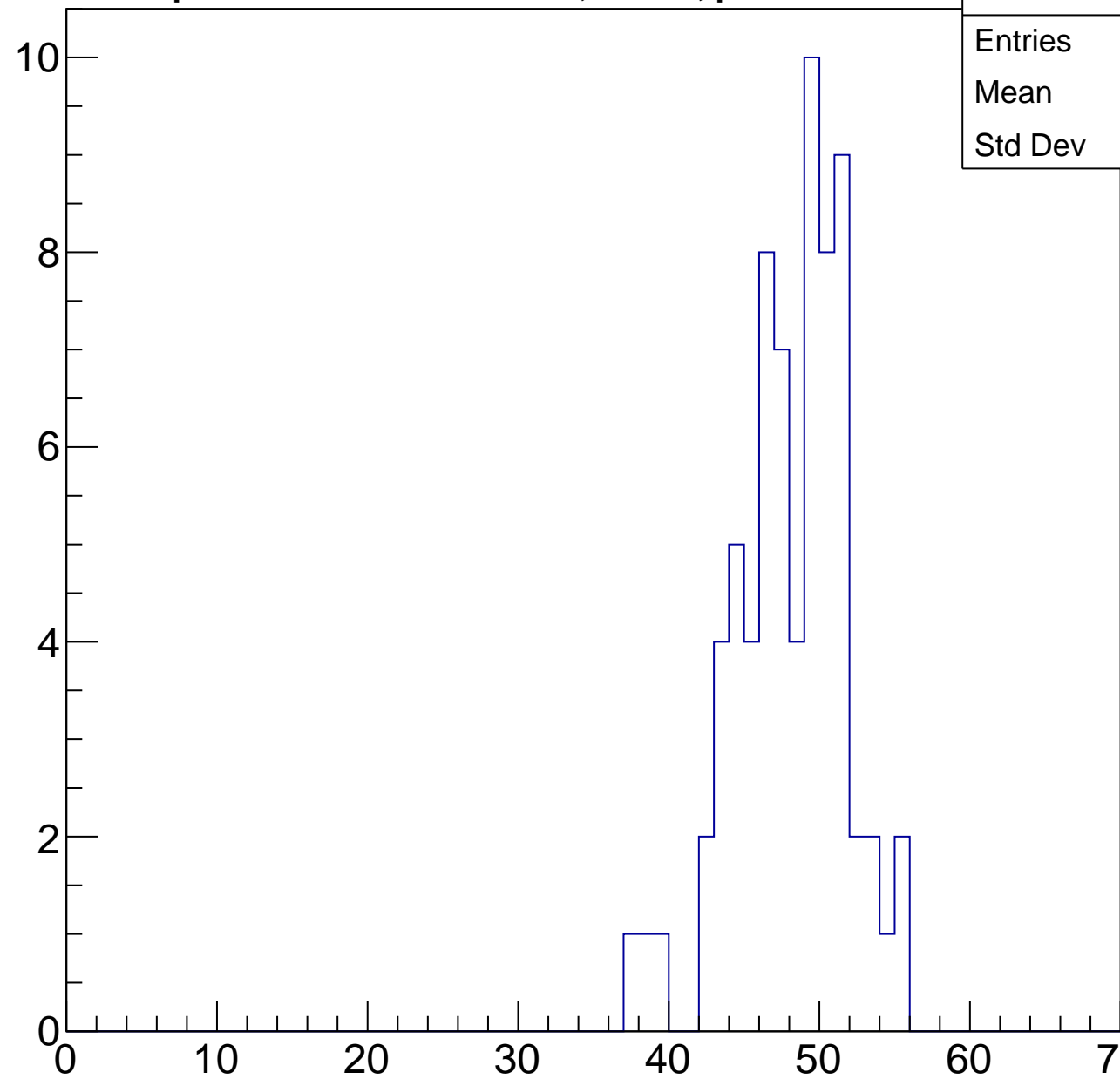
Entries	71
Mean	47.63
Std Dev	3.674

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

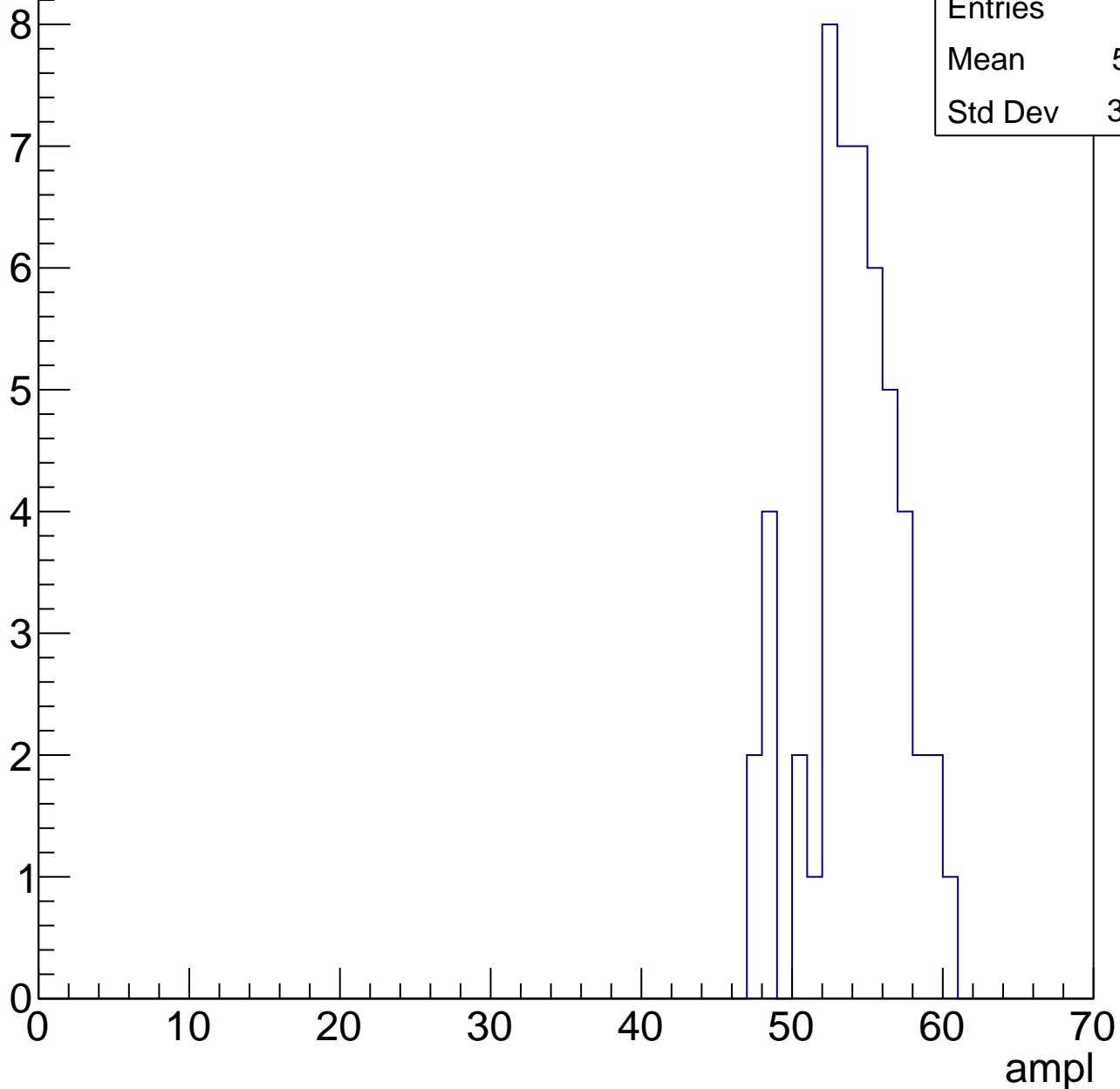


# B1L002S, U26-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	53.61
Std Dev	3.113

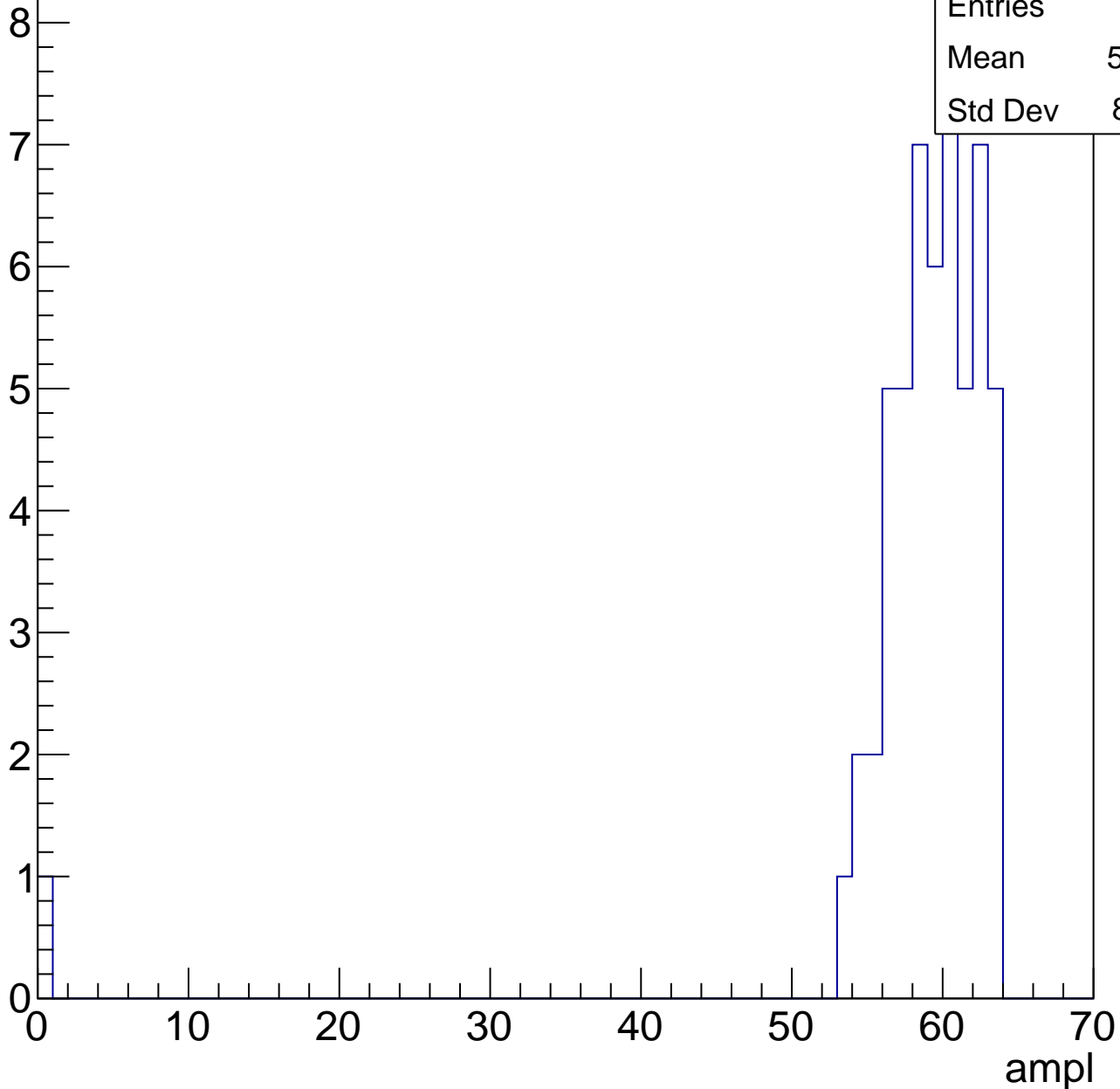


# B1L002S, U26-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.96
Std Dev	8.371

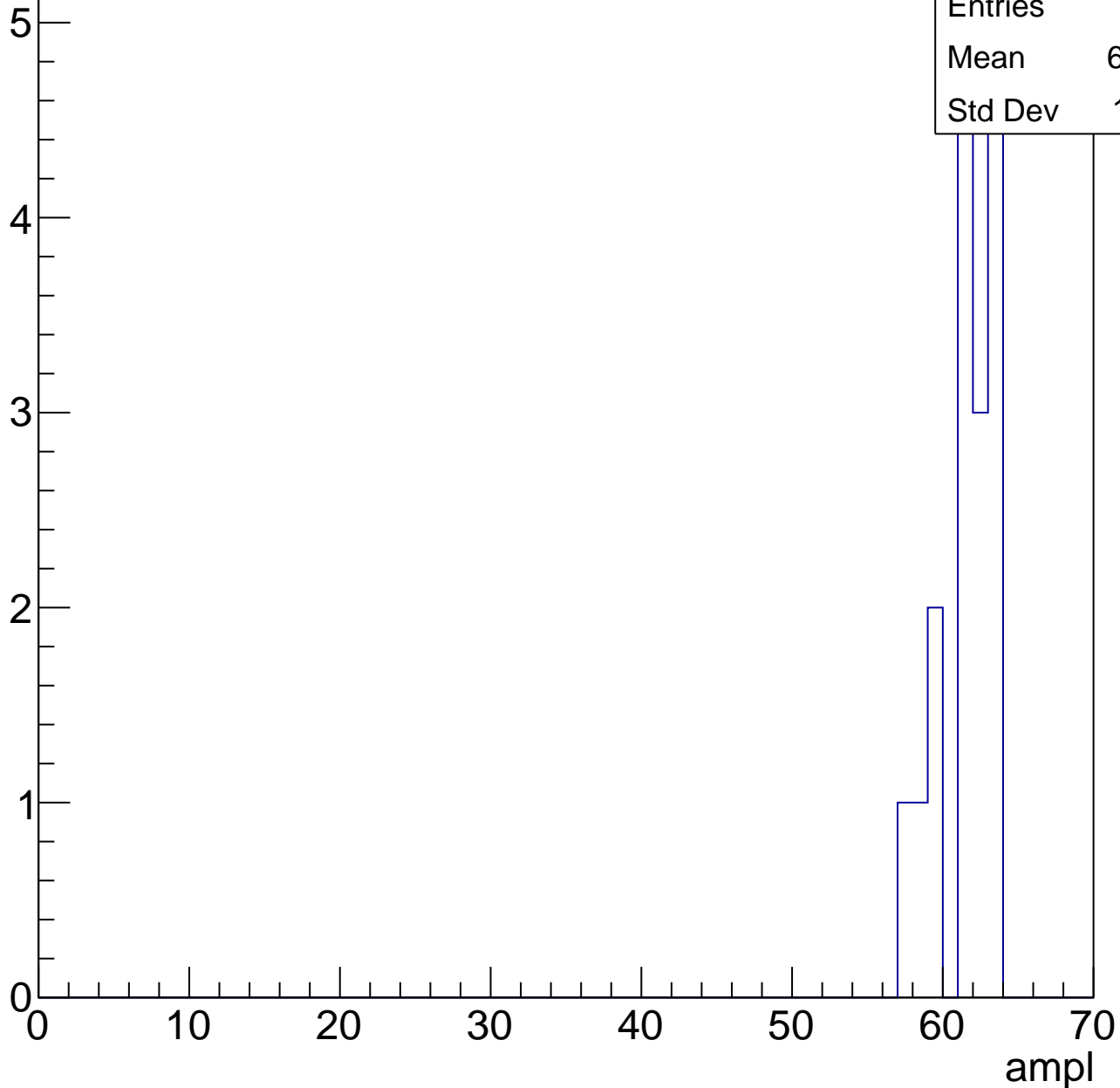


# B1L002S, U26-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.12
Std Dev	1.811





# B1L002S, U26-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch82, adc0

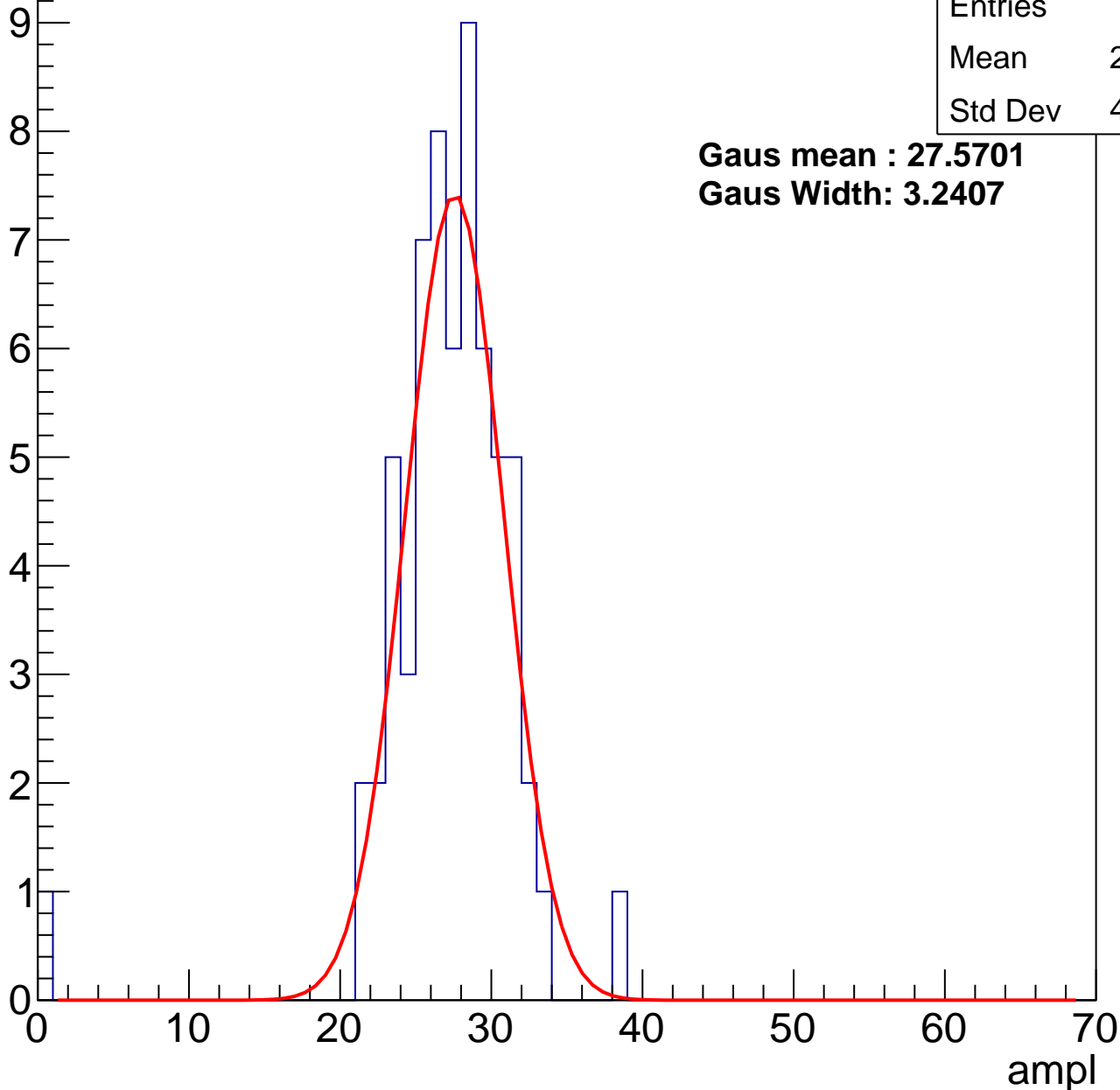
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	26.73
Std Dev	4.637

**Gaus mean : 27.5701**

**Gaus Width: 3.2407**



# B1L002S, U26-ch82, adc1

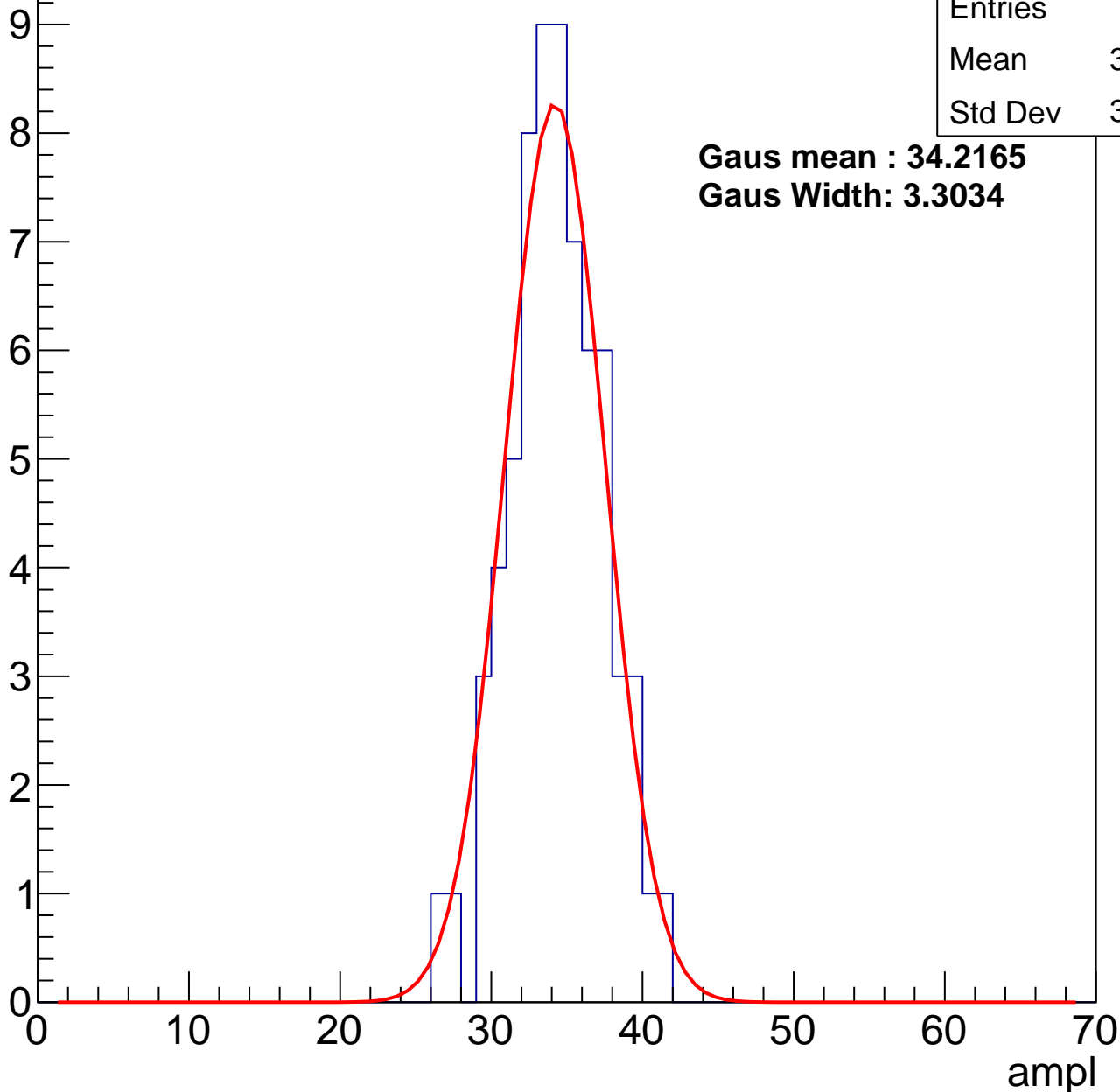
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.87
Std Dev	3.066

**Gaus mean : 34.2165**

**Gaus Width: 3.3034**



# B1L002S, U26-ch82, adc2

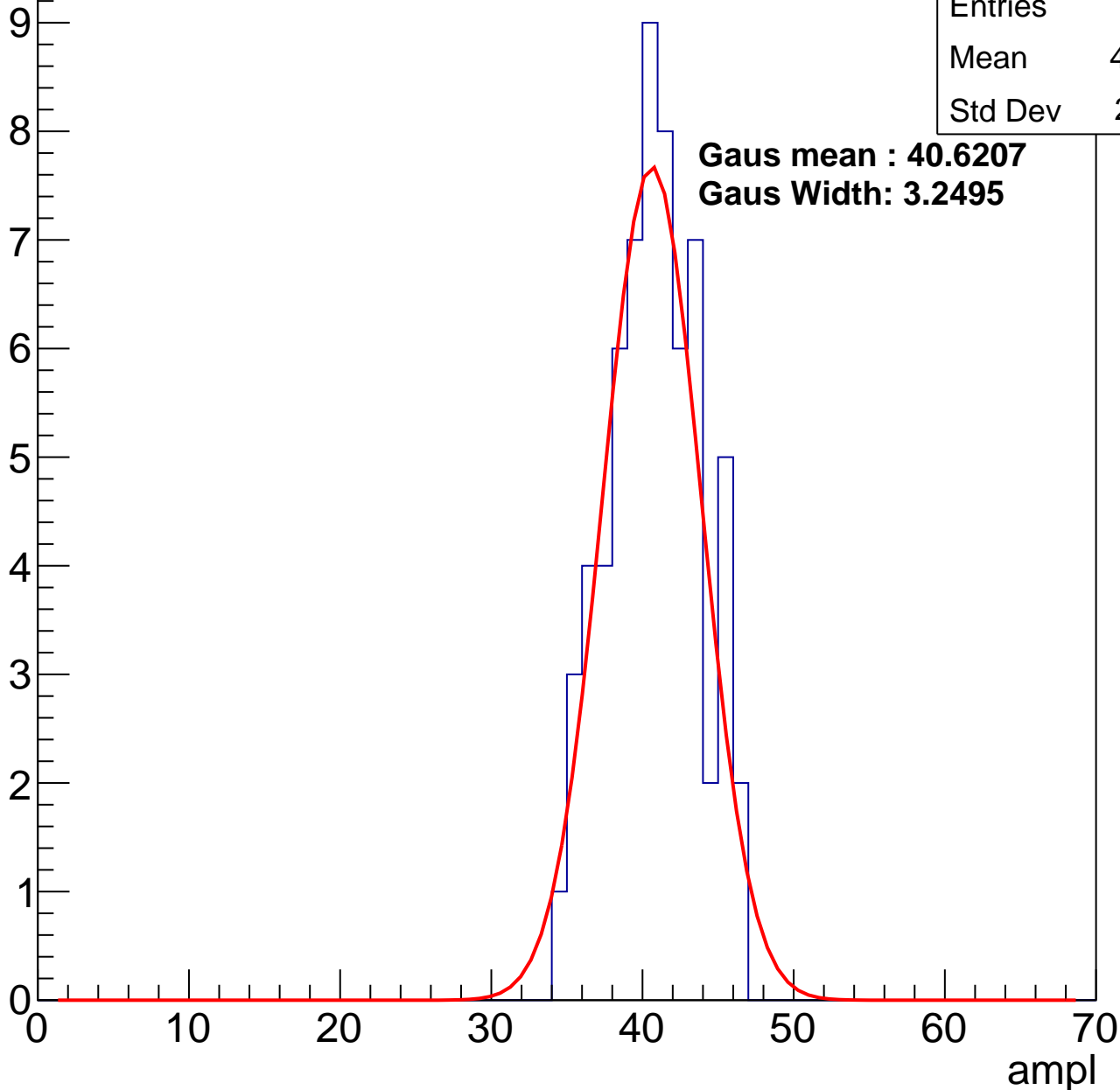
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.28
Std Dev	2.961

**Gaus mean : 40.6207**

**Gaus Width: 3.2495**

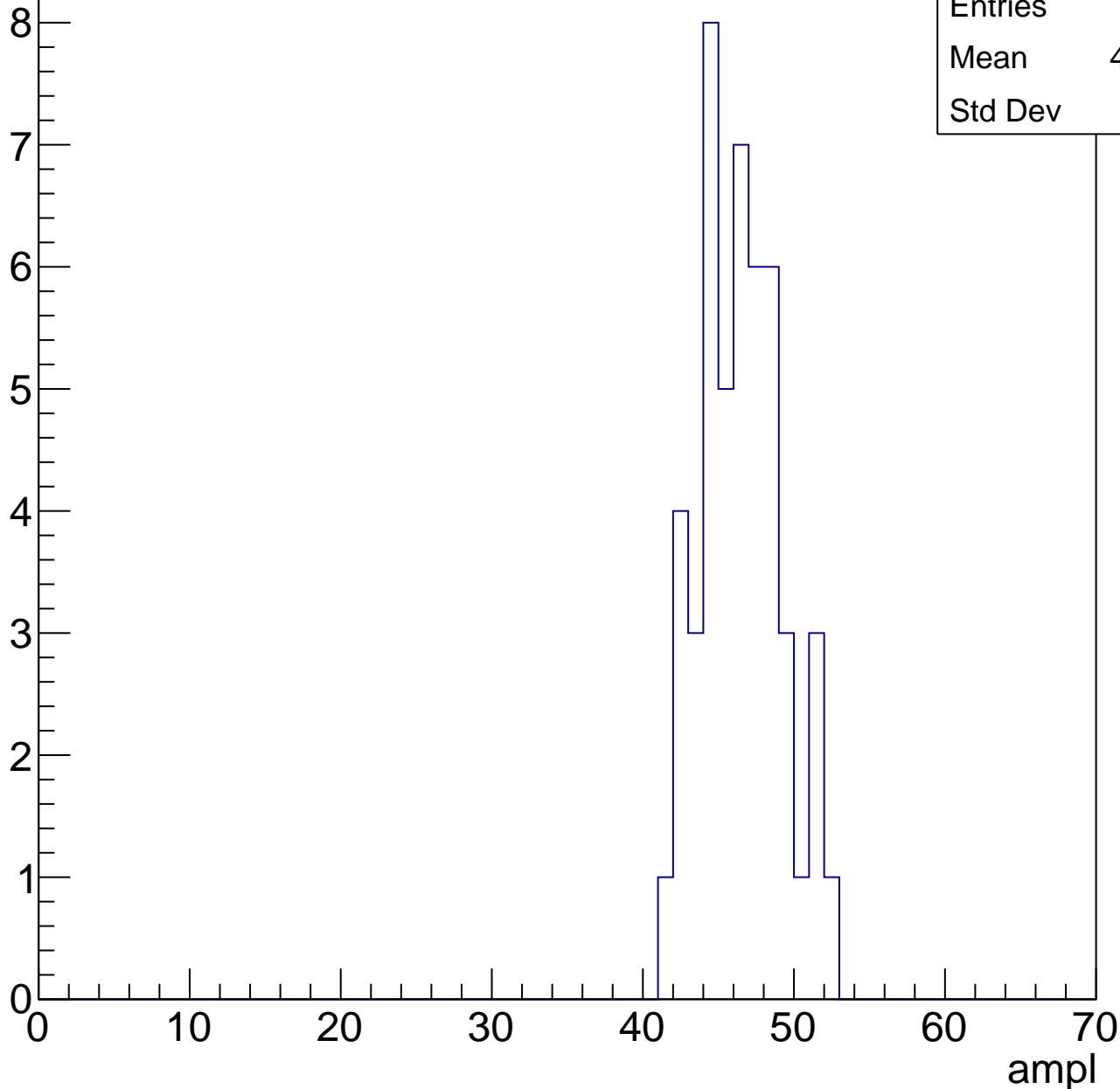


# B1L002S, U26-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

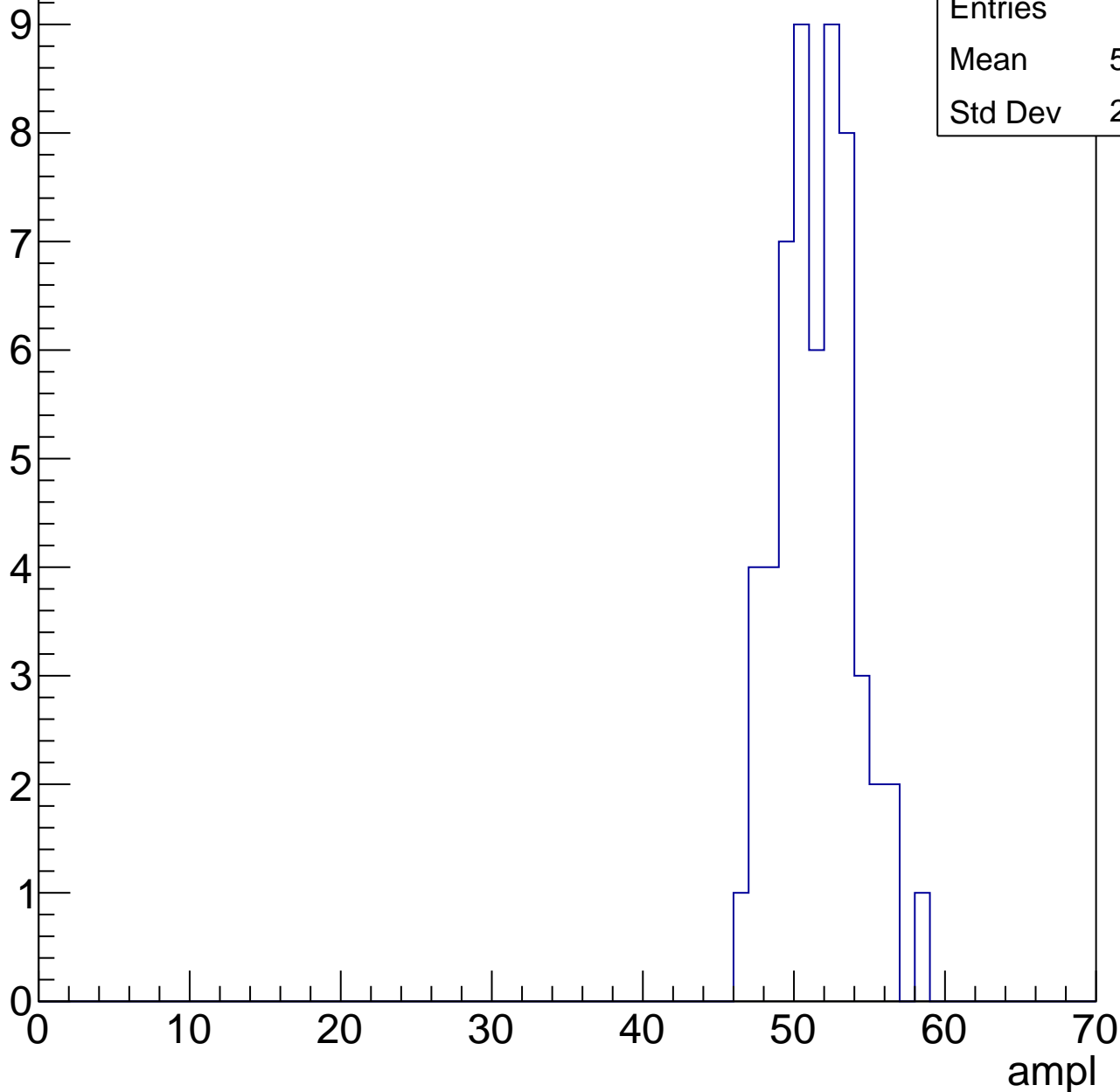
Entries	48
Mean	46.02
Std Dev	2.65



# B1L002S, U26-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

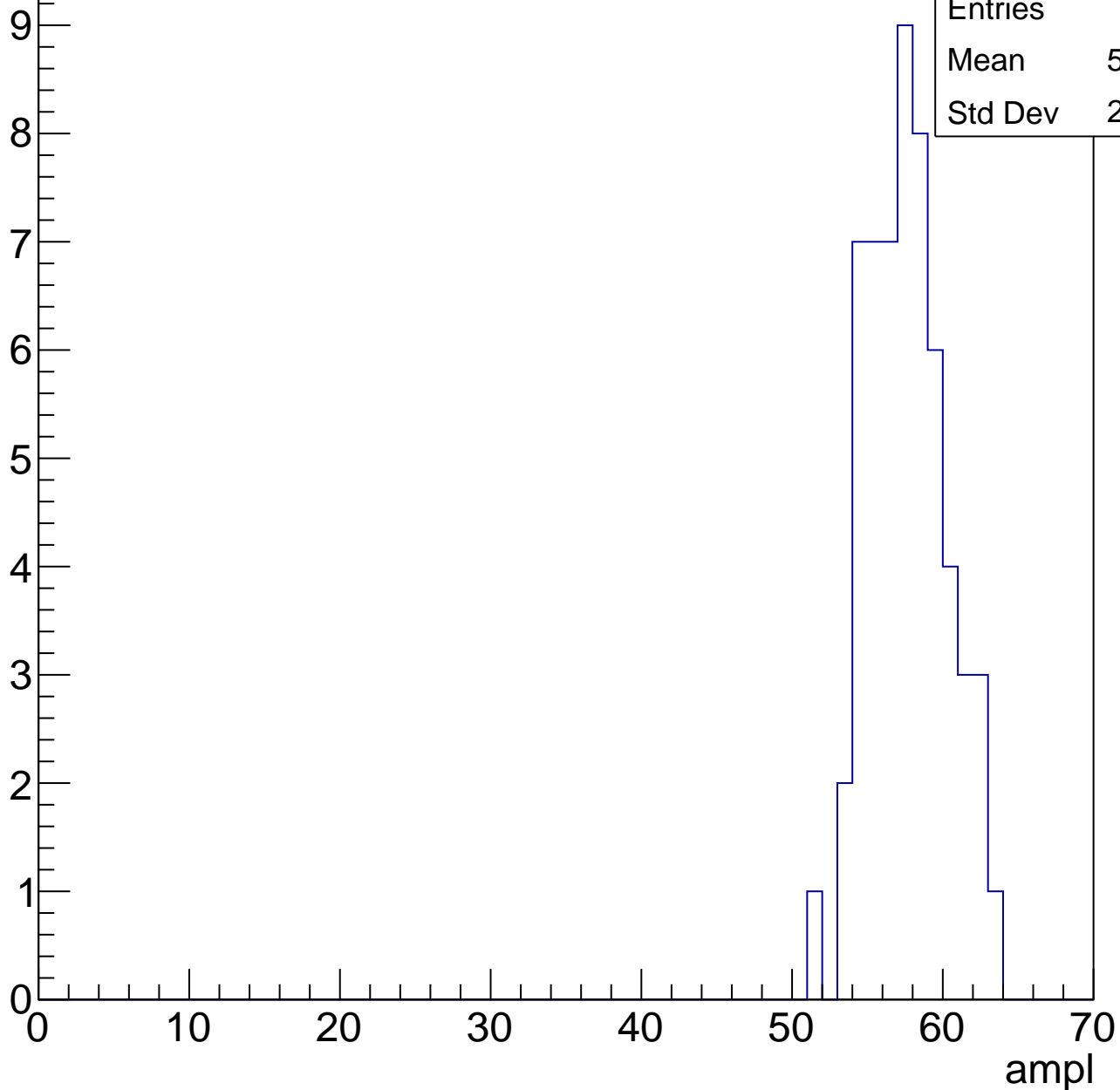
Entry



# B1L002S, U26-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	58
Mean	57.16
Std Dev	2.598

# B1L002S, U26-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	34
Mean	58.82
Std Dev	10.43

ampl

0

10

20

30

40

50

60

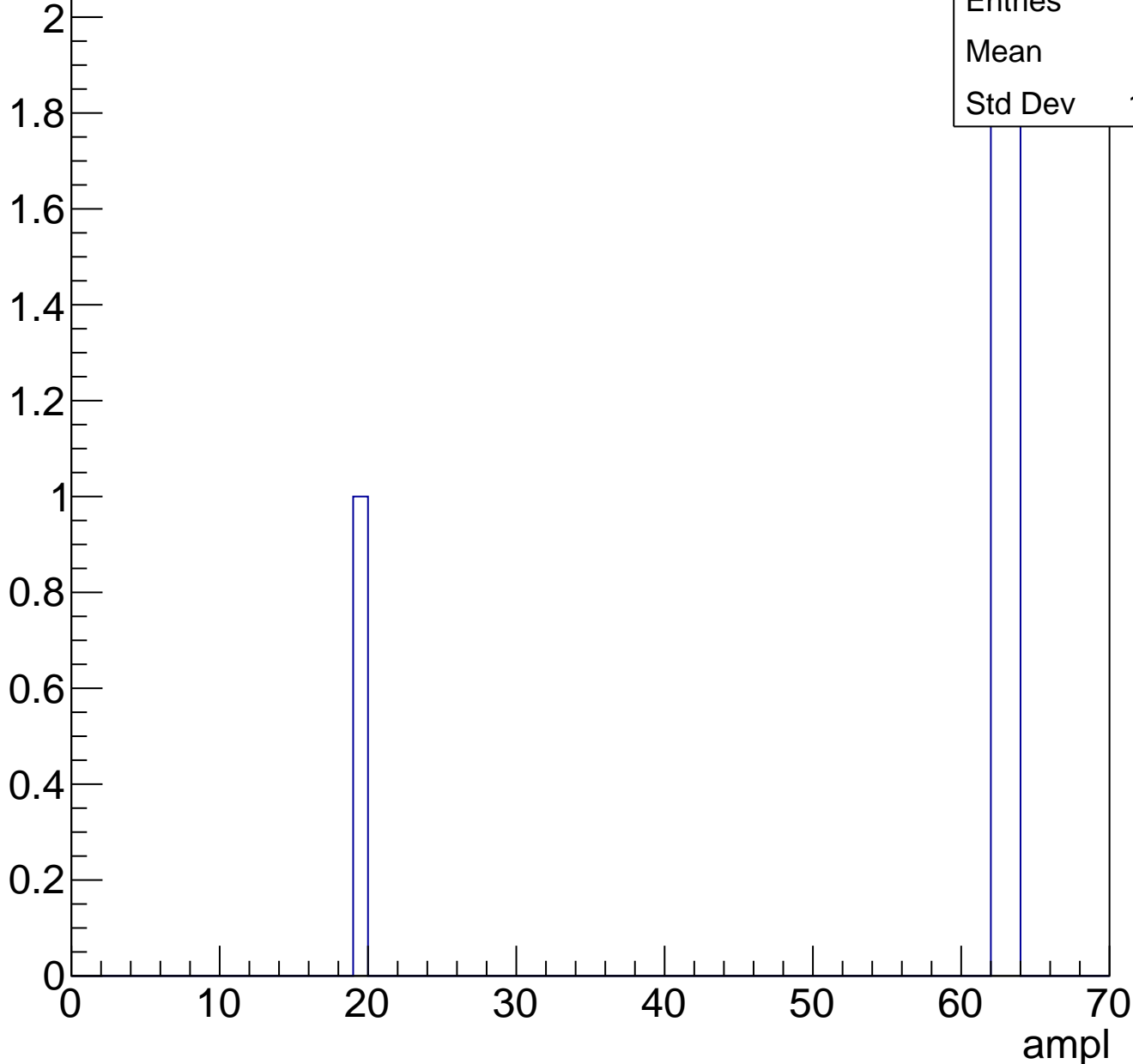
70



# B1L002S, U26-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch83, adc0

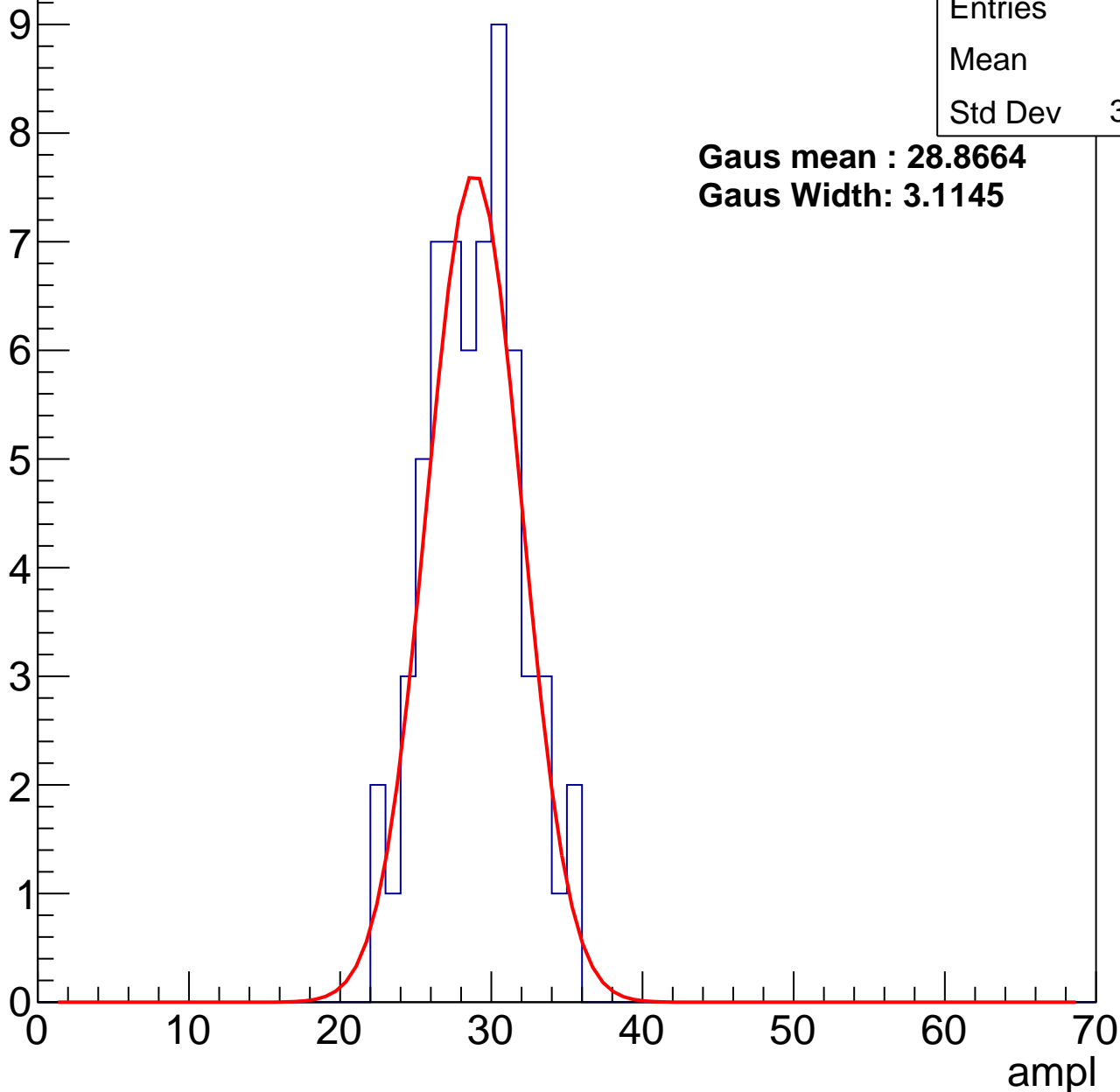
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.4
Std Dev	3.029

**Gaus mean : 28.8664**

**Gaus Width: 3.1145**



# B1L002S, U26-ch83, adc1

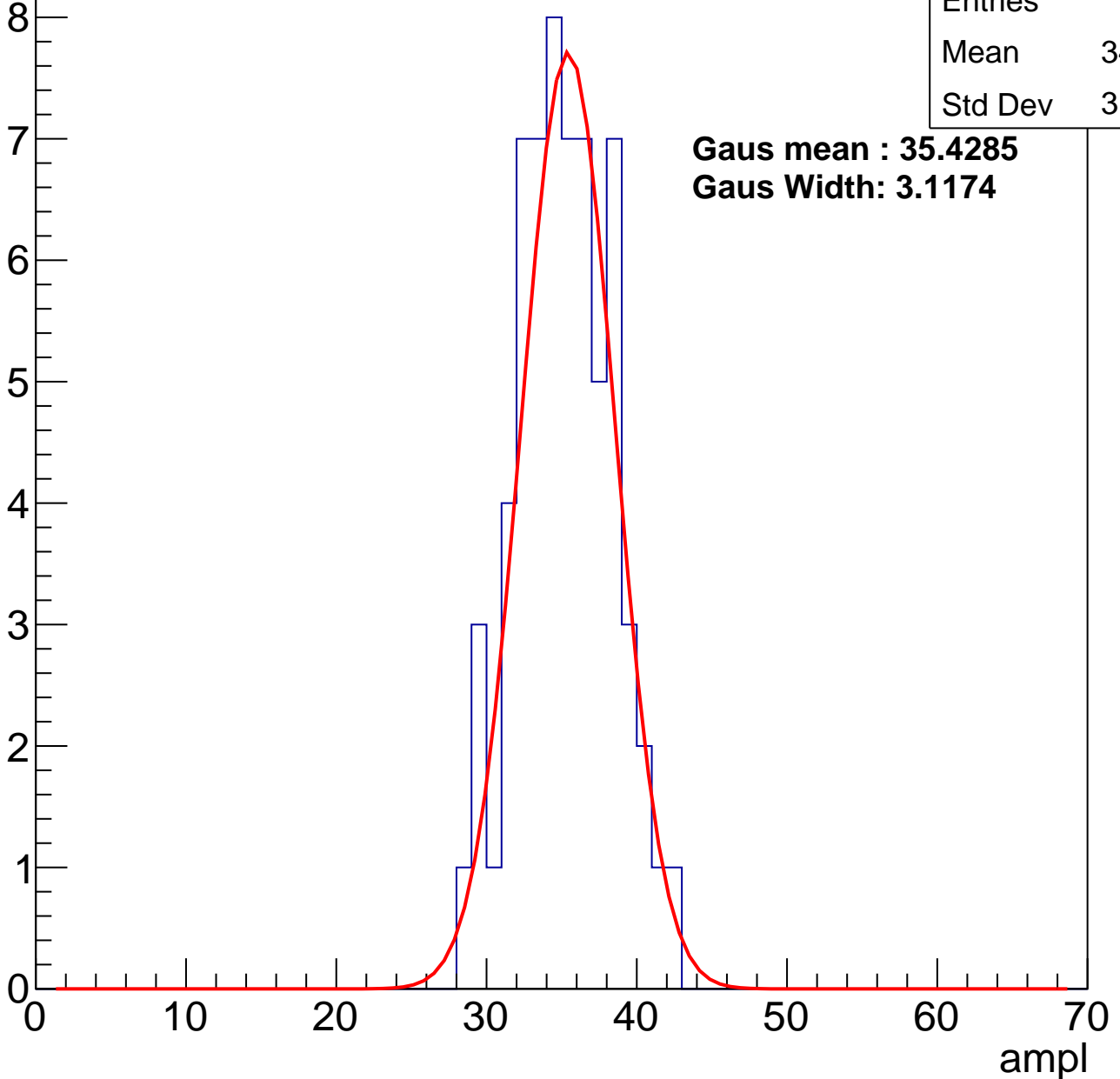
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.75
Std Dev	3.097

**Gaus mean : 35.4285**

**Gaus Width: 3.1174**



# B1L002S, U26-ch83, adc2

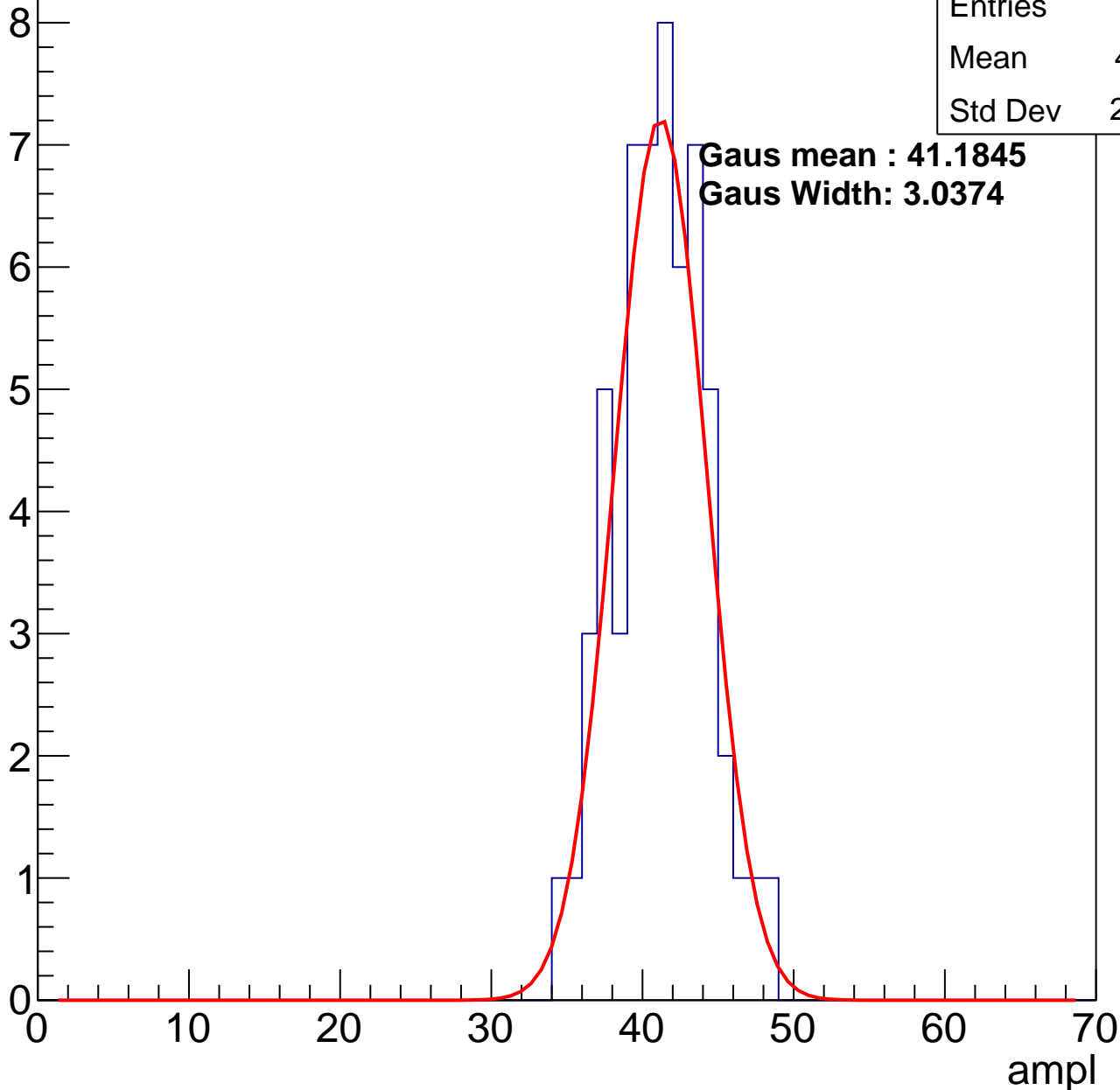
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.71
Std Dev	2.989

**Gaus mean : 41.1845**

**Gaus Width: 3.0374**

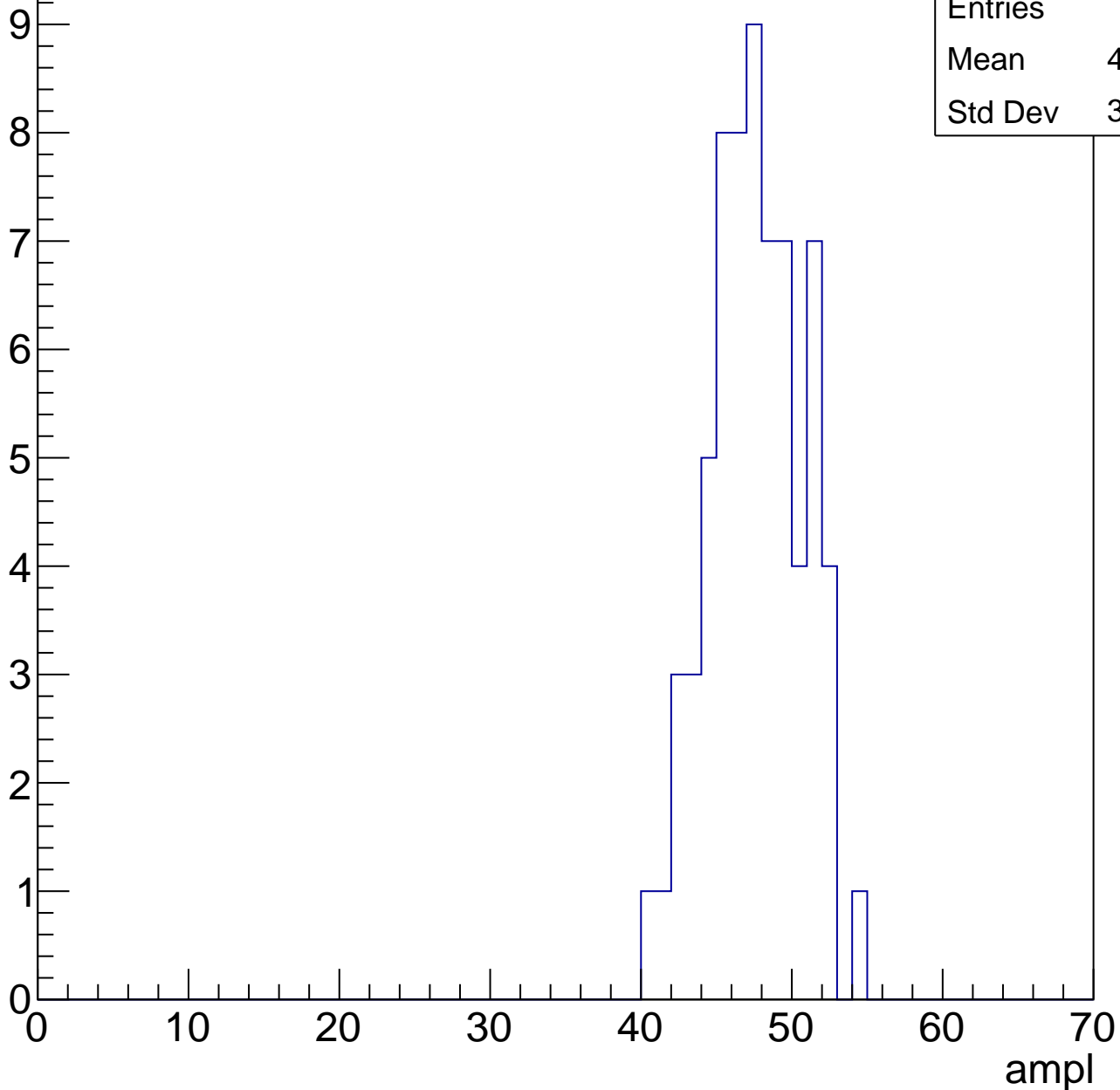


# B1L002S, U26-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	47.13
Std Dev	3.029

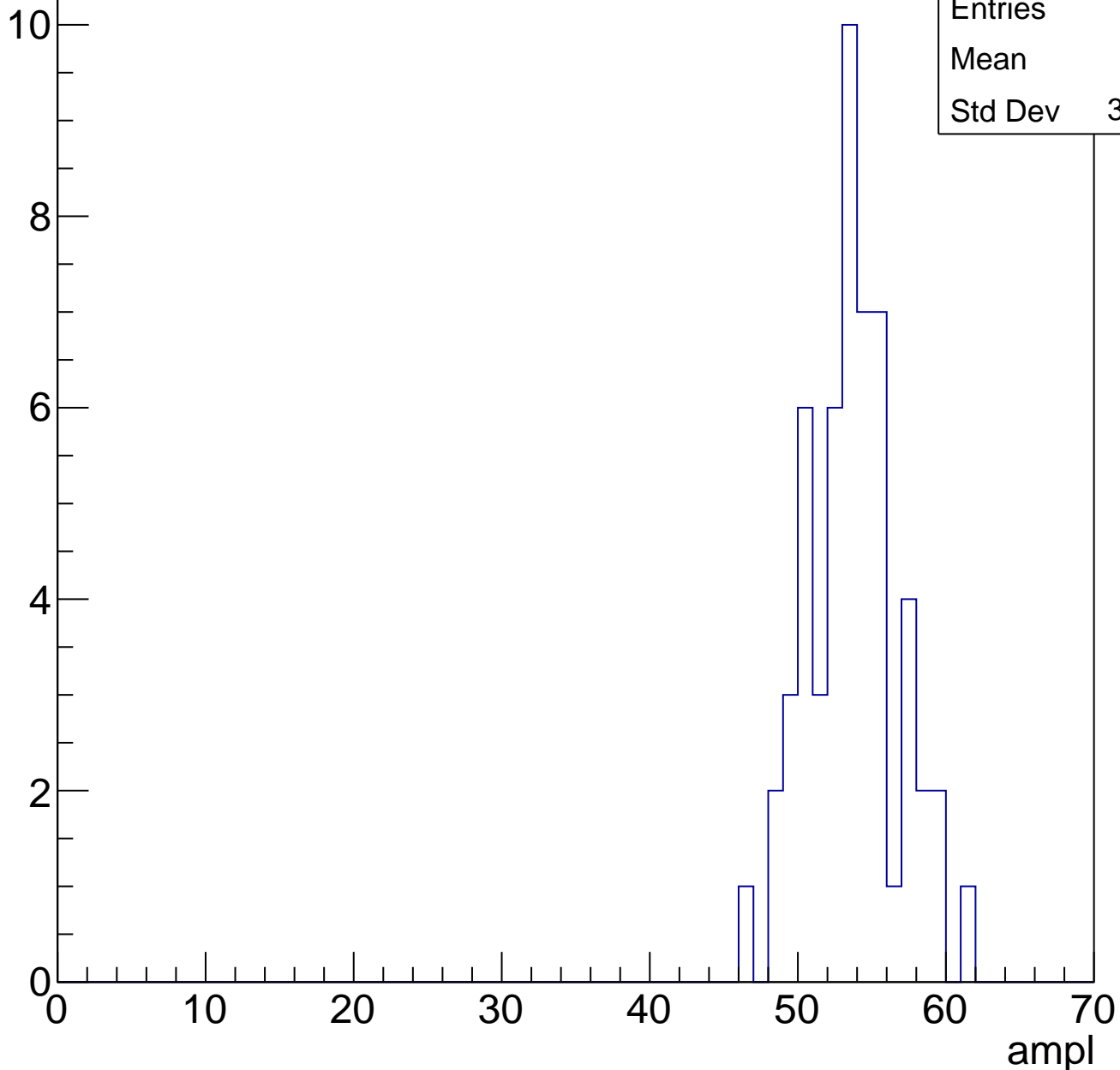


# B1L002S, U26-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

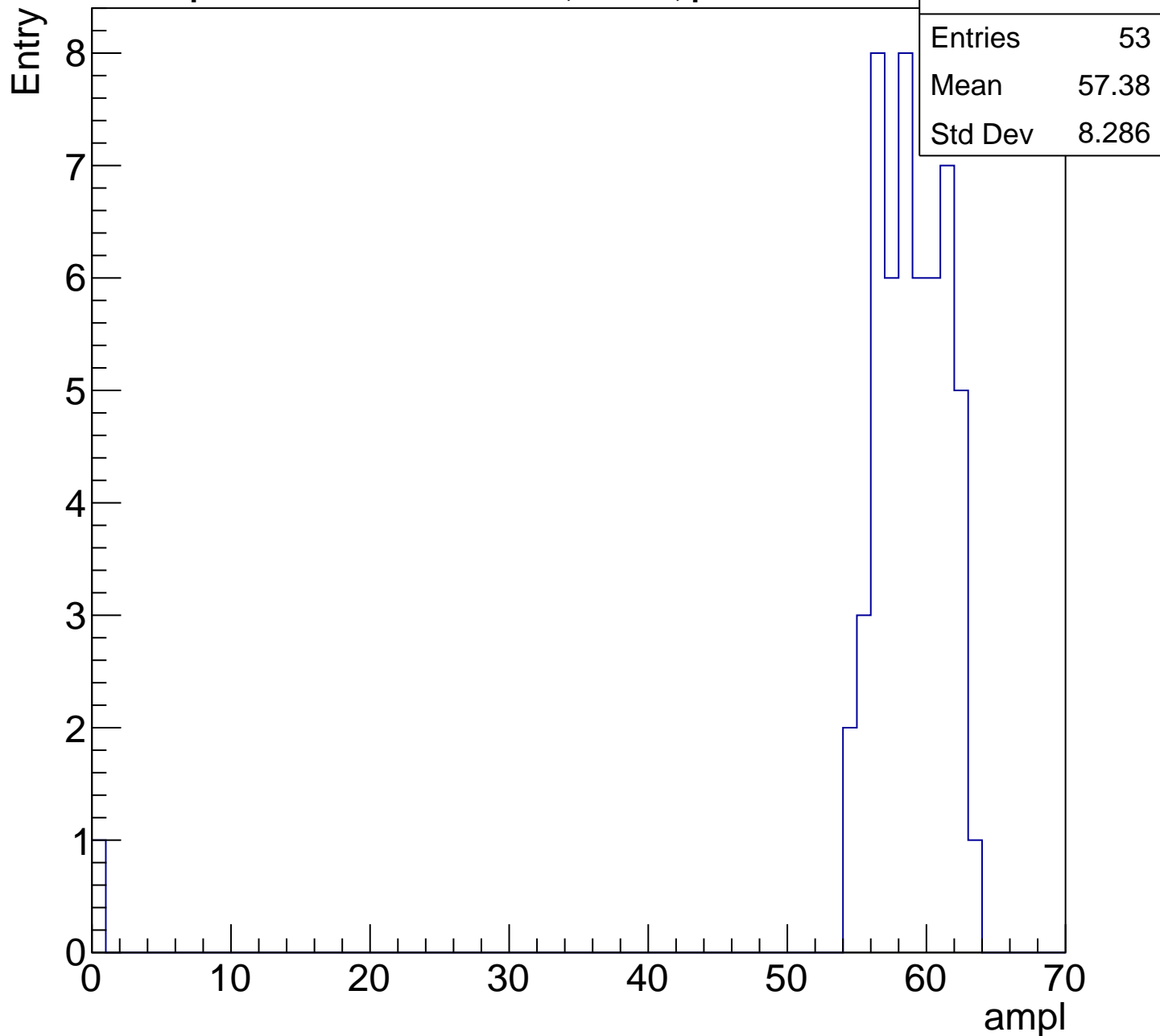
Entry

Entries	55
Mean	53.2
Std Dev	3.048



# B1L002S, U26-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

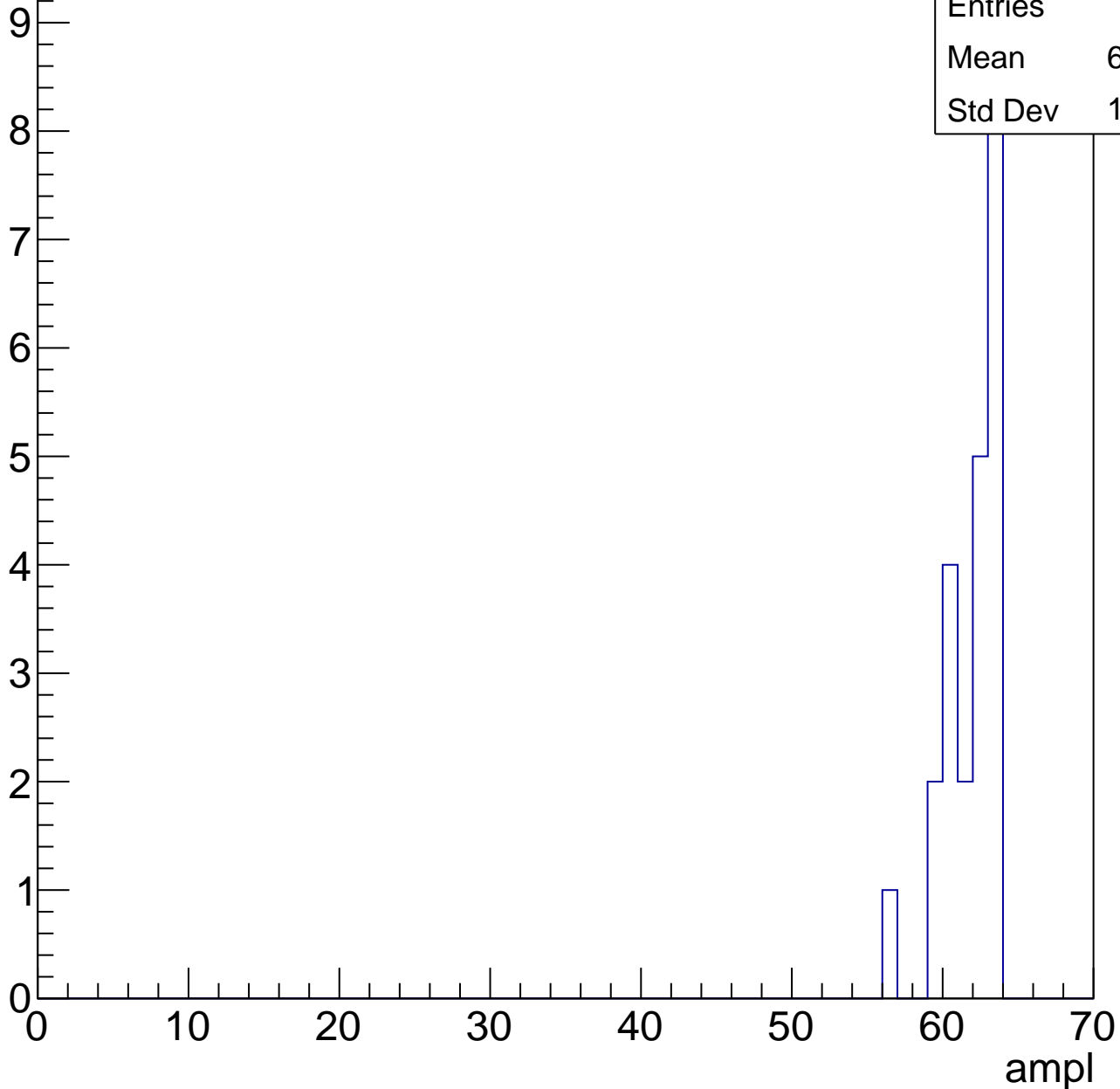


# B1L002S, U26-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.43
Std Dev	1.789





# B1L002S, U26-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch84, adc0

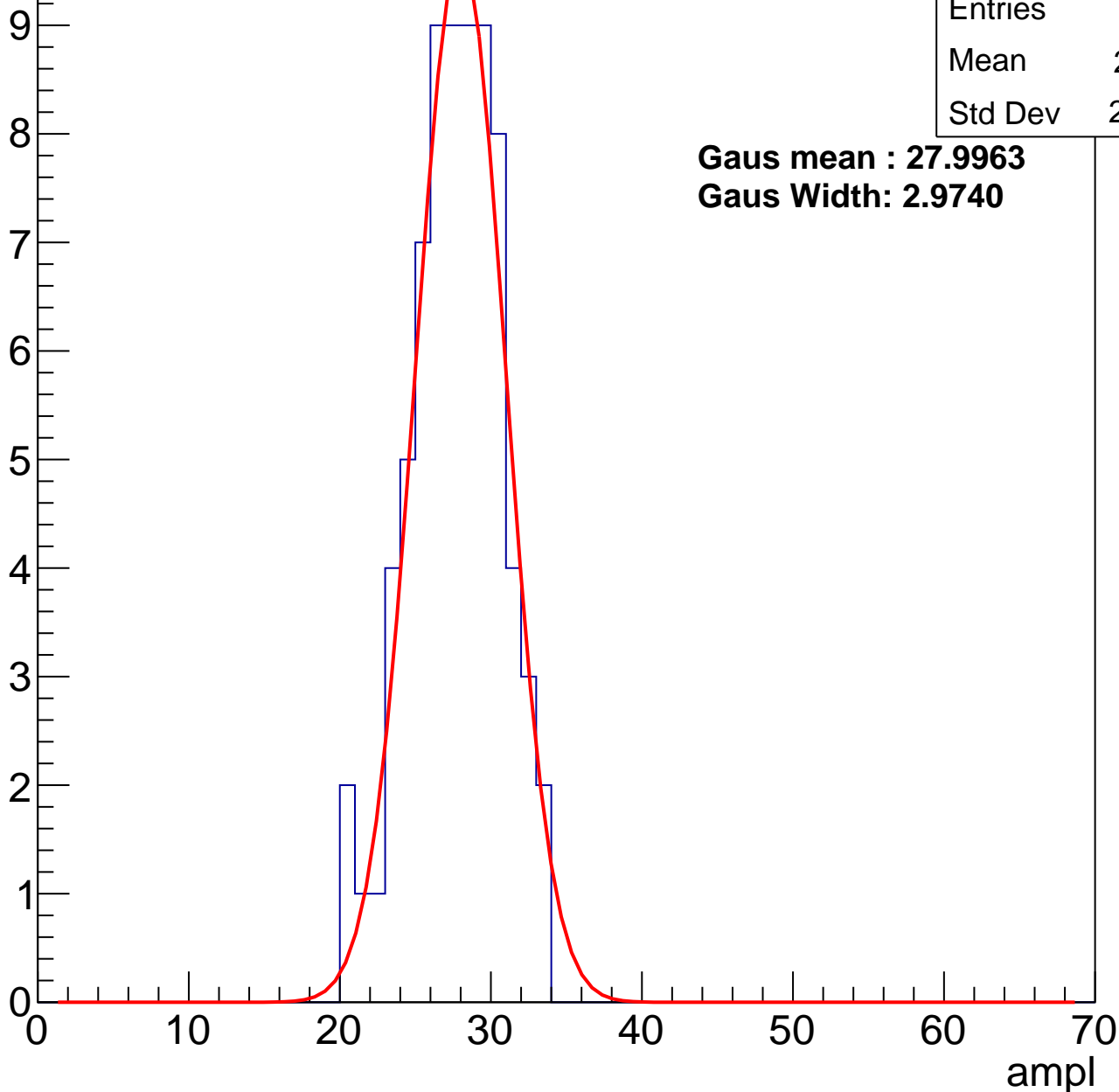
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	27.21
Std Dev	2.938

**Gaus mean : 27.9963**

**Gaus Width: 2.9740**



# B1L002S, U26-ch84, adc1

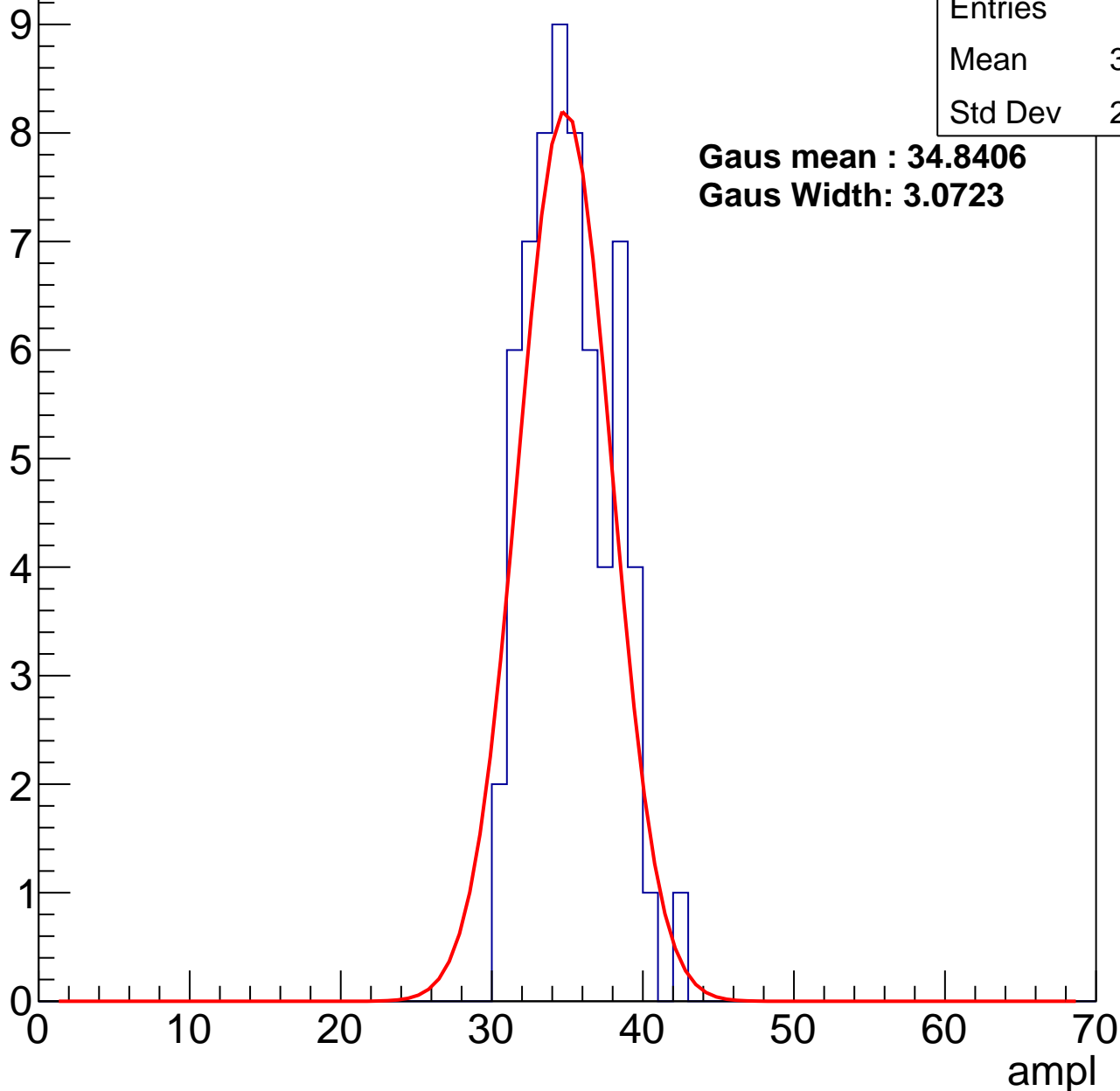
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.73
Std Dev	2.727

**Gaus mean : 34.8406**

**Gaus Width: 3.0723**



# B1L002S, U26-ch84, adc2

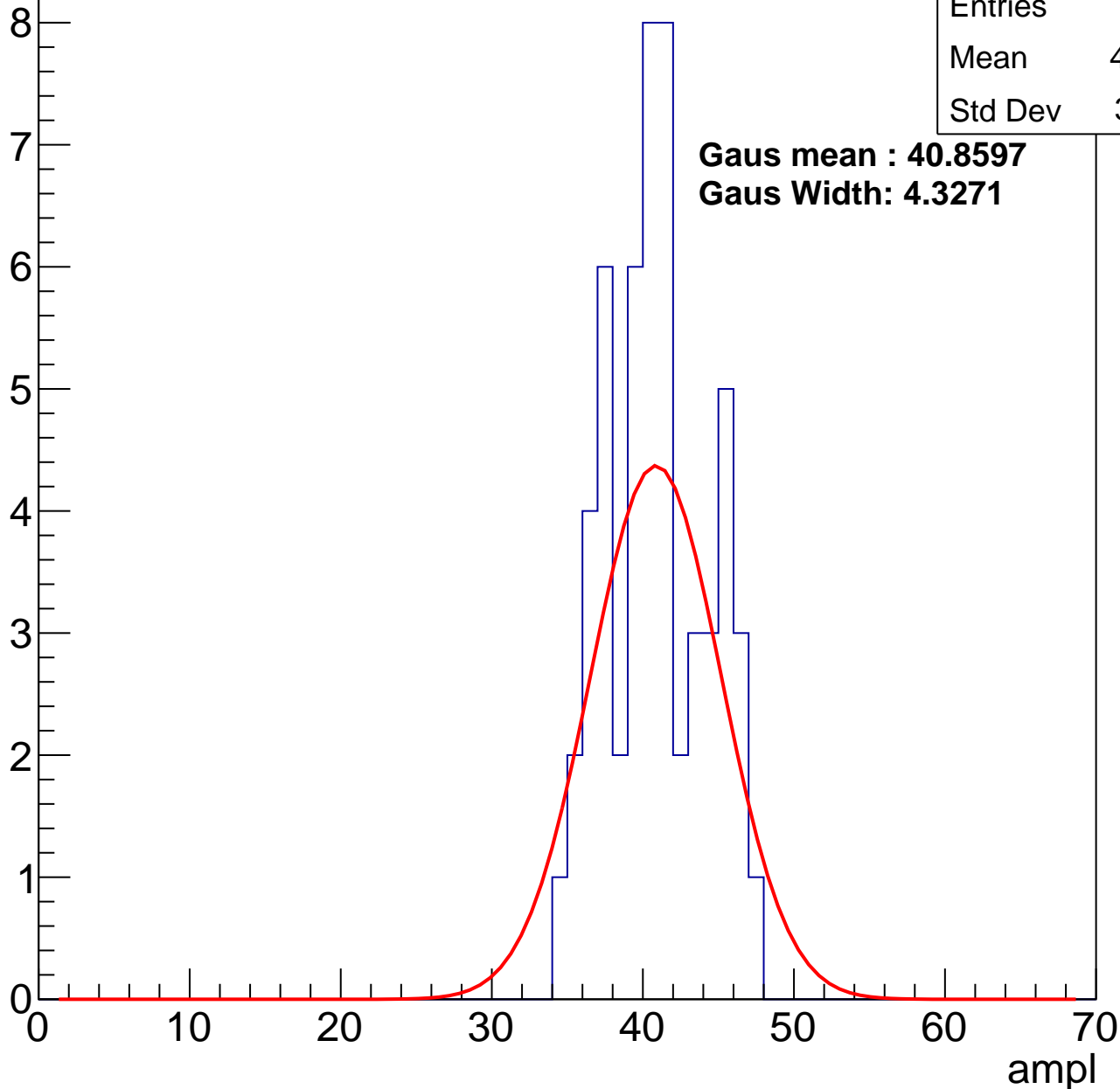
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.43
Std Dev	3.281

**Gaus mean : 40.8597**

**Gaus Width: 4.3271**

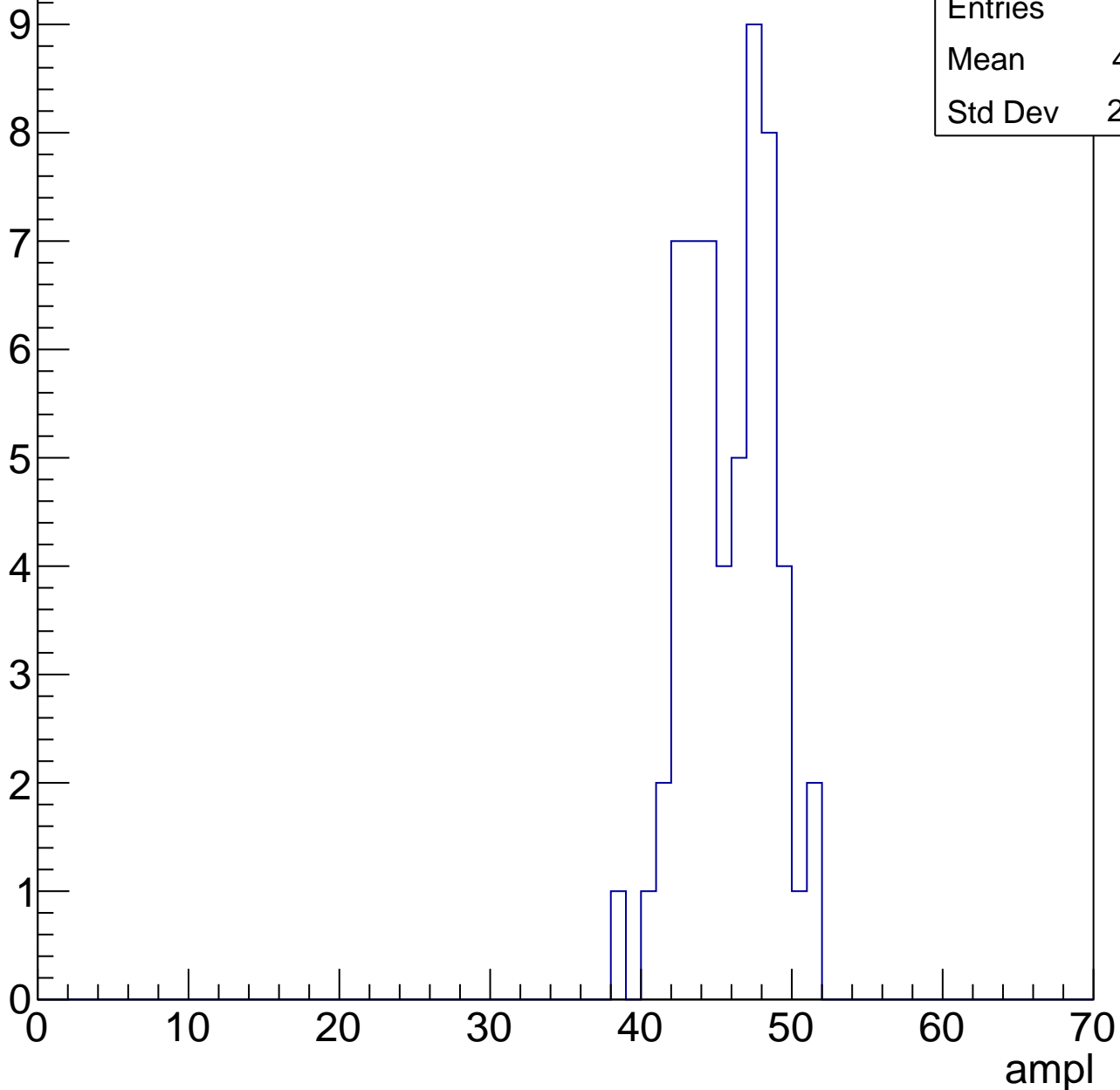


# B1L002S, U26-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.31
Std Dev	2.854

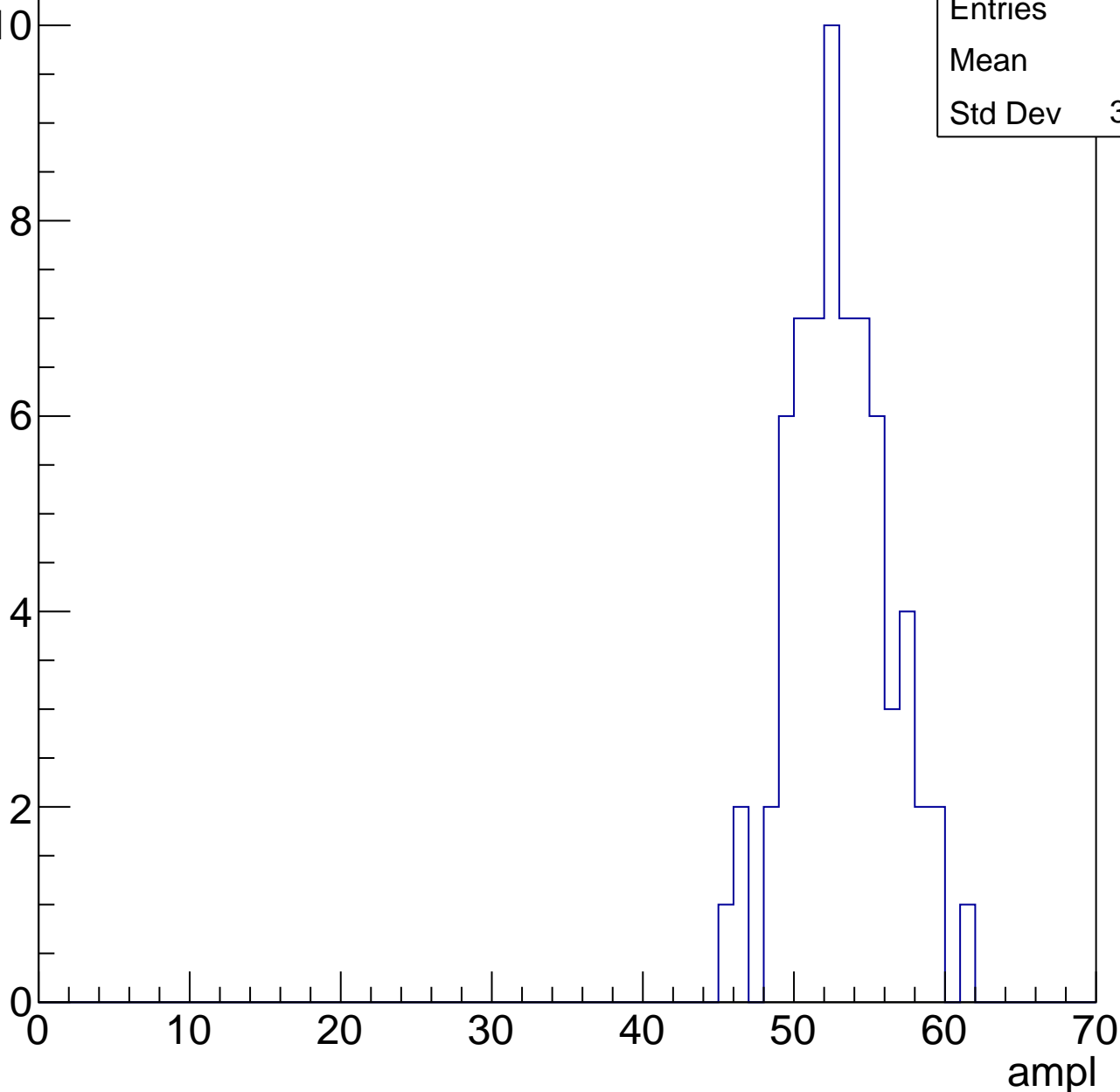


# B1L002S, U26-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	52.6
Std Dev	3.246

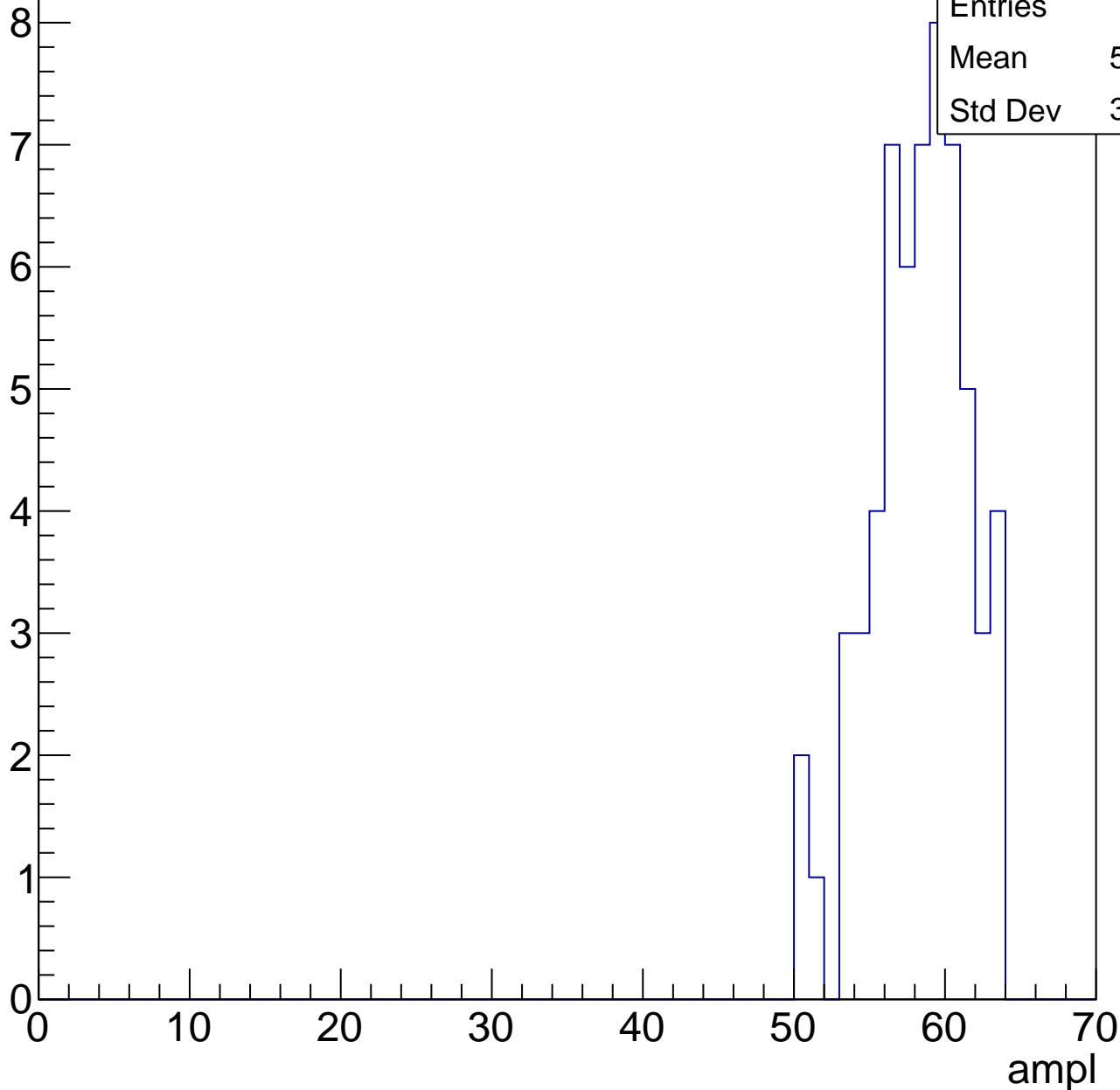


# B1L002S, U26-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	57.78
Std Dev	3.152

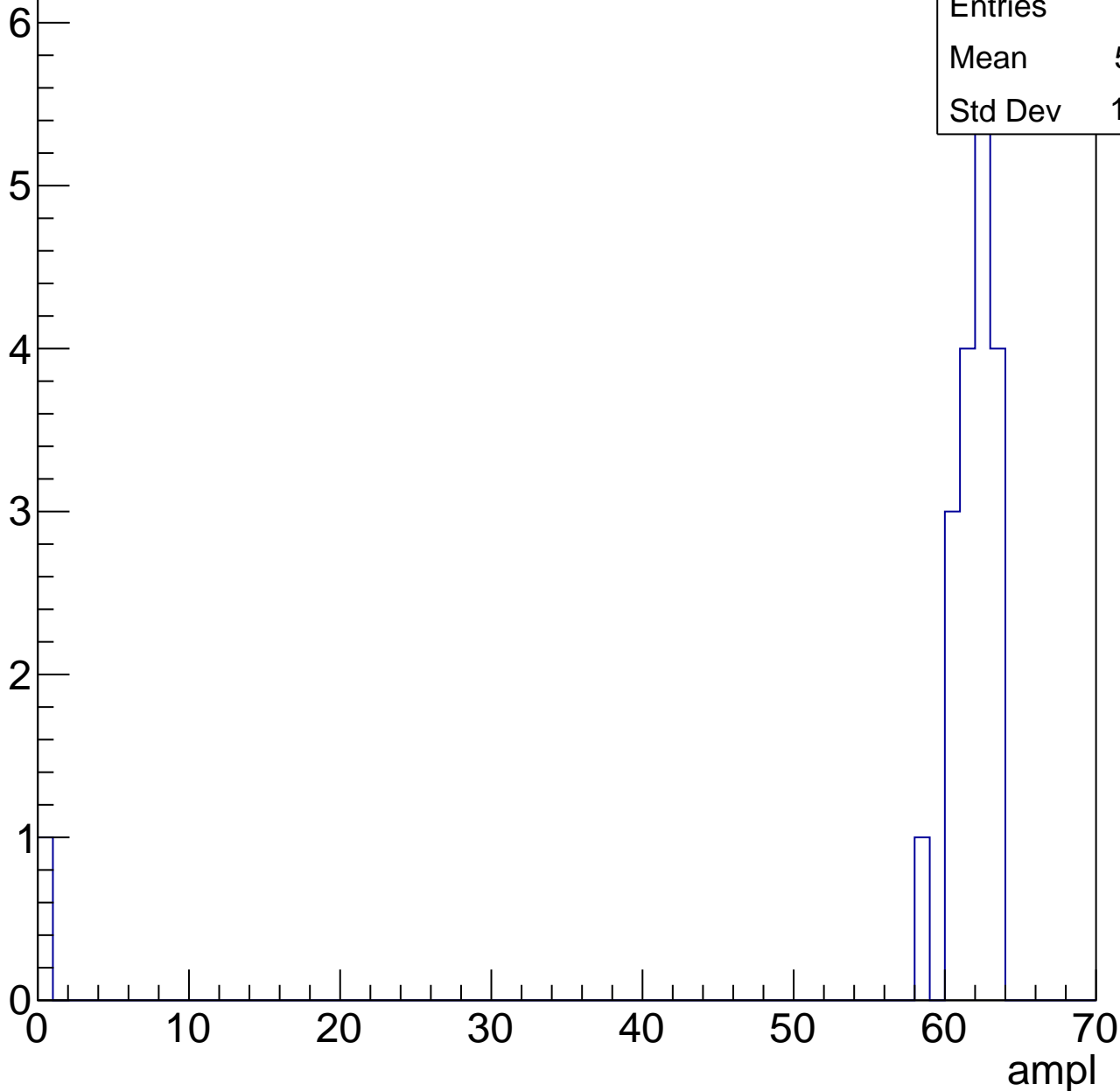


# B1L002S, U26-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.21
Std Dev	13.78

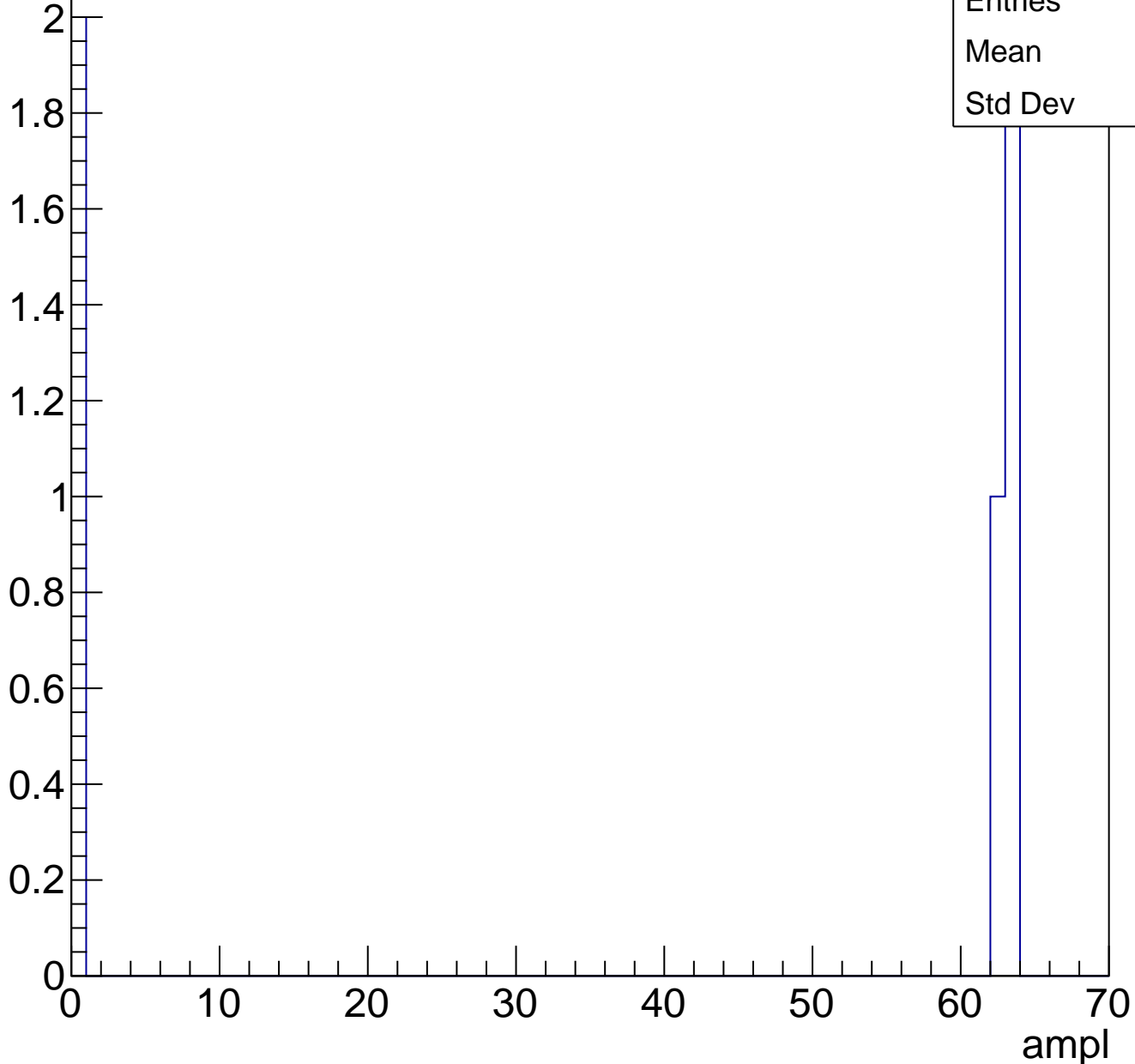




# B1L002S, U26-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch85, adc0

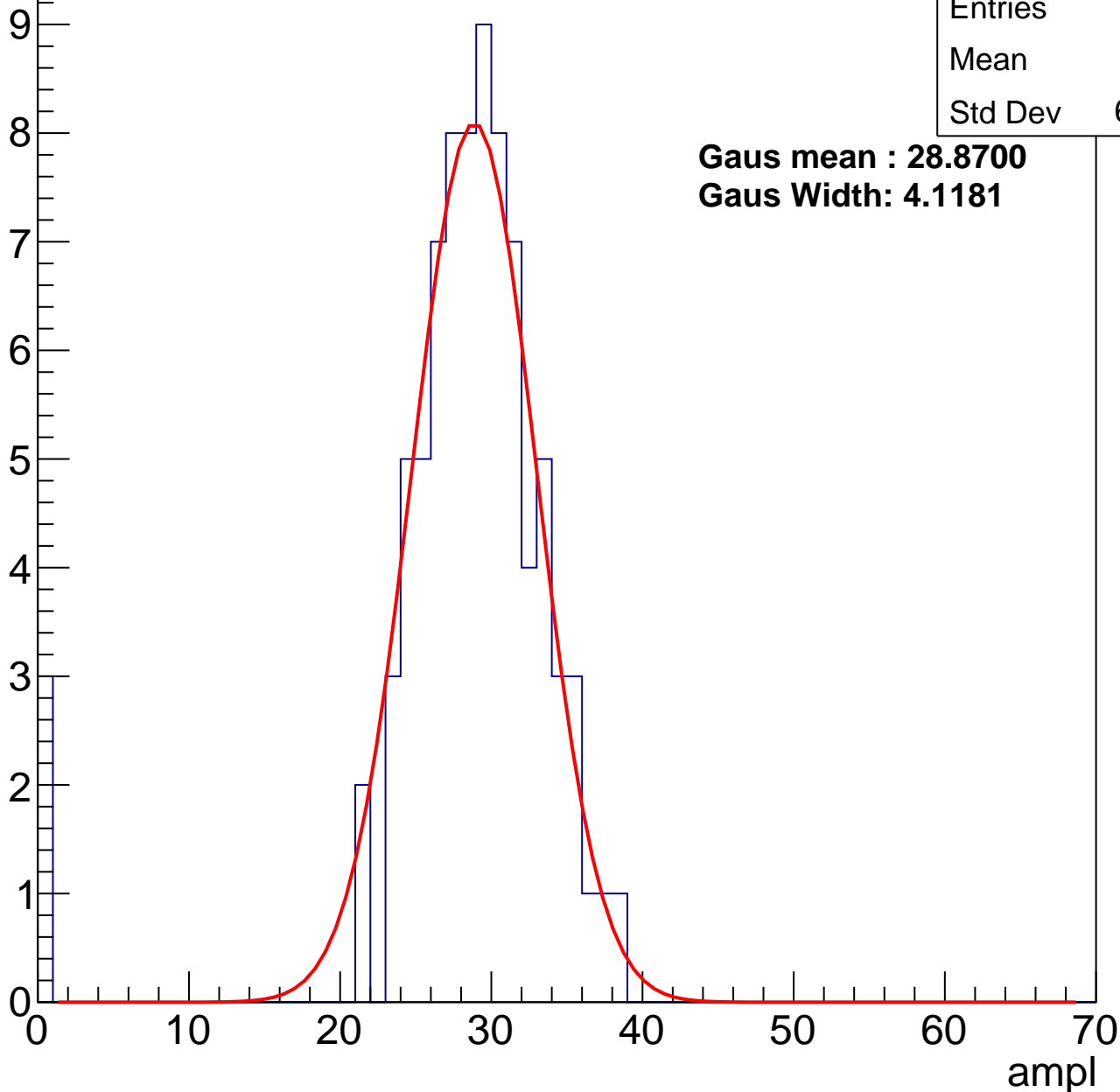
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	27.8
Std Dev	6.471

**Gaus mean : 28.8700**

**Gaus Width: 4.1181**



# B1L002S, U26-ch85, adc1

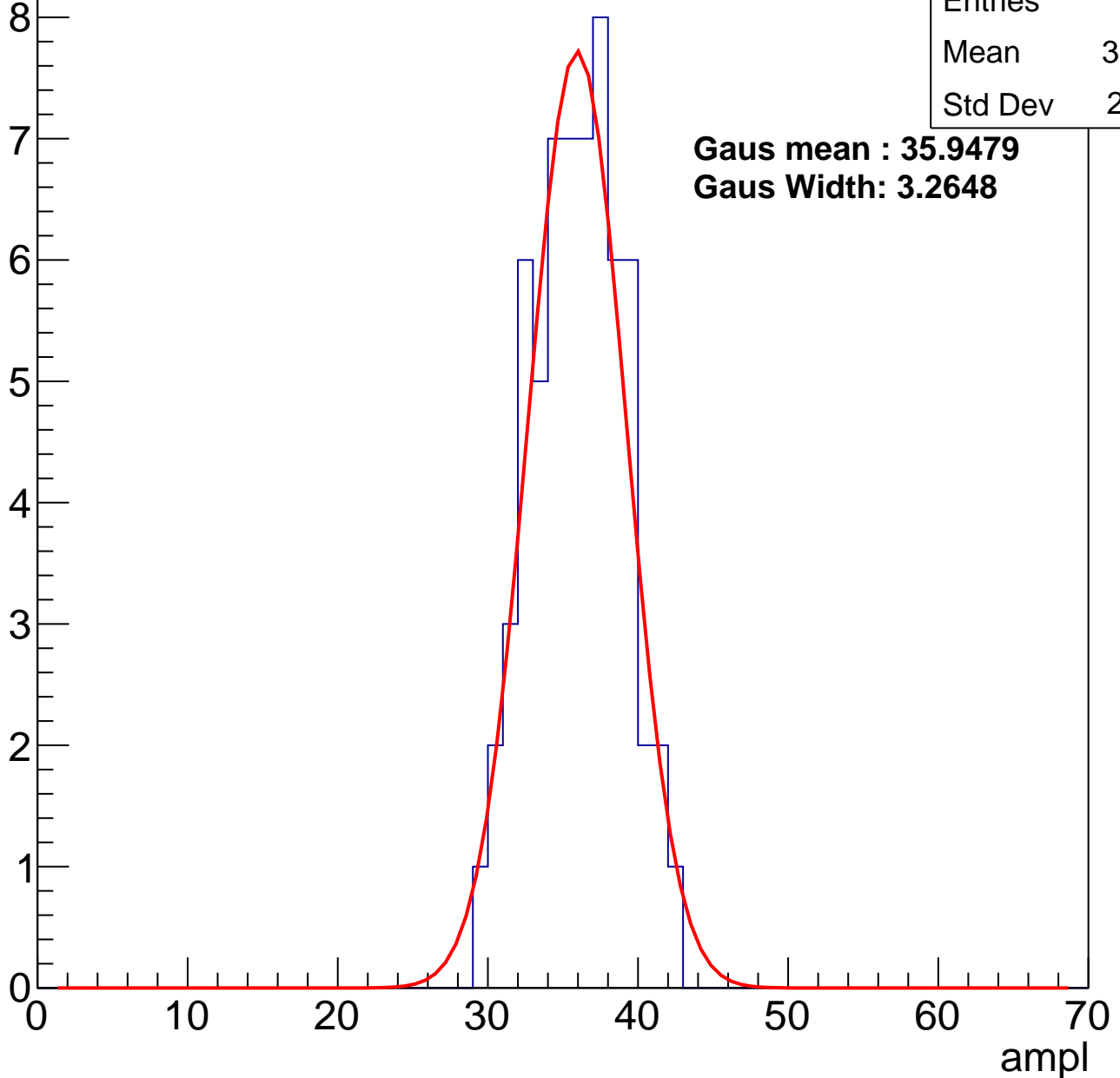
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.49
Std Dev	2.981

**Gaus mean : 35.9479**

**Gaus Width: 3.2648**



# B1L002S, U26-ch85, adc2

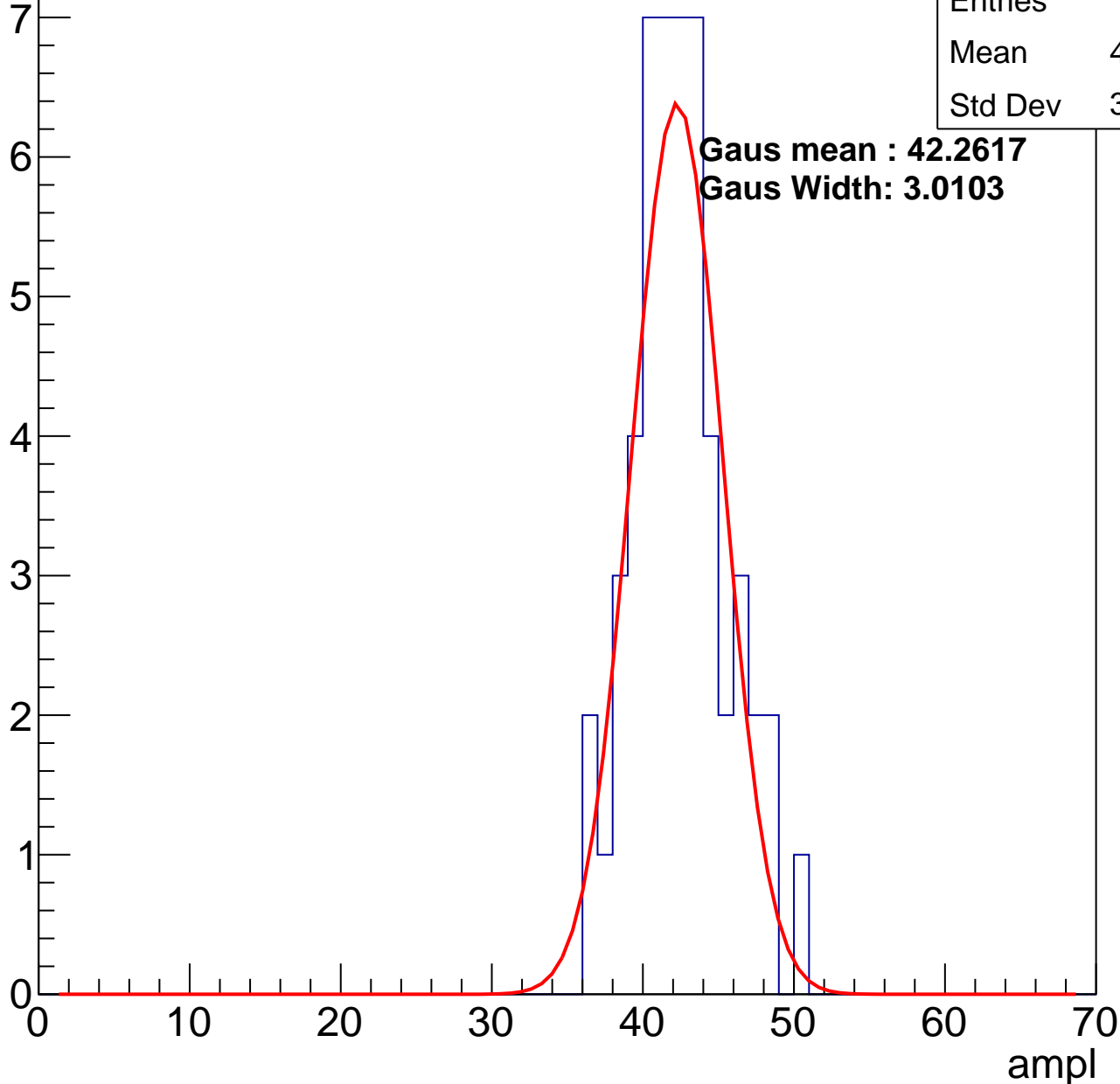
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	42.02
Std Dev	3.073

**Gaus mean : 42.2617**

**Gaus Width: 3.0103**

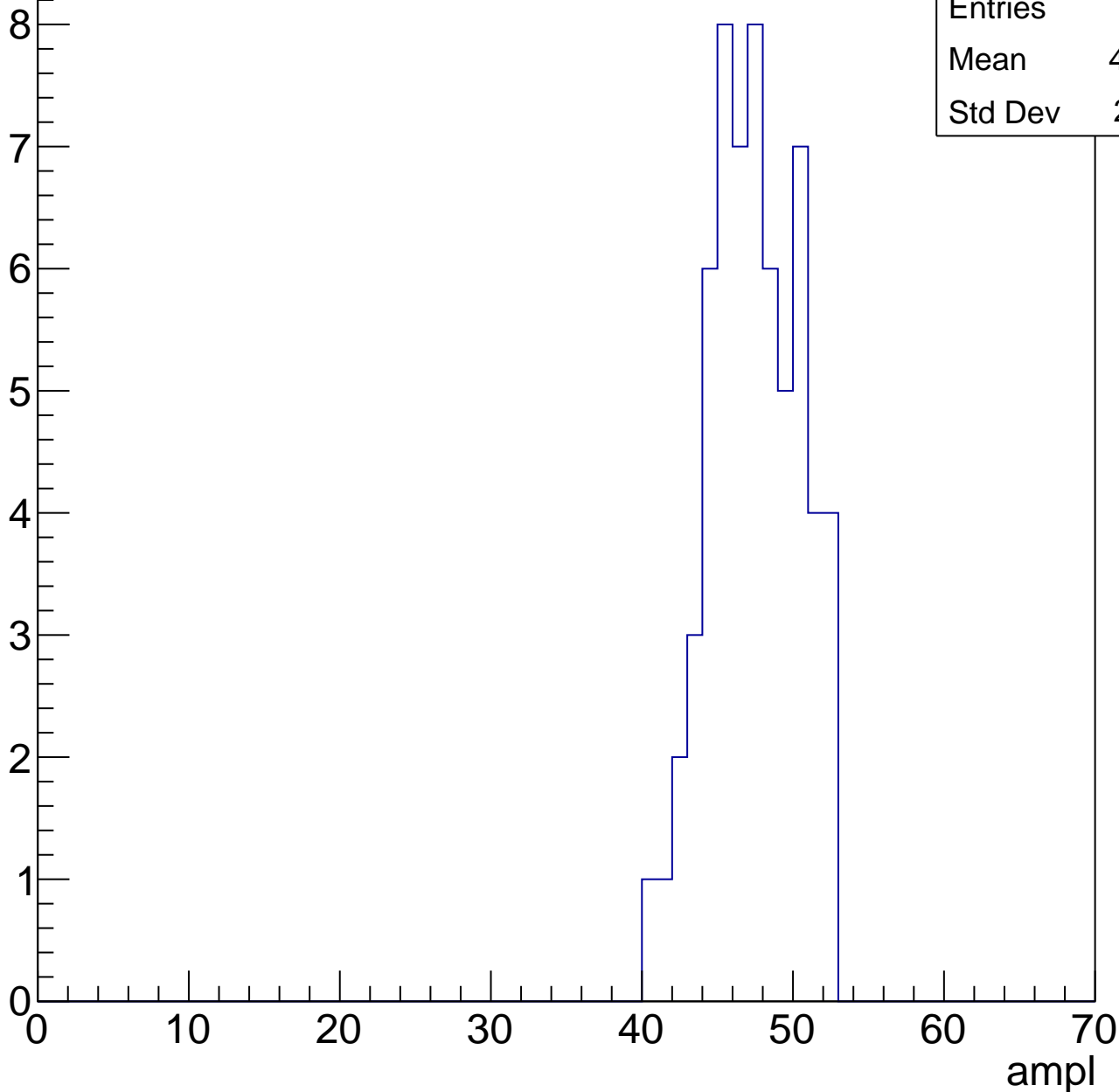


# B1L002S, U26-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.95
Std Dev	2.921



# B1L002S, U26-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

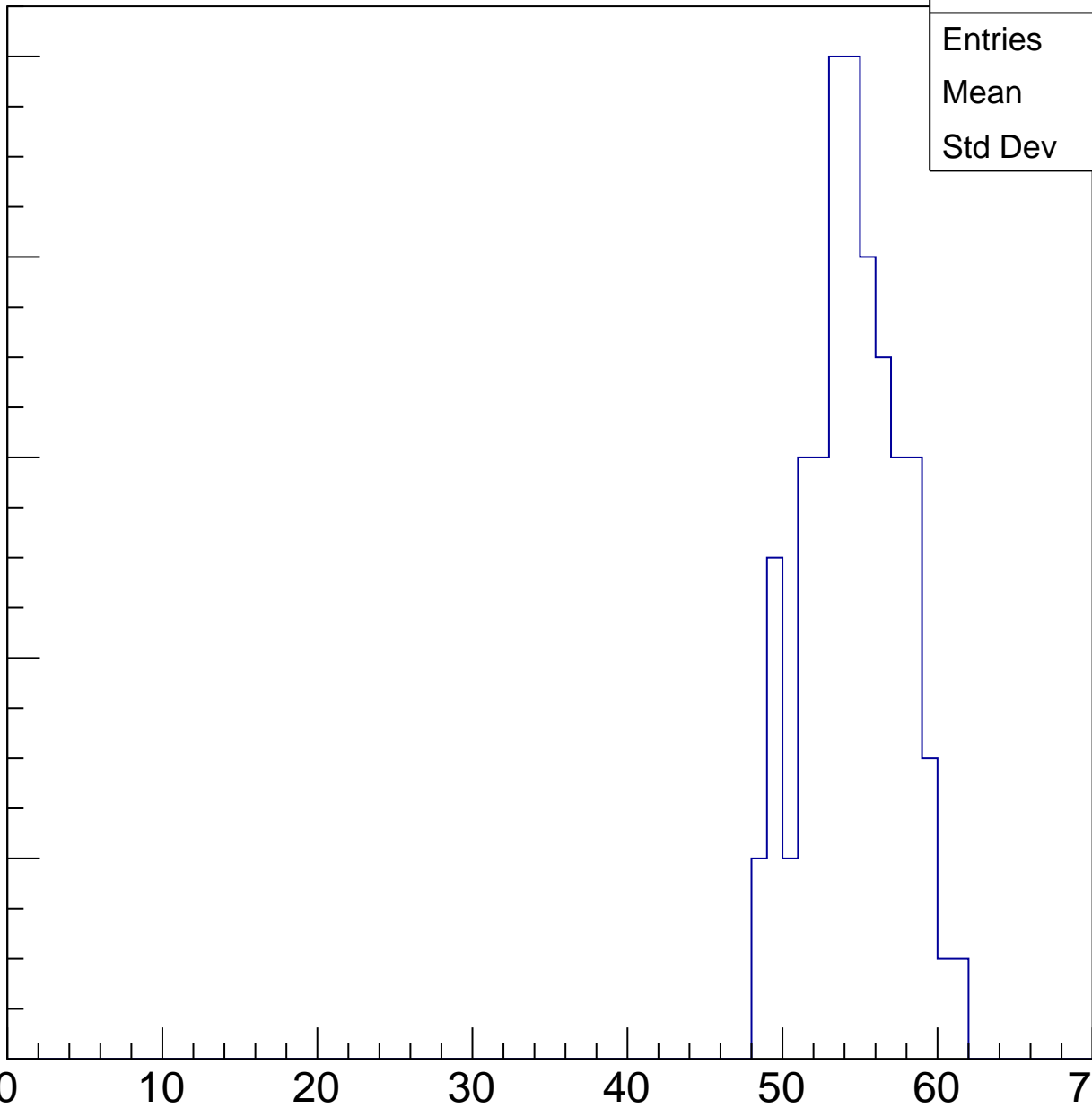
Entries	73
Mean	54.1
Std Dev	3.012

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

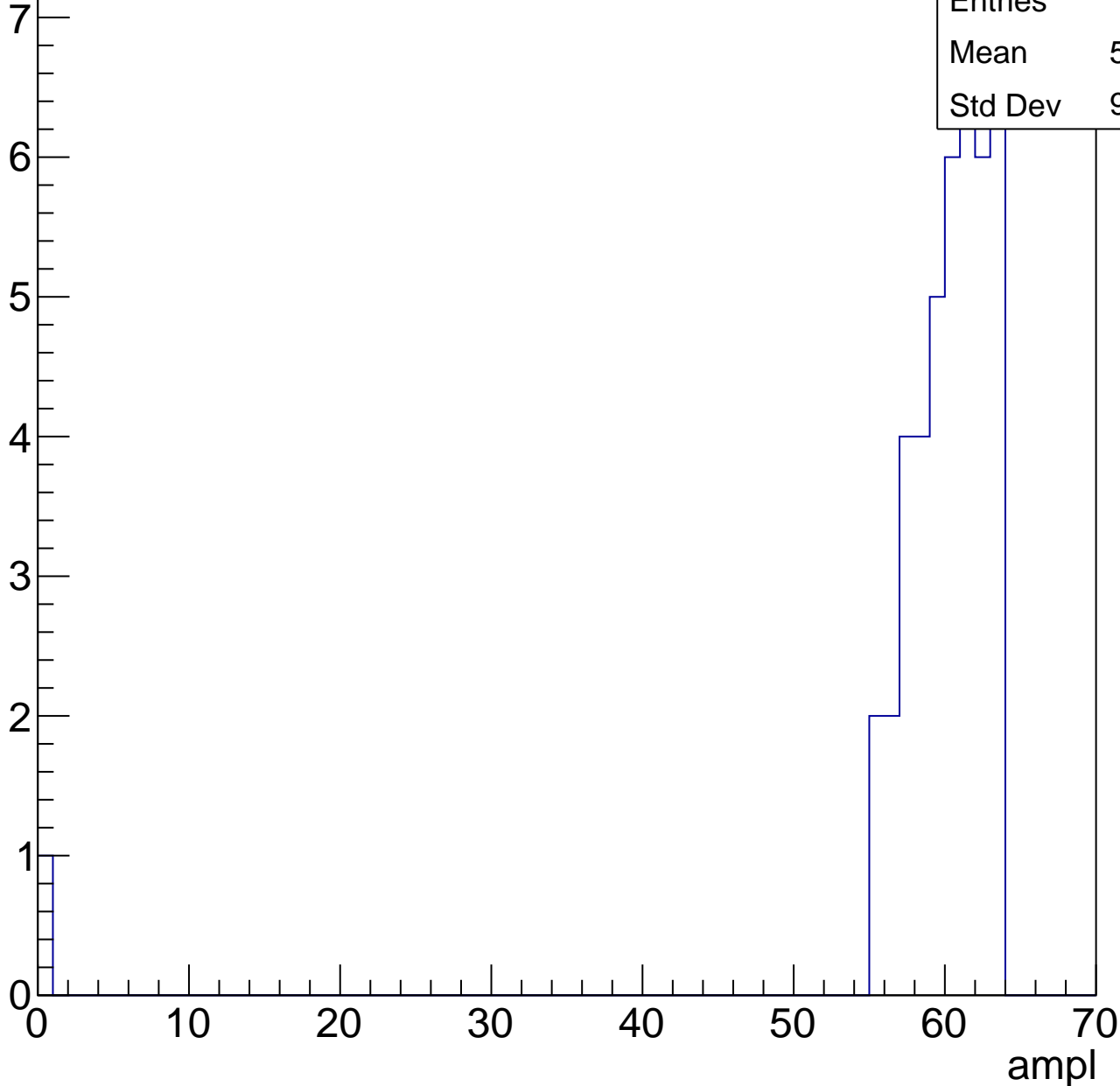


# B1L002S, U26-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

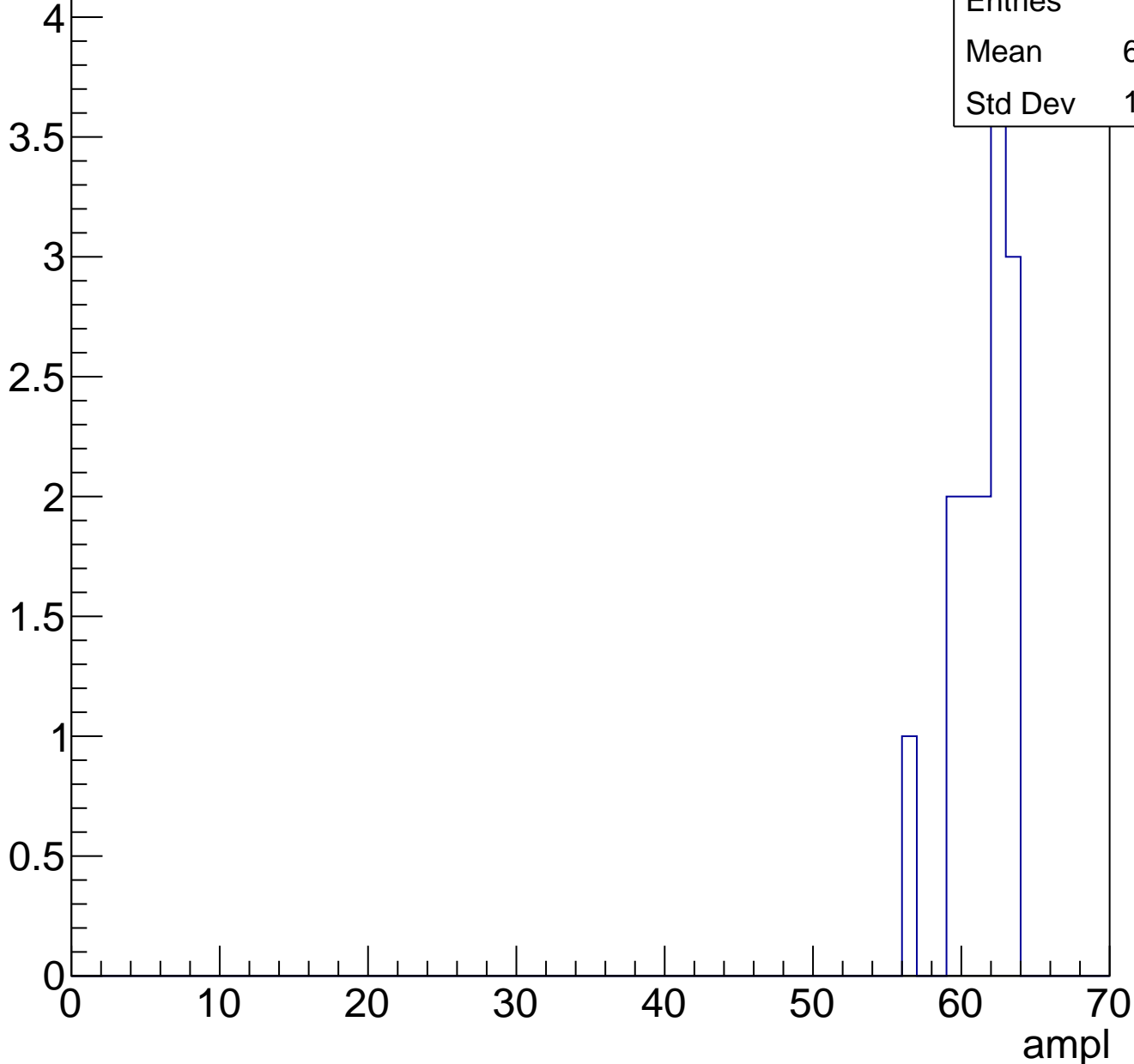
Entries	44
Mean	58.57
Std Dev	9.223



# B1L002S, U26-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

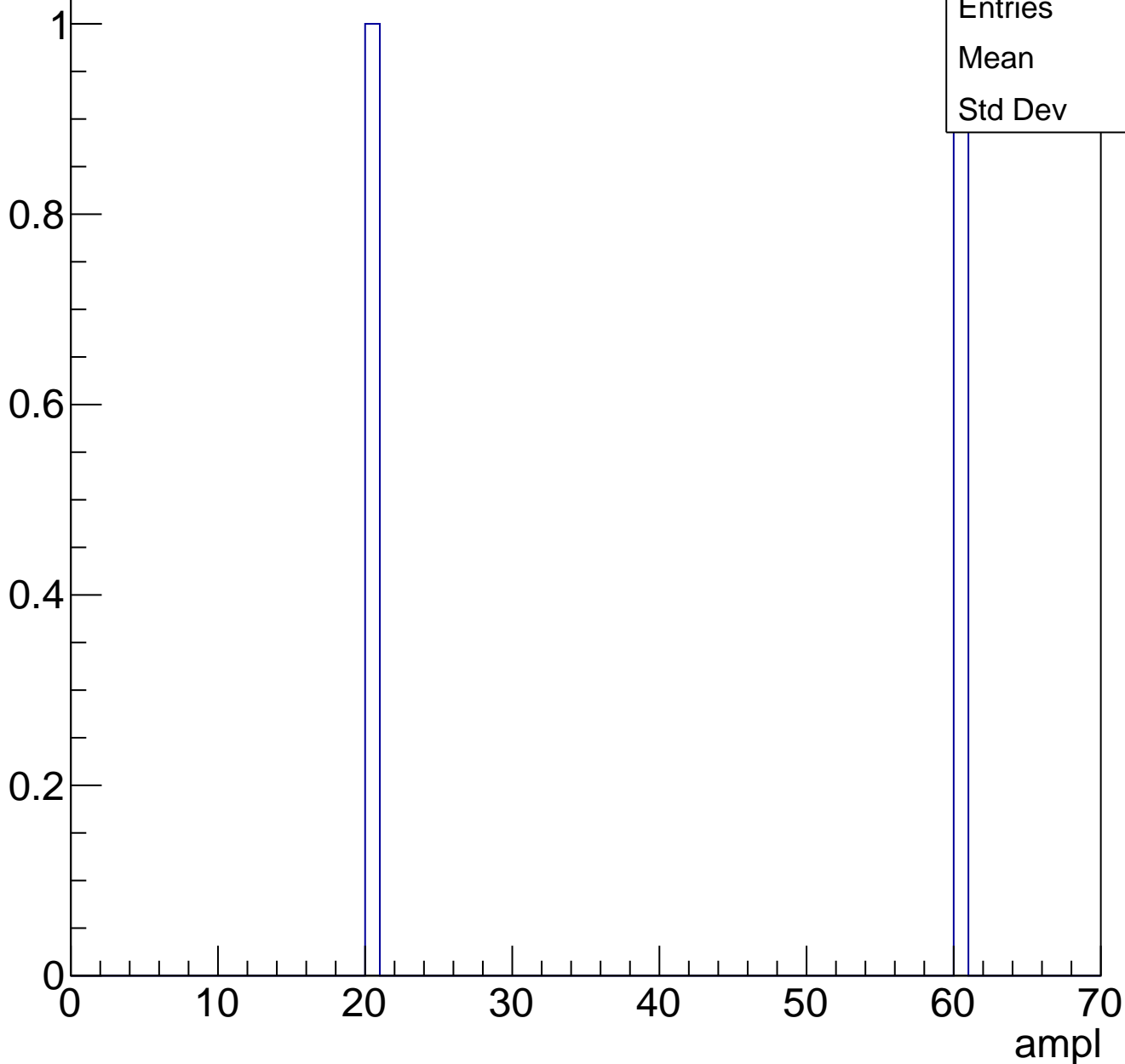




# B1L002S, U26-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	40
Std Dev	20

# B1L002S, U26-ch86, adc0

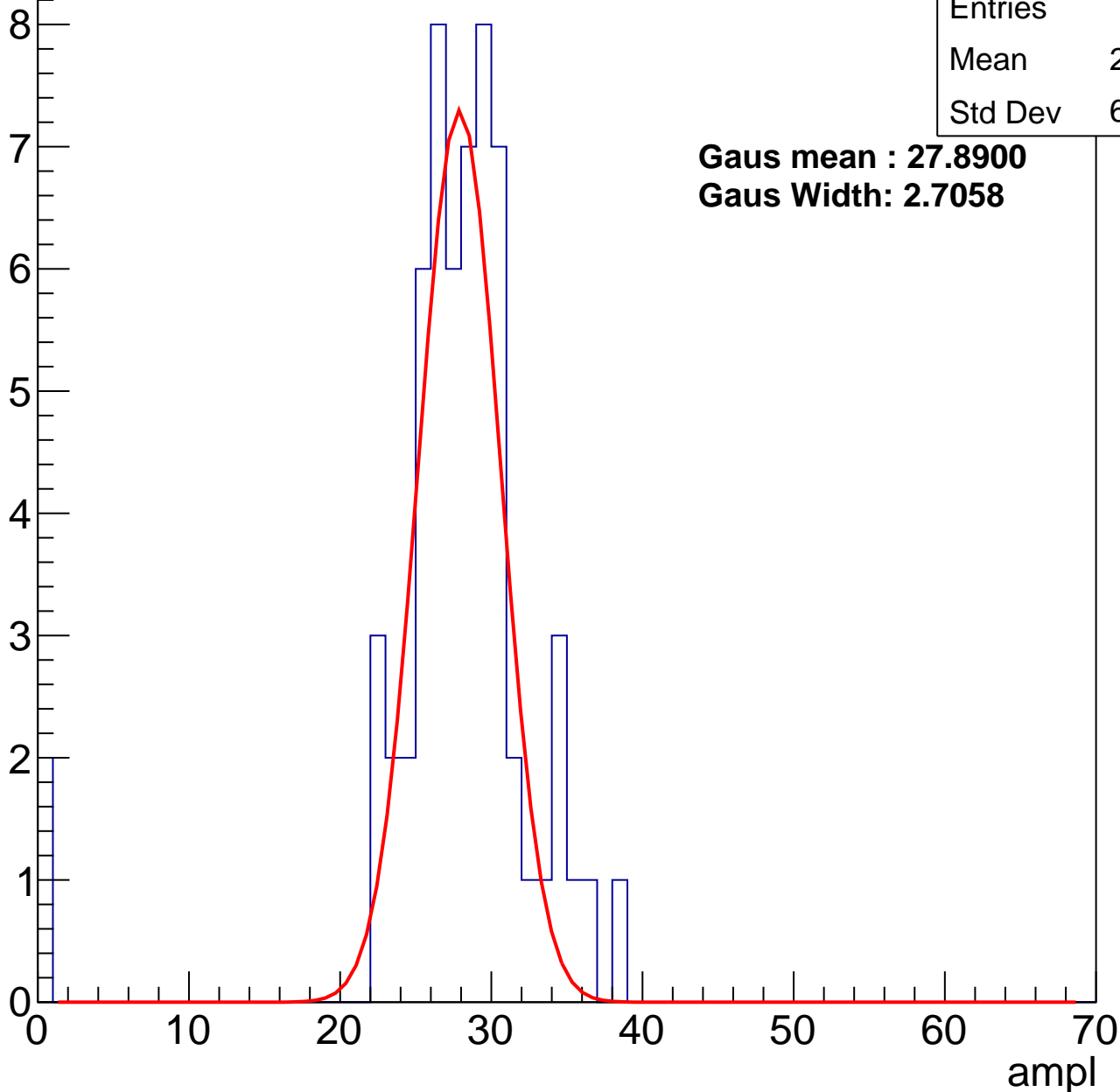
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.15
Std Dev	6.035

**Gaus mean : 27.8900**

**Gaus Width: 2.7058**



# B1L002S, U26-ch86, adc1

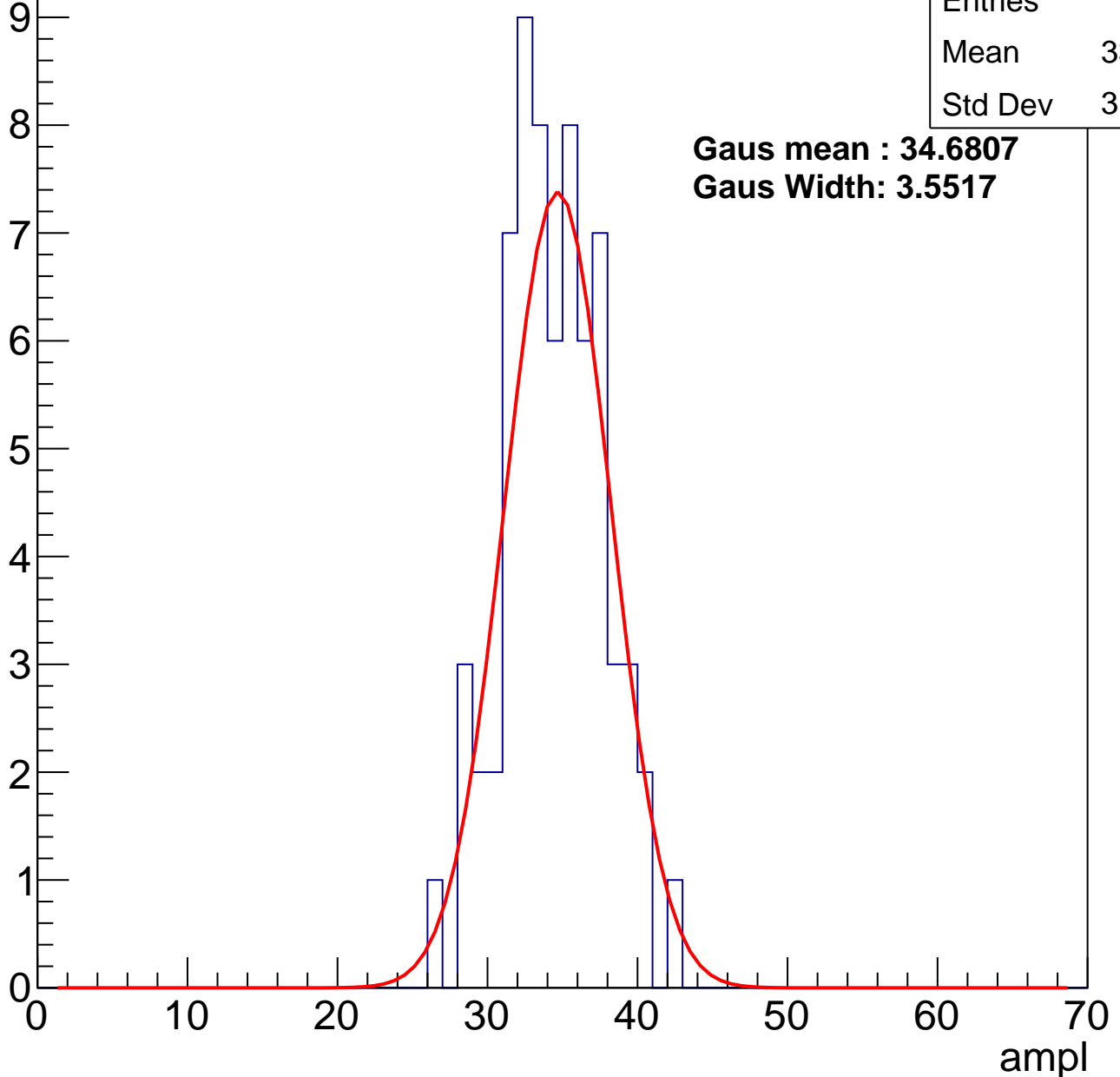
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.96
Std Dev	3.242

**Gaus mean : 34.6807**

**Gaus Width: 3.5517**



# B1L002S, U26-ch86, adc2

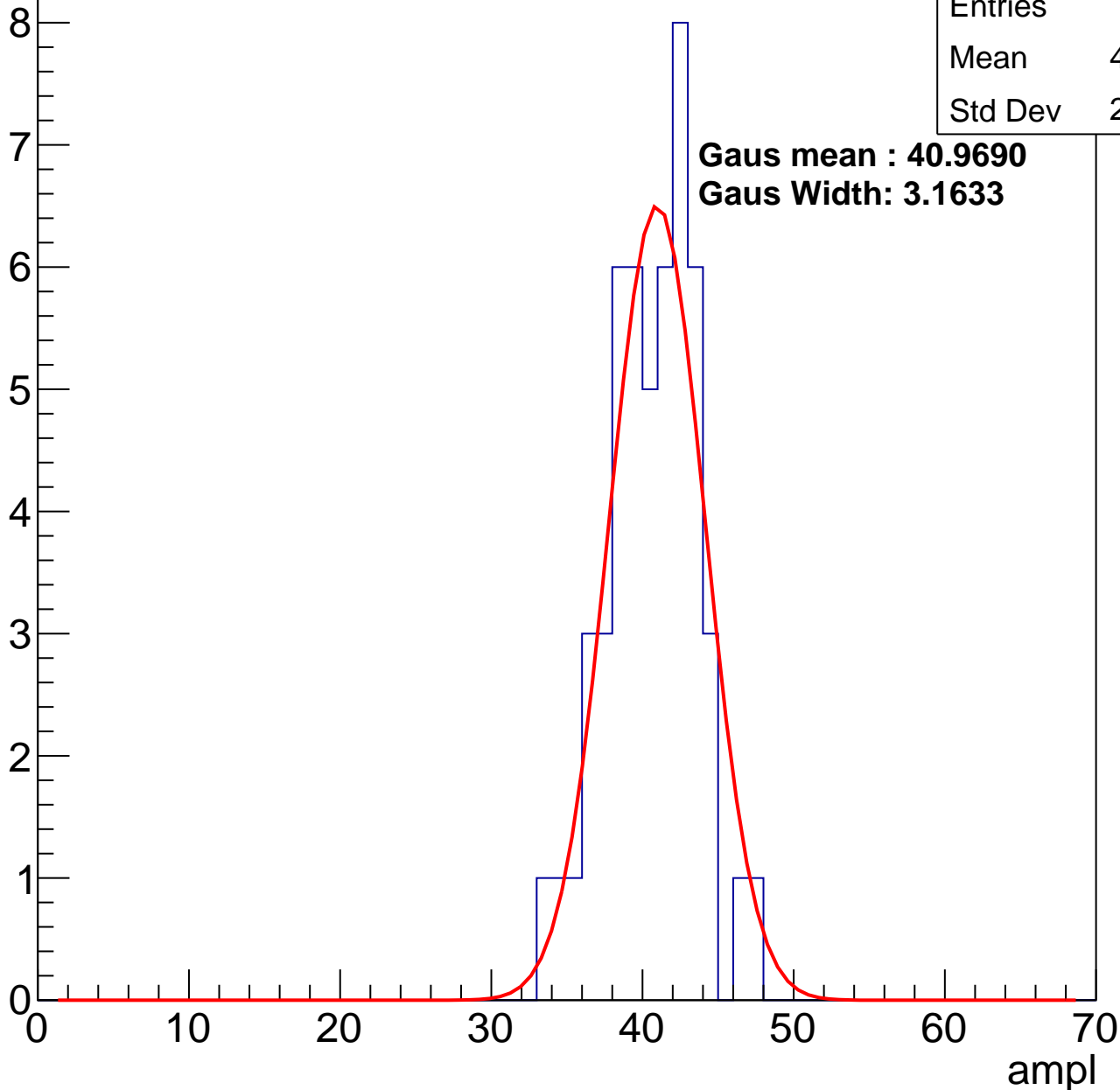
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.16
Std Dev	2.933

**Gaus mean : 40.9690**

**Gaus Width: 3.1633**

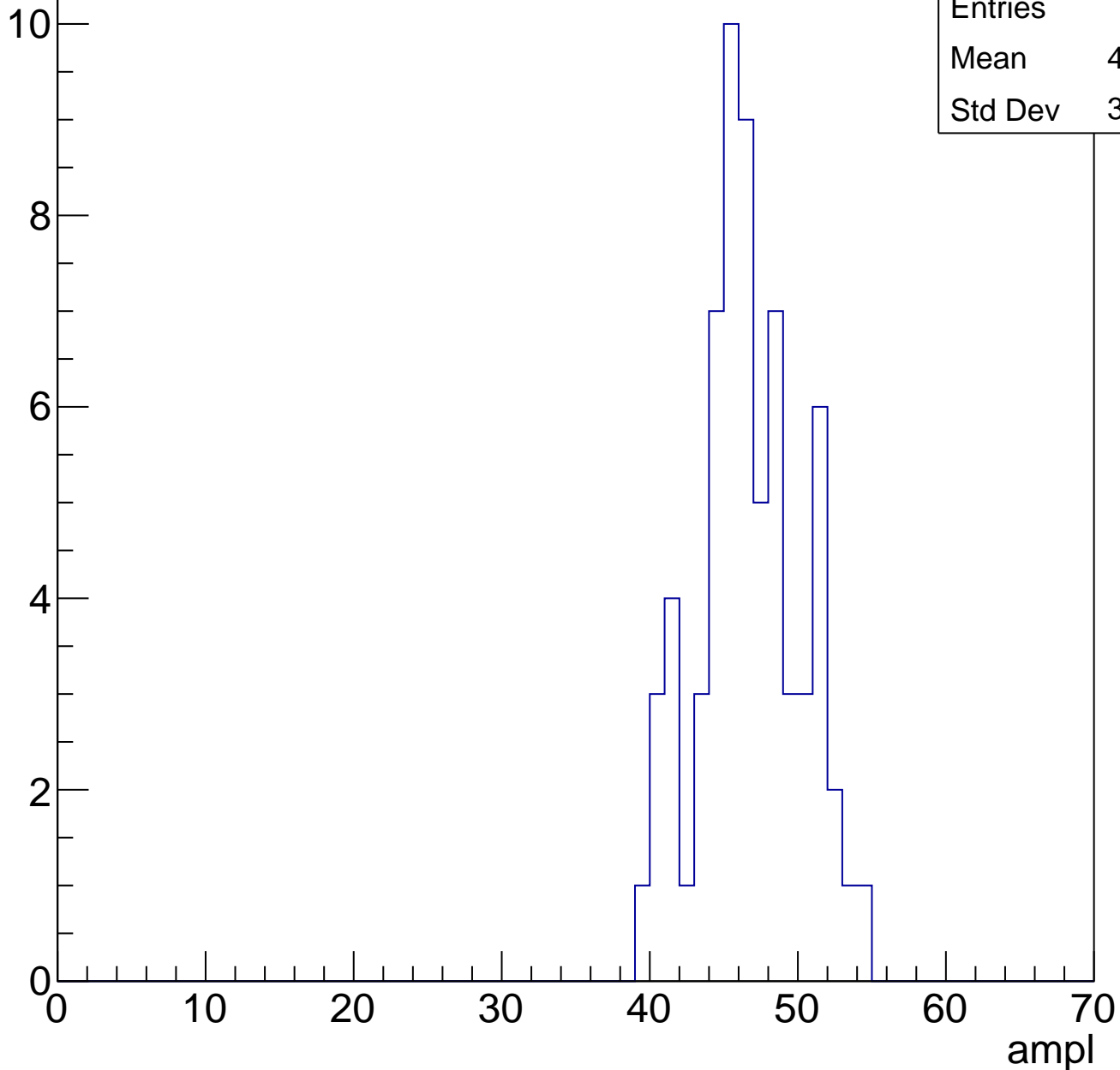


# B1L002S, U26-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	46.23
Std Dev	3.432

Entry

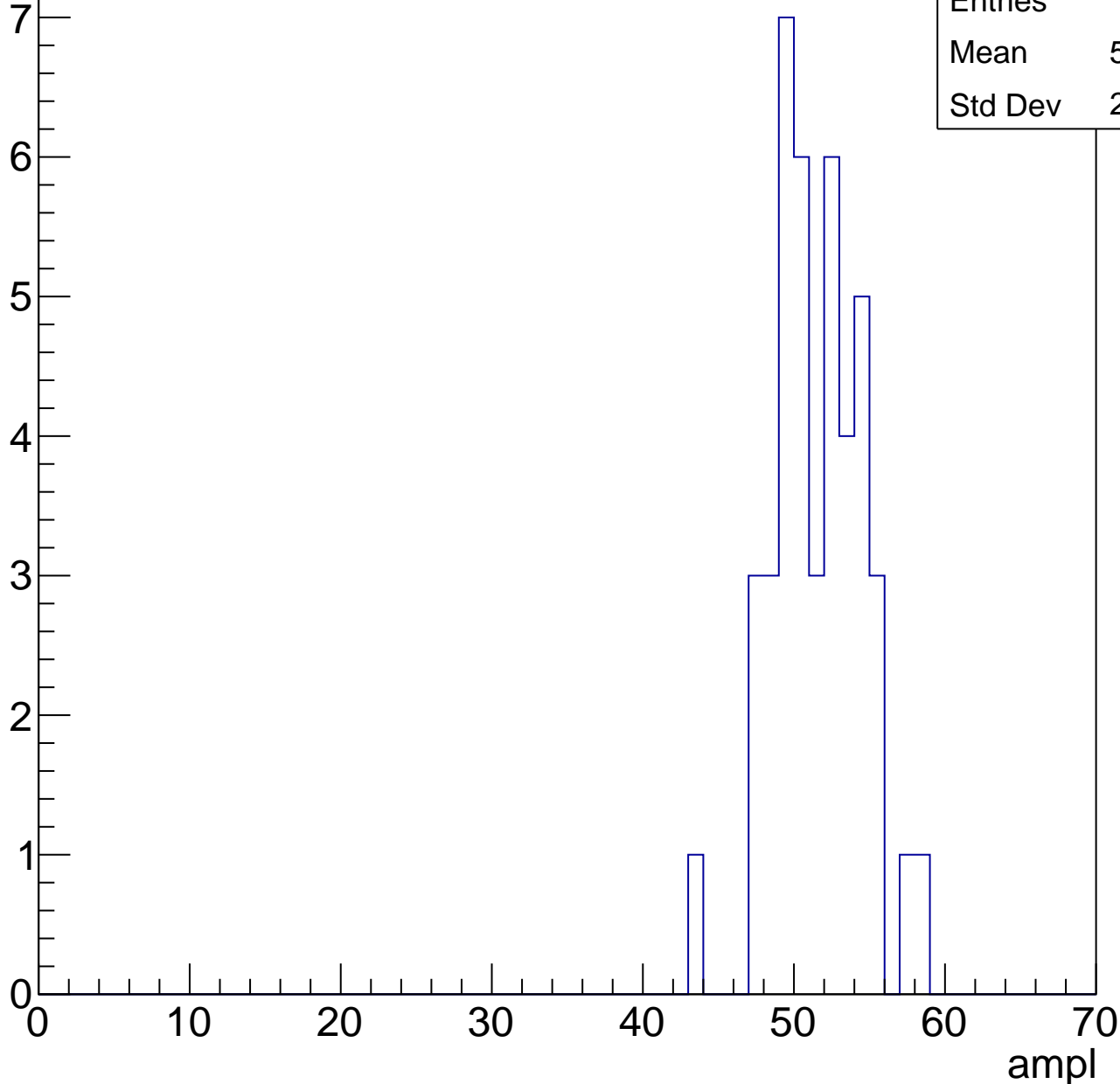


# B1L002S, U26-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	51.12
Std Dev	2.943



# B1L002S, U26-ch86, adc5

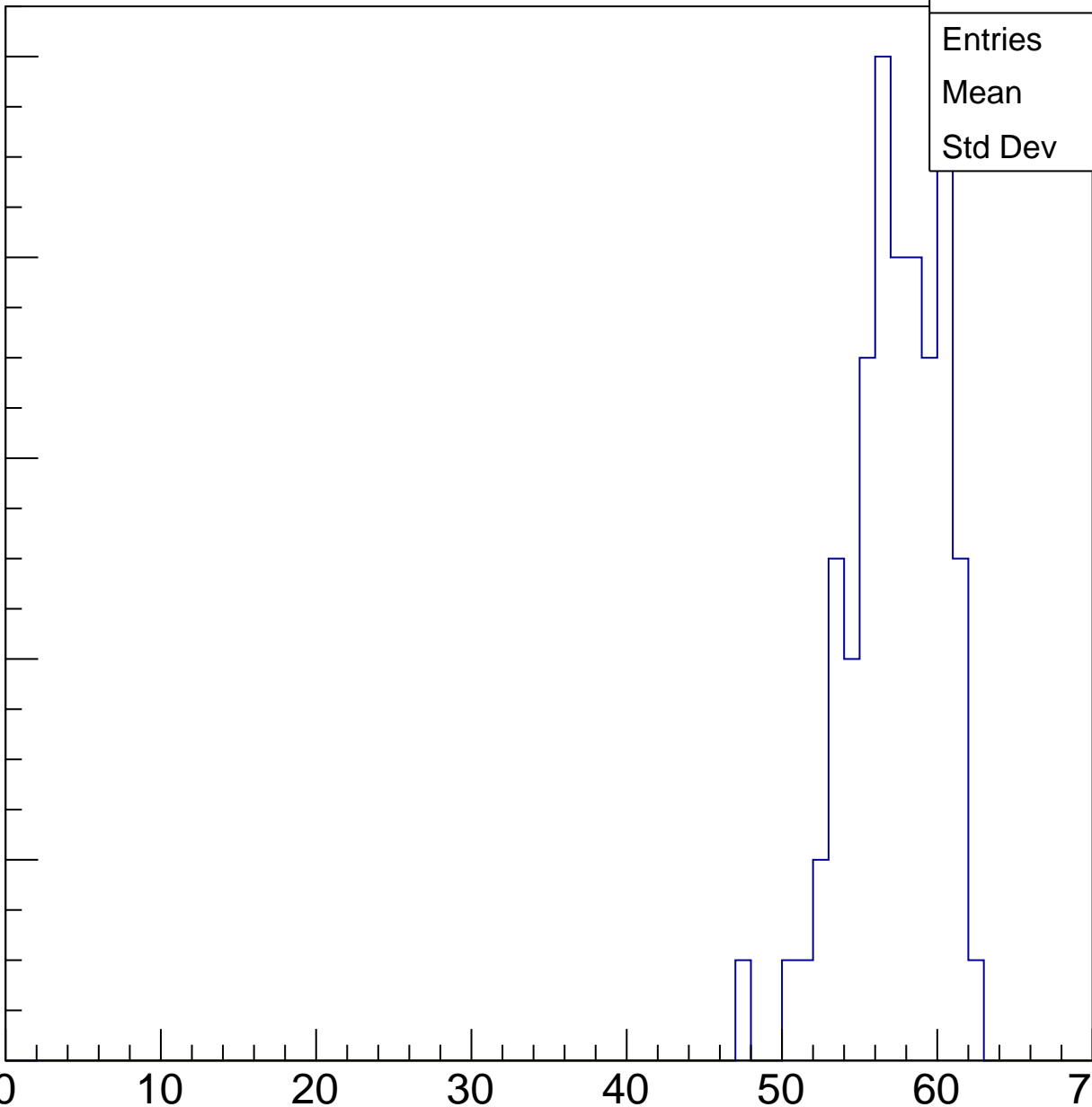
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10  
8  
6  
4  
2  
0

Entries	69
Mean	56.78
Std Dev	2.968

ampl

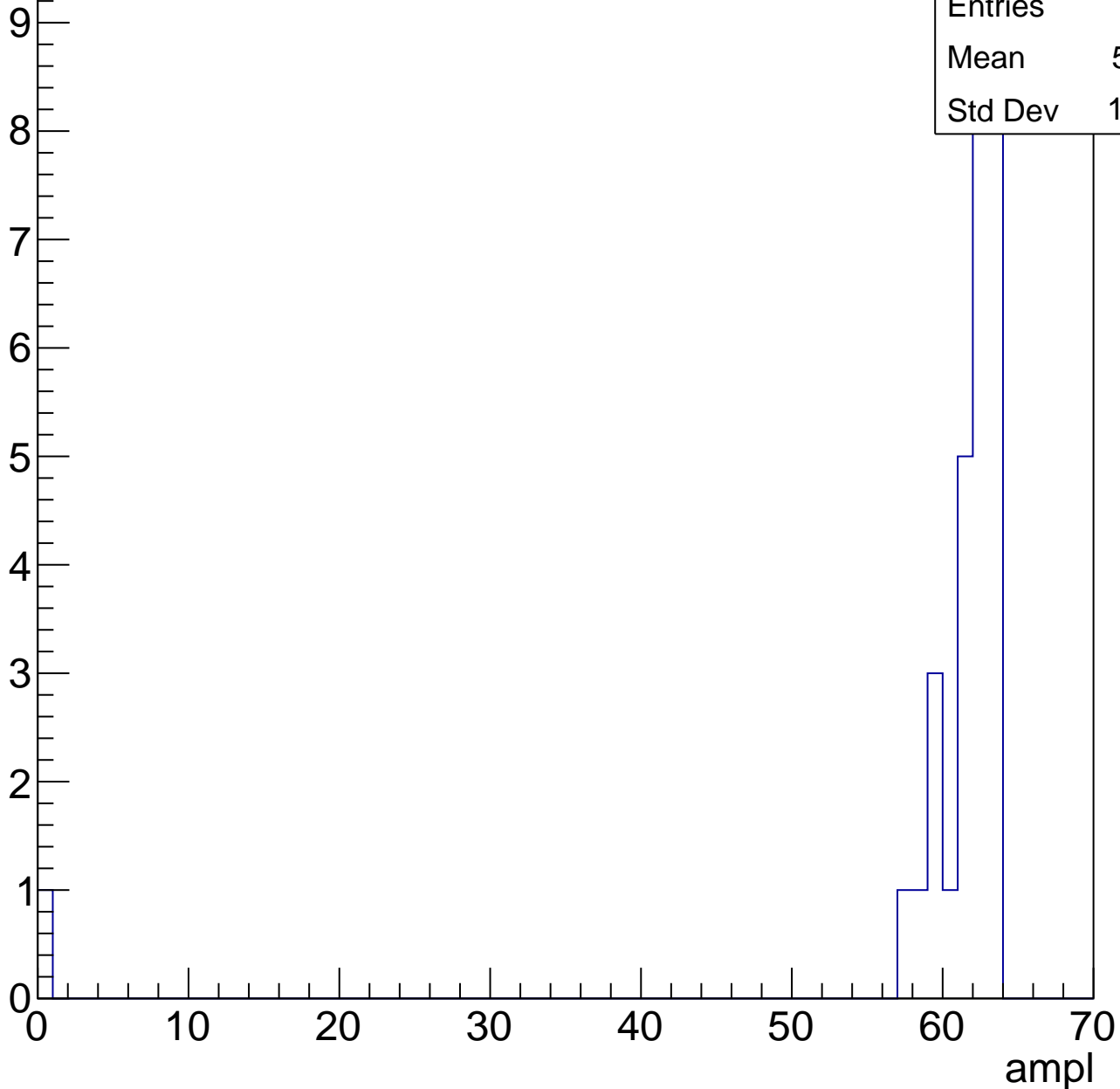


# B1L002S, U26-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	59.31
Std Dev	11.33





# B1L002S, U26-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch87, adc0

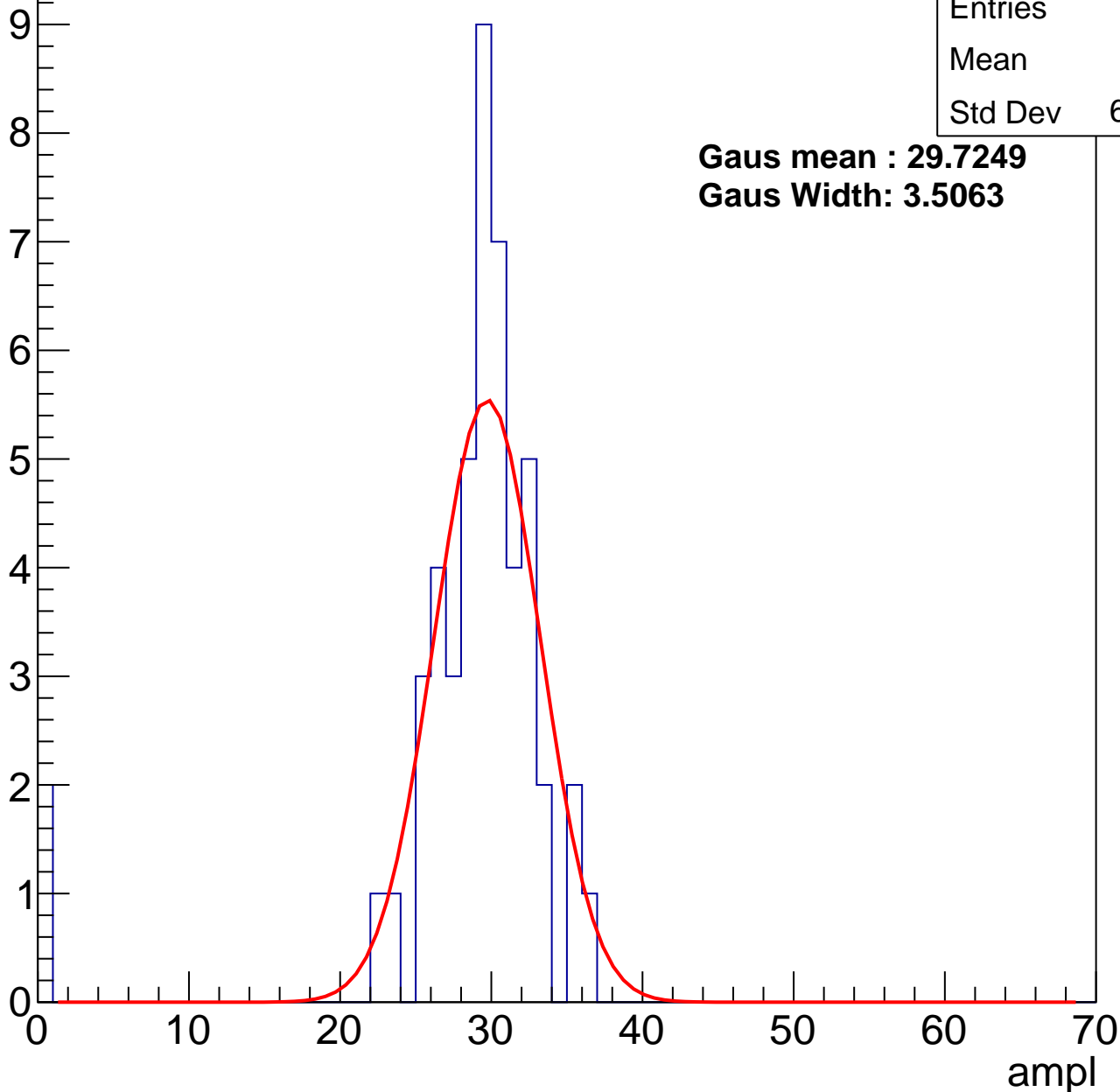
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	28
Std Dev	6.452

**Gaus mean : 29.7249**

**Gaus Width: 3.5063**



# B1L002S, U26-ch87, adc1

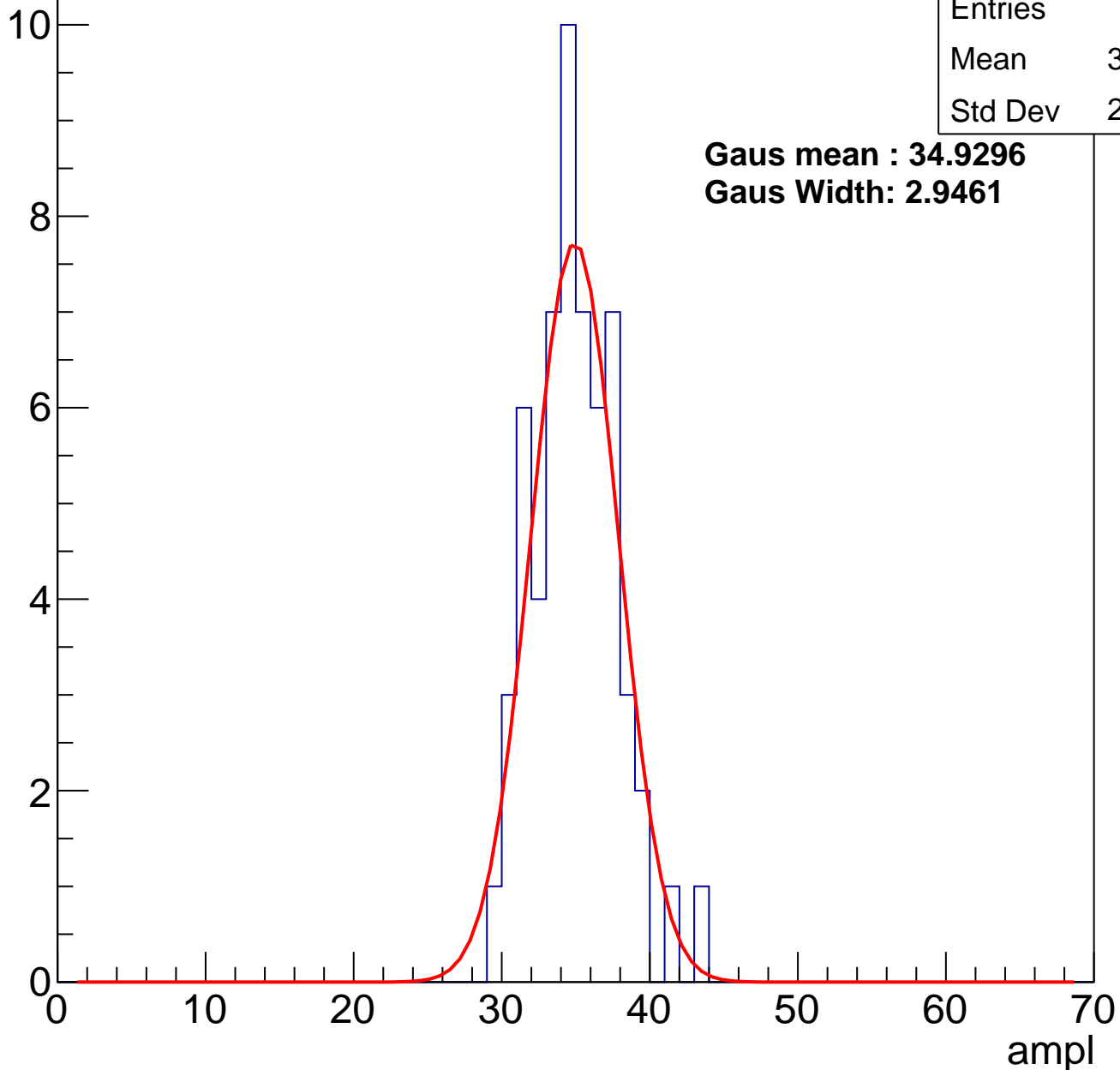
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	34.48
Std Dev	2.812

**Gaus mean : 34.9296**

**Gaus Width: 2.9461**

Entry



# B1L002S, U26-ch87, adc2

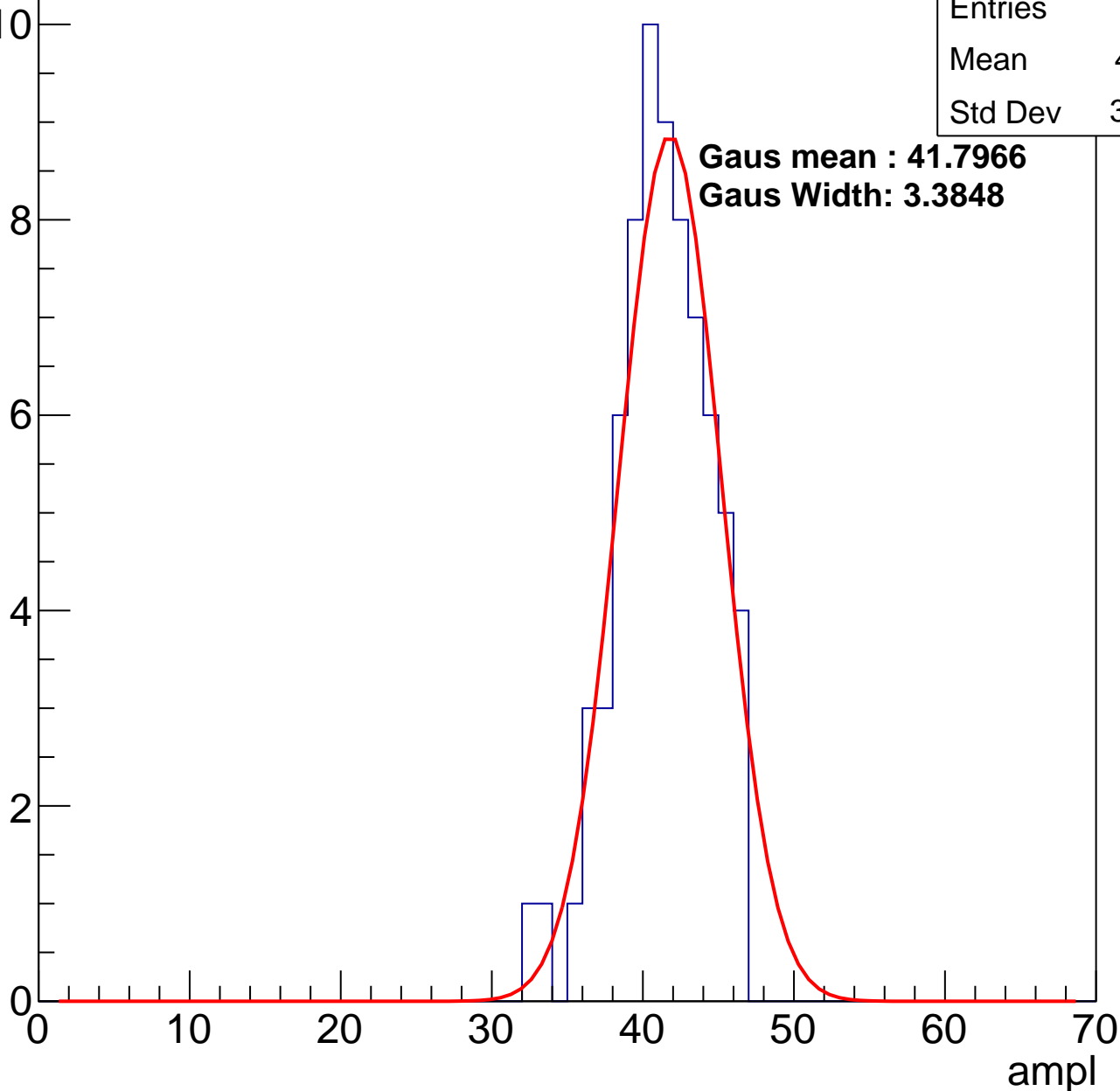
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	40.81
Std Dev	3.044

**Gaus mean : 41.7966**

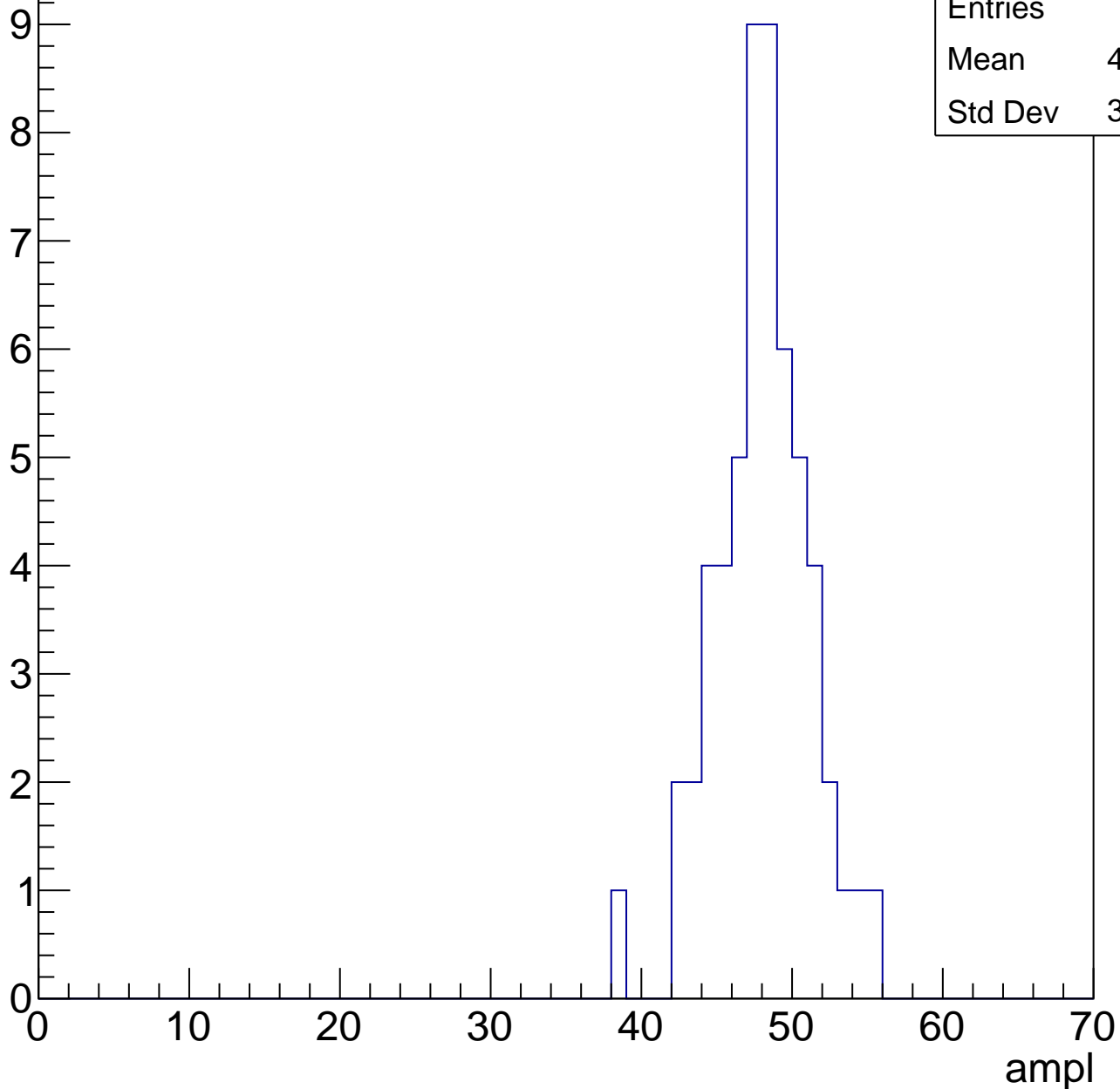
**Gaus Width: 3.3848**



# B1L002S, U26-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

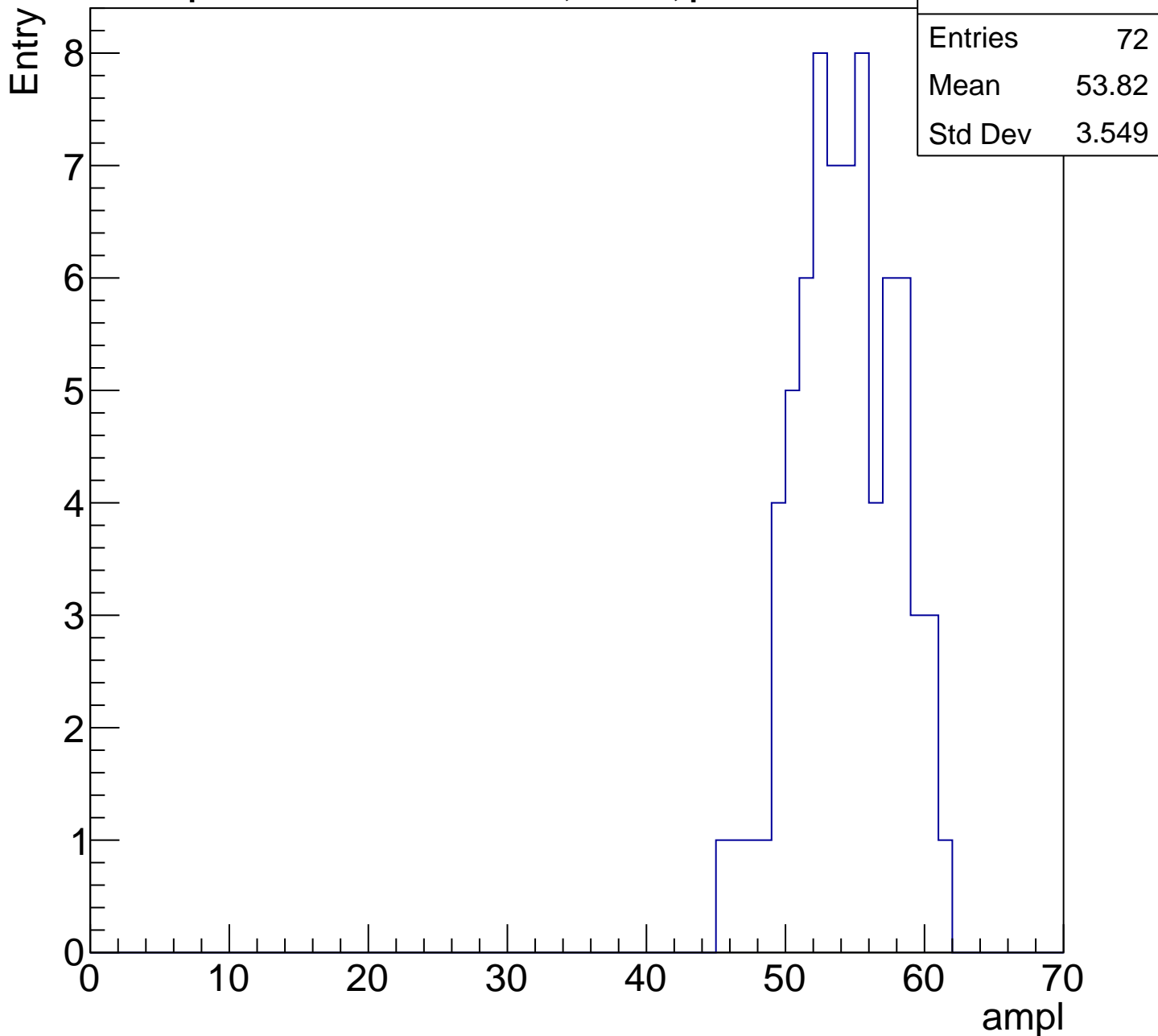
Entry



Entries	56
Mean	47.55
Std Dev	3.116

# B1L002S, U26-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

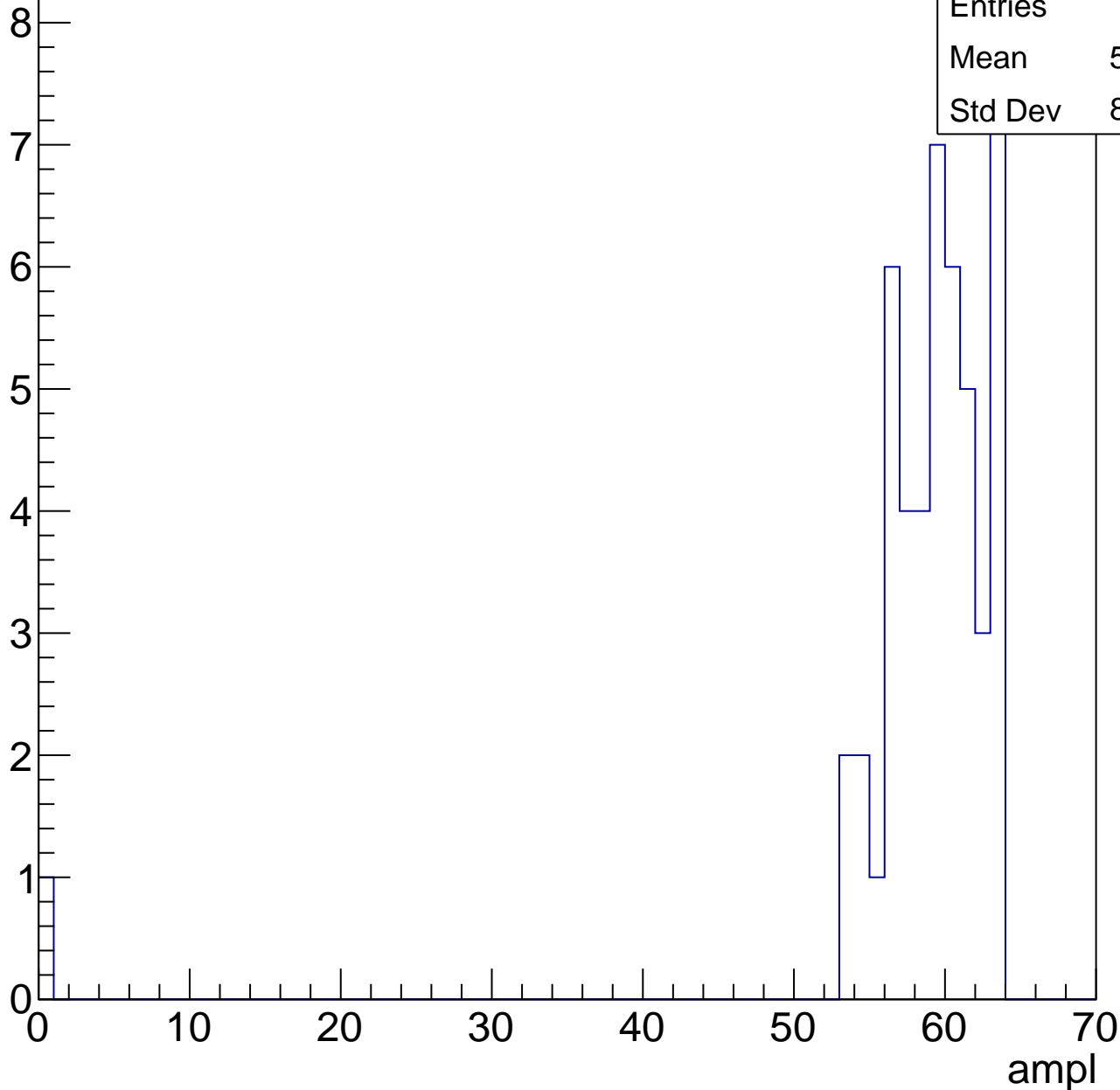


# B1L002S, U26-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	57.82
Std Dev	8.812

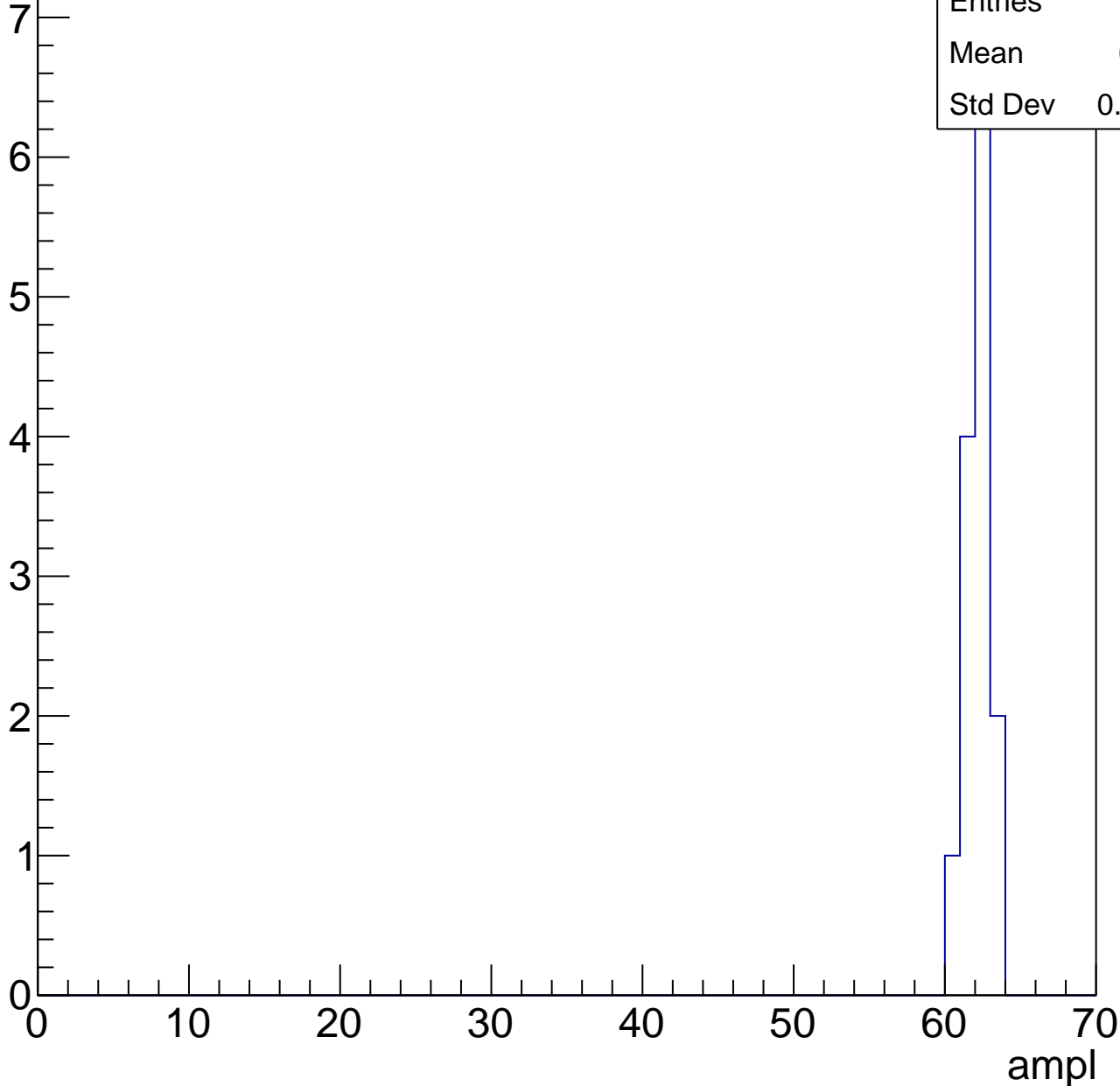


# B1L002S, U26-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.71
Std Dev	0.7954

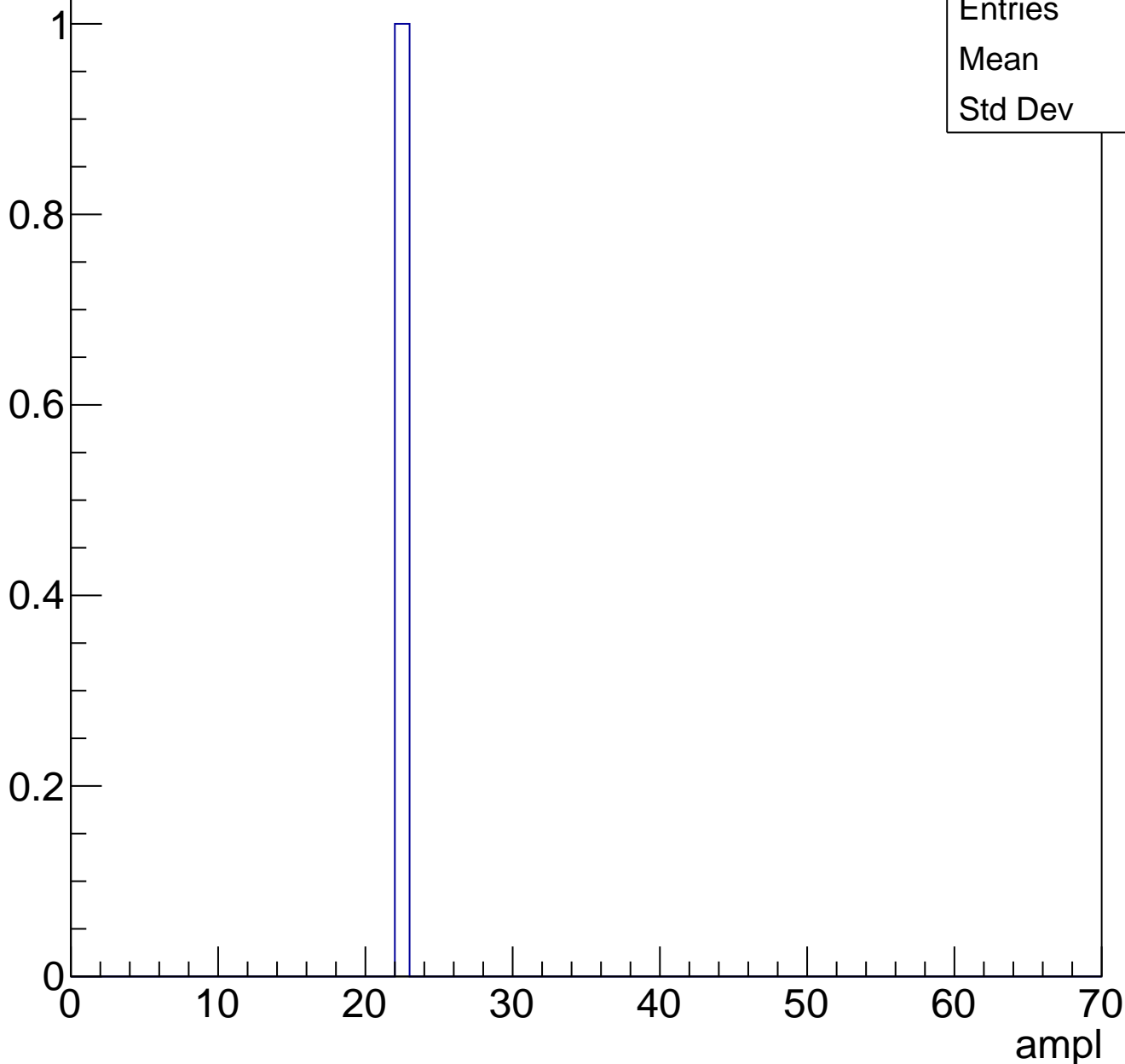




# B1L002S, U26-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L002S, U26-ch88, adc0

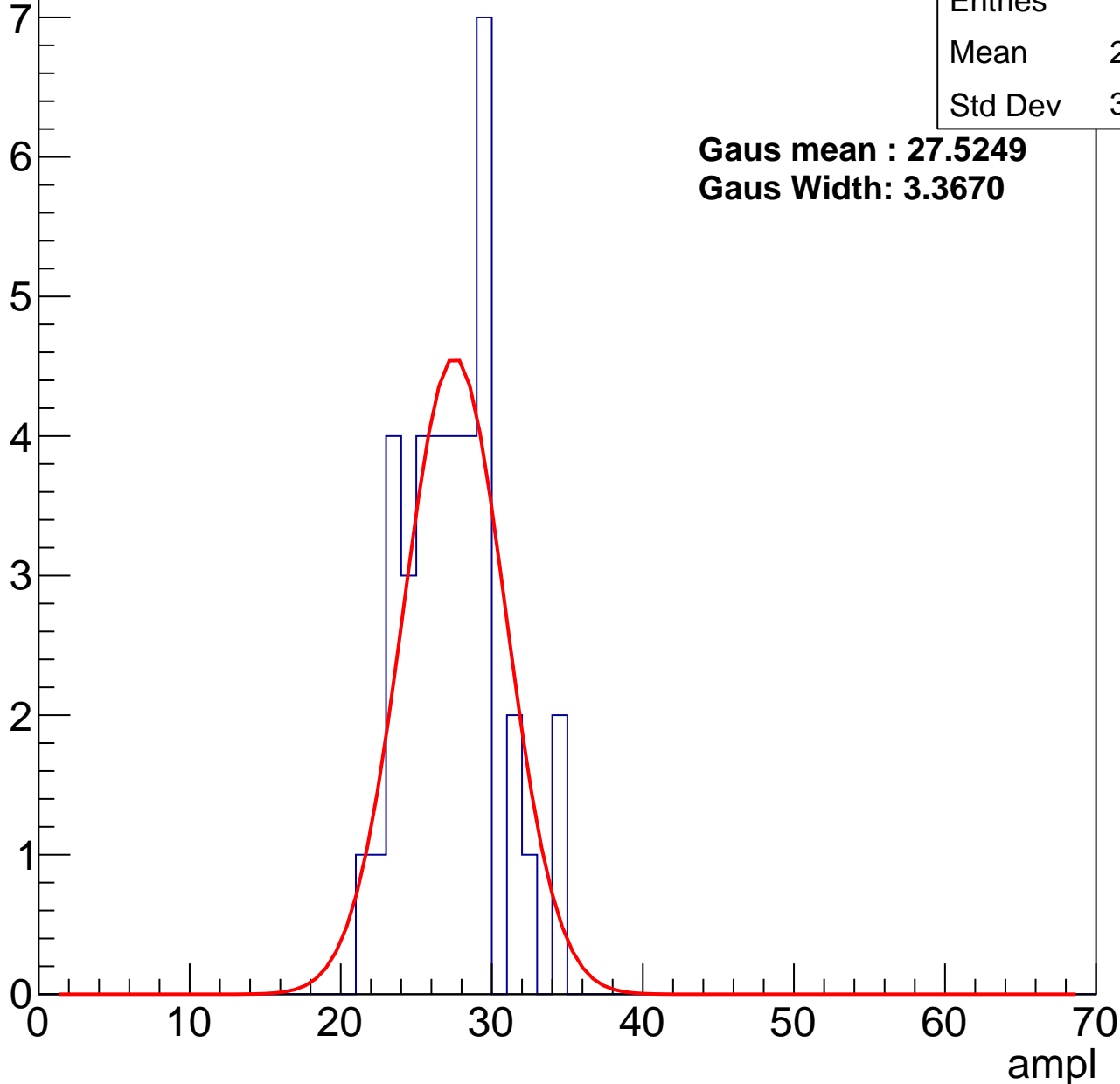
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	26.92
Std Dev	3.114

**Gaus mean : 27.5249**

**Gaus Width: 3.3670**



# B1L002S, U26-ch88, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	31.77
Std Dev	2.772

**Gaus mean : 32.4841**

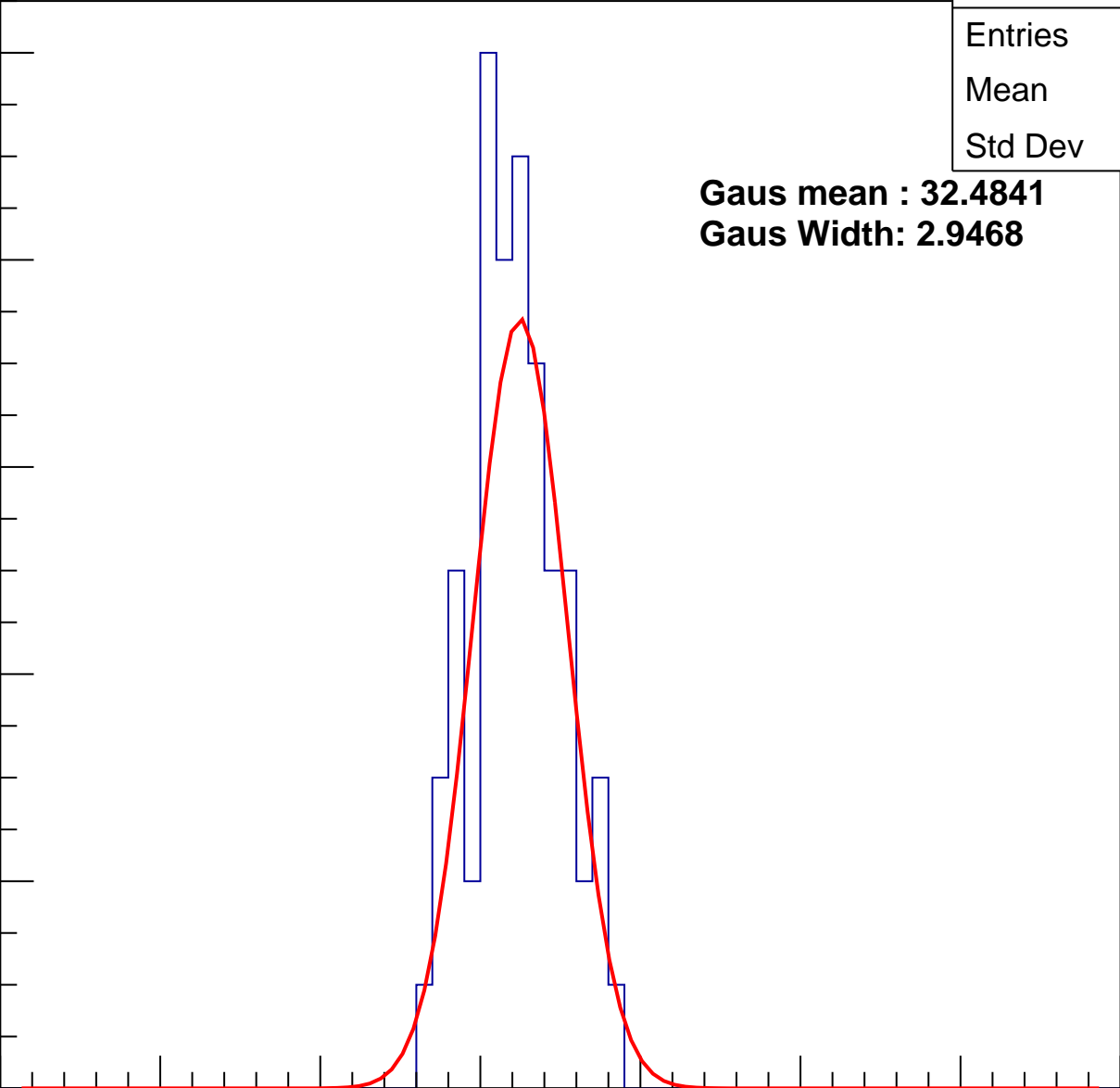
**Gaus Width: 2.9468**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch88, adc2

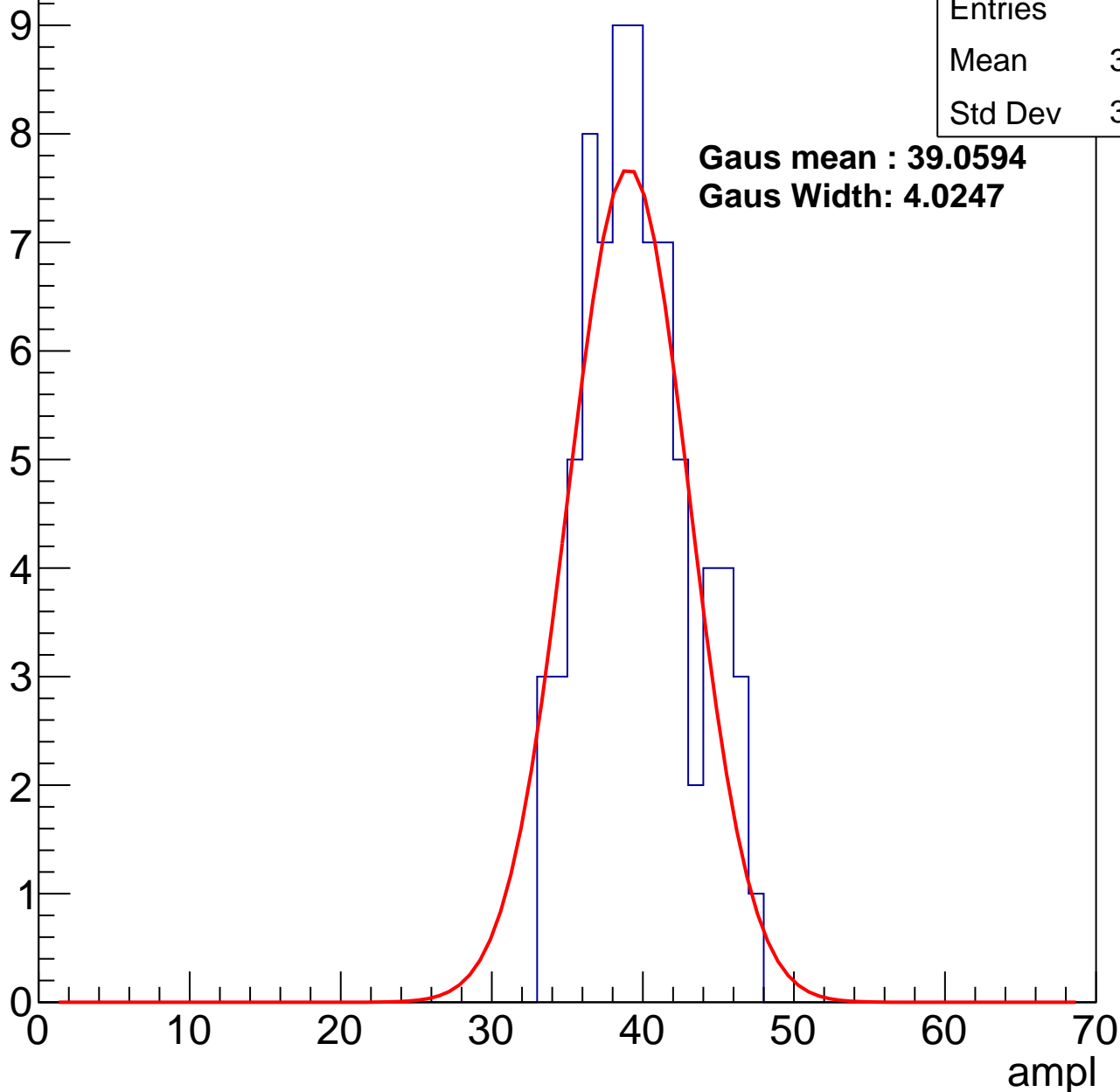
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	39.22
Std Dev	3.485

**Gaus mean : 39.0594**

**Gaus Width: 4.0247**

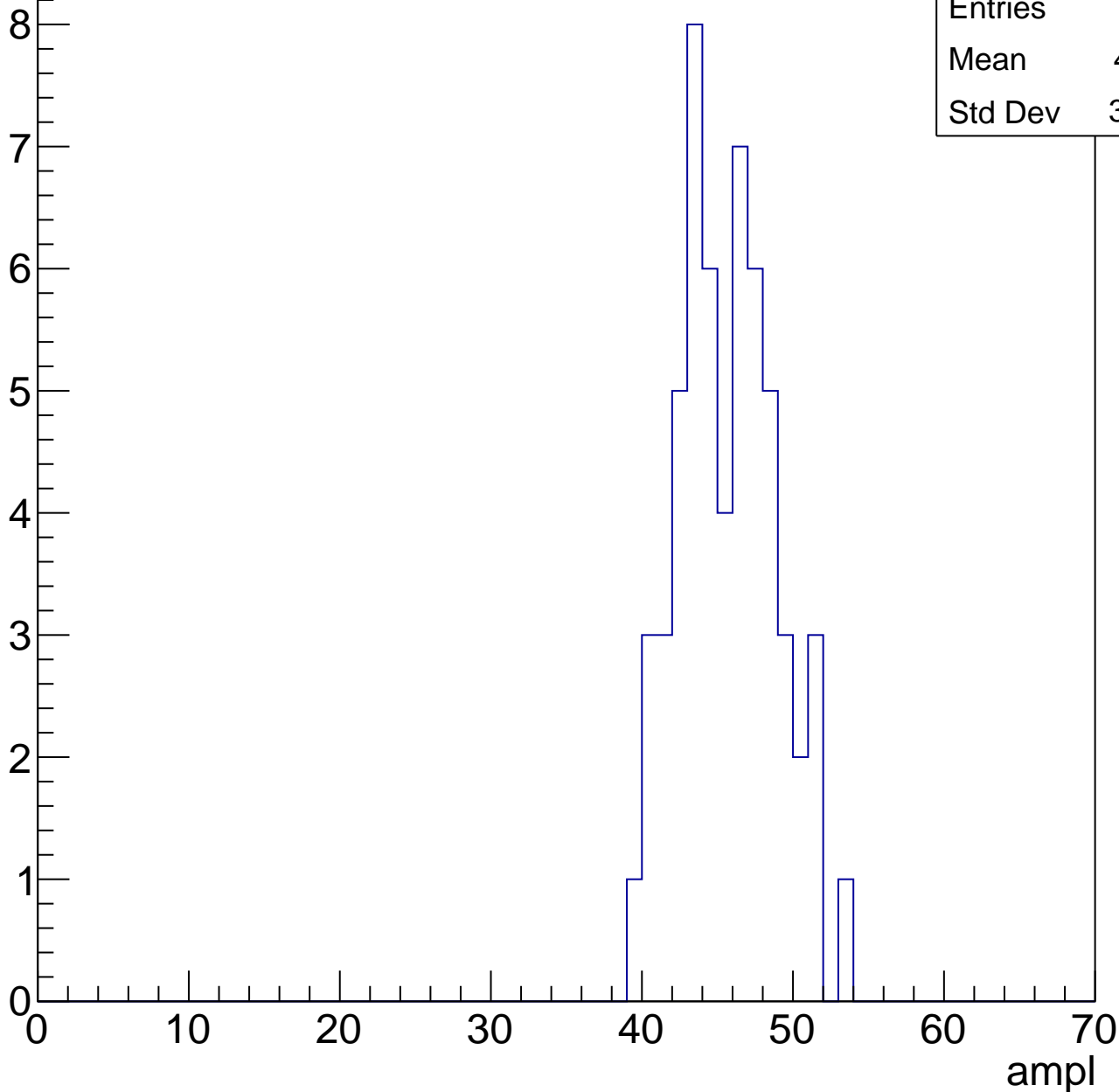


# B1L002S, U26-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	45.21
Std Dev	3.205

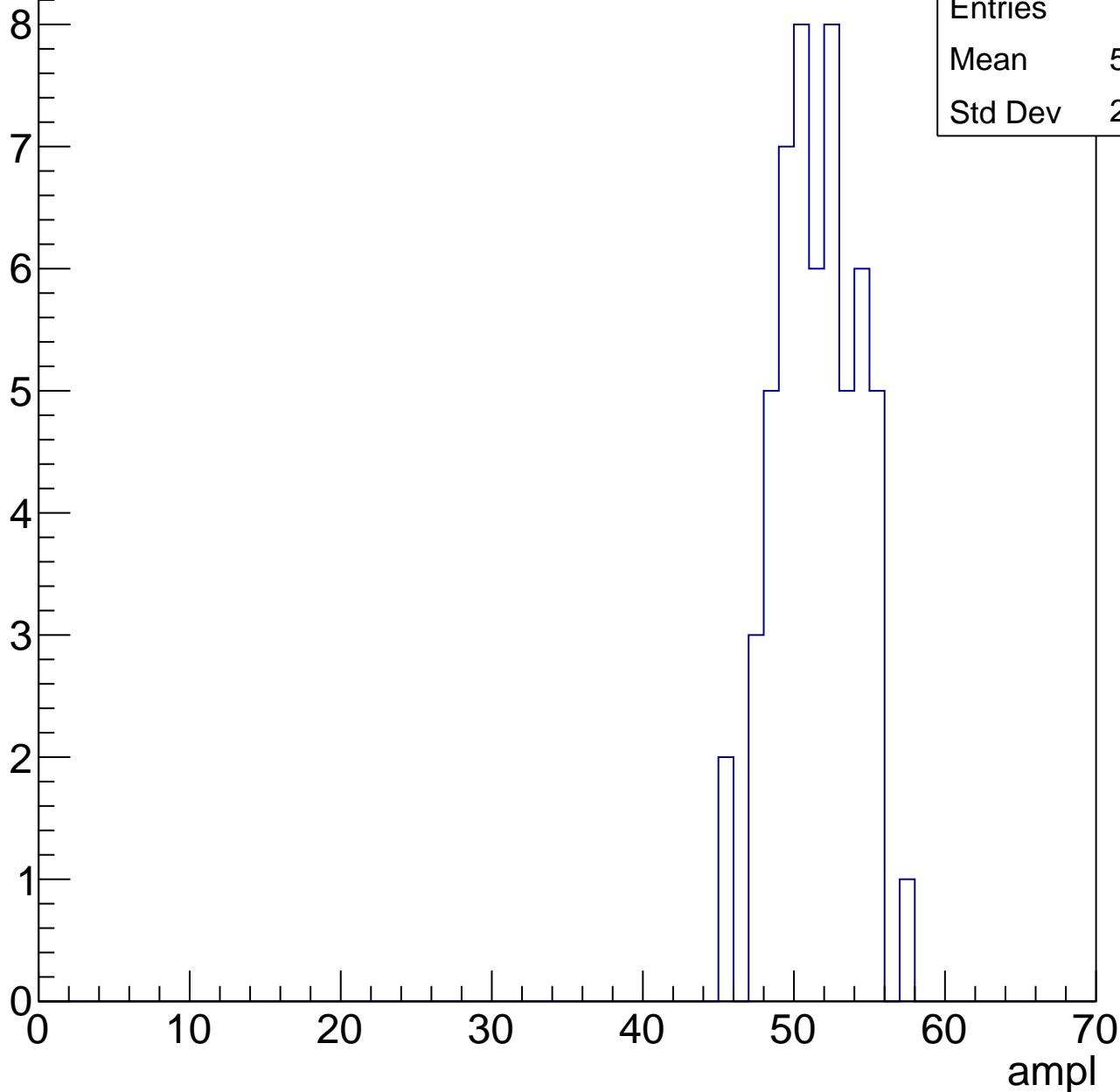


# B1L002S, U26-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

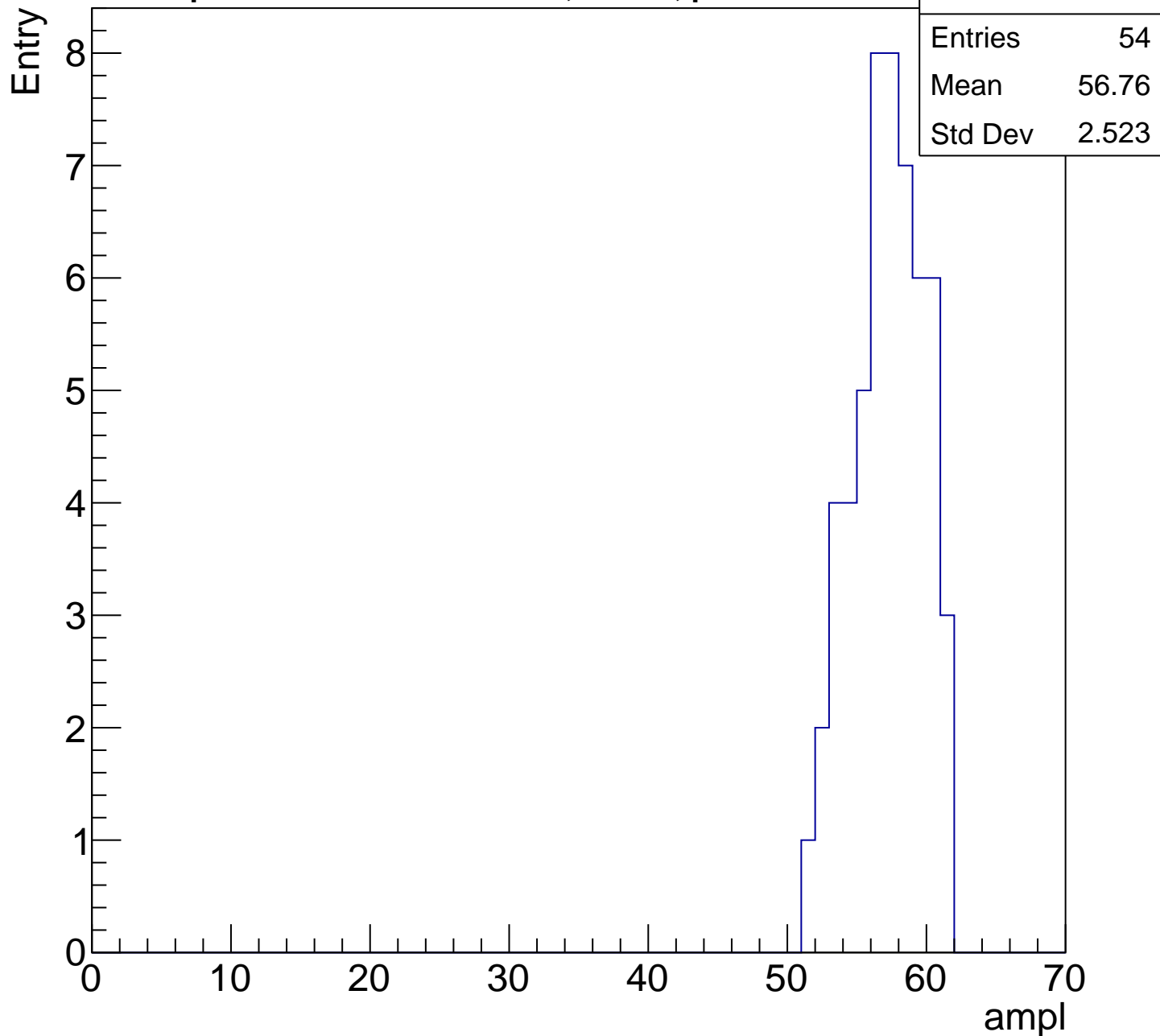
Entry

Entries	56
Mean	51.02
Std Dev	2.669



# B1L002S, U26-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

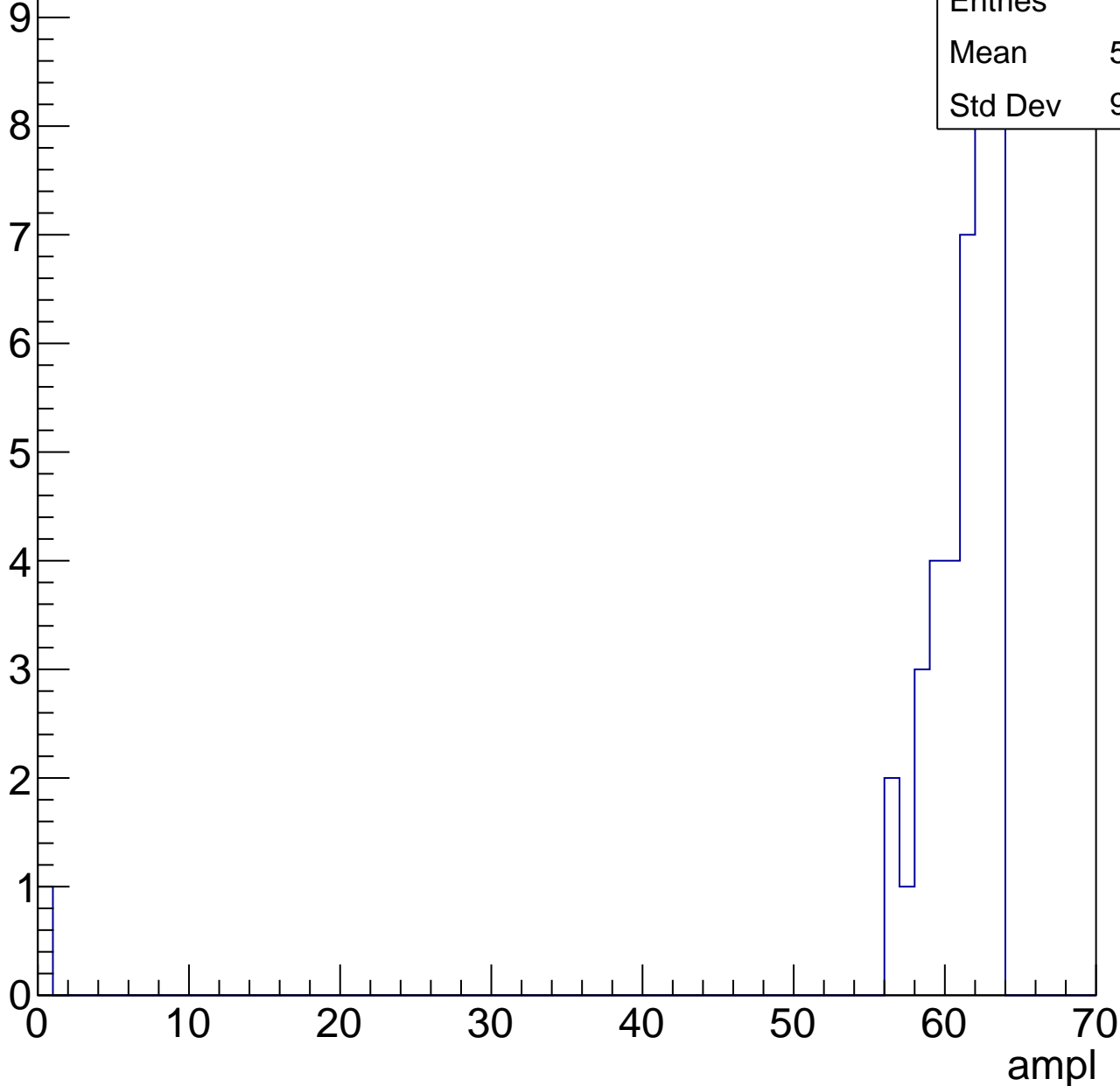


# B1L002S, U26-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	59.27
Std Dev	9.695





# B1L002S, U26-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch89, adc0

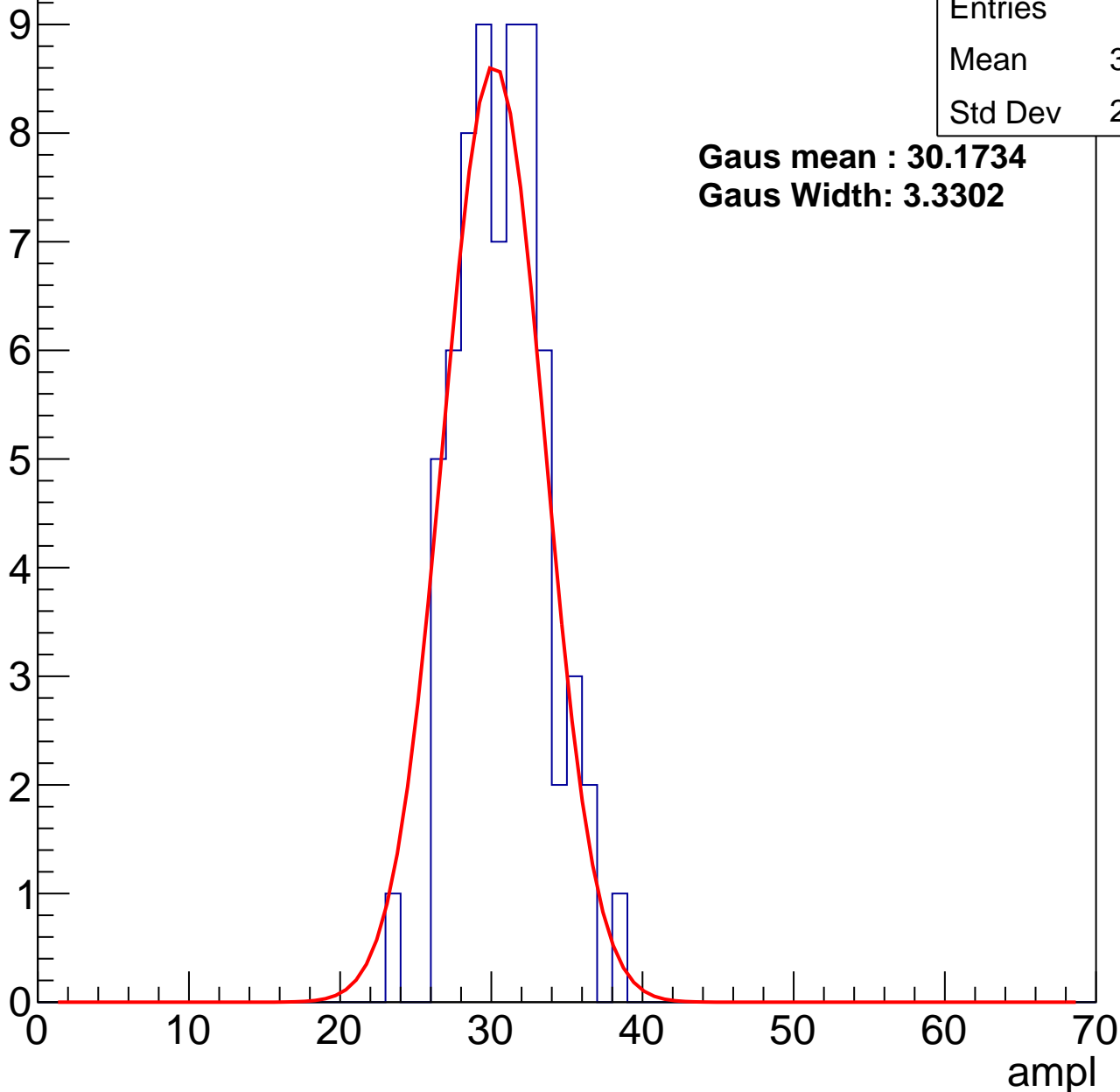
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	30.26
Std Dev	2.873

**Gaus mean : 30.1734**

**Gaus Width: 3.3302**



# B1L002S, U26-ch89, adc1

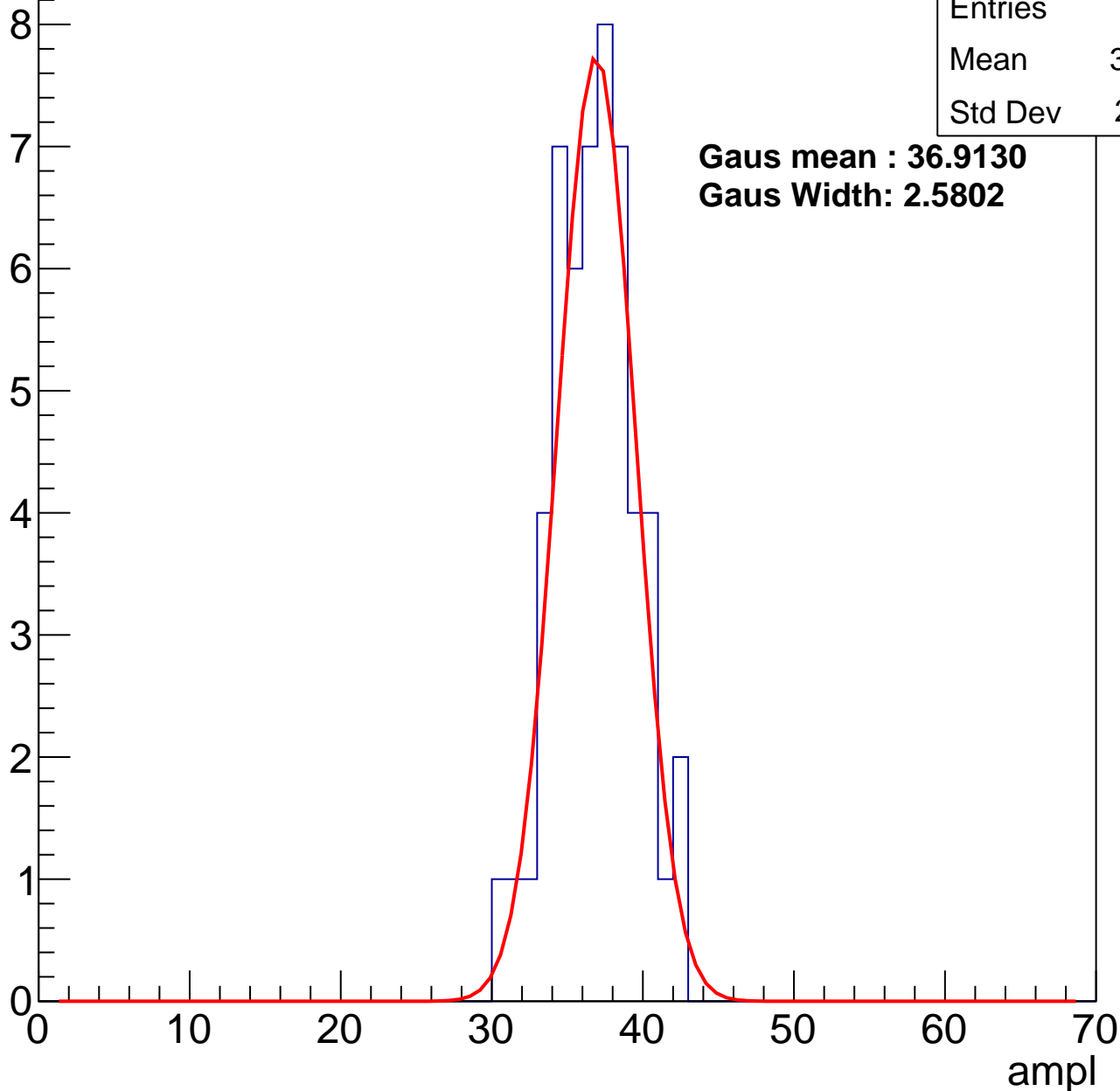
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	36.38
Std Dev	2.651

**Gaus mean : 36.9130**

**Gaus Width: 2.5802**



# B1L002S, U26-ch89, adc2

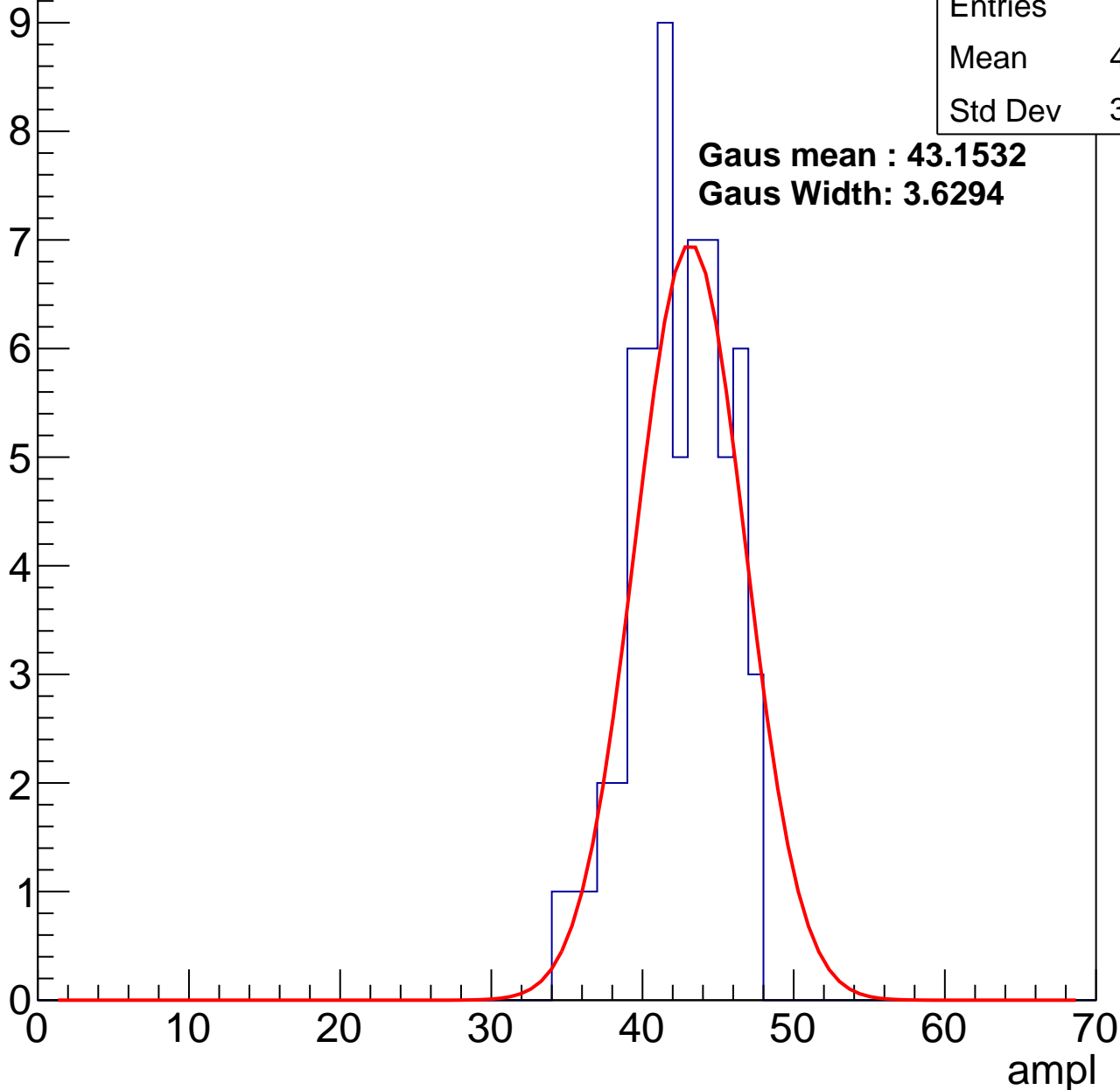
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.95
Std Dev	3.054

**Gaus mean : 43.1532**

**Gaus Width: 3.6294**

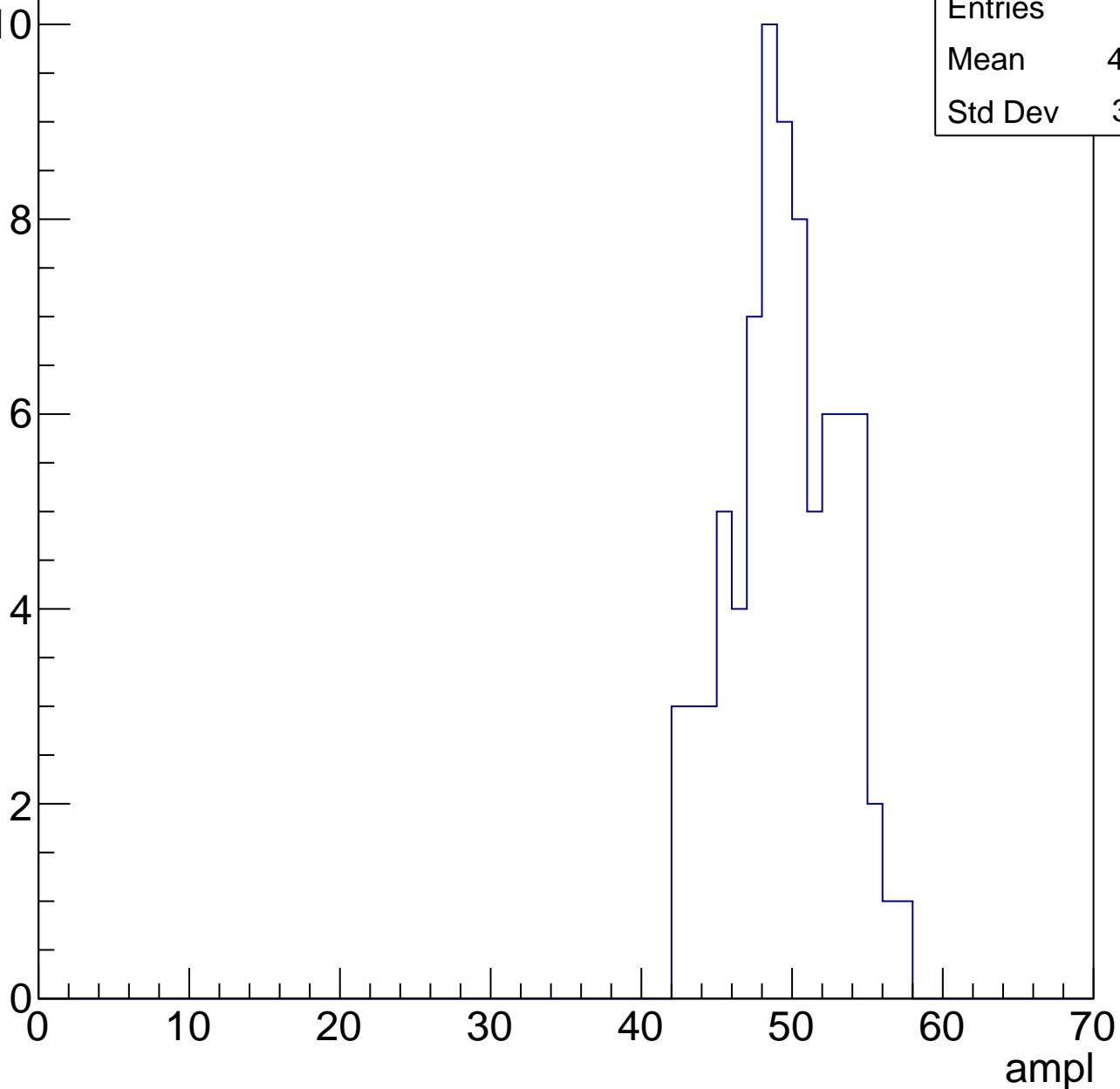


# B1L002S, U26-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	49.09
Std Dev	3.551

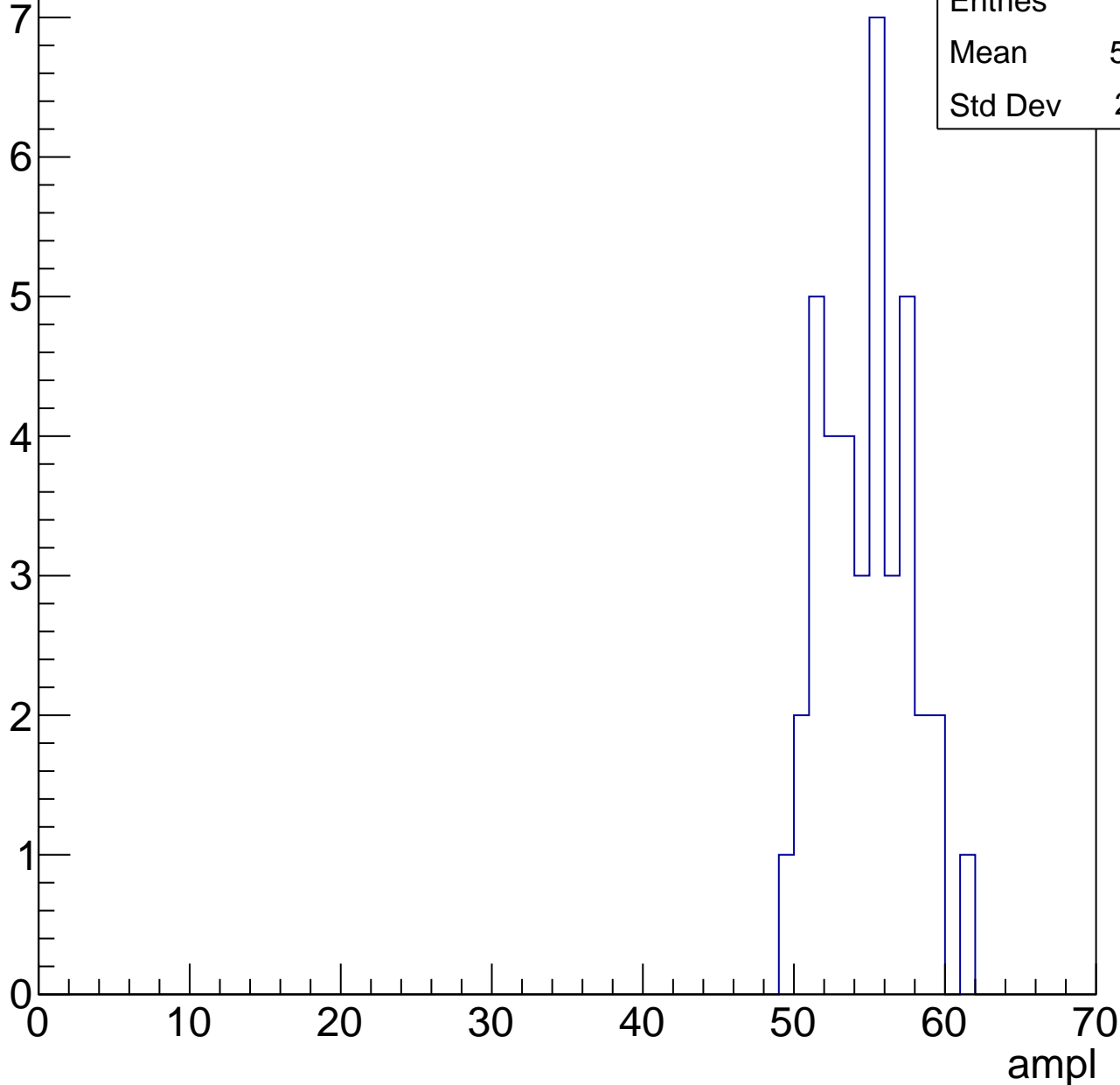


# B1L002S, U26-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	54.33
Std Dev	2.831



# B1L002S, U26-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

Entries 63

Mean 58.71

Std Dev 7.808

8

6

4

2

0

0

10

20

30

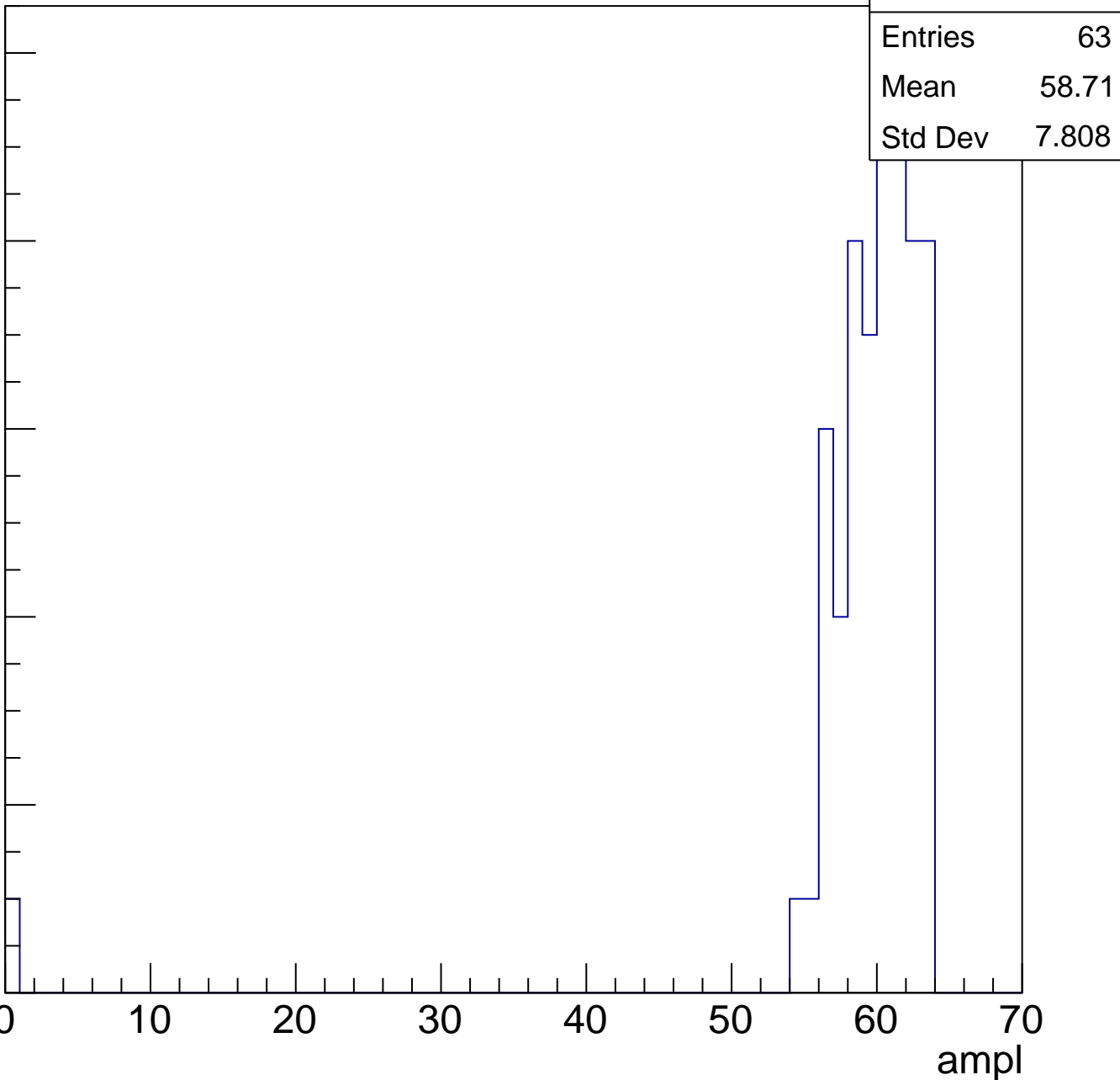
40

50

60

70

ampl



# B1L002S, U26-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

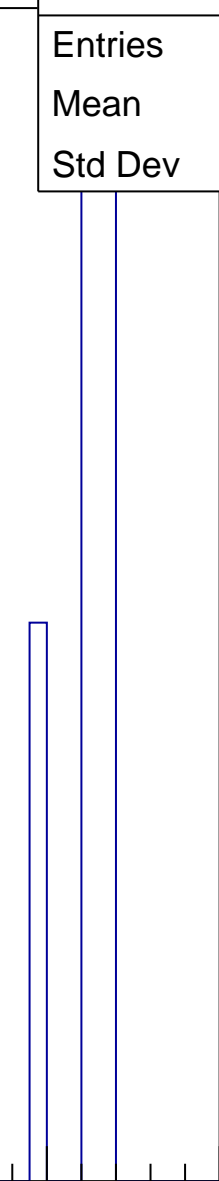
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.8
Std Dev	1.47

0 10 20 30 40 50 60 70

ampl





# B1L002S, U26-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U26-ch90, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	28.47
Std Dev	2.949

**Gaus mean : 28.9269**

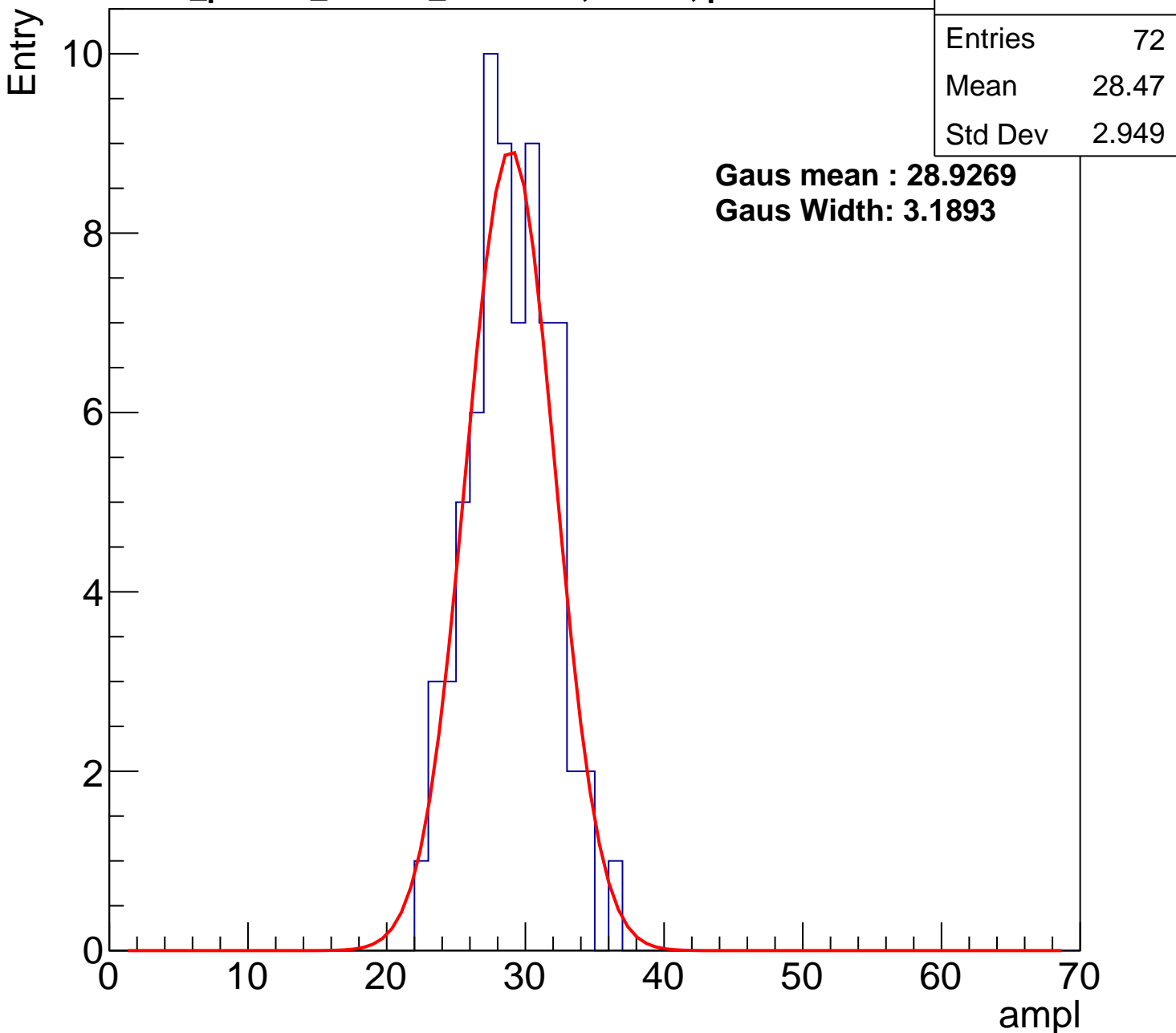
**Gaus Width: 3.1893**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	34.95
Std Dev	2.95

**Gaus mean : 35.6446**

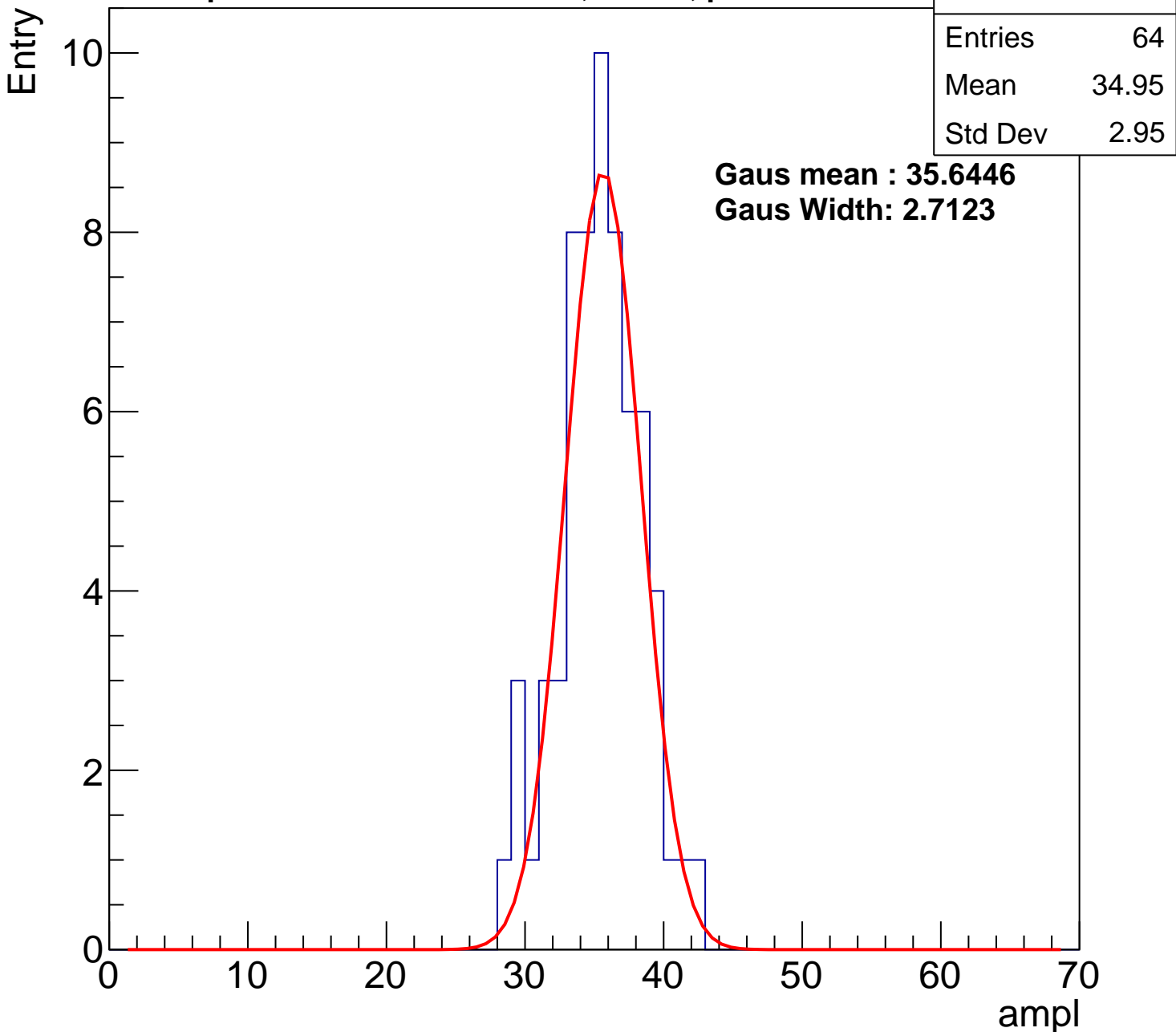
**Gaus Width: 2.7123**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch90, adc2

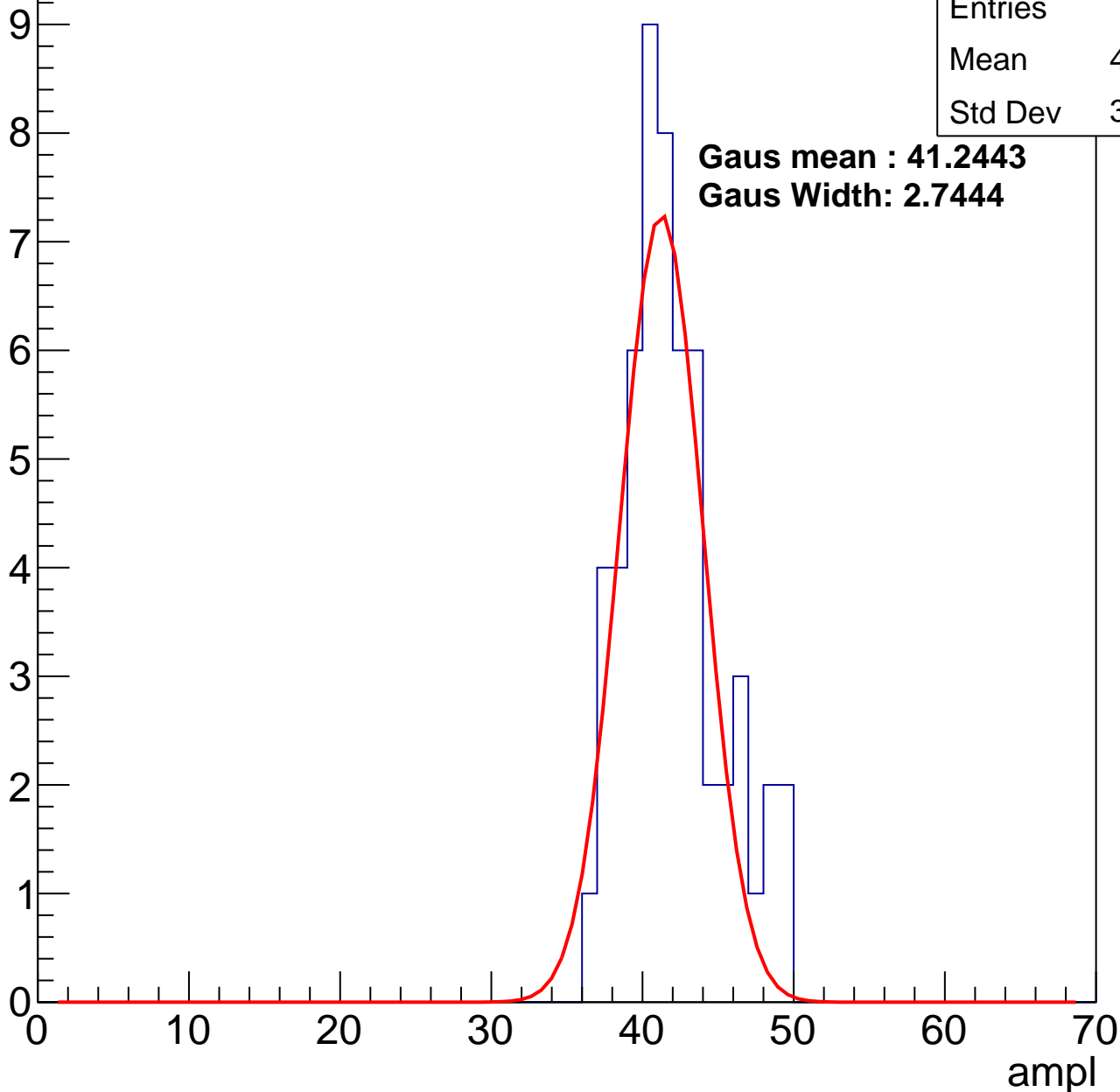
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.52
Std Dev	3.162

**Gaus mean : 41.2443**

**Gaus Width: 2.7444**

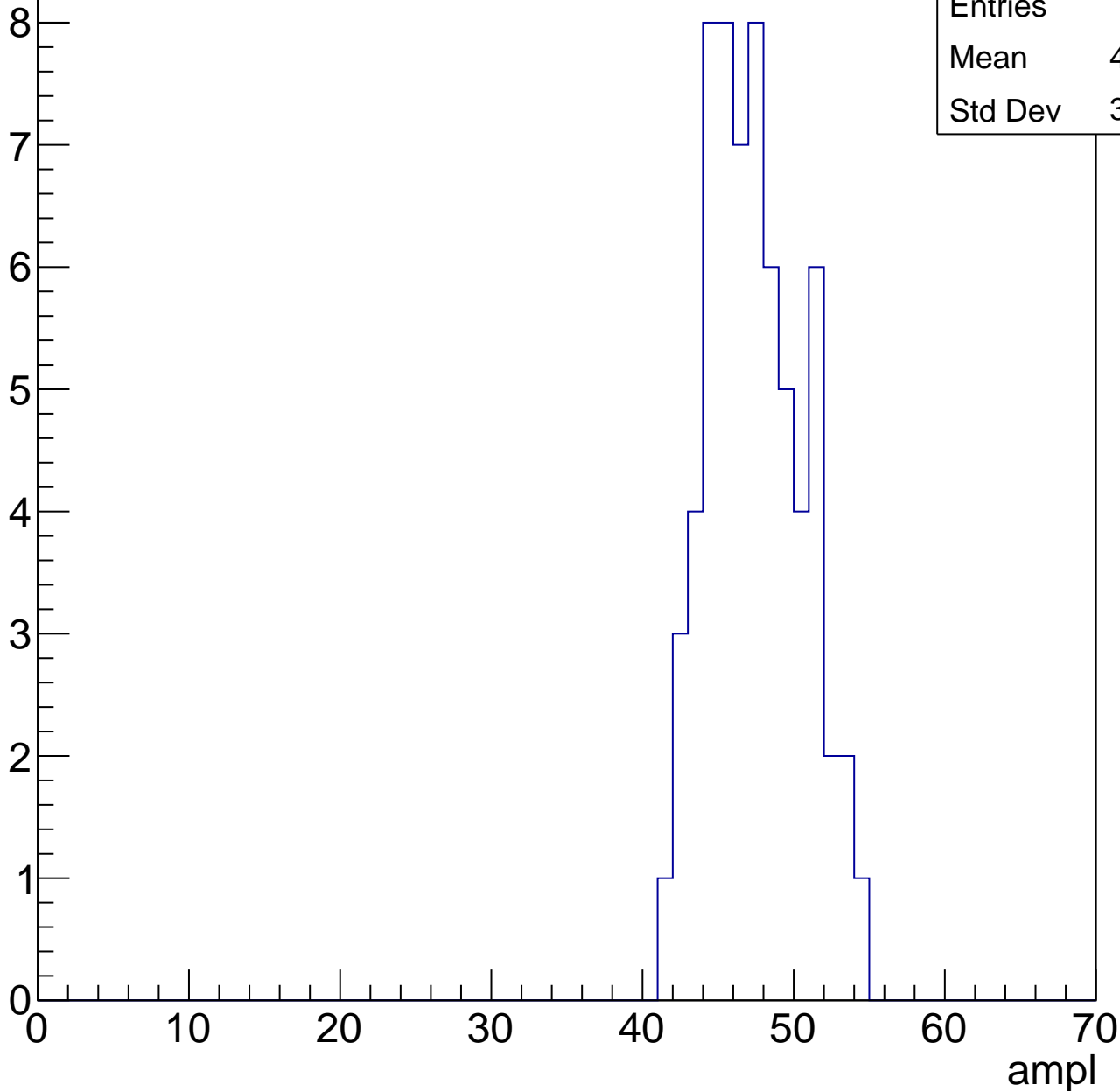


# B1L002S, U26-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

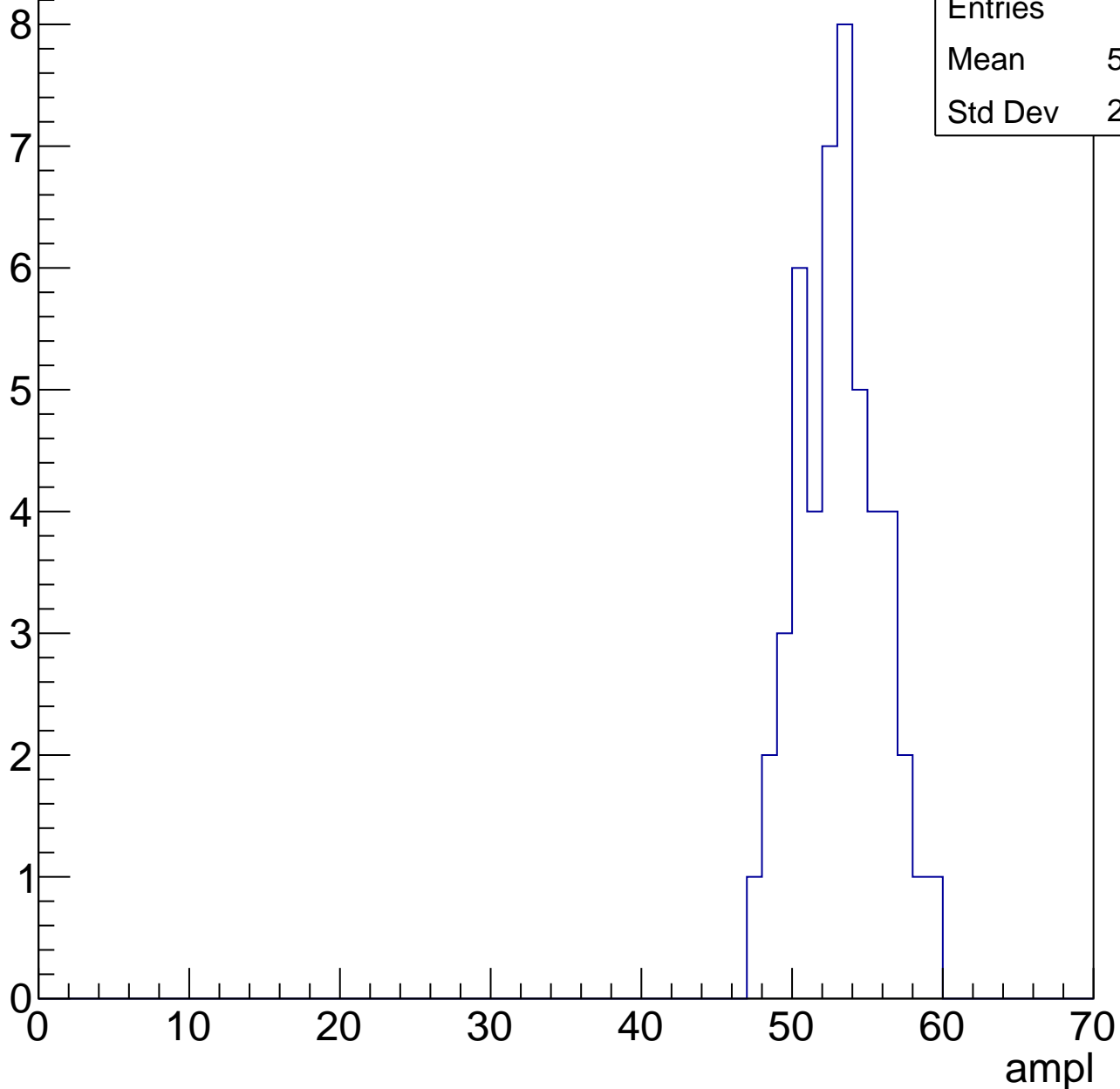
Entries	65
Mean	46.95
Std Dev	3.076



# B1L002S, U26-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



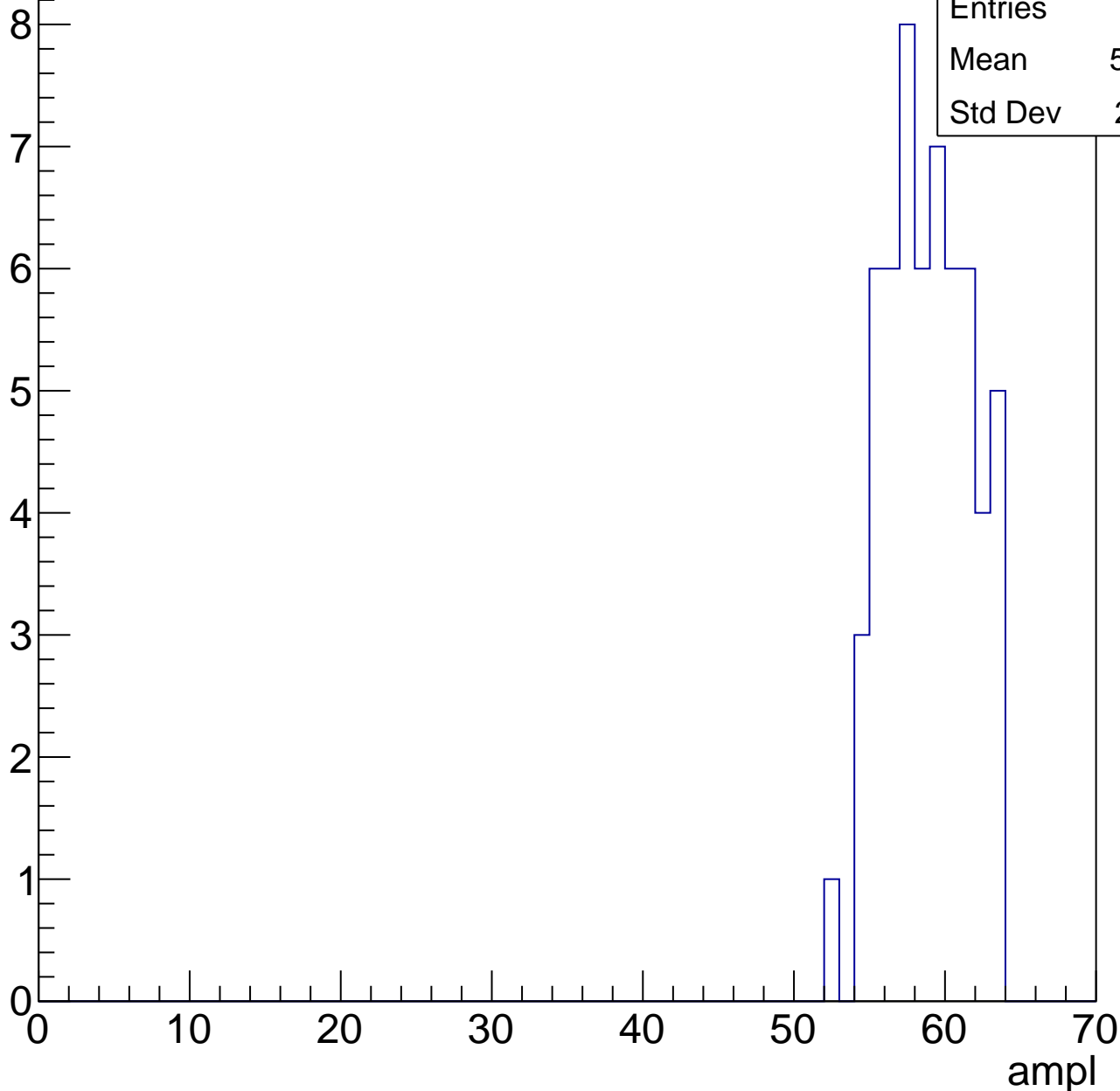
Entries	48
Mean	52.65
Std Dev	2.719

# B1L002S, U26-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.38
Std Dev	2.741

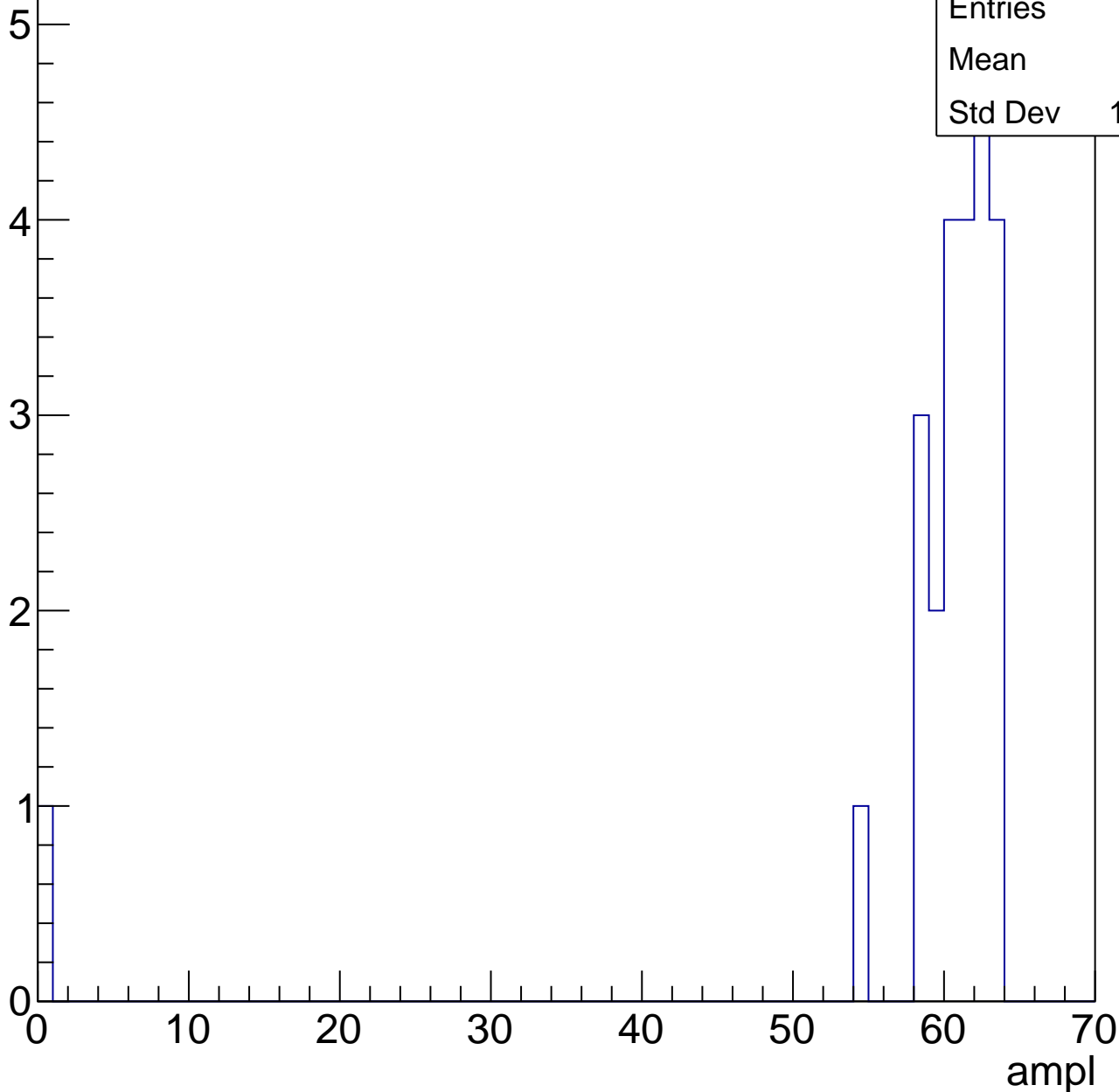


# B1L002S, U26-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58
Std Dev	12.27

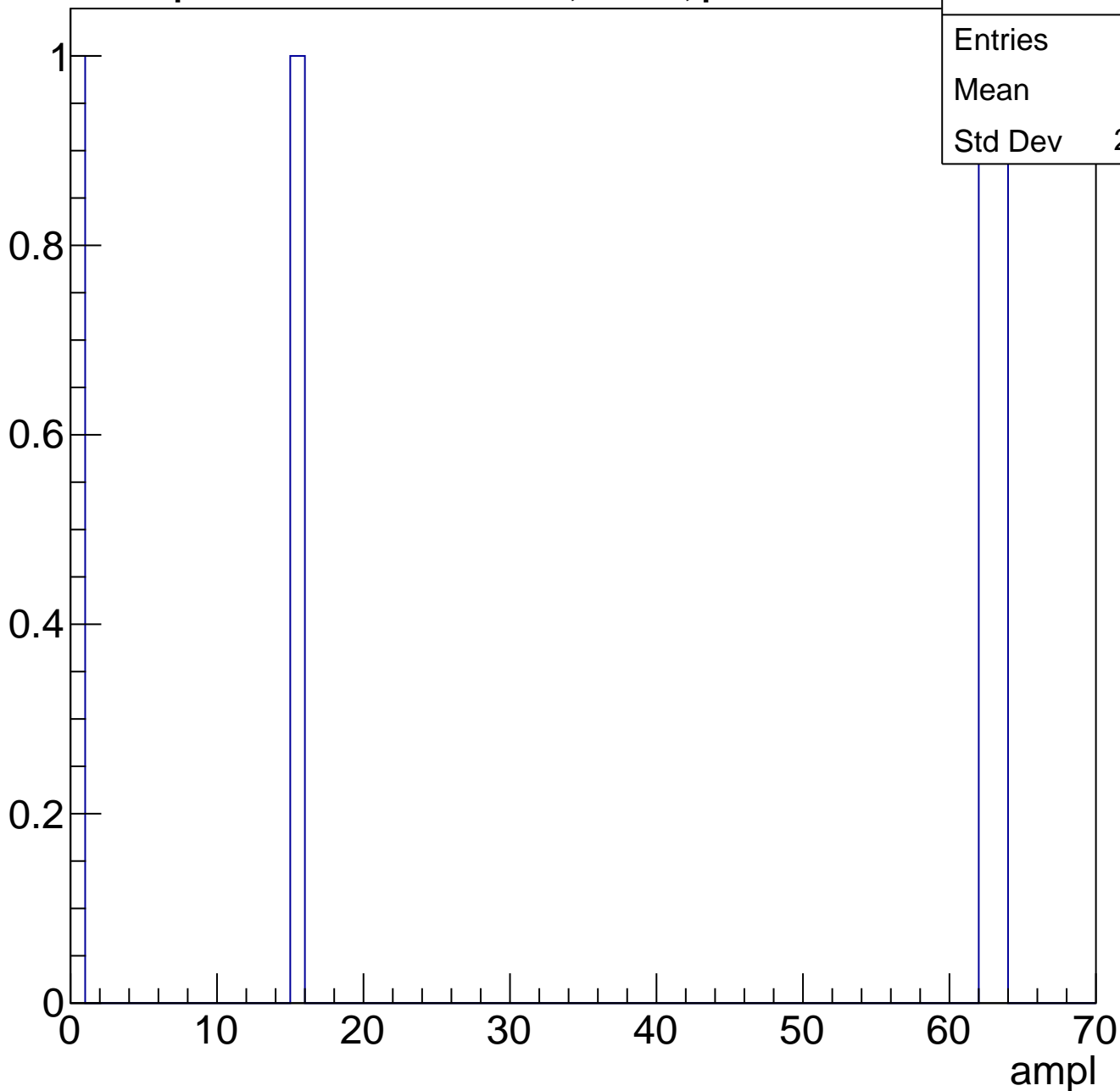




# B1L002S, U26-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch91, adc0

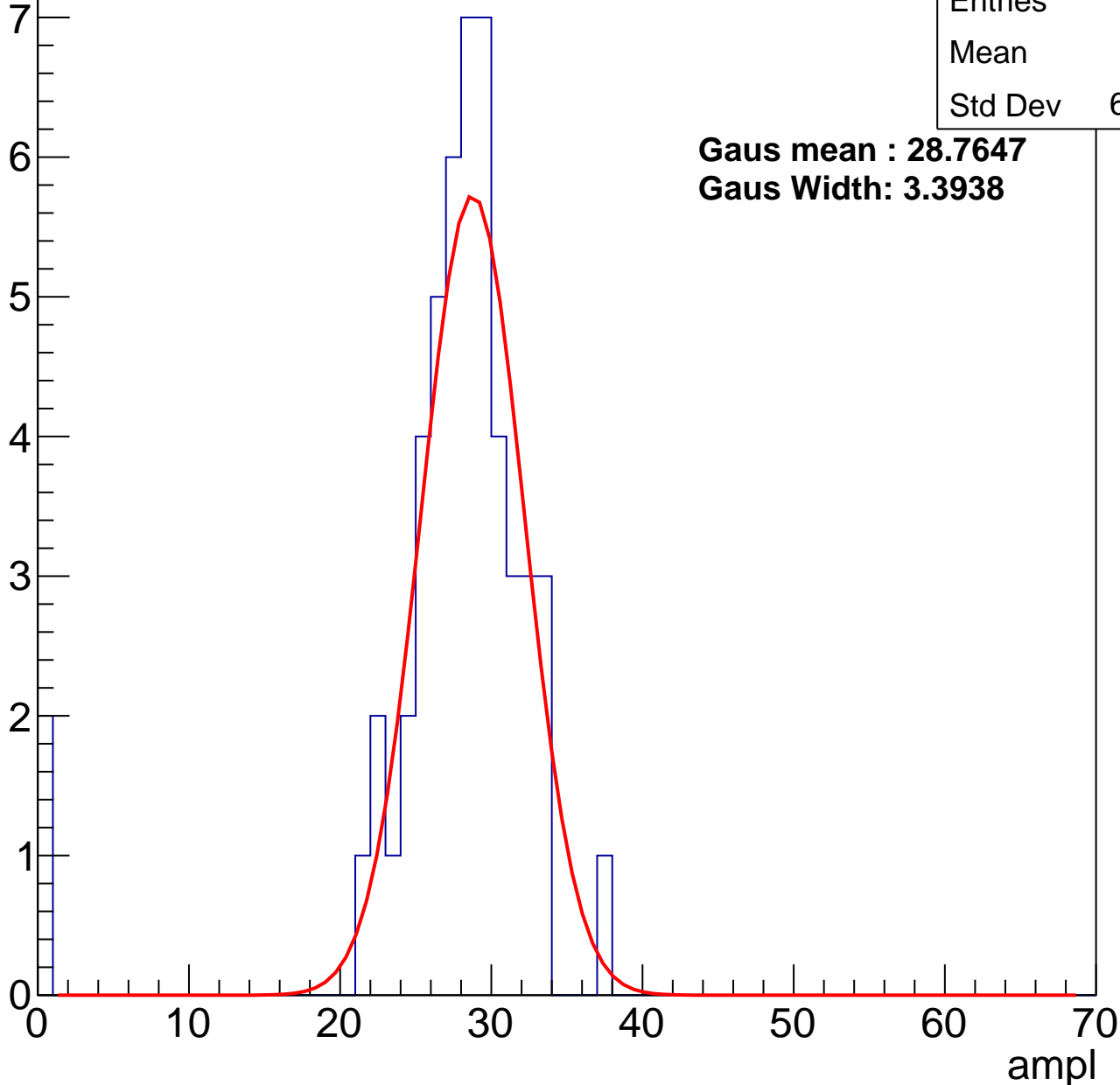
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	26.9
Std Dev	6.263

**Gaus mean : 28.7647**

**Gaus Width: 3.3938**



# B1L002S, U26-ch91, adc1

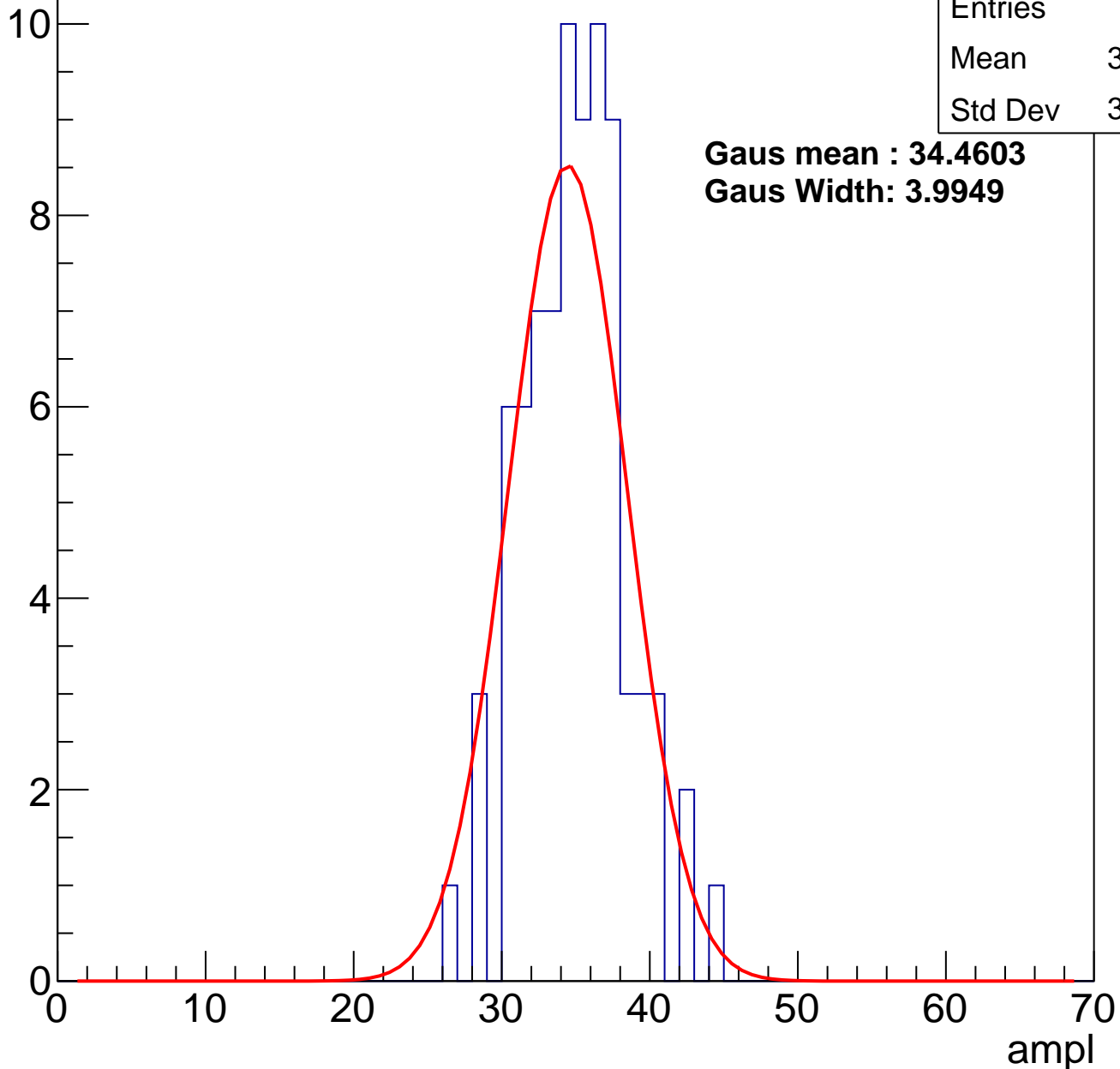
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	80
Mean	34.48
Std Dev	3.406

**Gaus mean : 34.4603**

**Gaus Width: 3.9949**

Entry



# B1L002S, U26-ch91, adc2

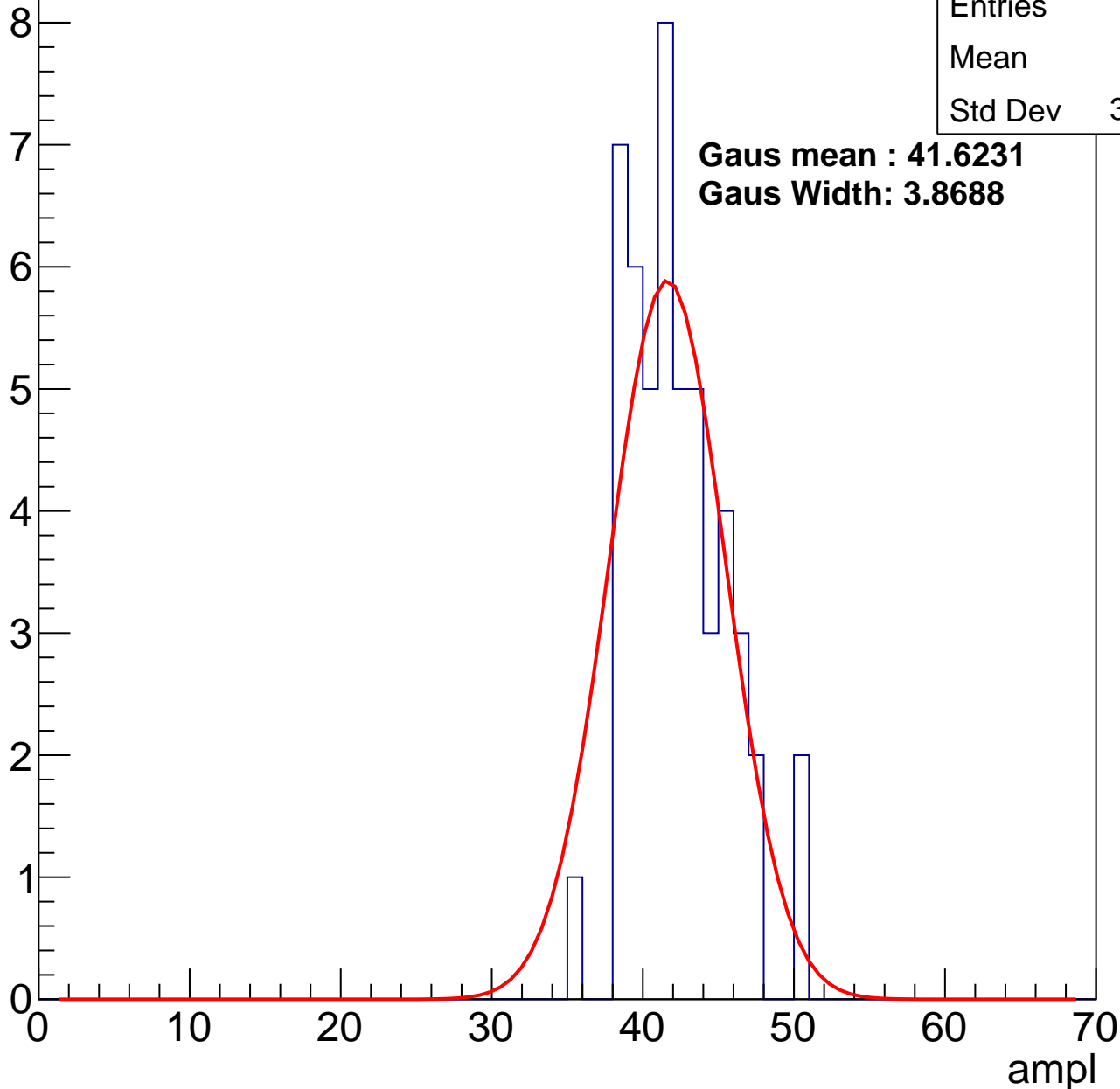
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	41.8
Std Dev	3.193

**Gaus mean : 41.6231**

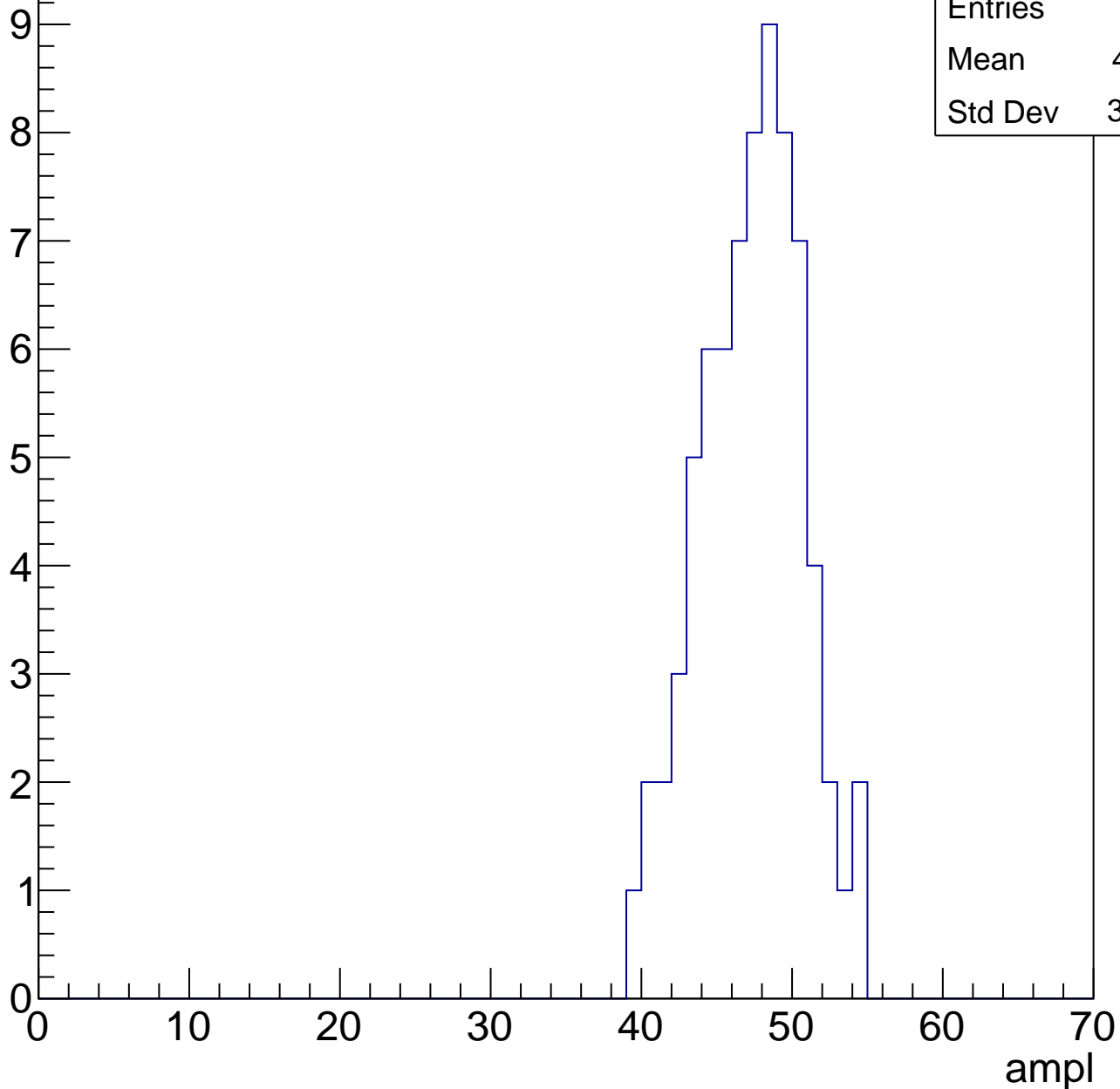
**Gaus Width: 3.8688**



# B1L002S, U26-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

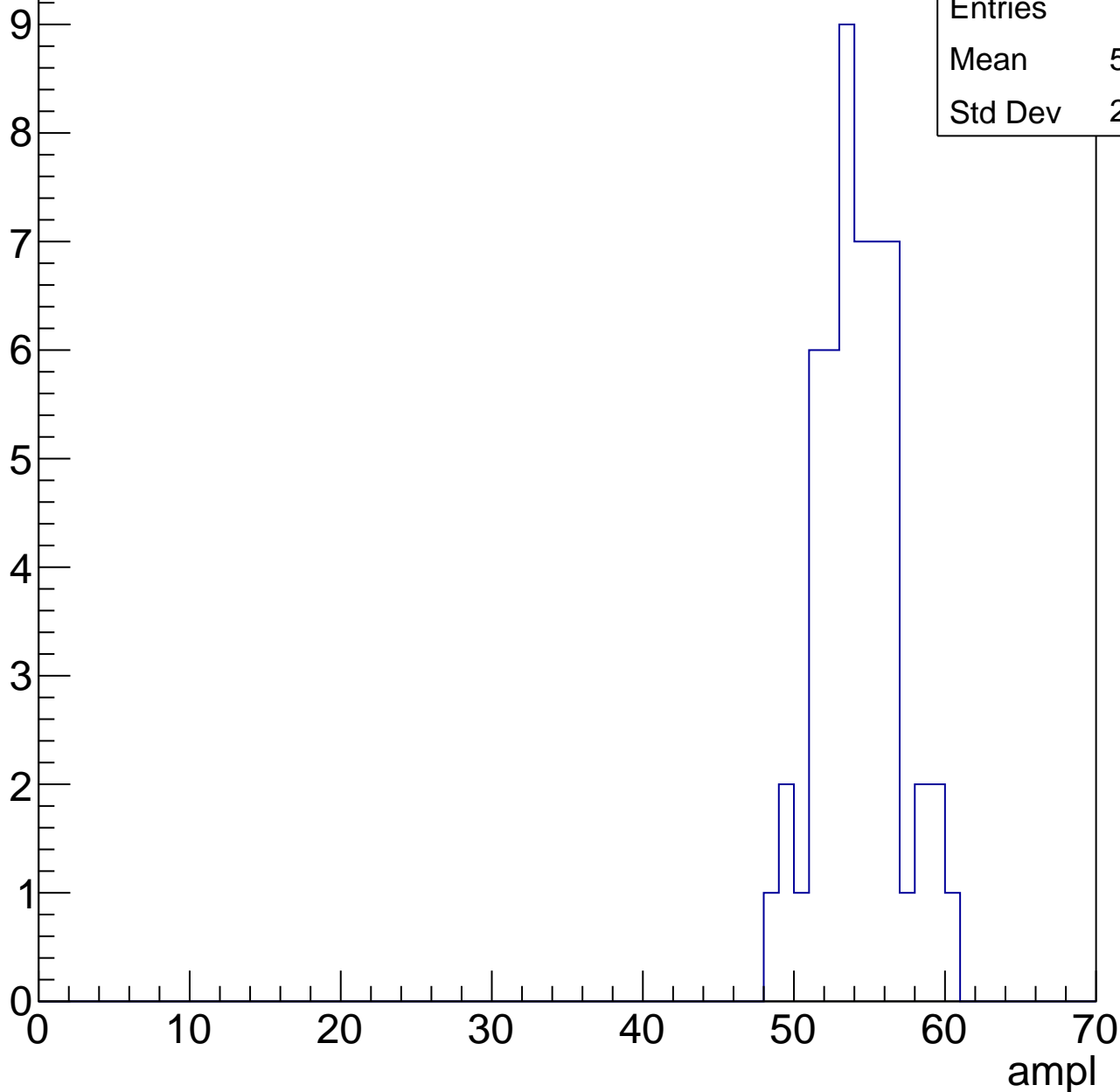


Entries	73
Mean	46.81
Std Dev	3.358

# B1L002S, U26-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

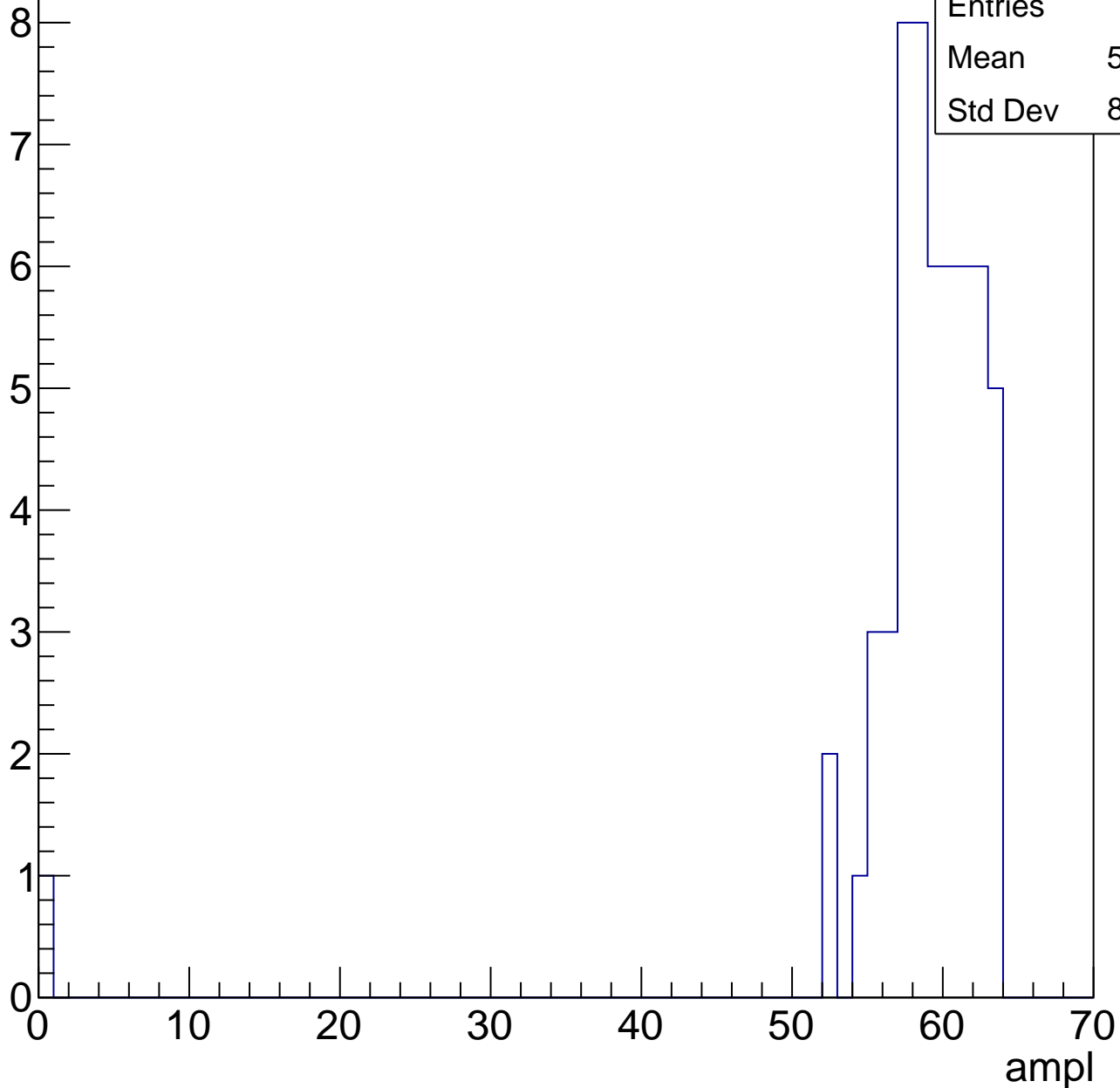


# B1L002S, U26-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.78
Std Dev	8.314

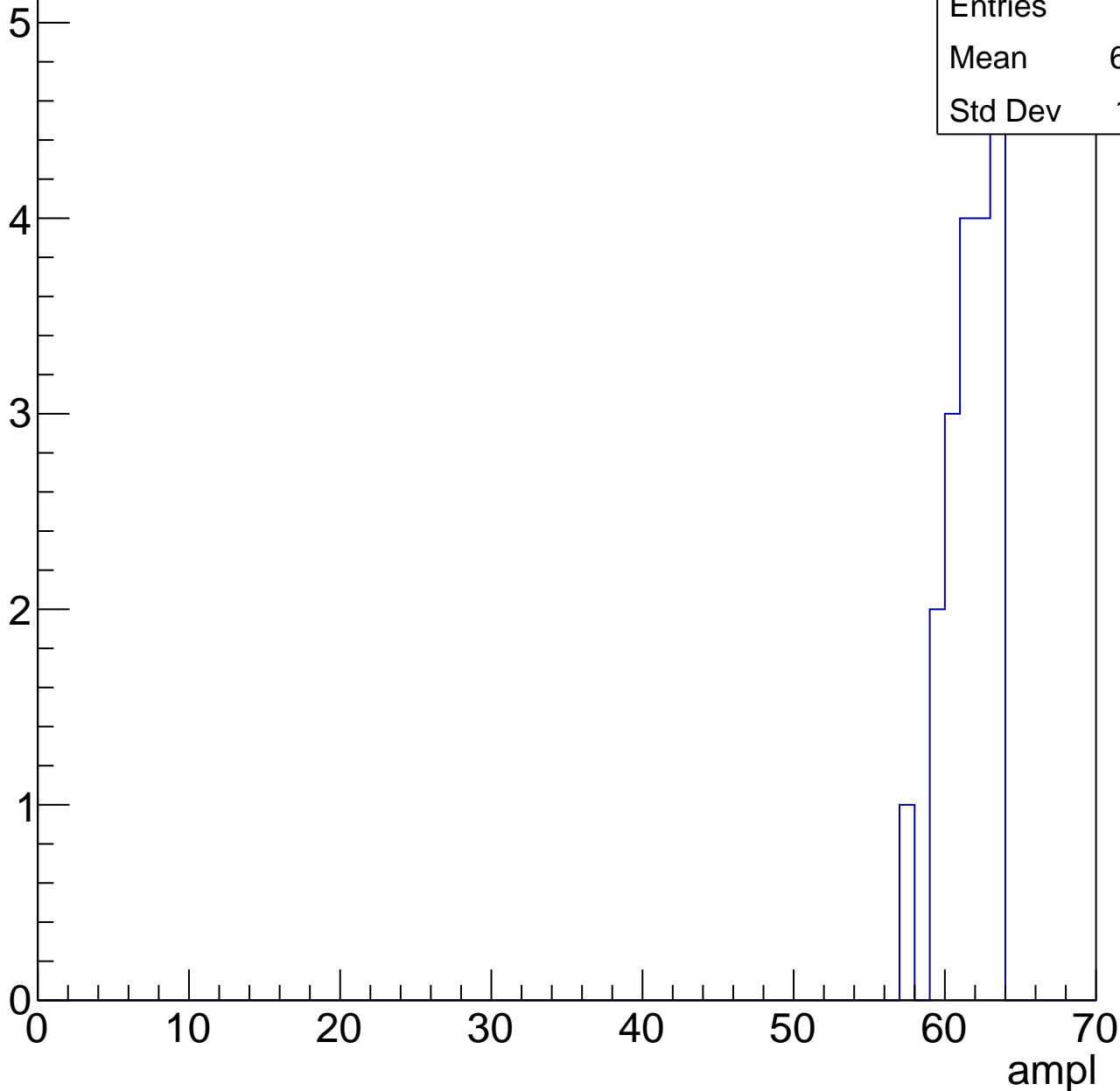


# B1L002S, U26-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.16
Std Dev	1.631





# B1L002S, U26-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch92, adc0

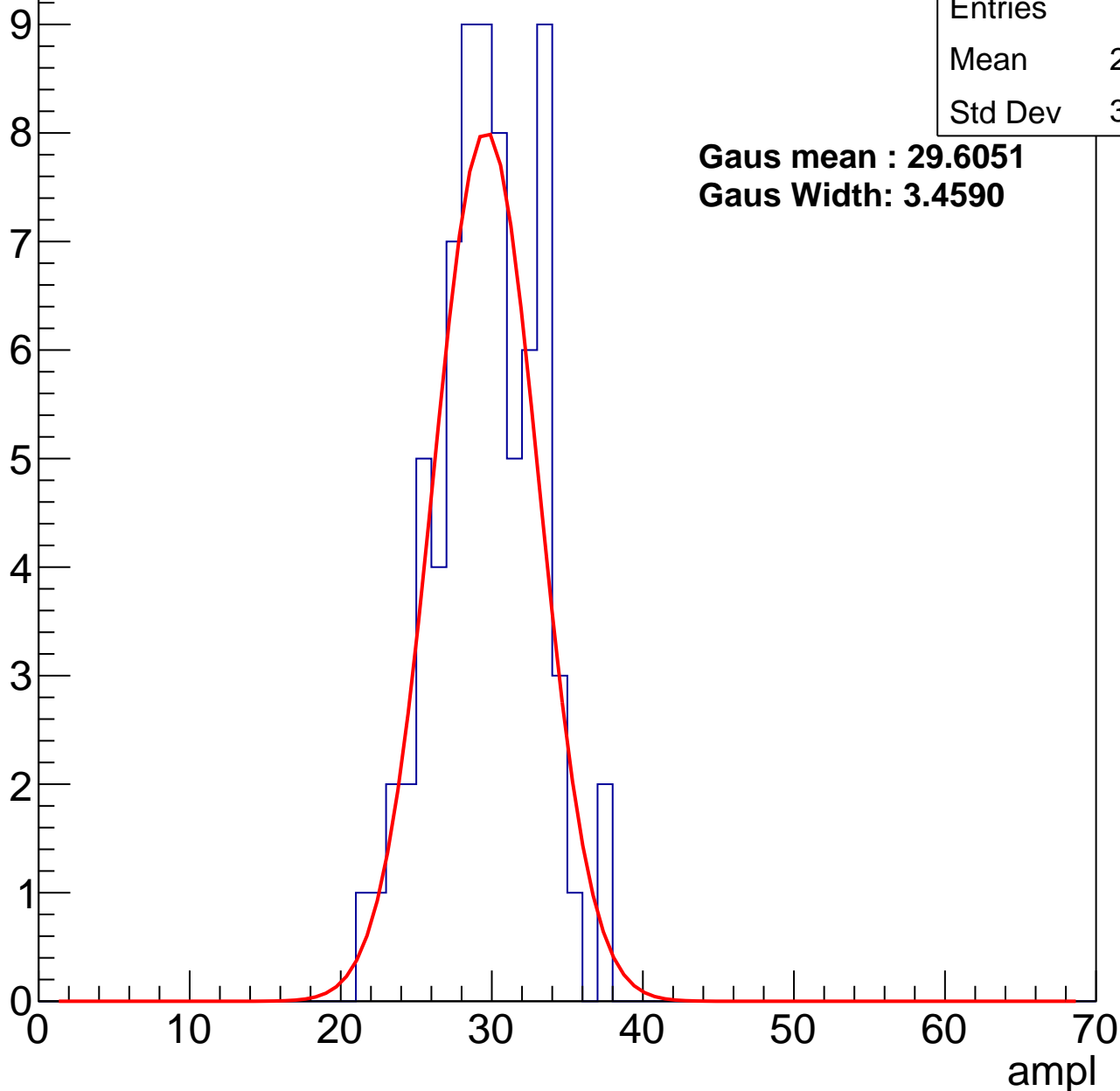
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	29.23
Std Dev	3.387

**Gaus mean : 29.6051**

**Gaus Width: 3.4590**



# B1L002S, U26-ch92, adc1

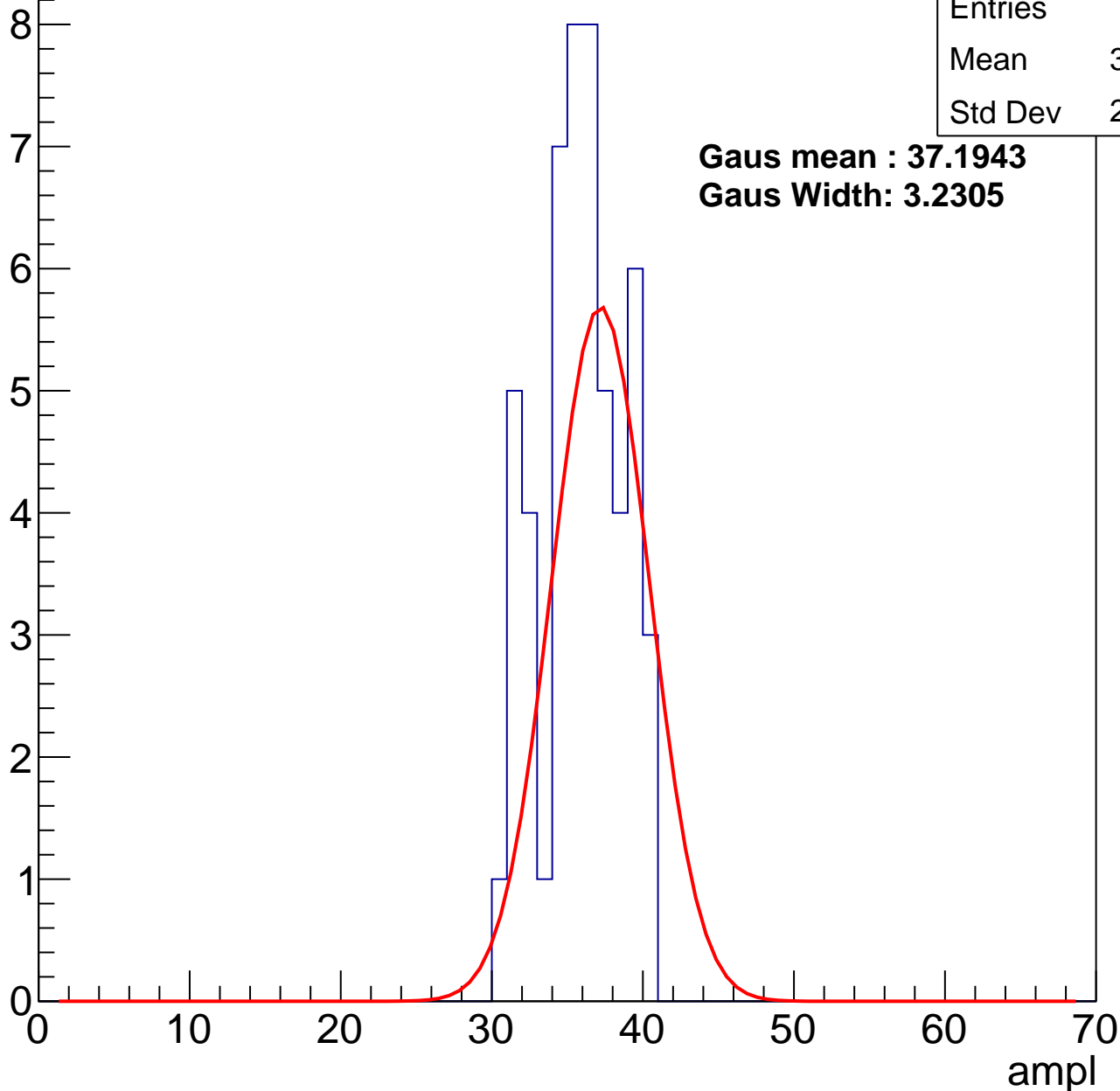
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	35.44
Std Dev	2.692

**Gaus mean : 37.1943**

**Gaus Width: 3.2305**



# B1L002S, U26-ch92, adc2

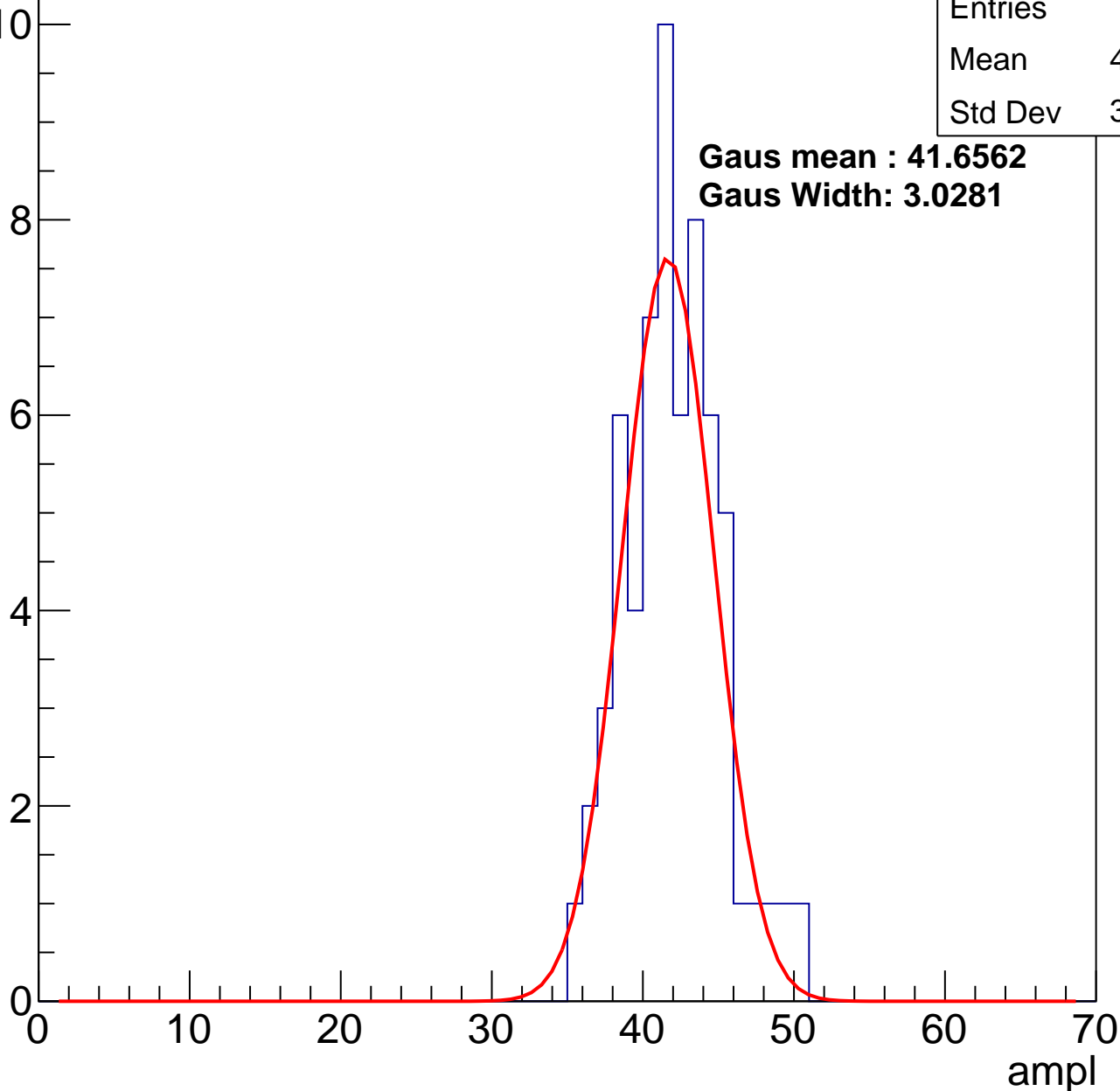
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.54
Std Dev	3.136

**Gaus mean : 41.6562**

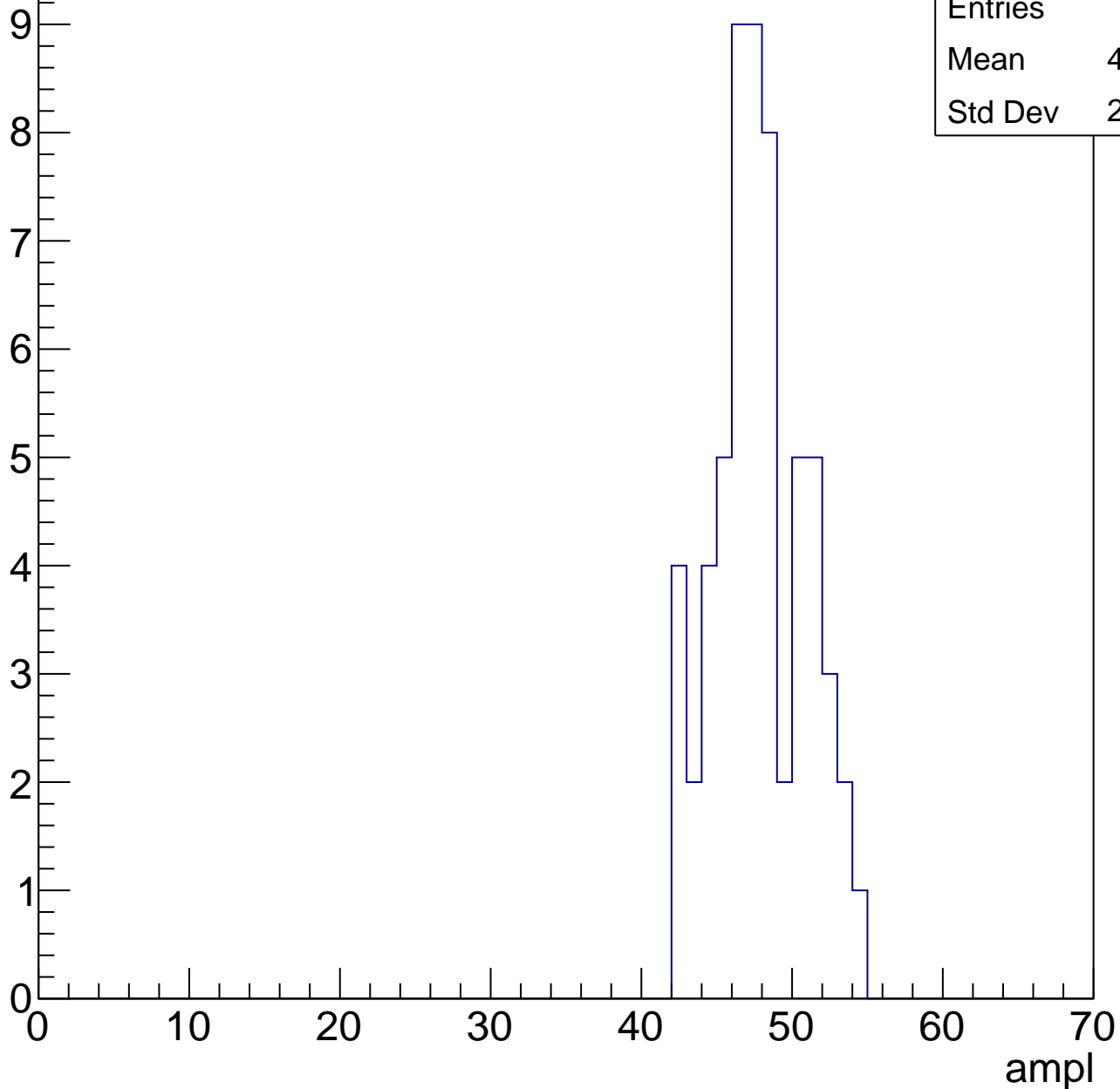
**Gaus Width: 3.0281**



# B1L002S, U26-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



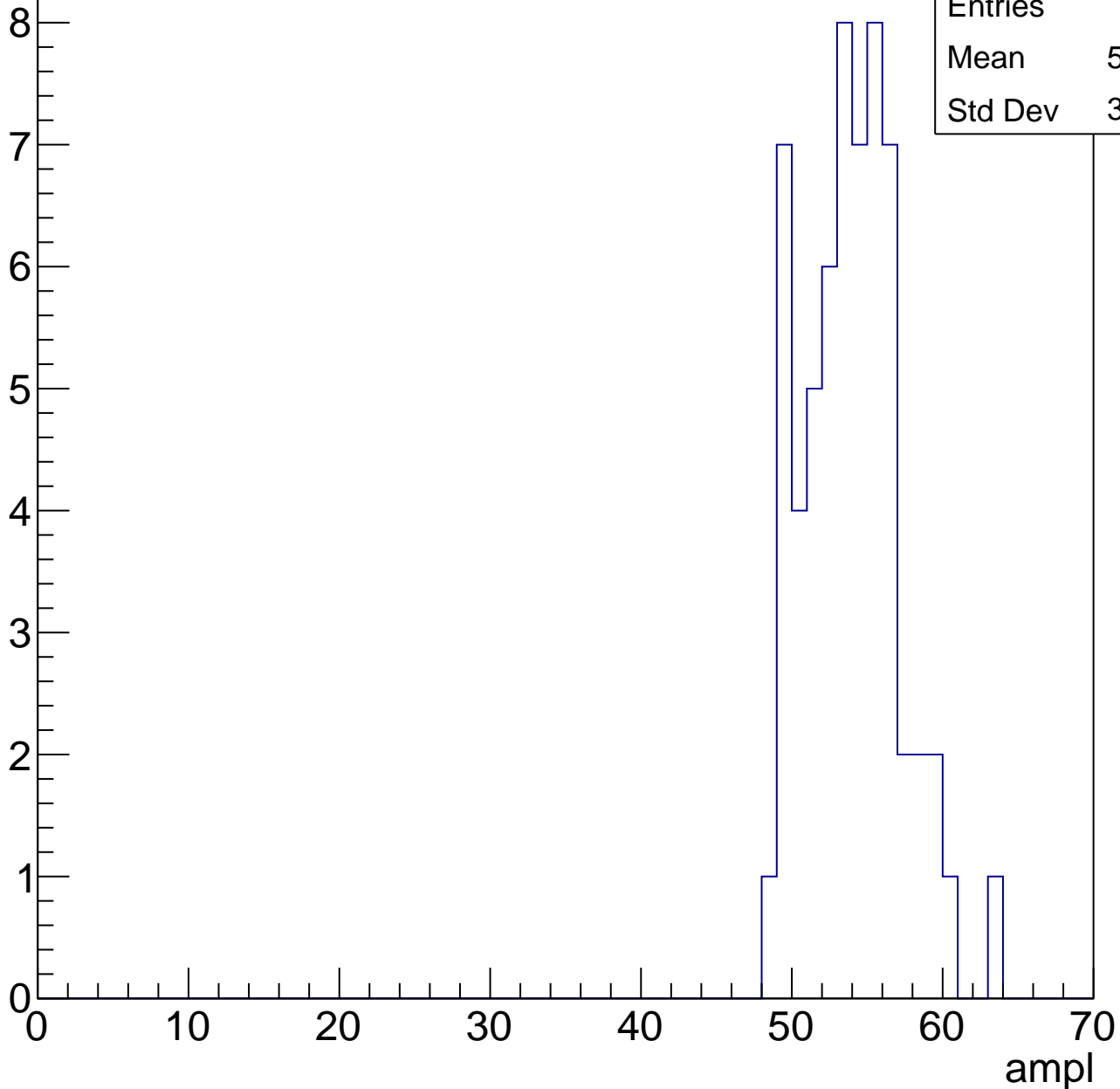
Entries	59
Mean	47.37
Std Dev	2.985

# B1L002S, U26-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	53.49
Std Dev	3.108

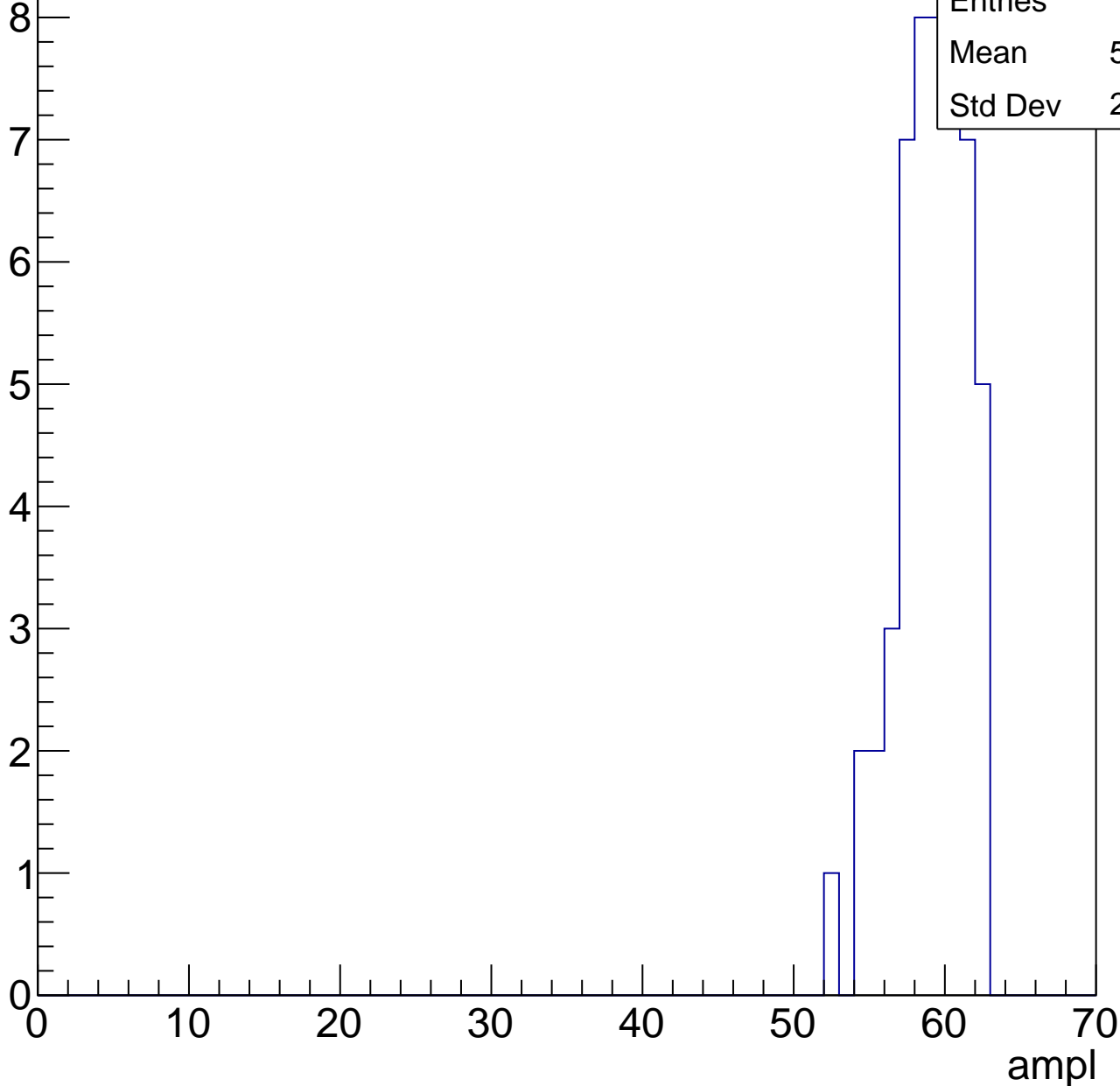


# B1L002S, U26-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	58.63
Std Dev	2.292

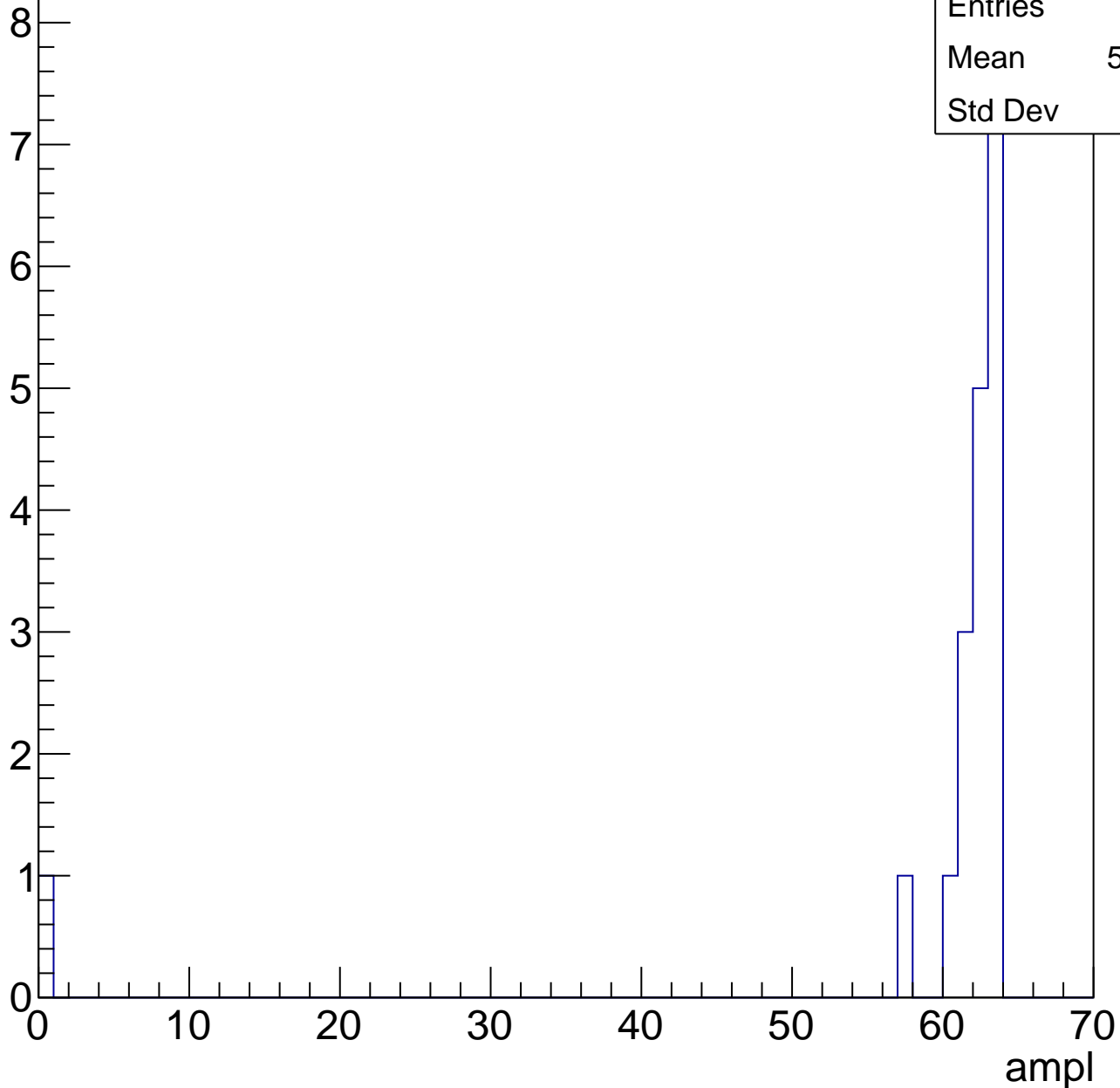


# B1L002S, U26-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.63
Std Dev	13.9

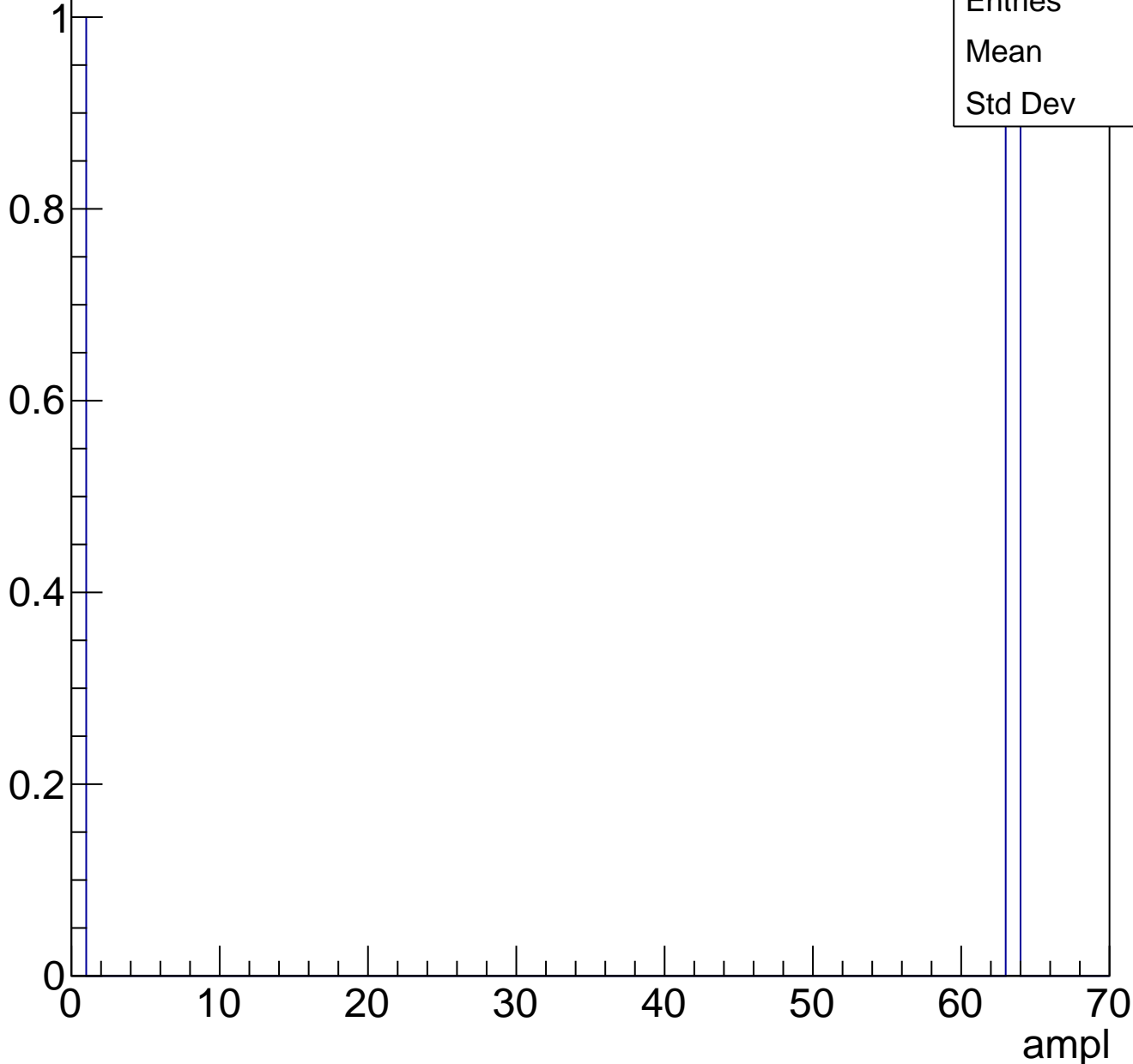




# B1L002S, U26-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch93, adc0

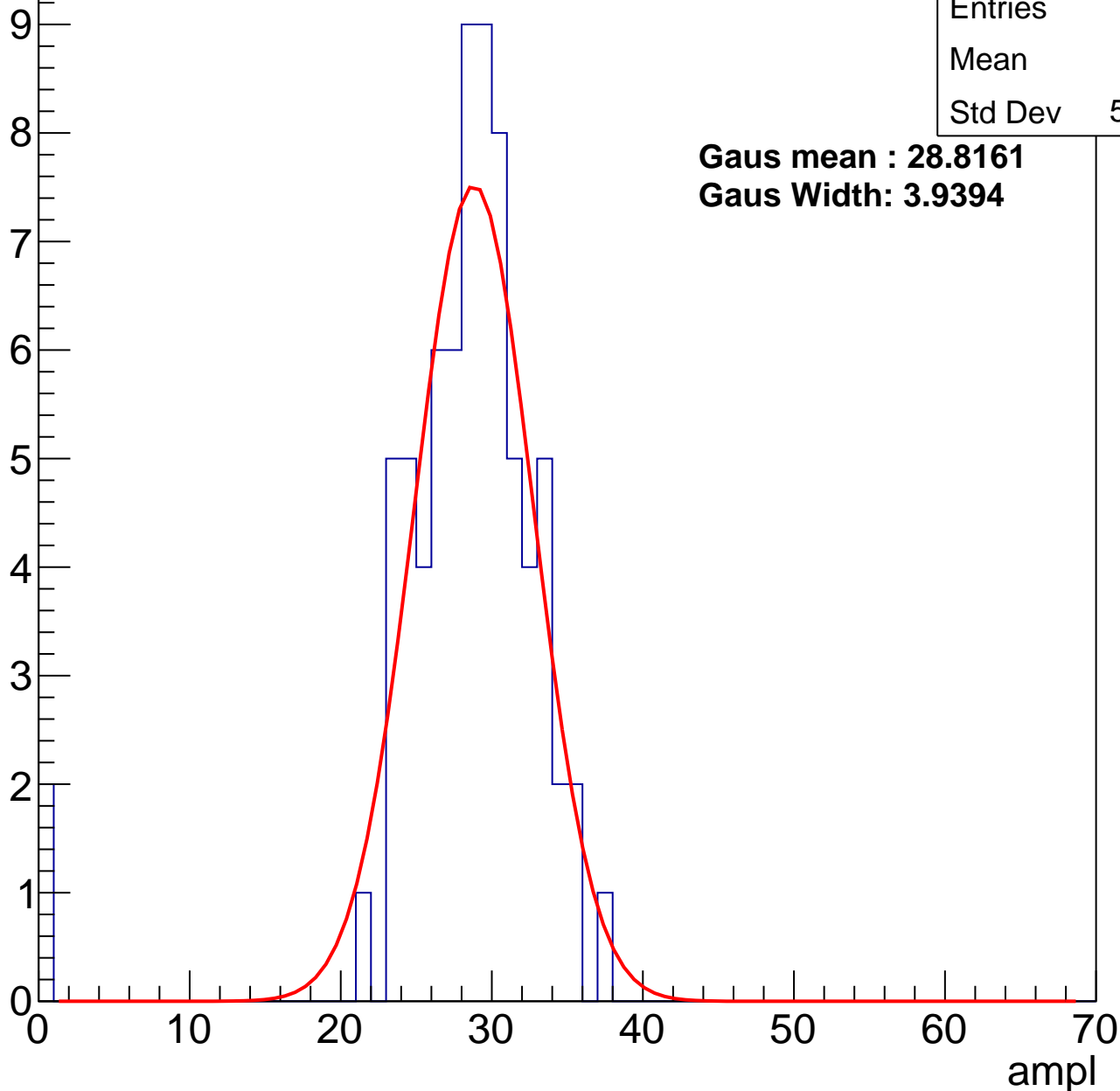
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	27.7
Std Dev	5.704

**Gaus mean : 28.8161**

**Gaus Width: 3.9394**



# B1L002S, U26-ch93, adc1

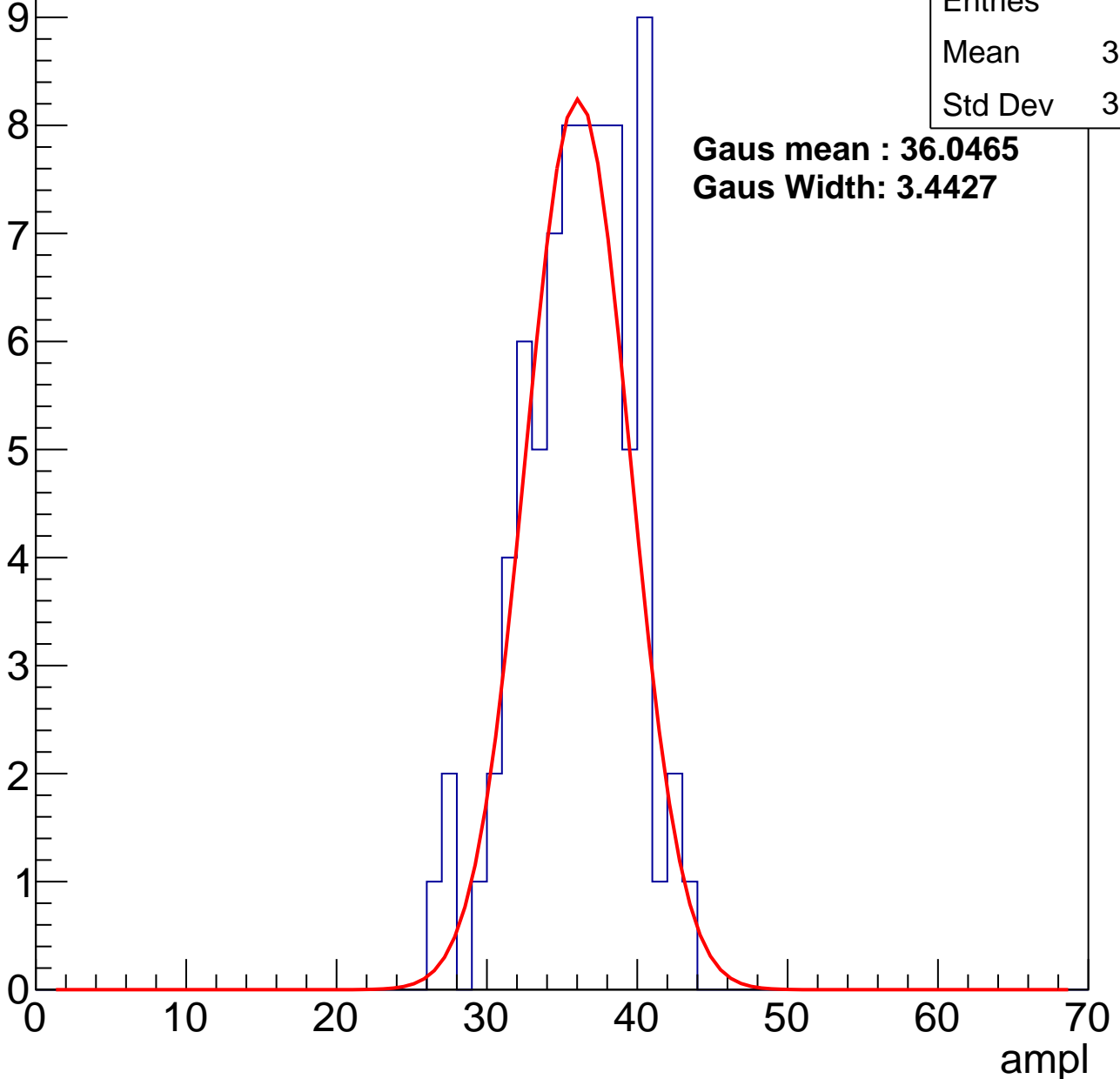
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	35.63
Std Dev	3.624

**Gaus mean : 36.0465**

**Gaus Width: 3.4427**



# B1L002S, U26-ch93, adc2

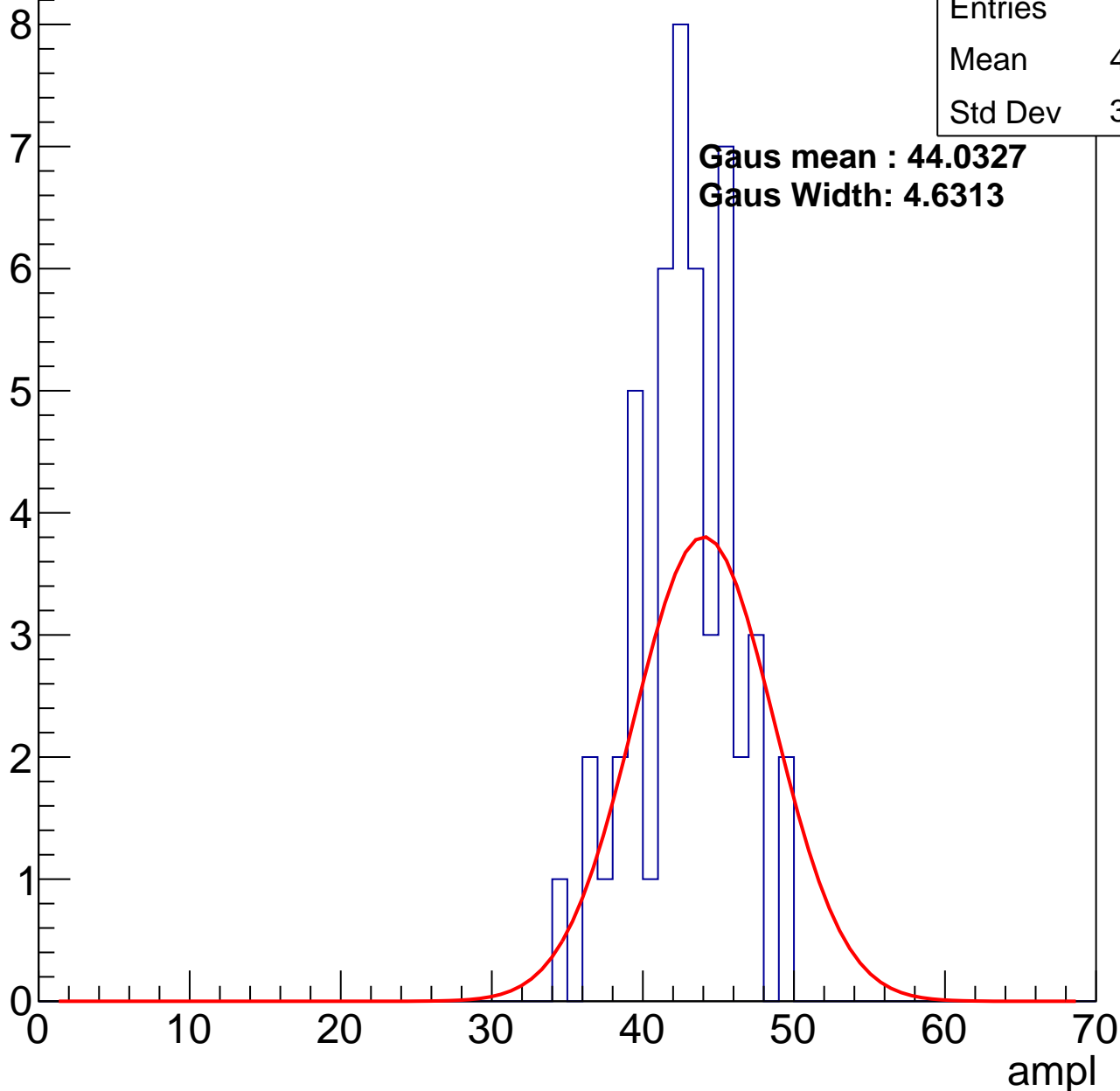
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	42.29
Std Dev	3.289

**Gaus mean : 44.0327**

**Gaus Width: 4.6313**

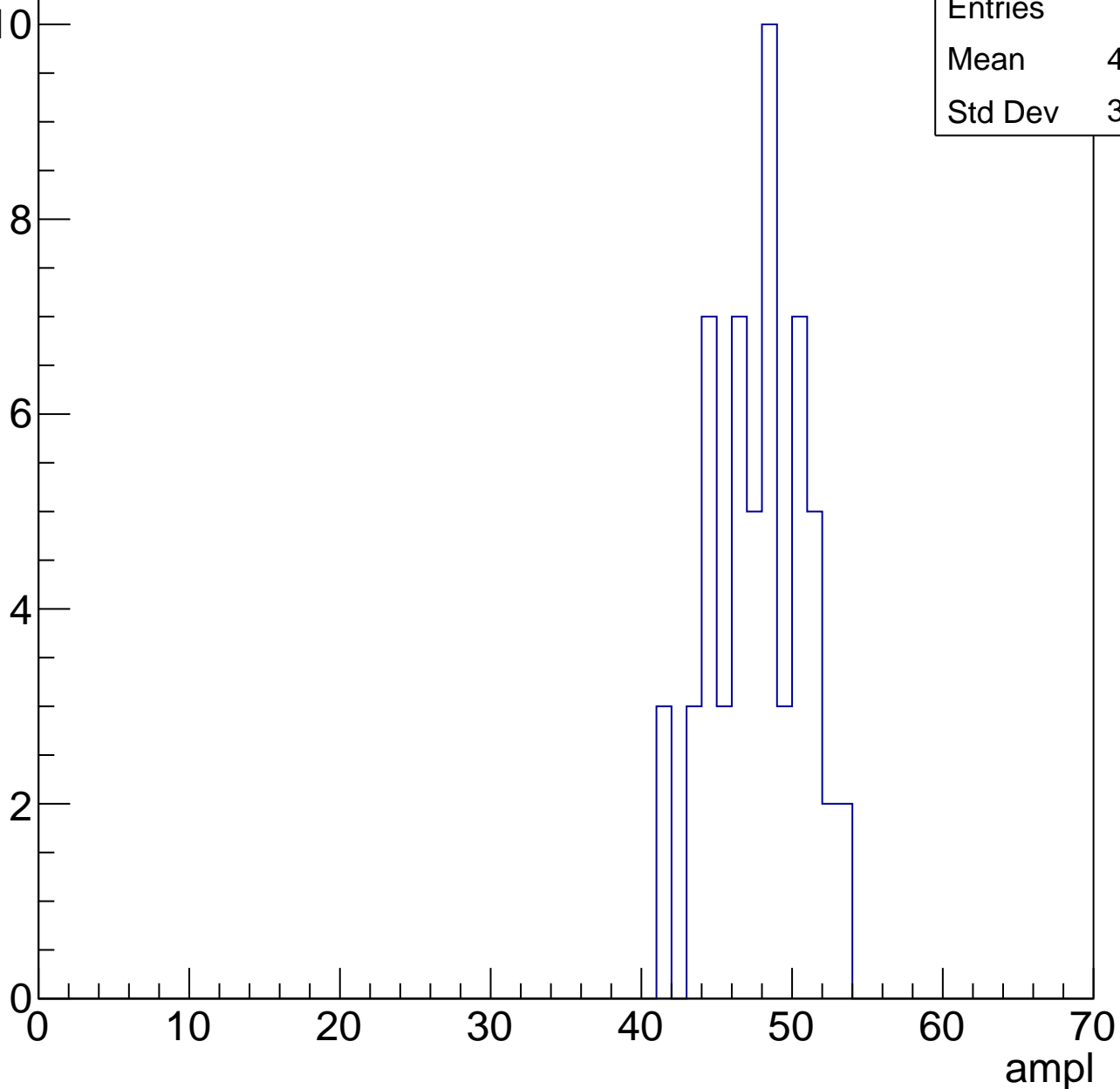


# B1L002S, U26-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

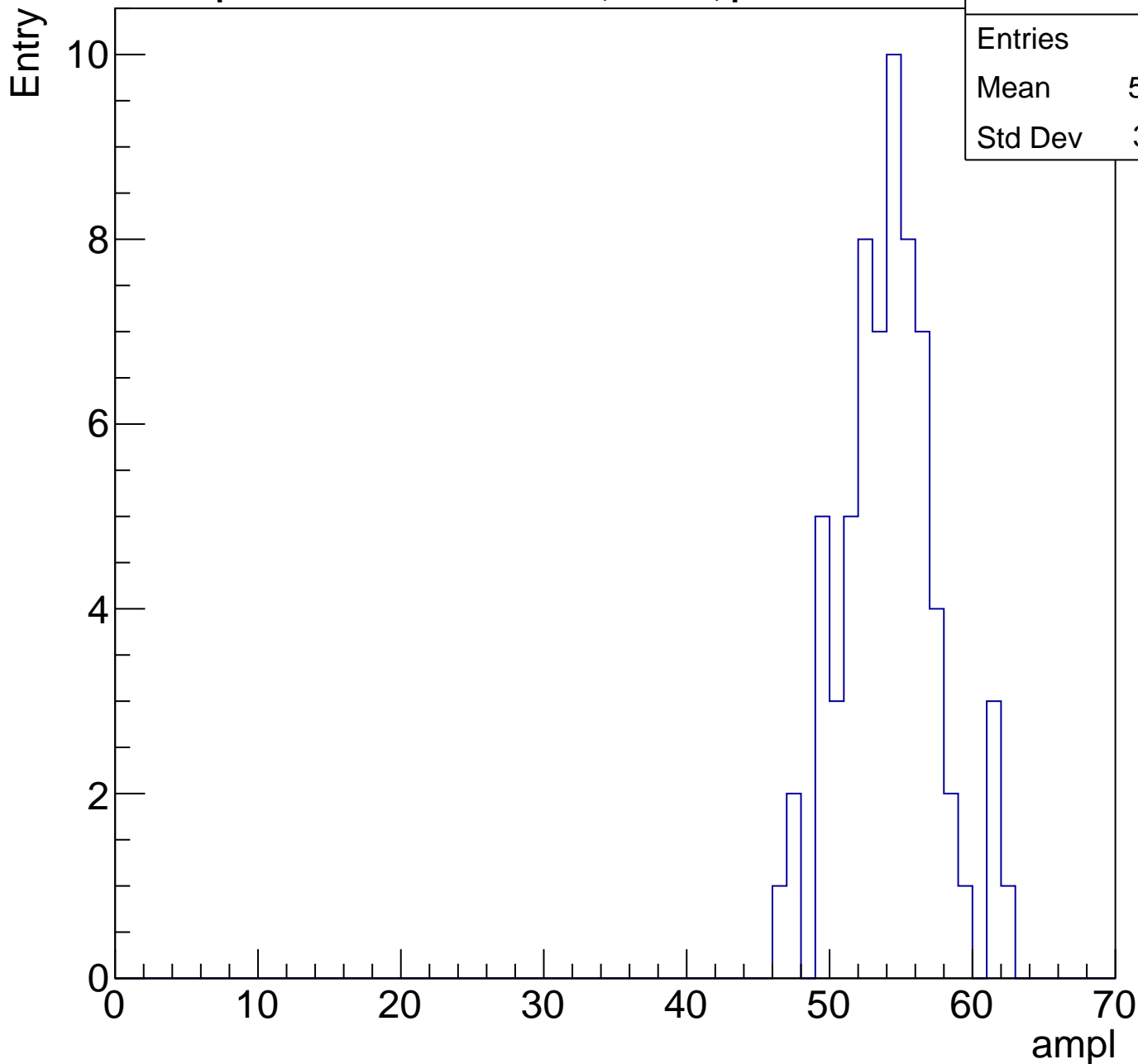
Entries	57
Mean	47.26
Std Dev	3.023



# B1L002S, U26-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	53.69
Std Dev	3.351

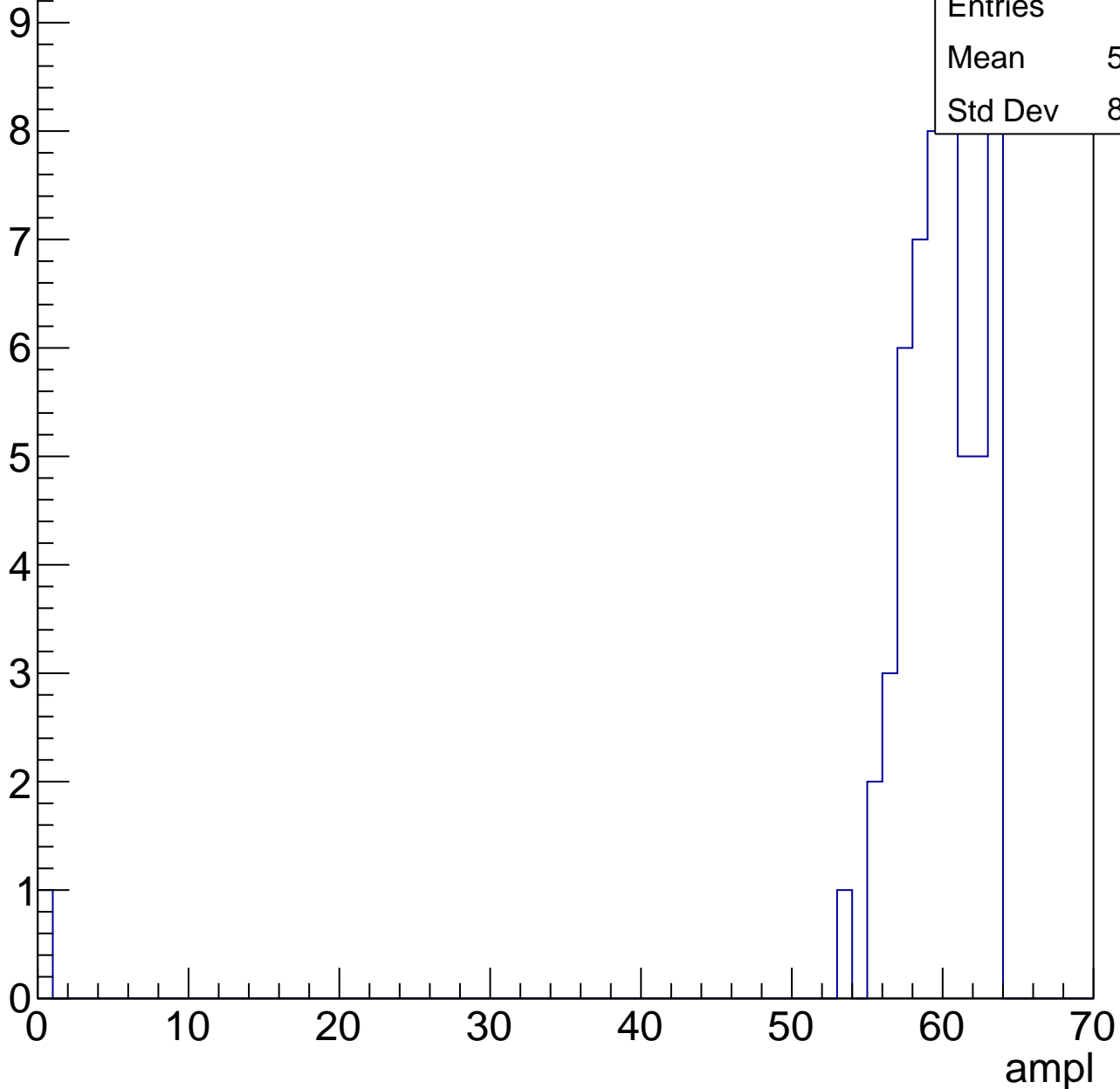


# B1L002S, U26-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

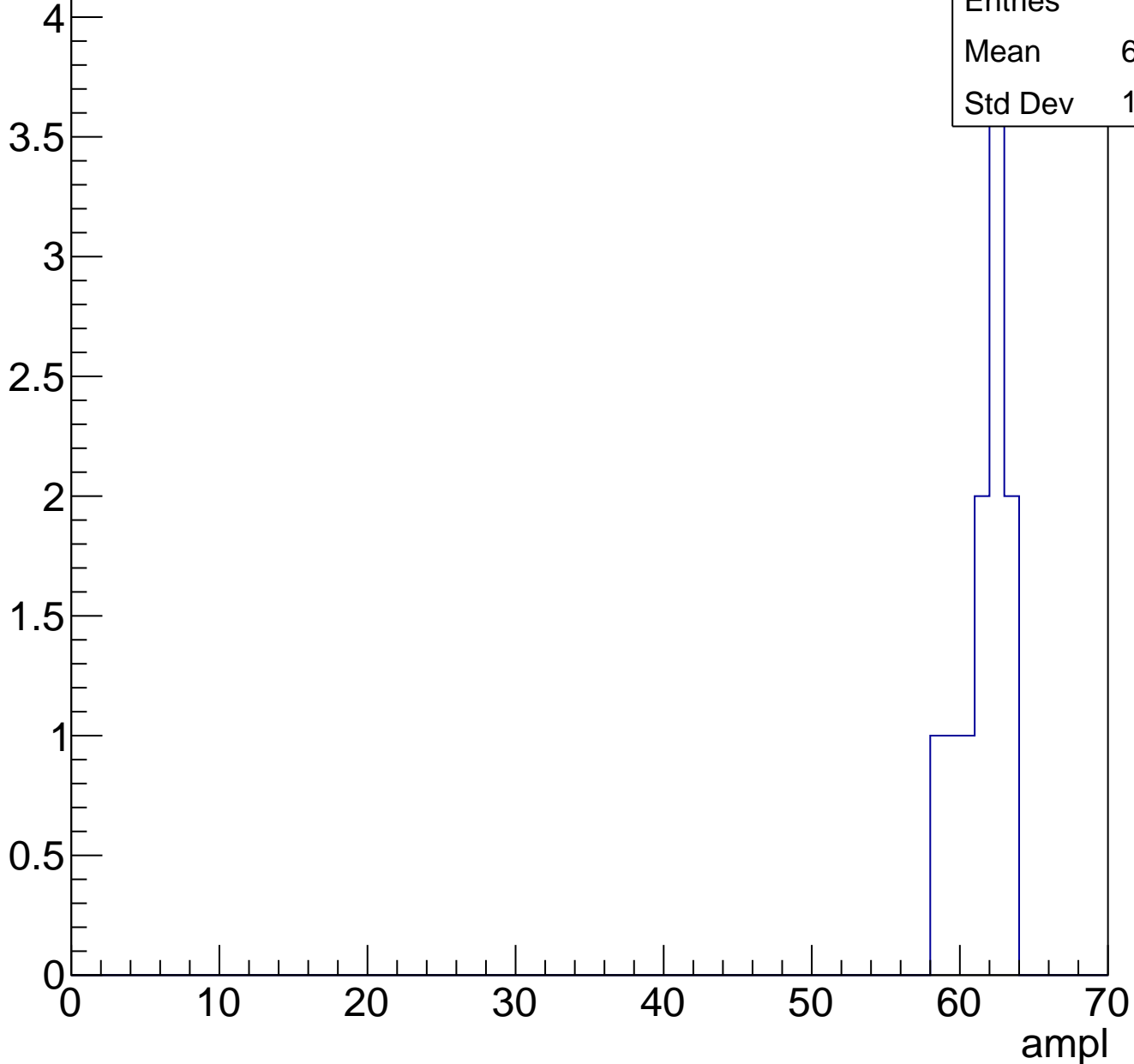
Entries	55
Mean	58.36
Std Dev	8.298



# B1L002S, U26-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch94, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	26.17
Std Dev	4.411

**Gaus mean : 26.7378**

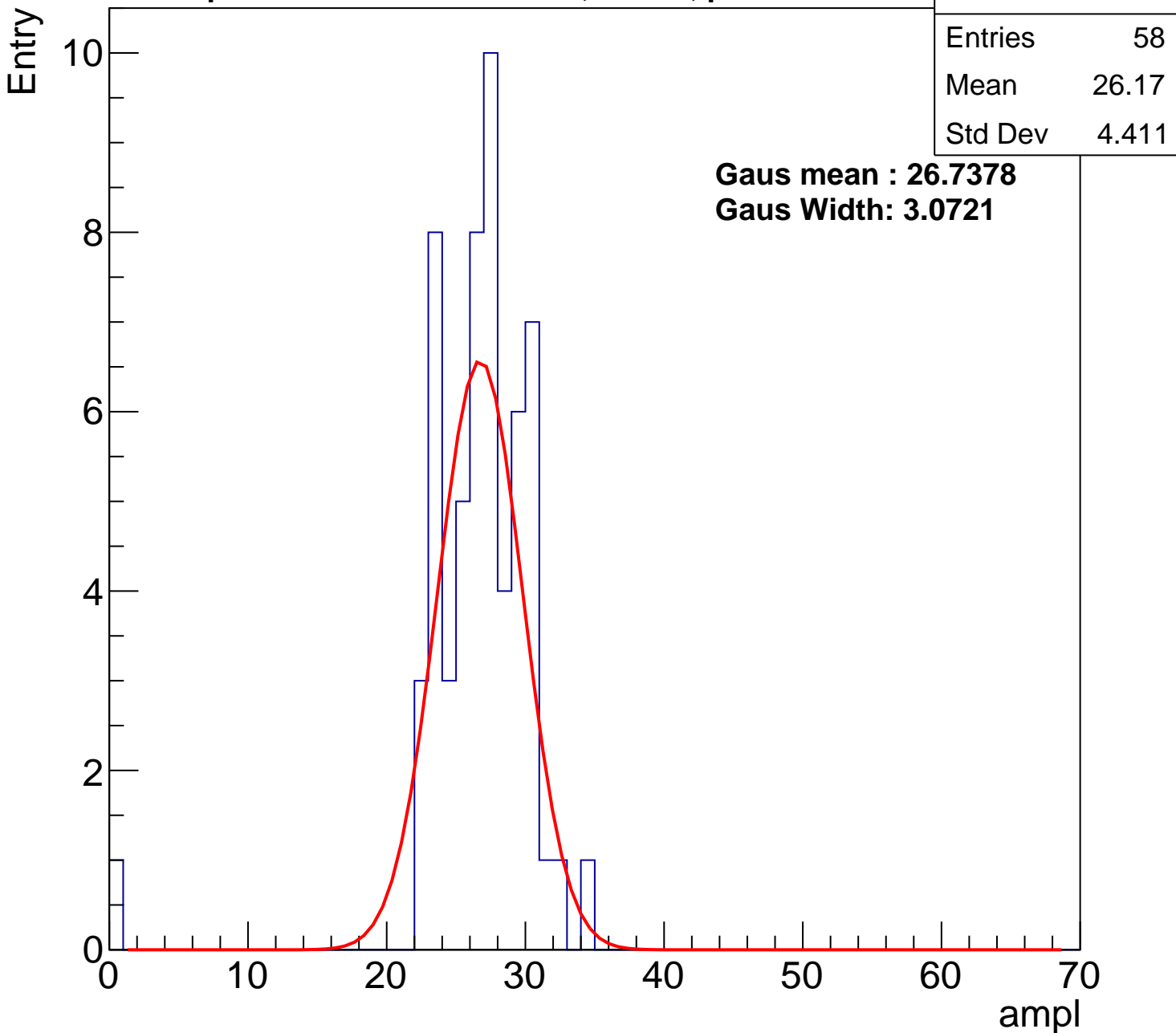
**Gaus Width: 3.0721**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch94, adc1

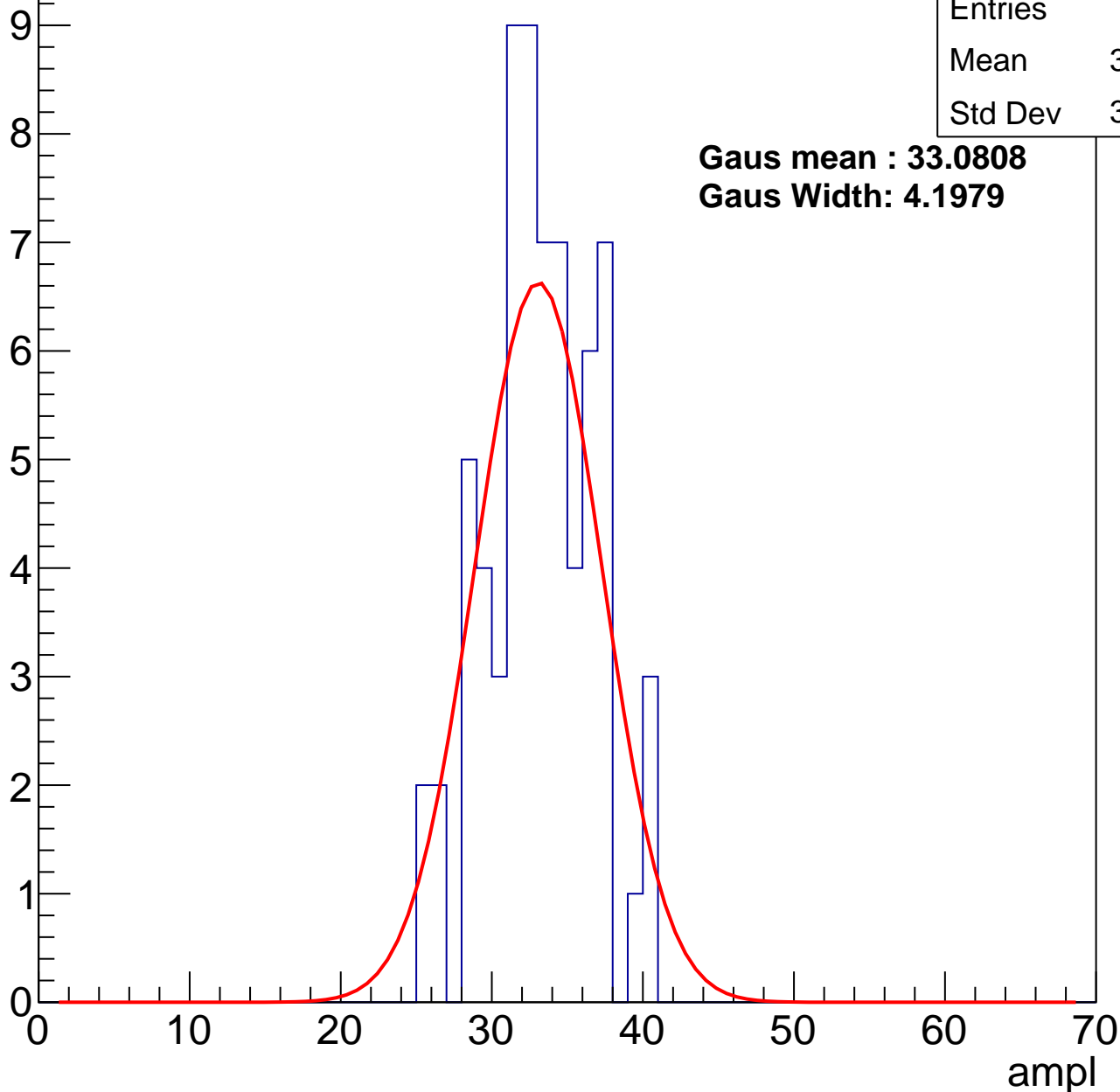
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	32.72
Std Dev	3.518

**Gaus mean : 33.0808**

**Gaus Width: 4.1979**



# B1L002S, U26-ch94, adc2

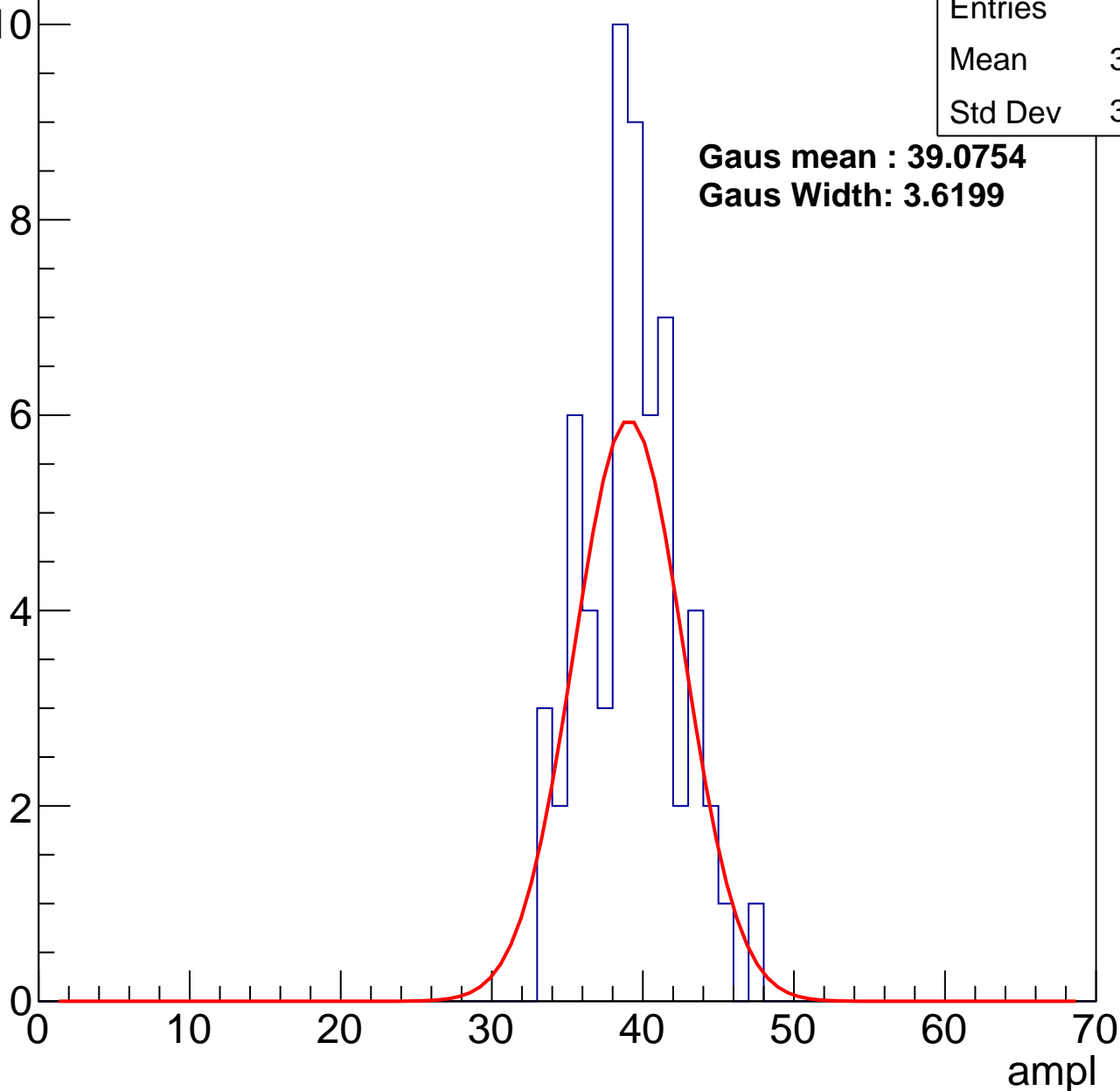
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	38.77
Std Dev	3.095

**Gaus mean : 39.0754**

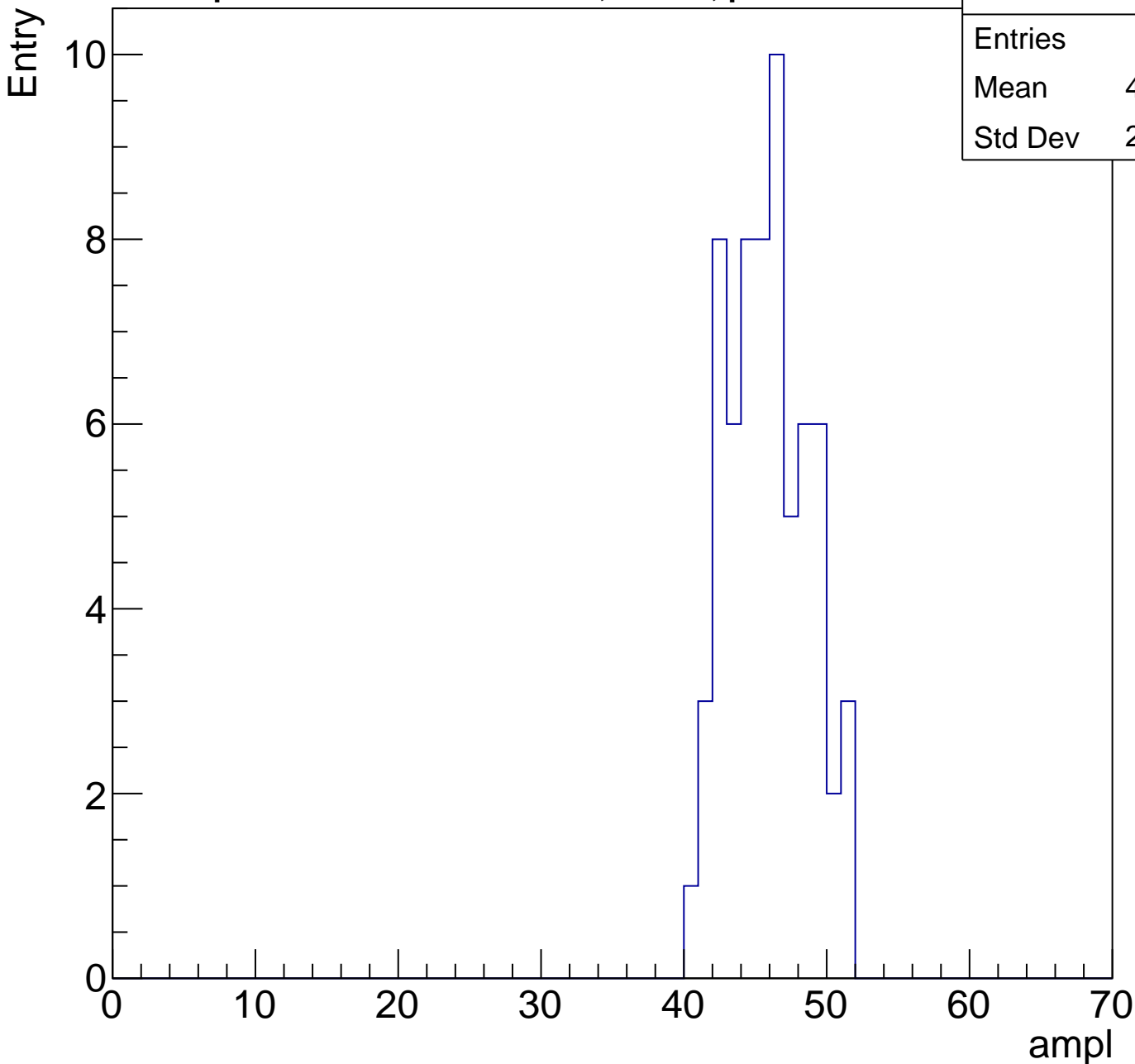
**Gaus Width: 3.6199**



# B1L002S, U26-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	45.44
Std Dev	2.759

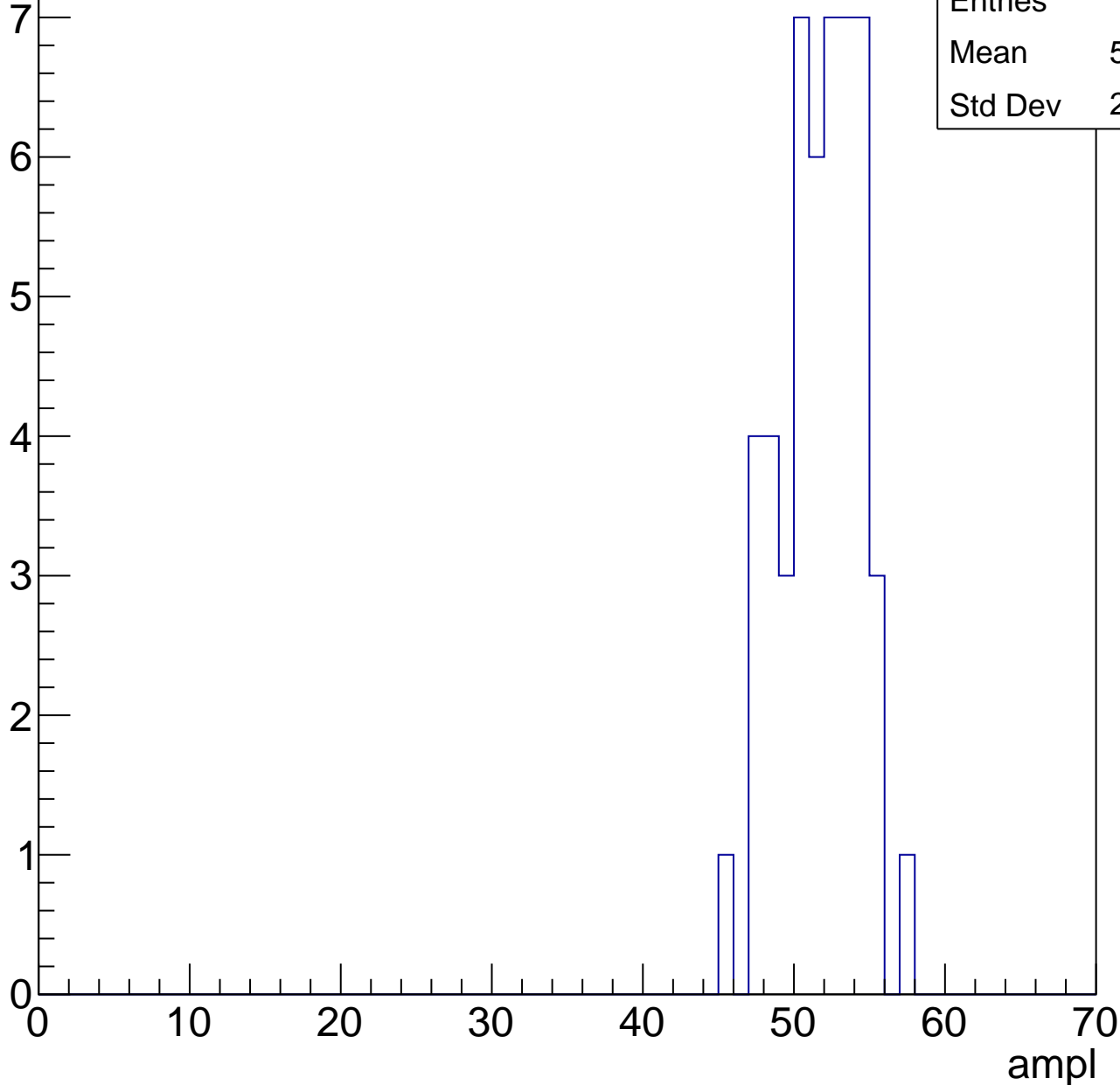


# B1L002S, U26-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	51.26
Std Dev	2.583

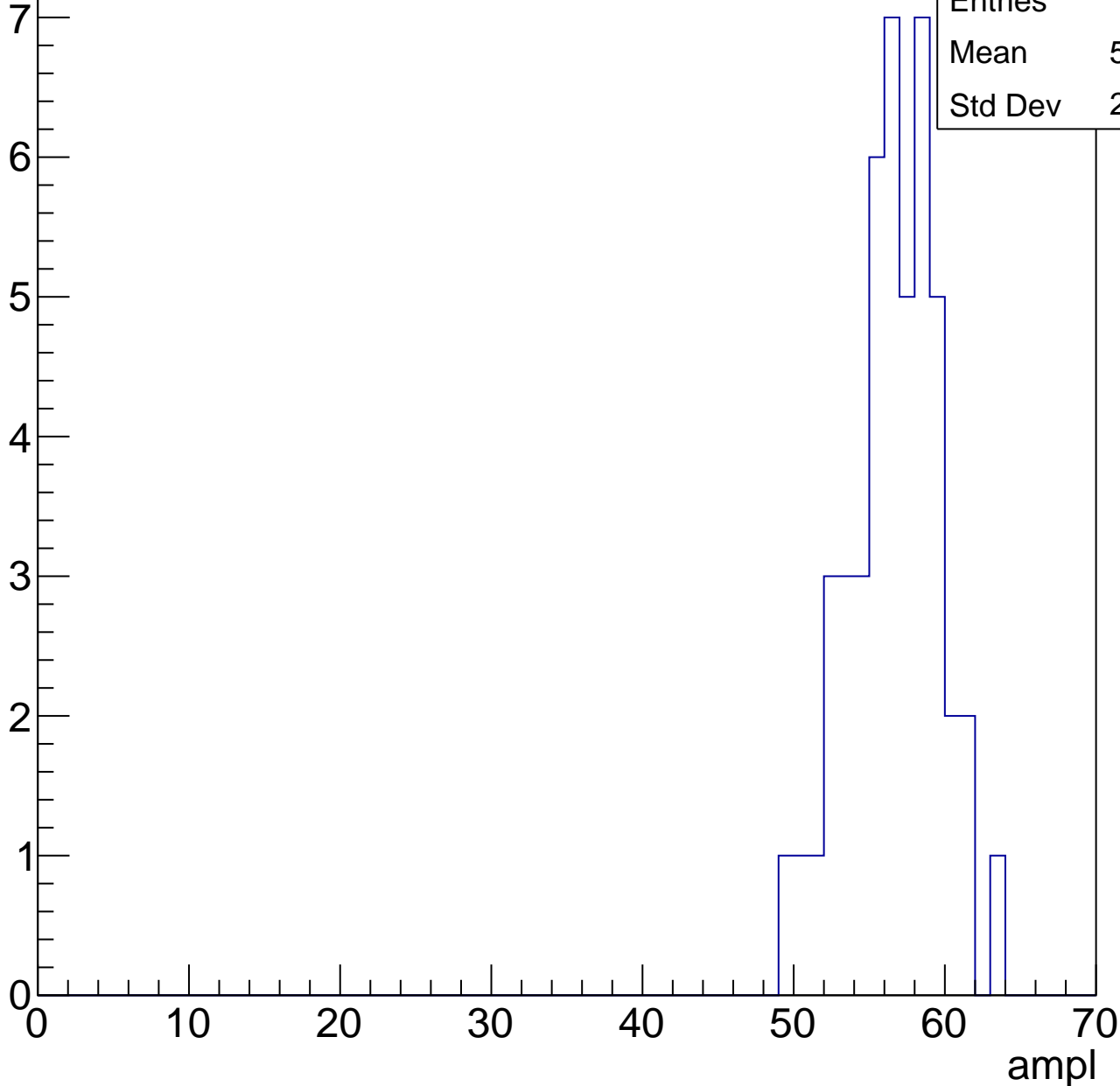


# B1L002S, U26-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	56.17
Std Dev	2.956

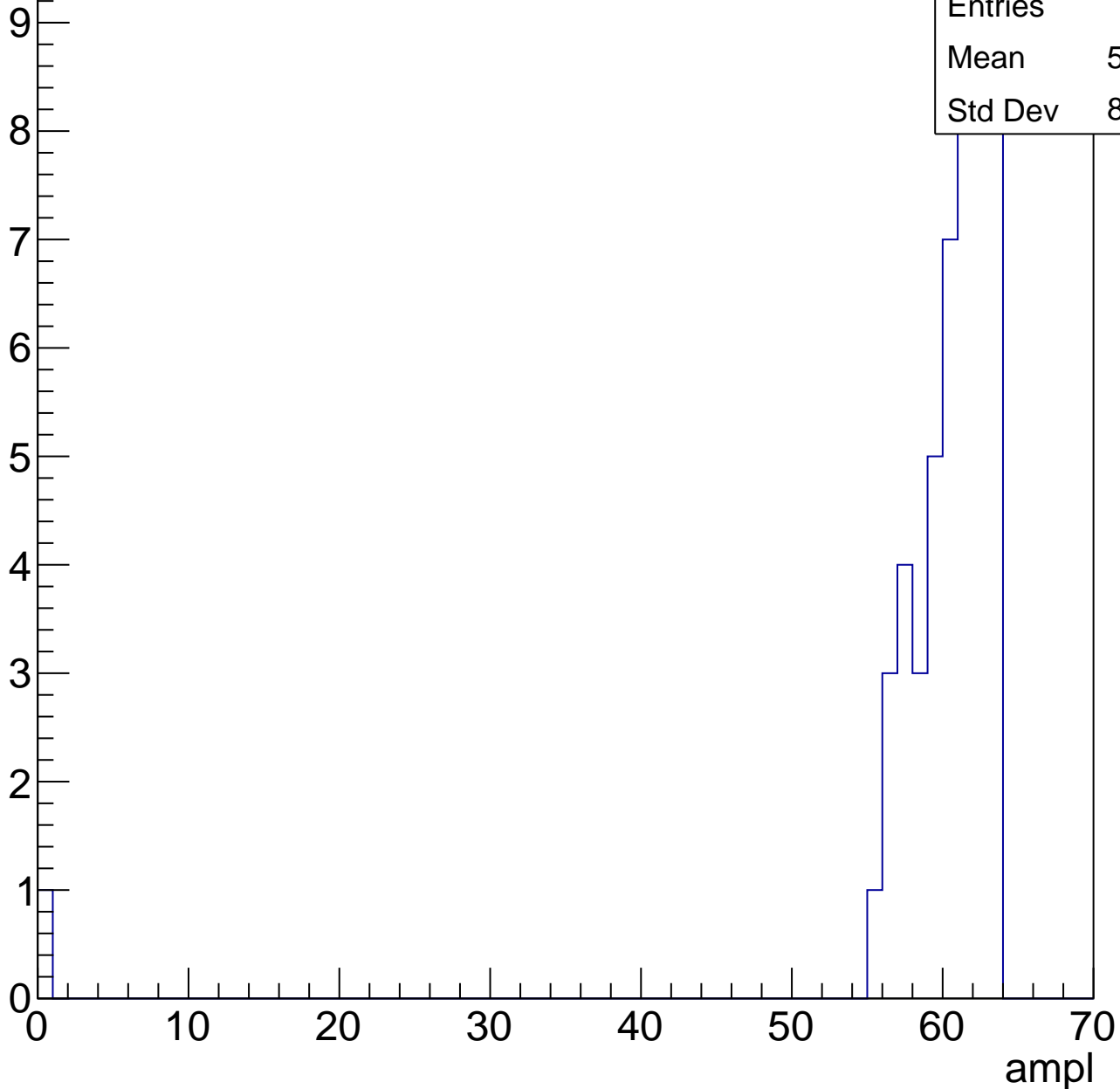


# B1L002S, U26-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	58.98
Std Dev	8.796

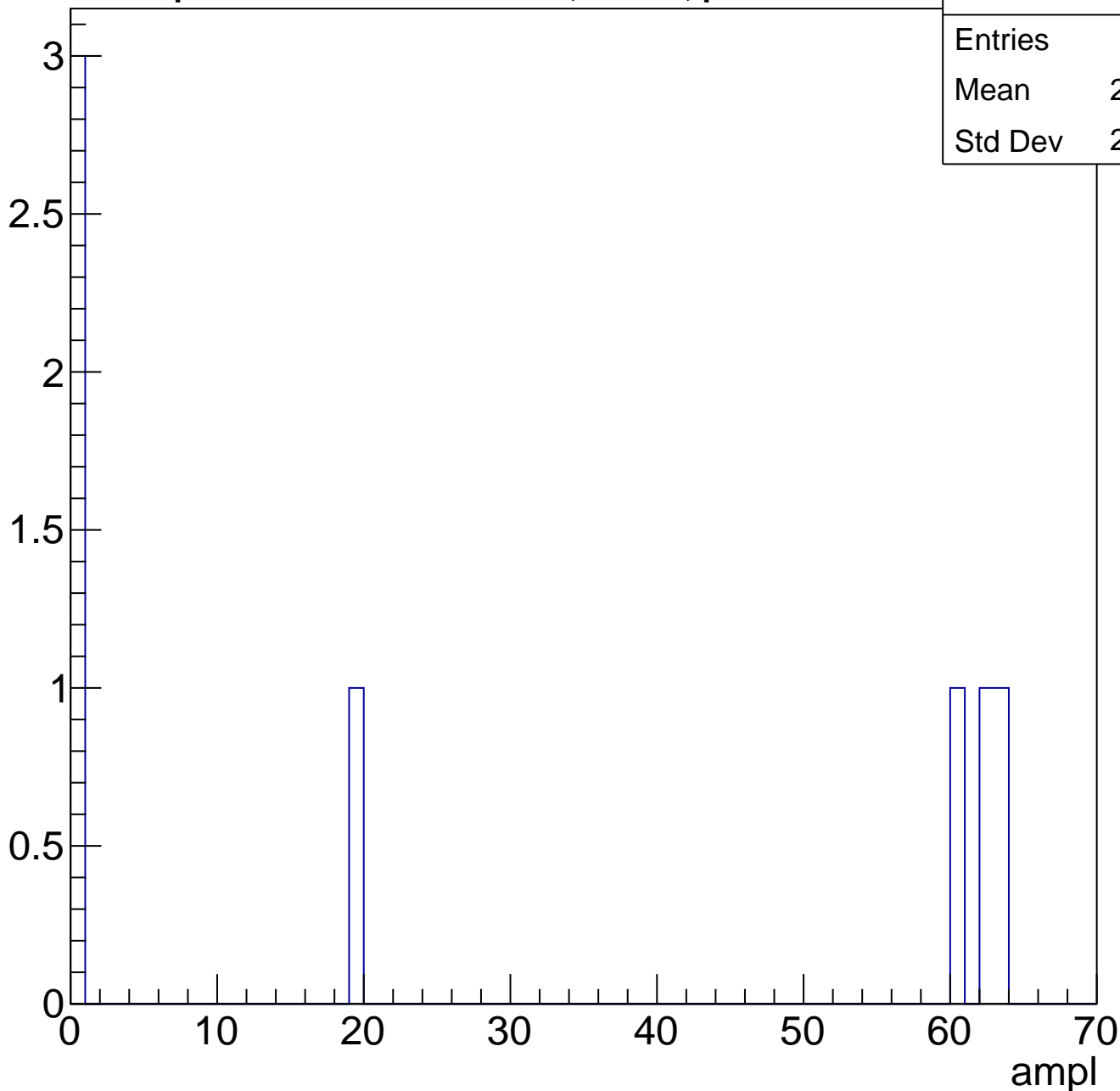




# B1L002S, U26-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	7
Mean	29.14
Std Dev	28.86

# B1L002S, U26-ch95, adc0

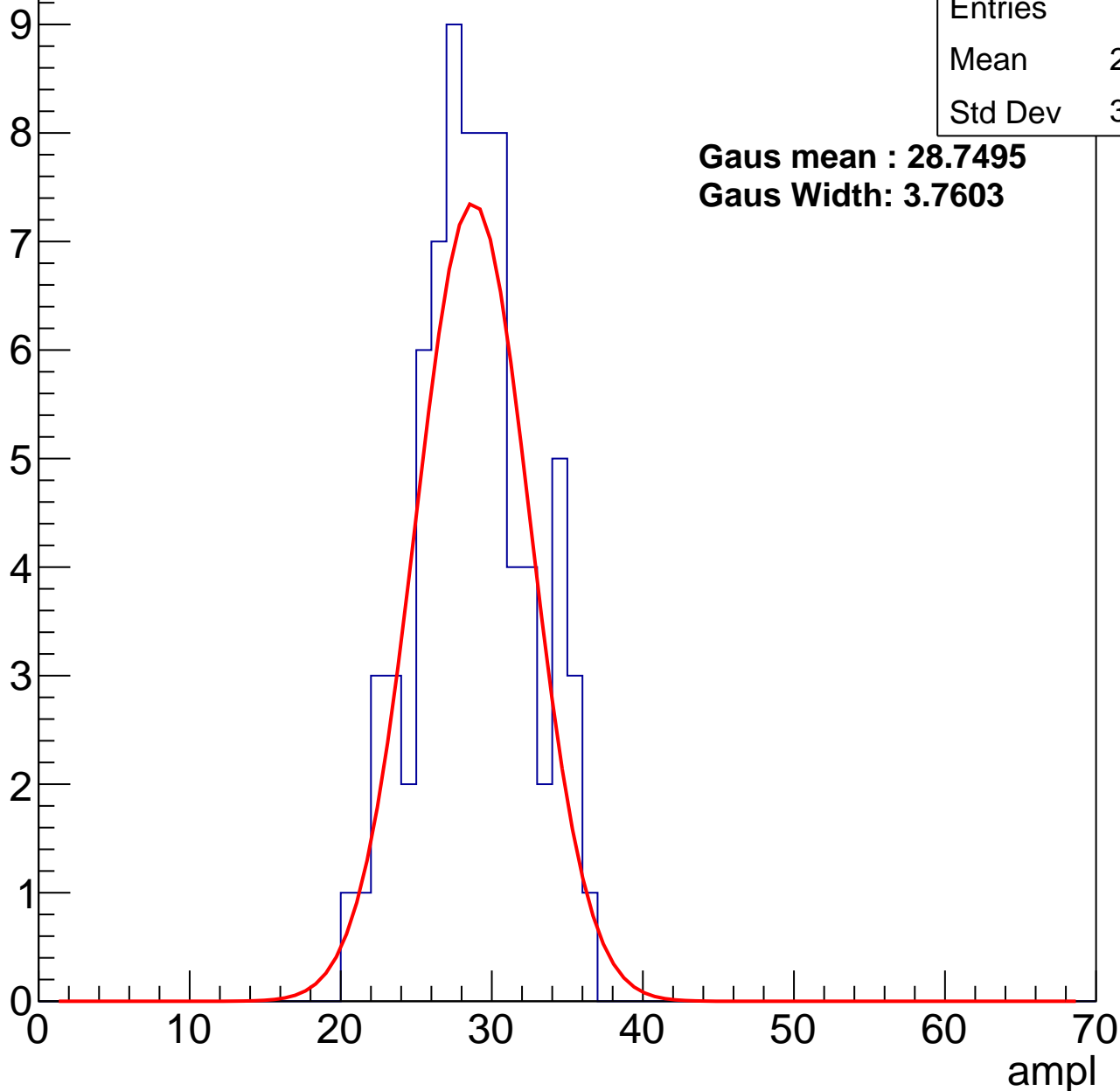
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	28.32
Std Dev	3.637

**Gaus mean : 28.7495**

**Gaus Width: 3.7603**



# B1L002S, U26-ch95, adc1

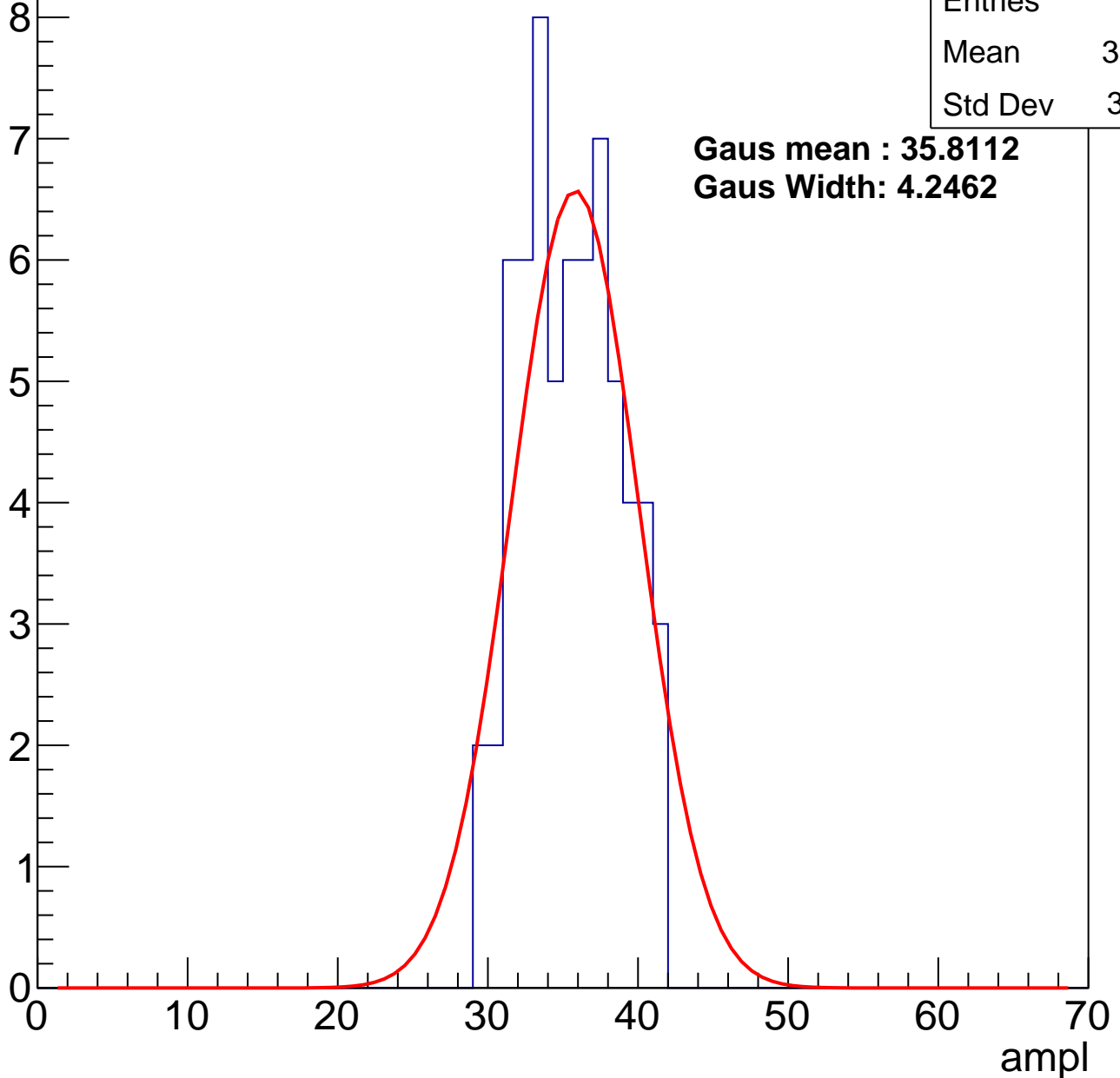
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.06
Std Dev	3.211

**Gaus mean : 35.8112**

**Gaus Width: 4.2462**



# B1L002S, U26-ch95, adc2

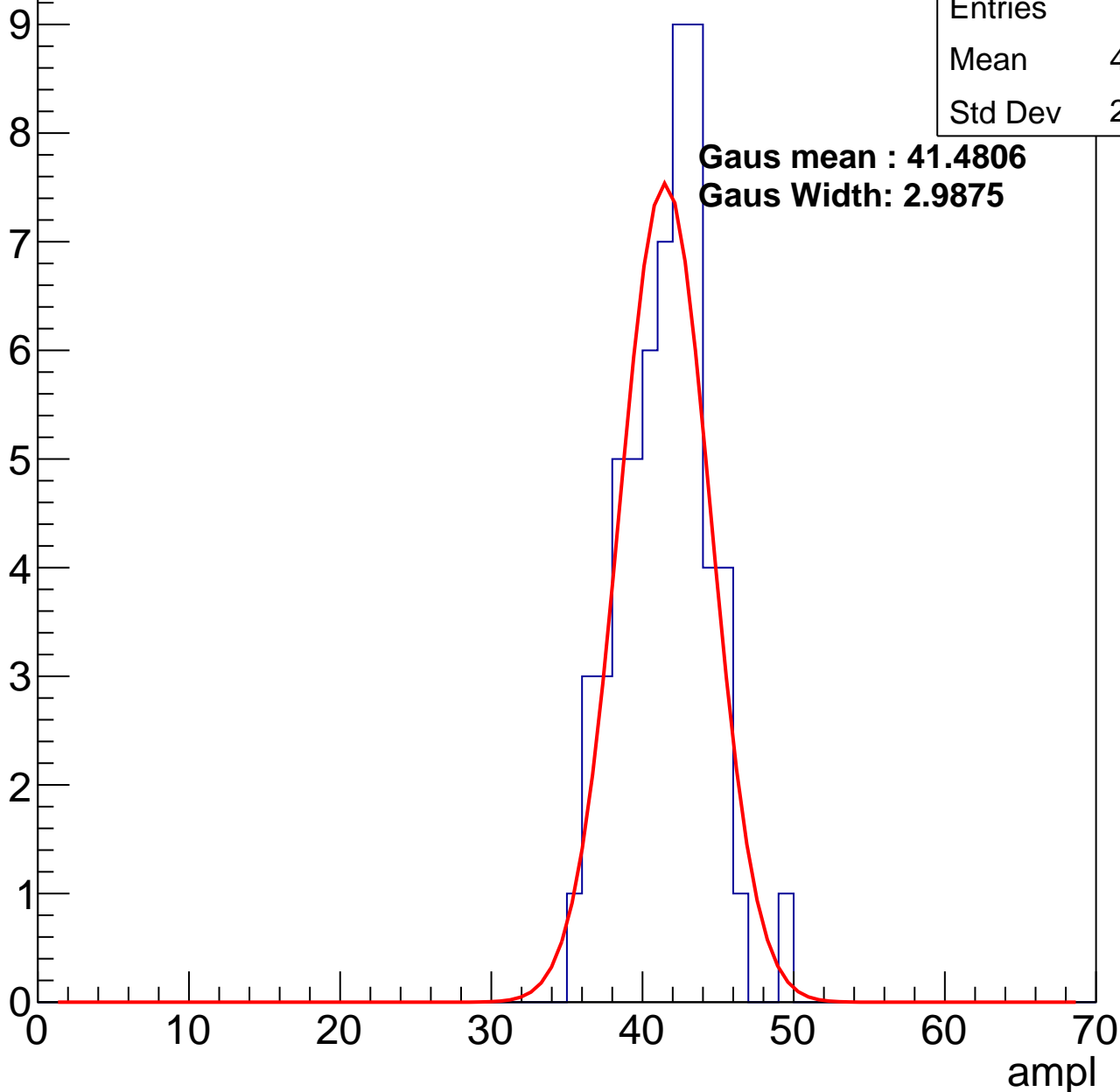
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.07
Std Dev	2.828

**Gaus mean : 41.4806**

**Gaus Width: 2.9875**

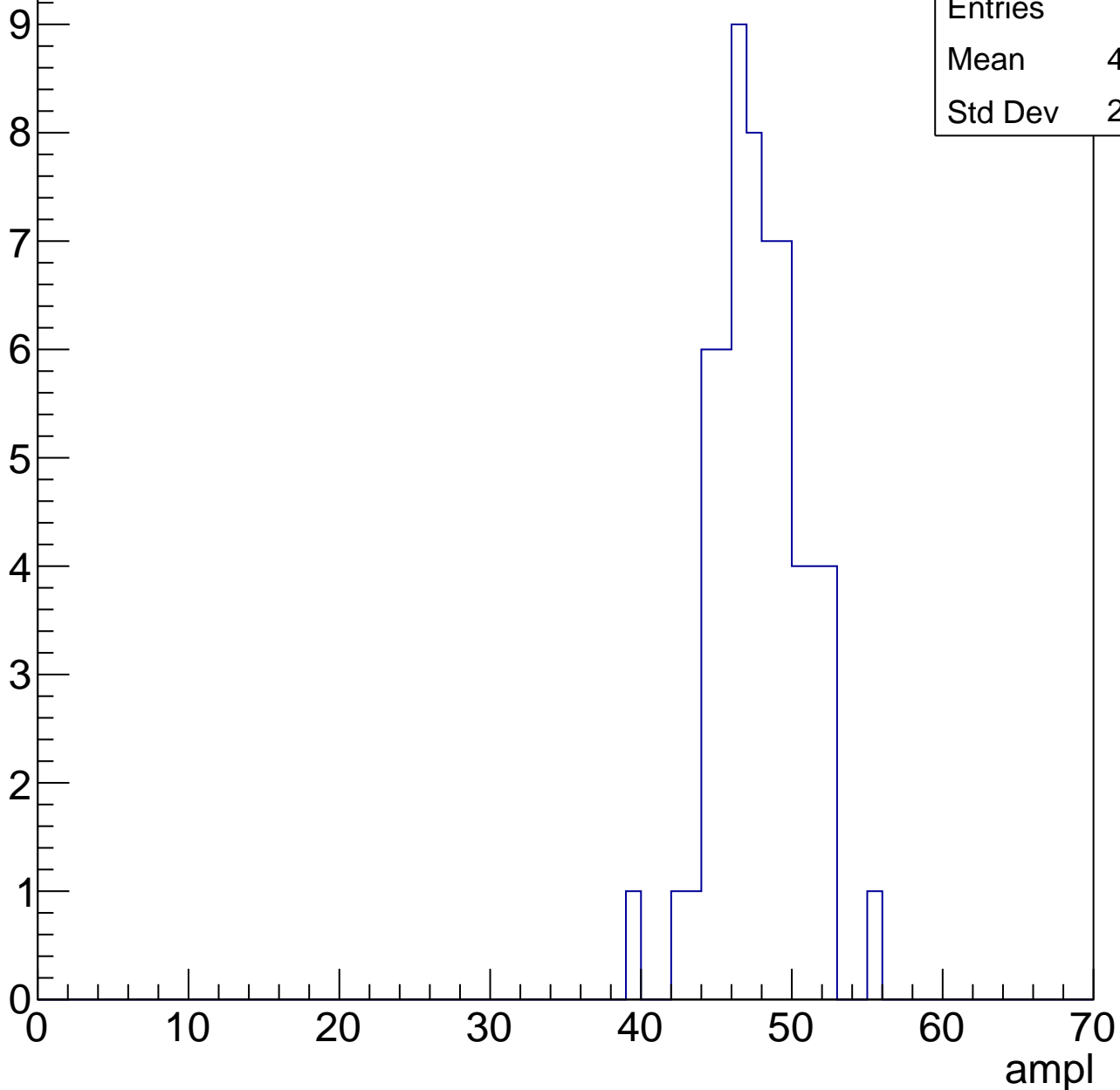


# B1L002S, U26-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.36
Std Dev	2.863

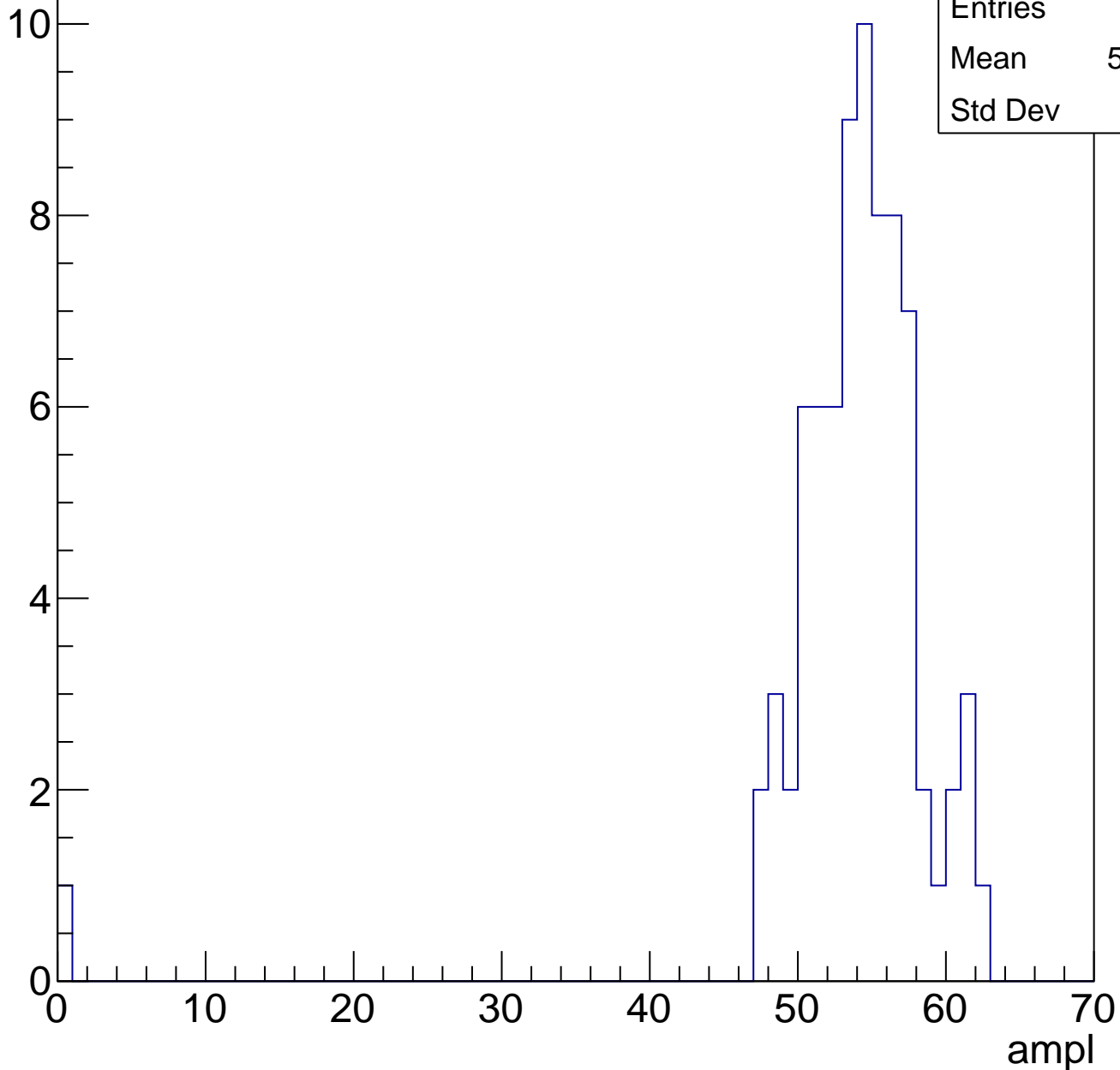


# B1L002S, U26-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	53.22
Std Dev	6.98

Entry



# B1L002S, U26-ch95, adc5

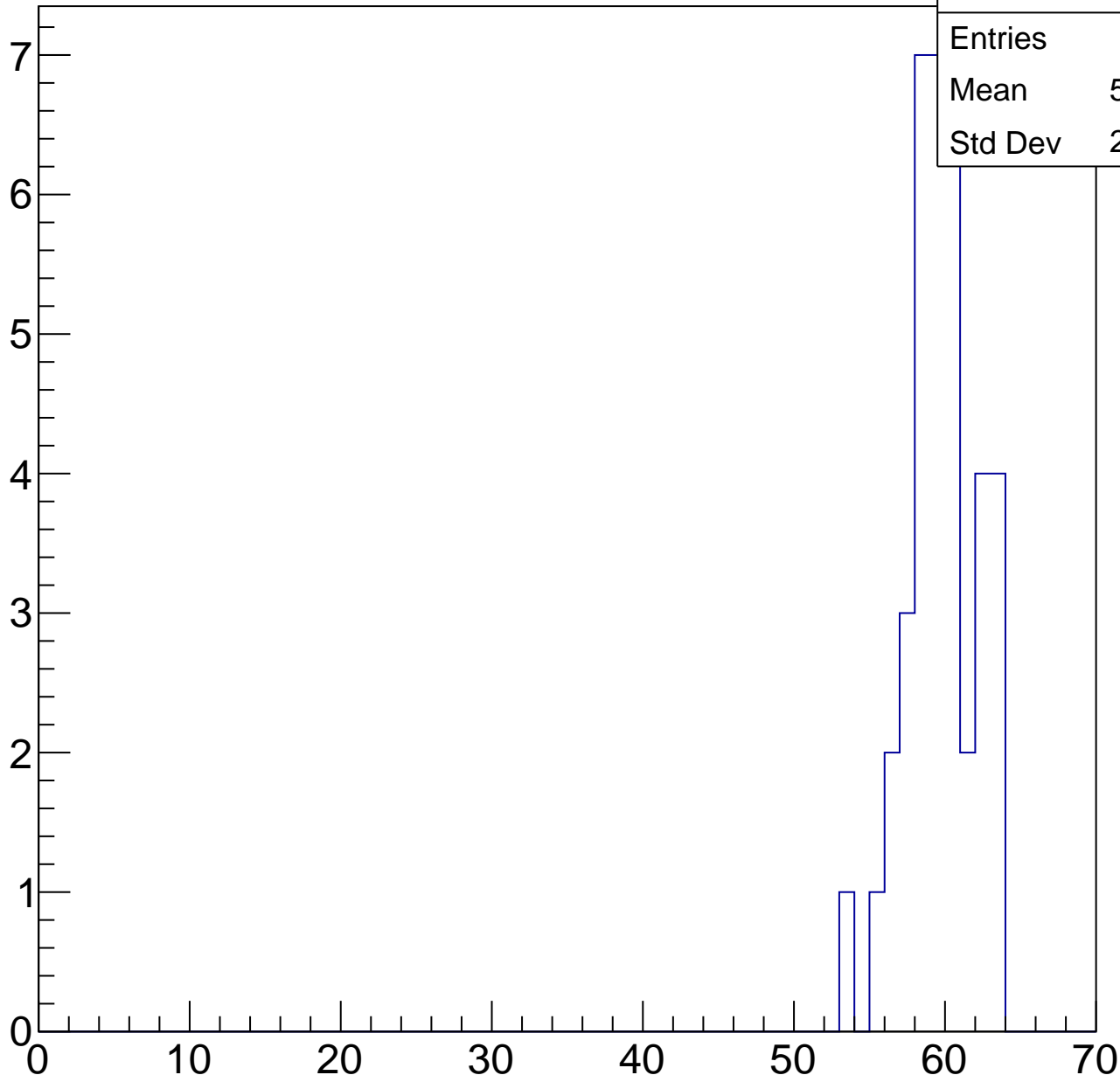
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	59.26
Std Dev	2.302

ampl

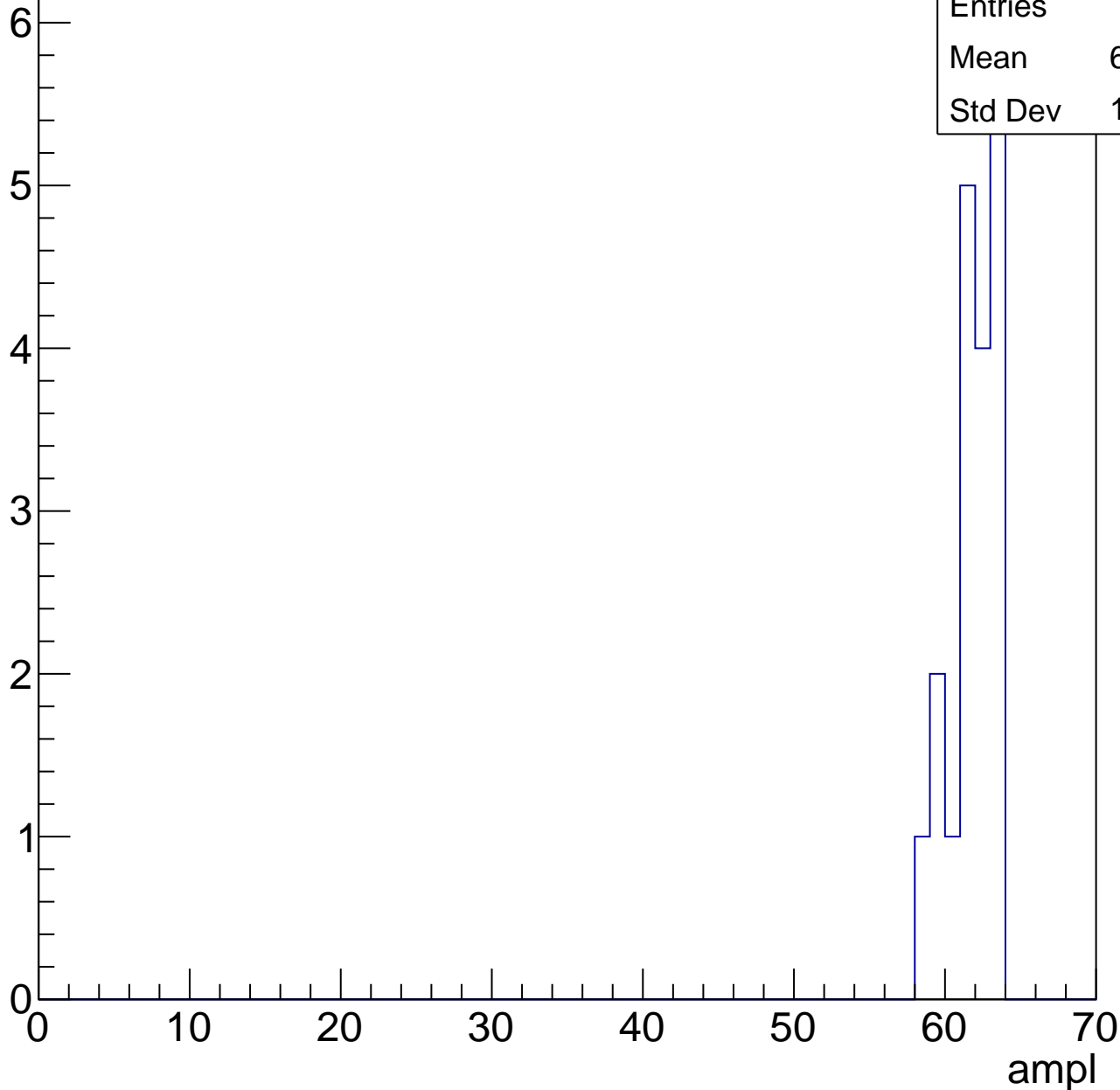


# B1L002S, U26-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.42
Std Dev	1.498





# B1L002S, U26-ch95, adc7

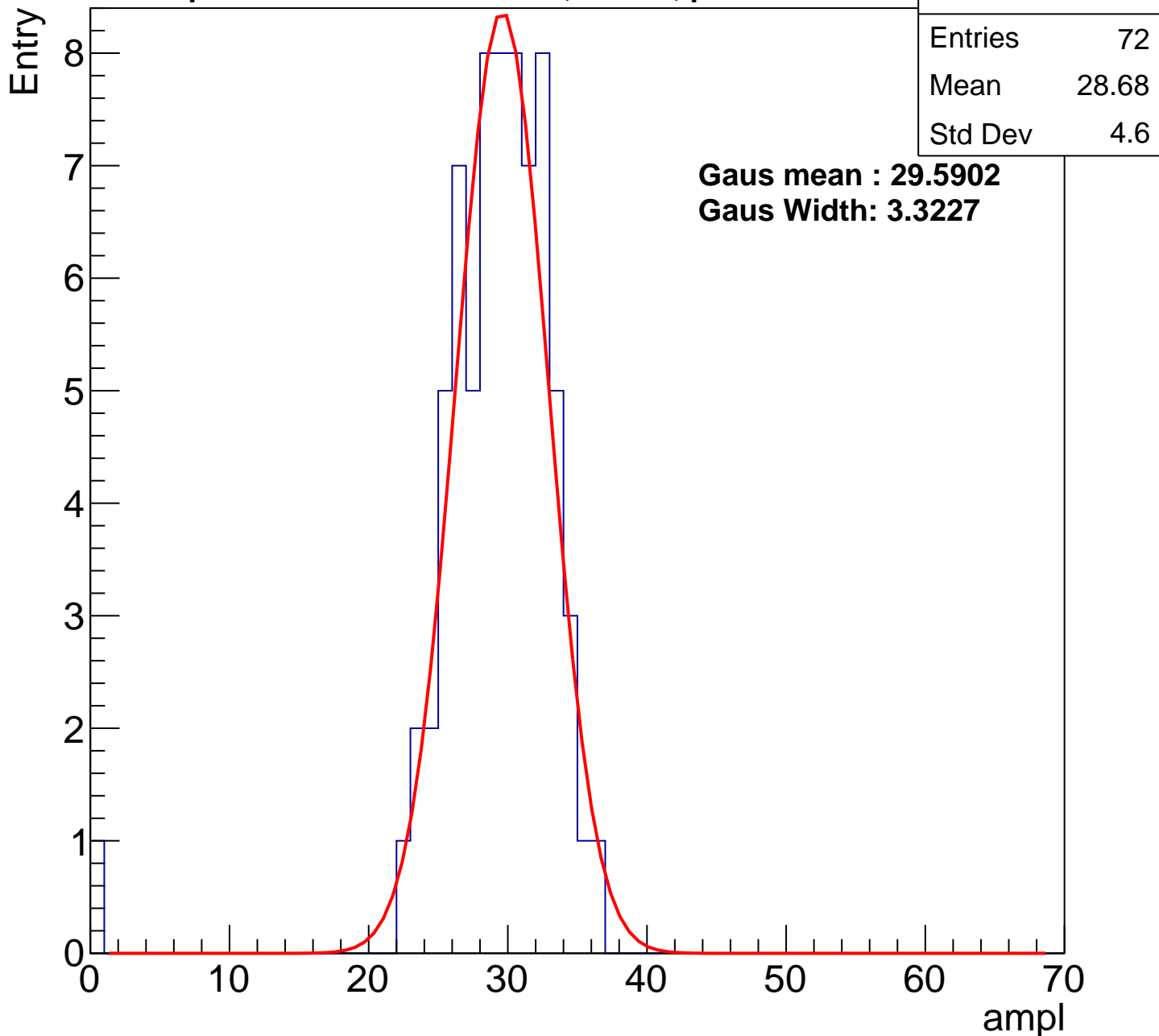
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch96, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch96, adc1

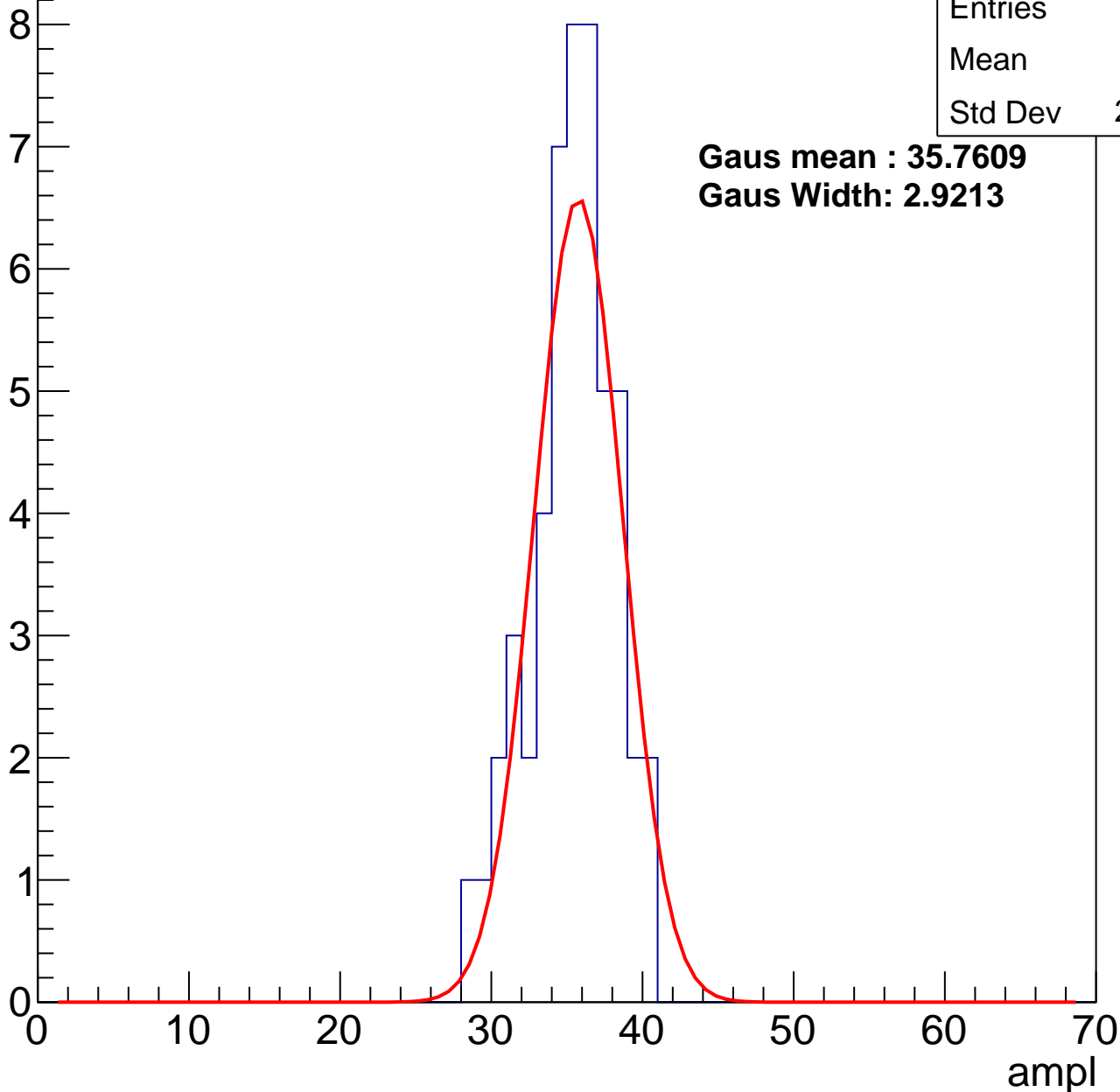
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	34.9
Std Dev	2.751

**Gaus mean : 35.7609**

**Gaus Width: 2.9213**



# B1L002S, U26-ch96, adc2

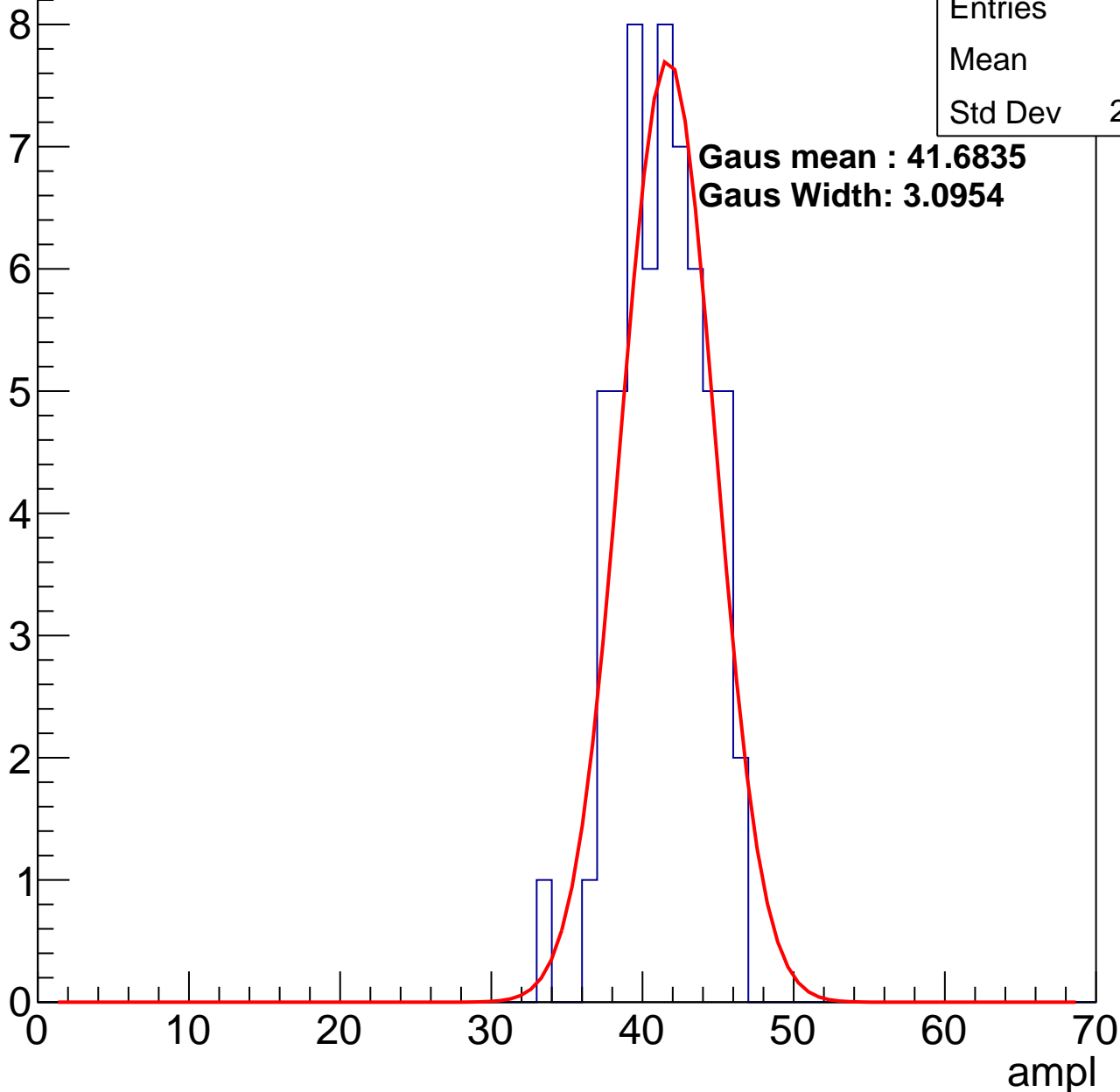
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.9
Std Dev	2.784

**Gaus mean : 41.6835**

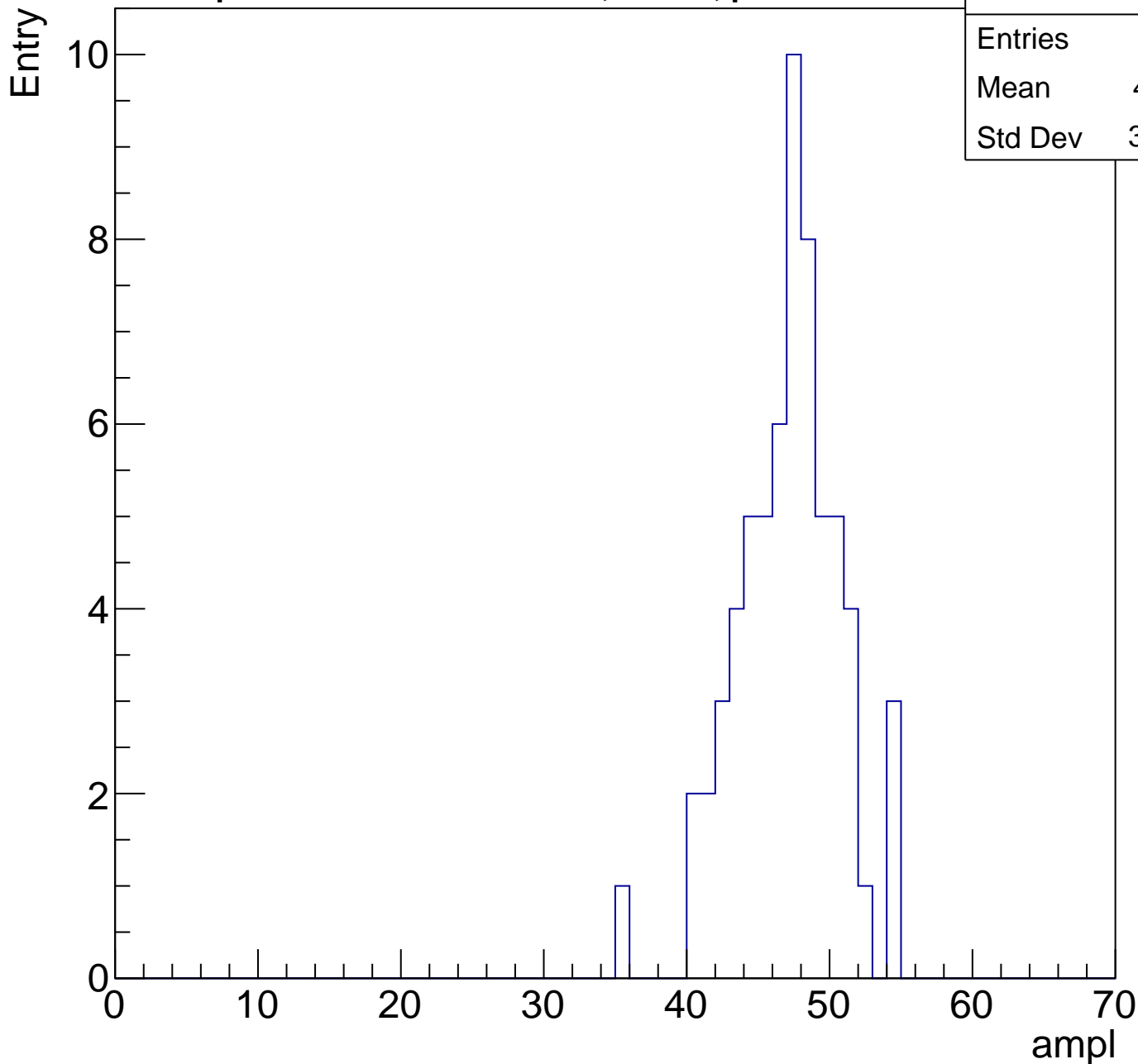
**Gaus Width: 3.0954**



# B1L002S, U26-ch96, adc3

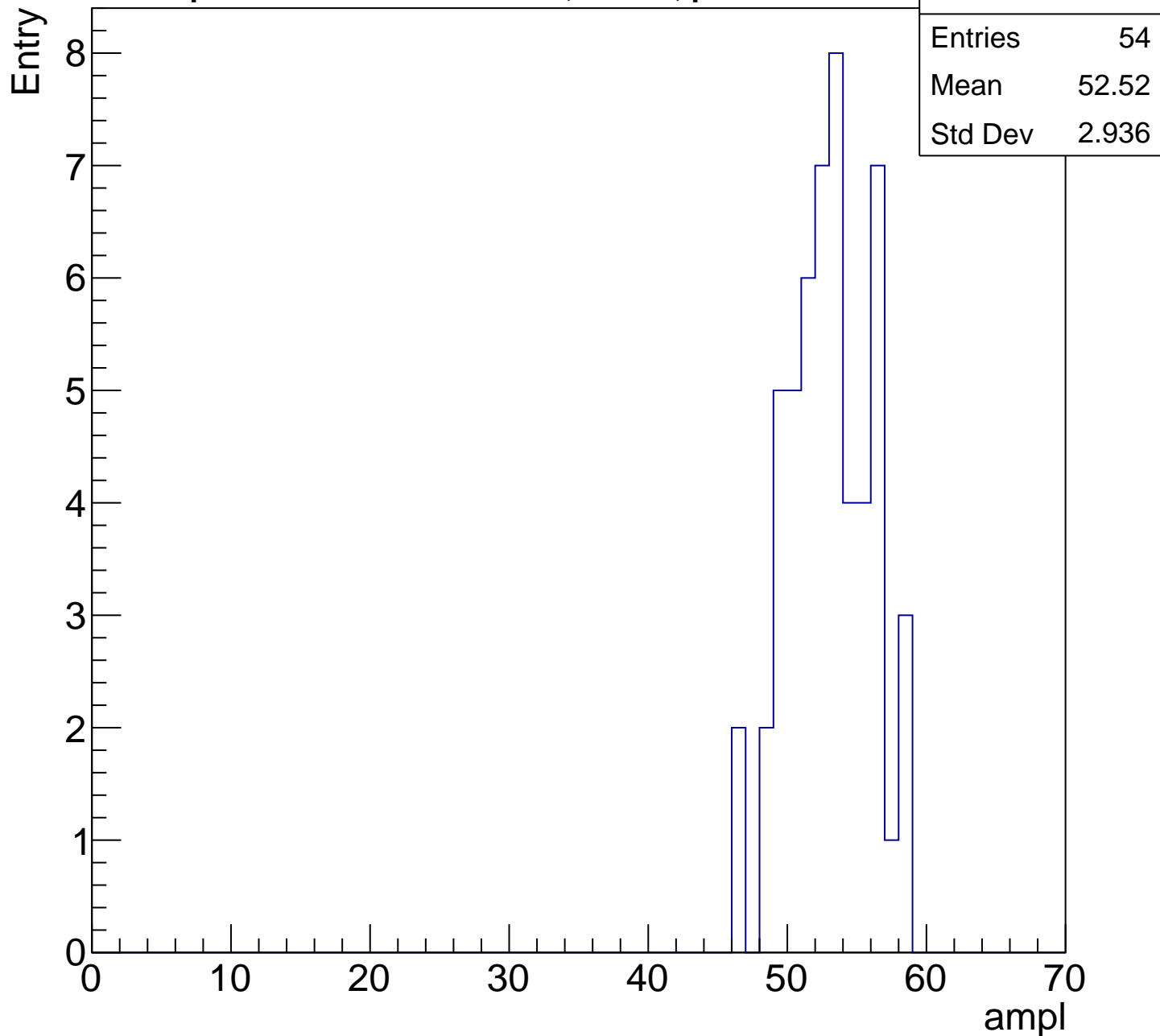
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	46.61
Std Dev	3.587



# B1L002S, U26-ch96, adc4

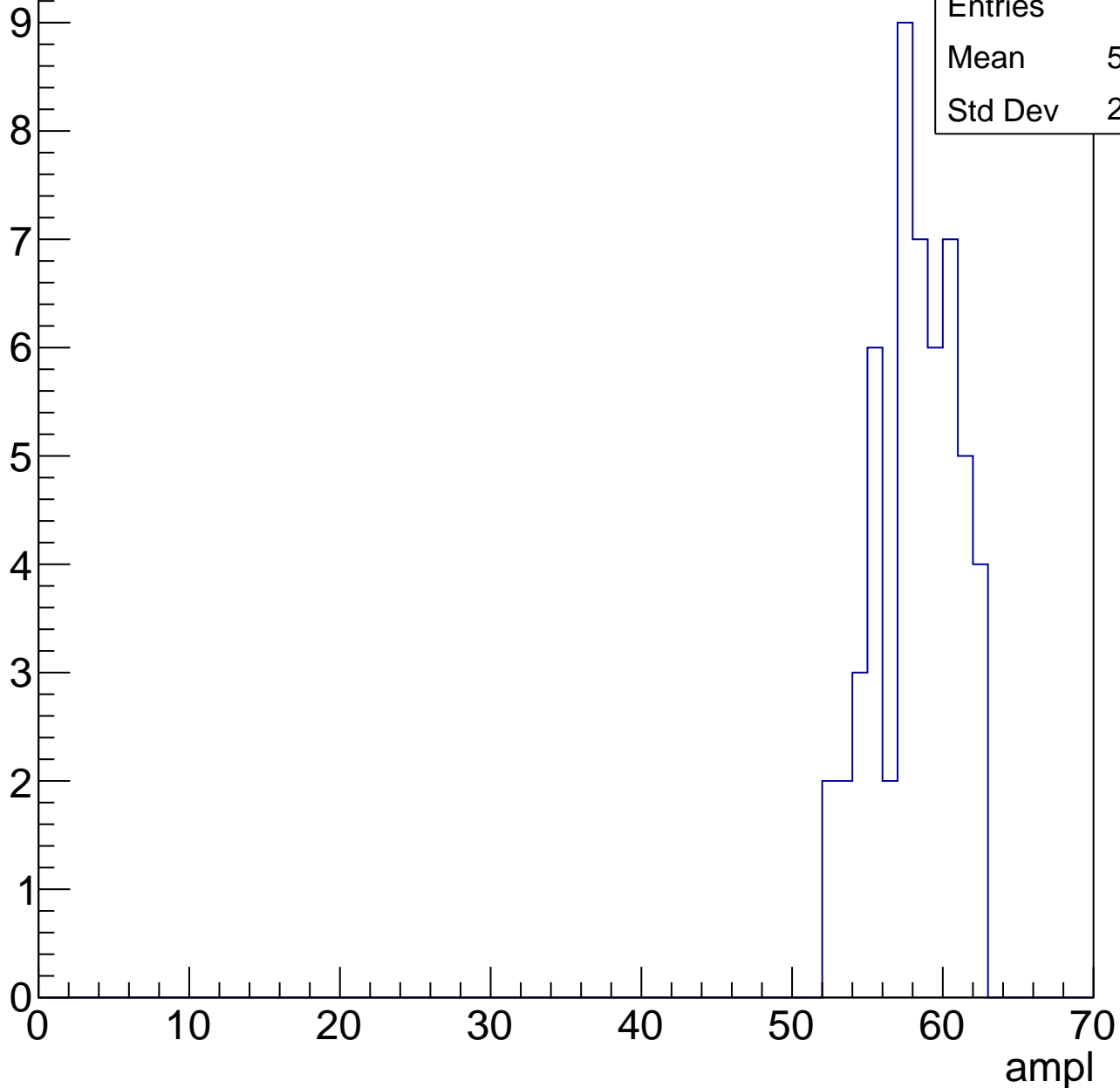
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	53
Mean	57.74
Std Dev	2.679

# B1L002S, U26-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	29
Mean	59.21
Std Dev	11.32

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

0

2

4

6

8

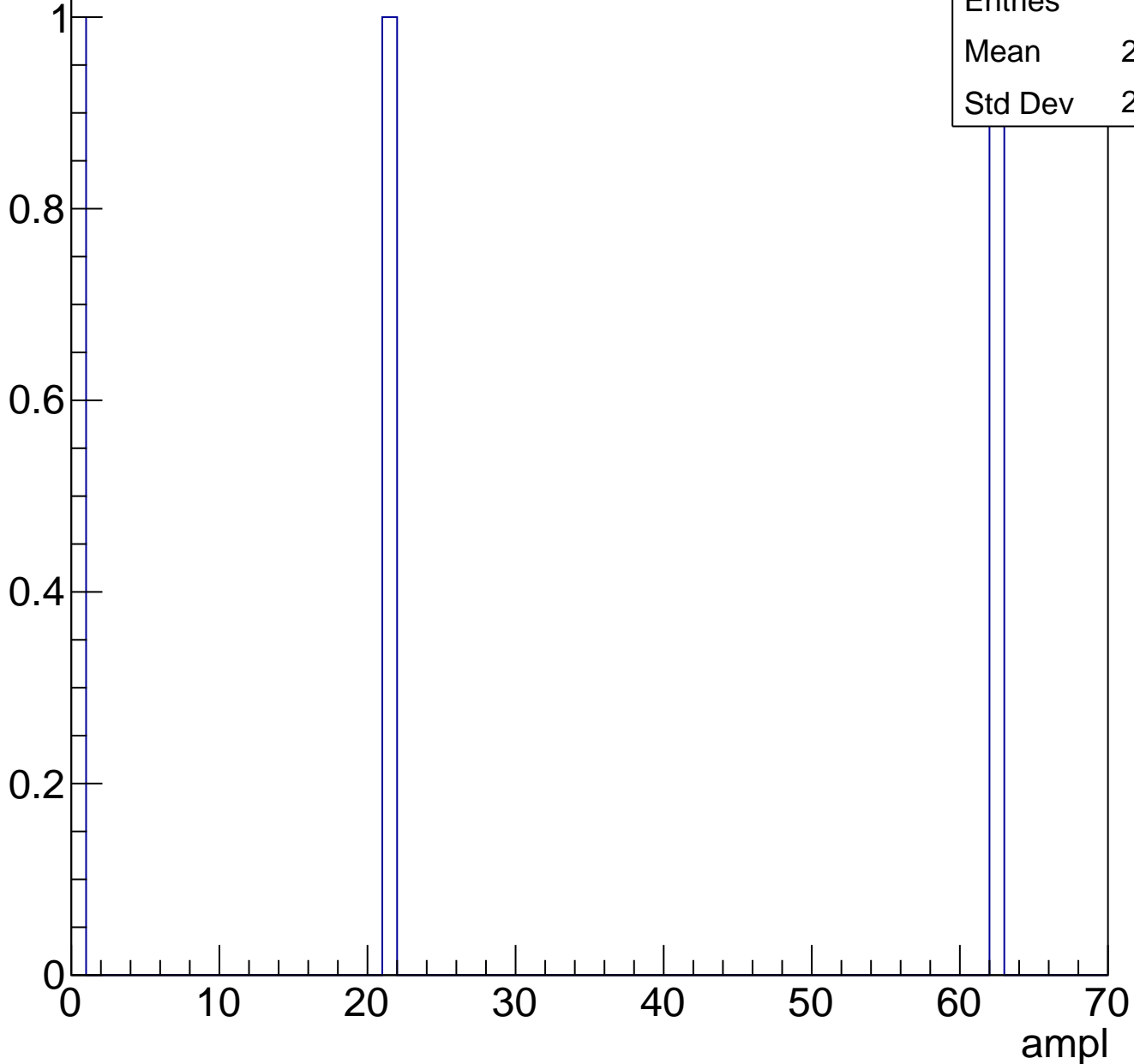
10



# B1L002S, U26-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L002S, U26-ch97, adc0

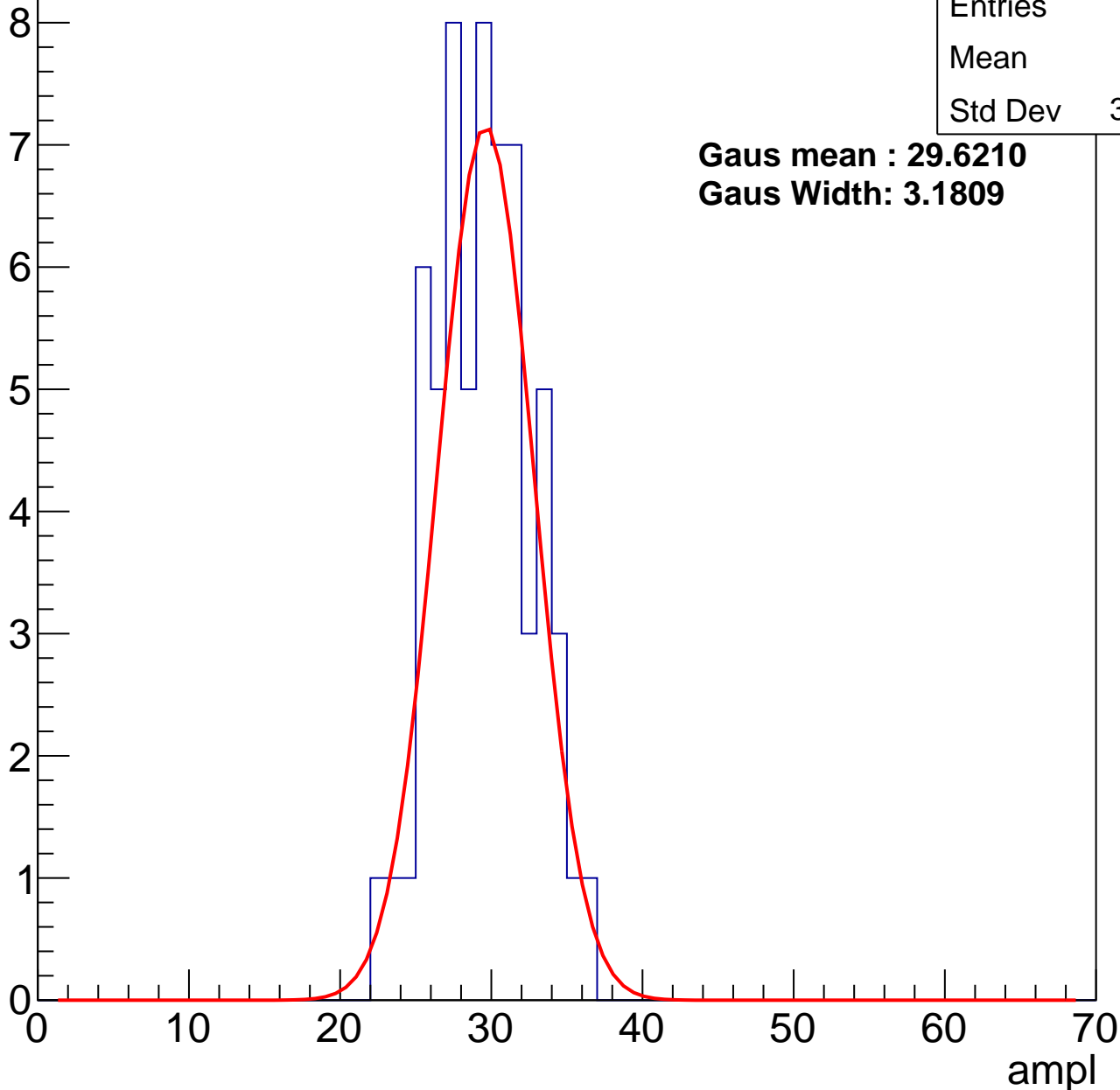
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29
Std Dev	3.085

**Gaus mean : 29.6210**

**Gaus Width: 3.1809**



# B1L002S, U26-ch97, adc1

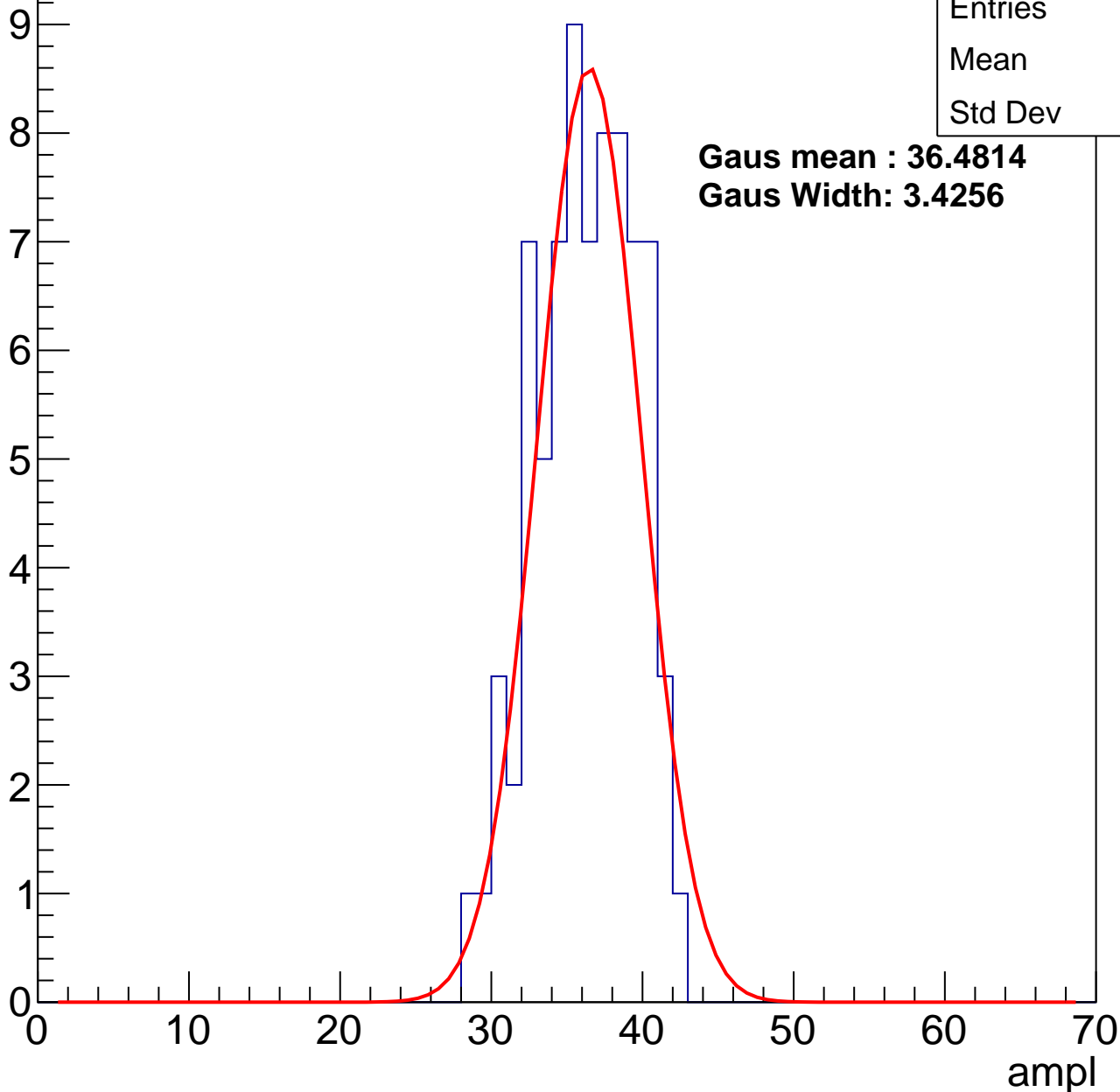
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	35.8
Std Dev	3.22

**Gaus mean : 36.4814**

**Gaus Width: 3.4256**



# B1L002S, U26-ch97, adc2

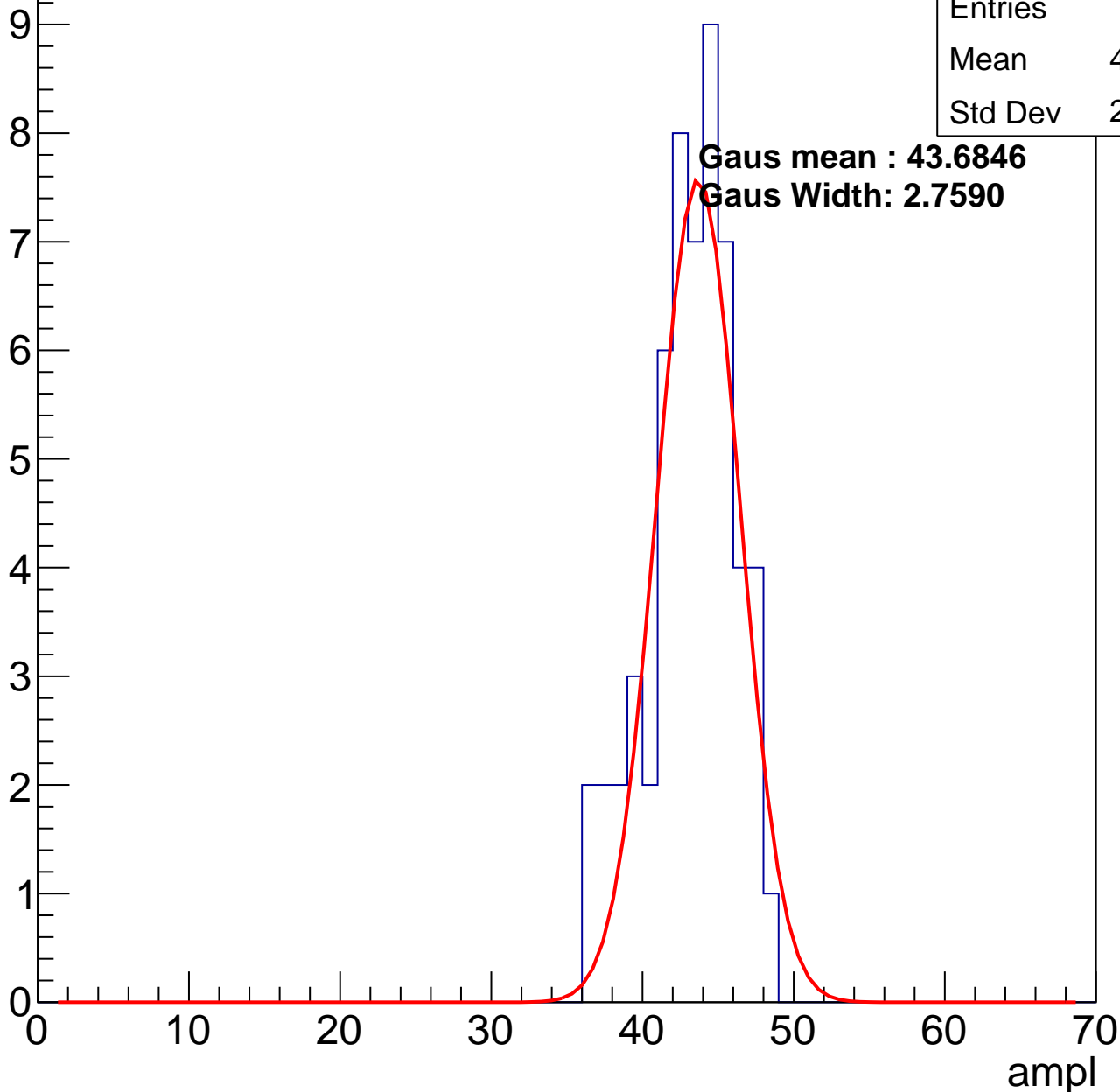
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	42.68
Std Dev	2.885

**Gaus mean : 43.6846**

**Gaus Width: 2.7590**

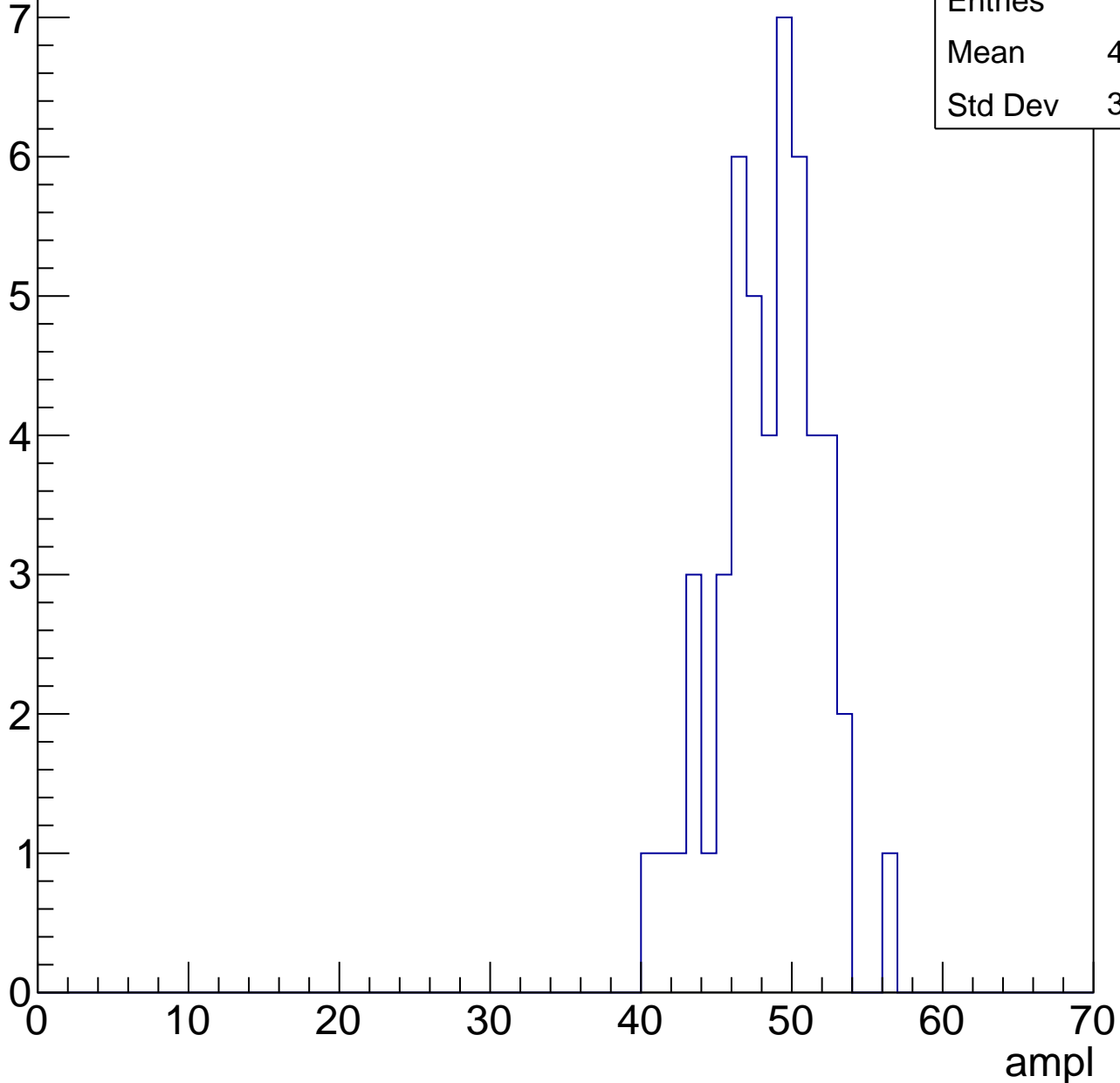


# B1L002S, U26-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	47.98
Std Dev	3.323

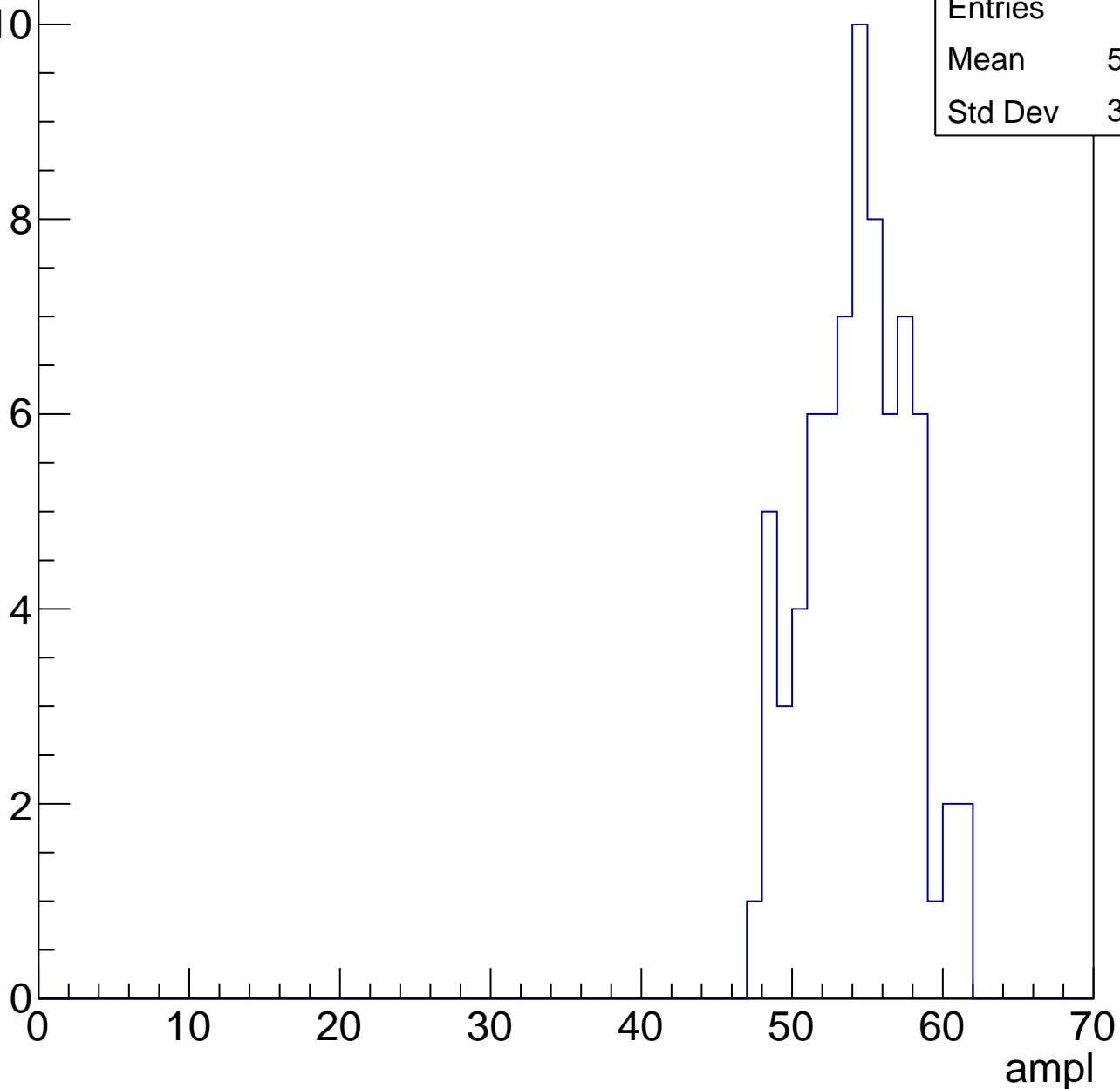


# B1L002S, U26-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	53.88
Std Dev	3.365



# B1L002S, U26-ch97, adc5

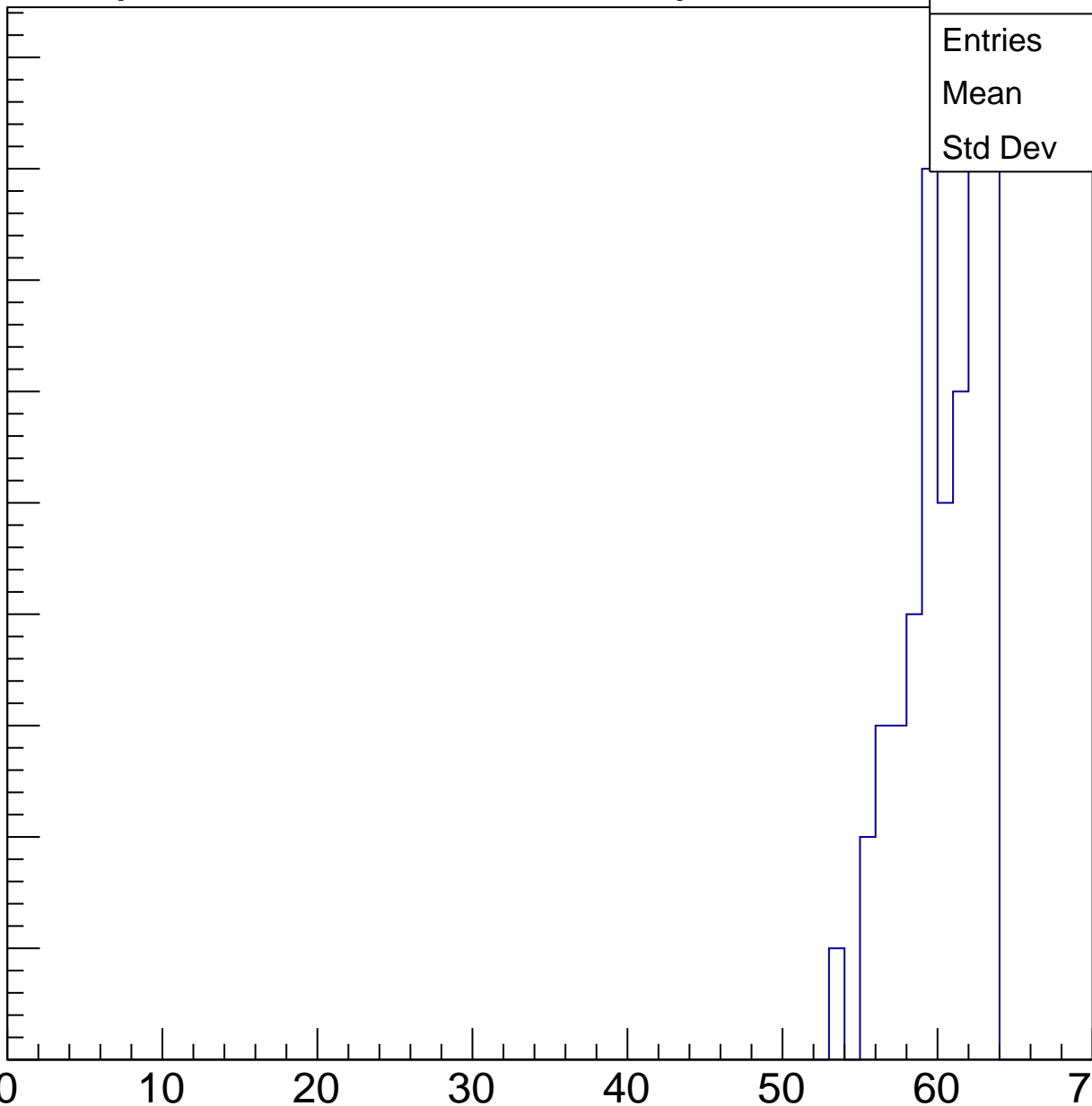
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	59.9
Std Dev	2.541

ampl



# B1L002S, U26-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

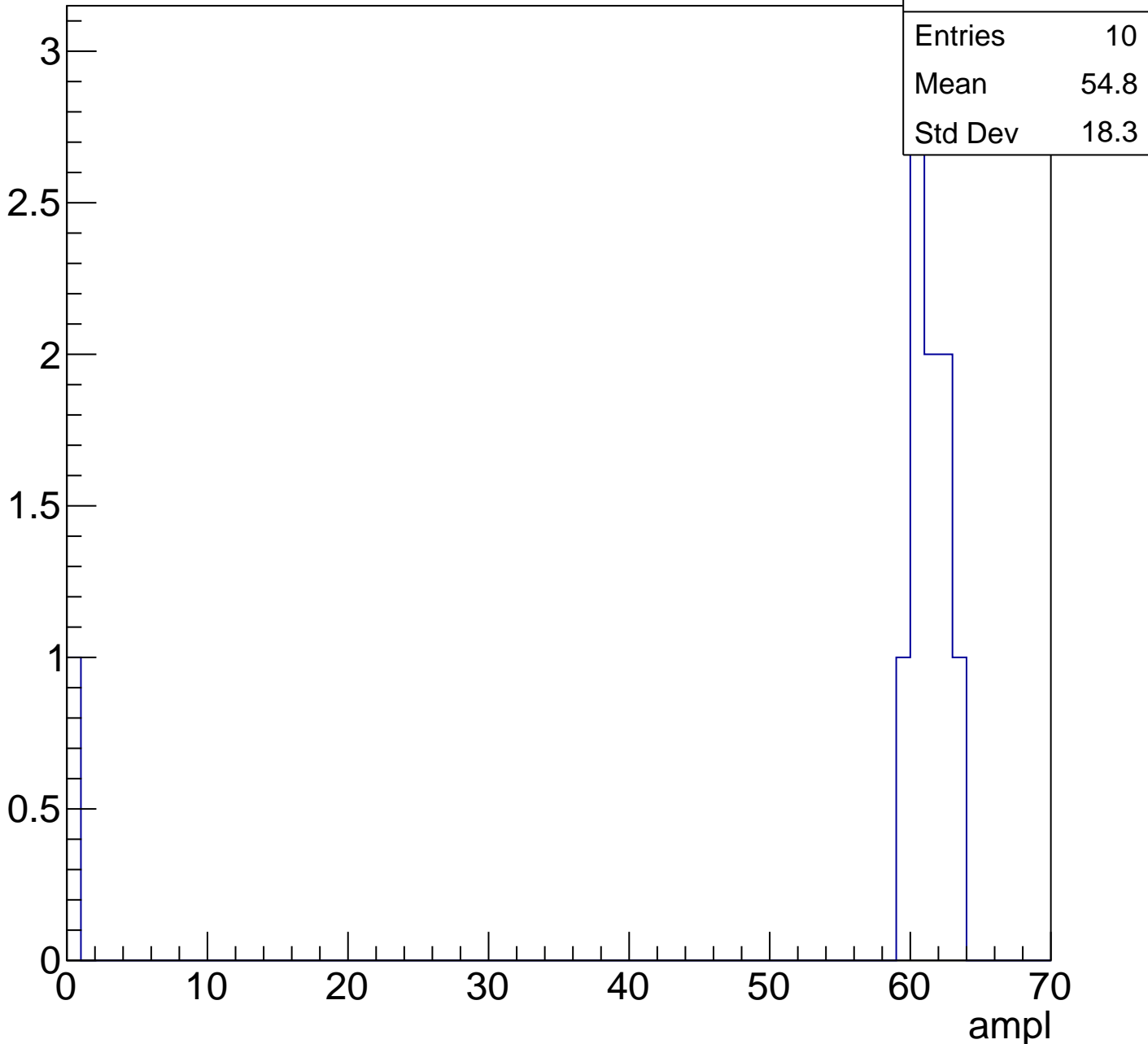
1

0.5

0

ampl

Entries	10
Mean	54.8
Std Dev	18.3





# B1L002S, U26-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch98, adc0

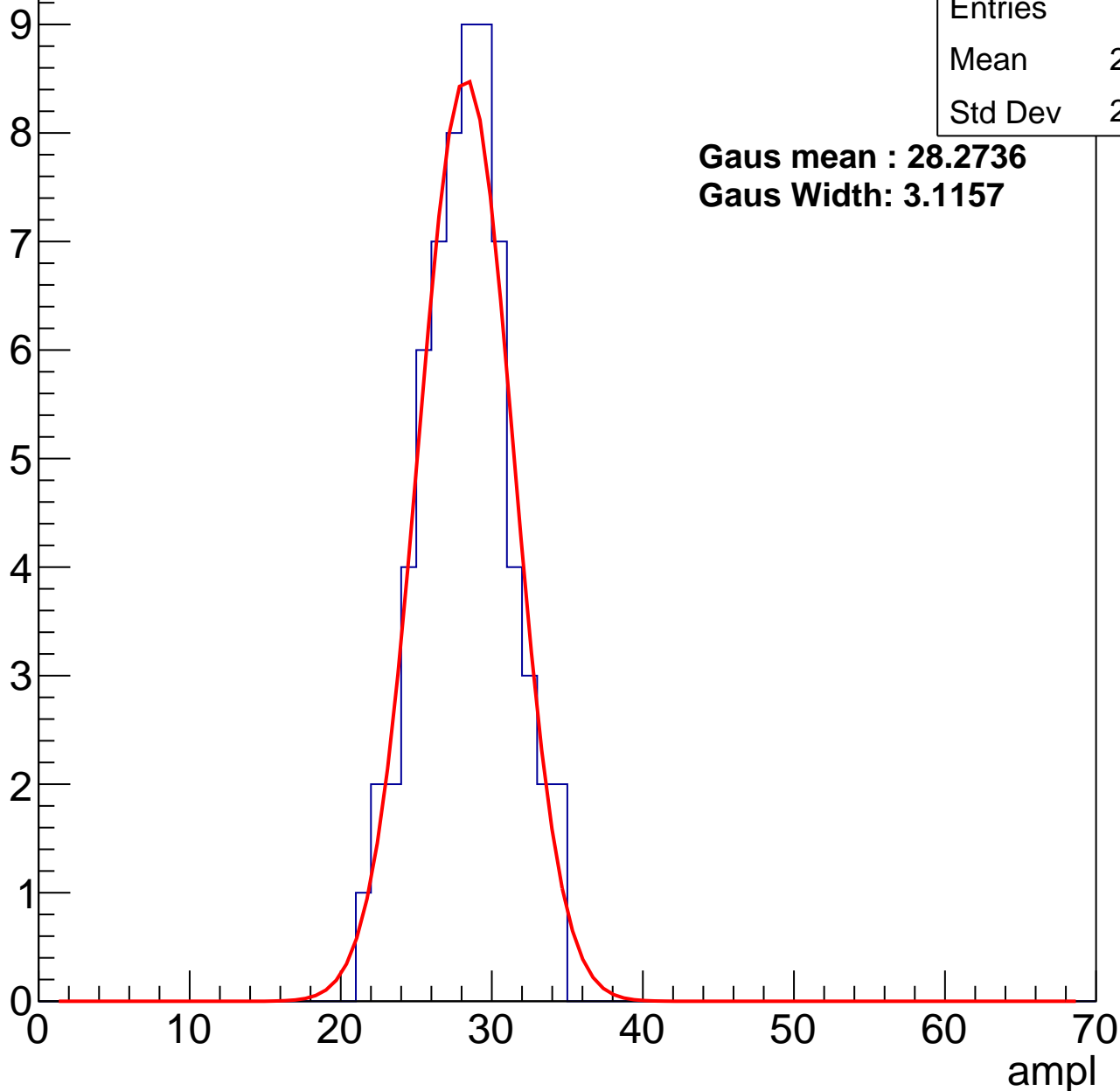
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.76
Std Dev	2.924

**Gaus mean : 28.2736**

**Gaus Width: 3.1157**



# B1L002S, U26-ch98, adc1

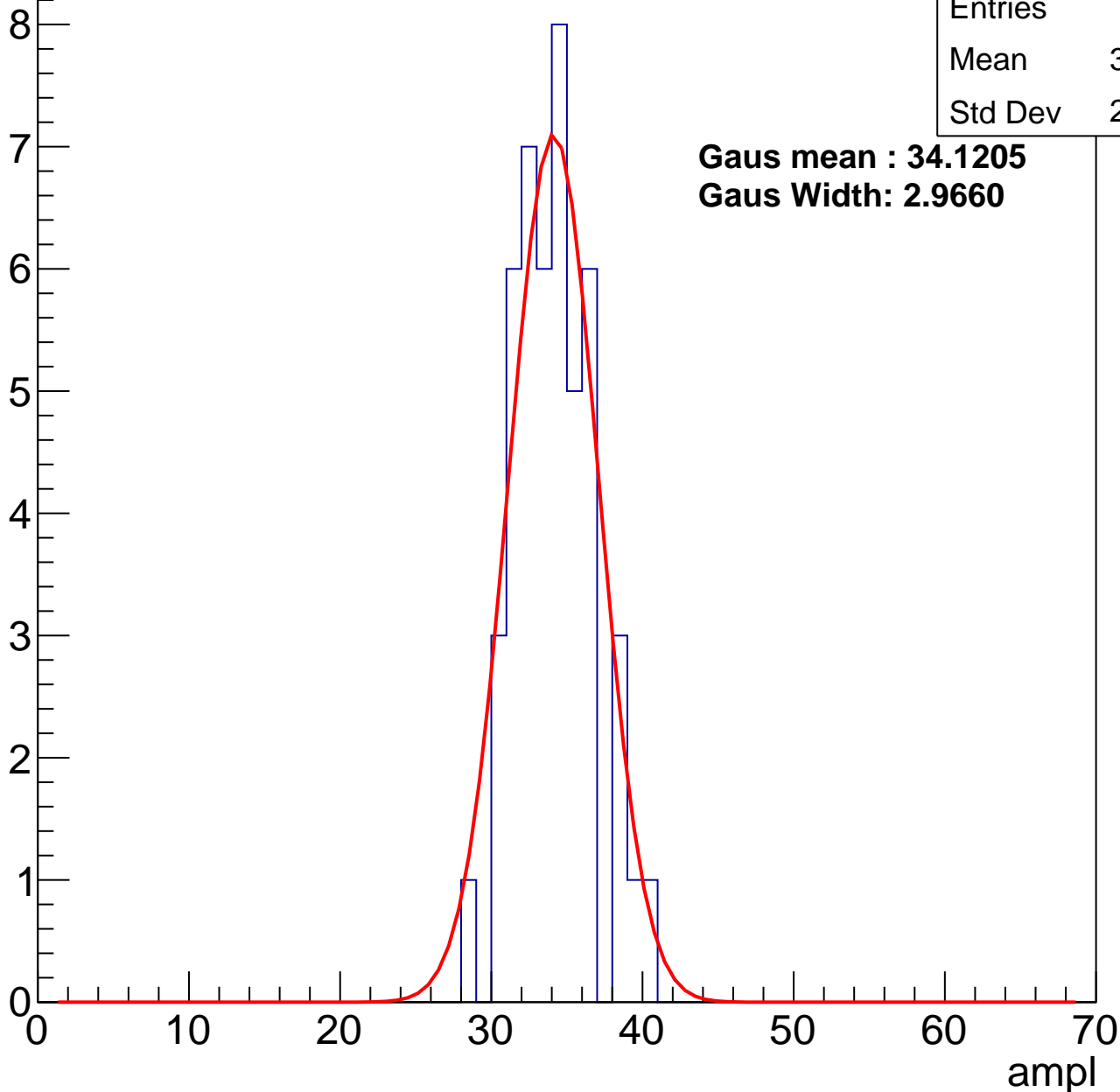
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	33.66
Std Dev	2.546

**Gaus mean : 34.1205**

**Gaus Width: 2.9660**



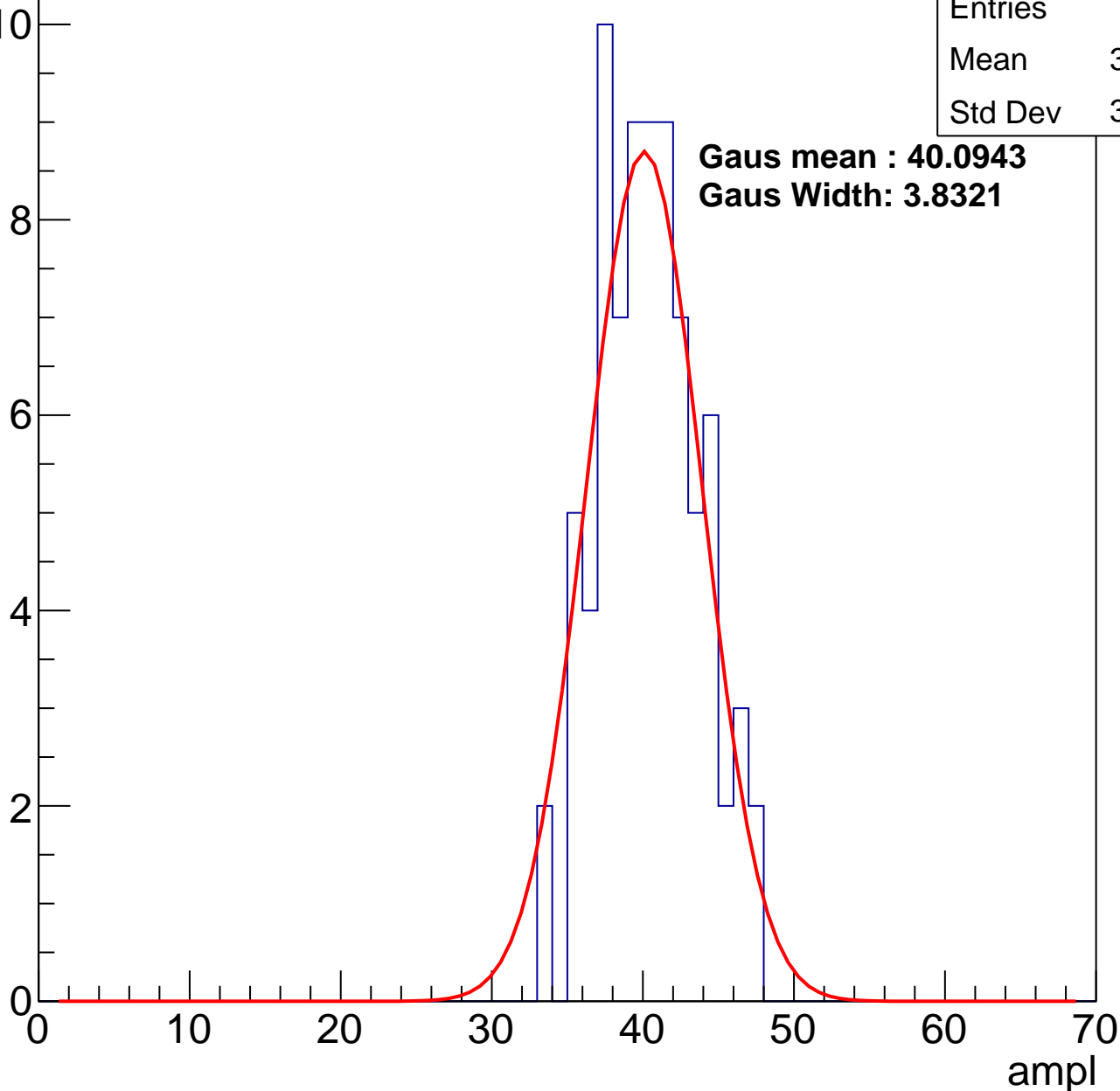
# B1L002S, U26-ch98, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	80
Mean	39.95
Std Dev	3.255

**Gaus mean : 40.0943**  
**Gaus Width: 3.8321**

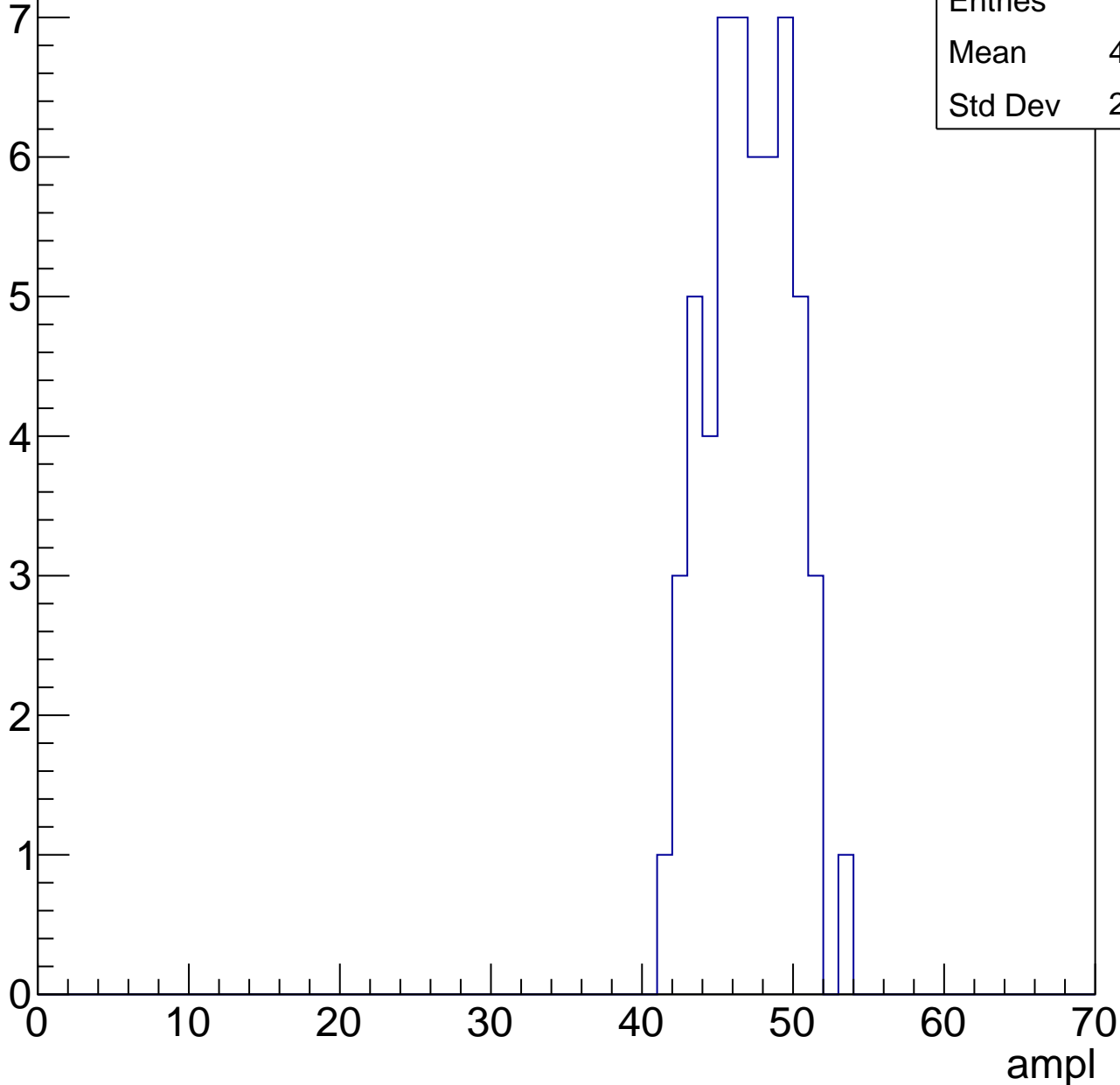


# B1L002S, U26-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	46.62
Std Dev	2.753

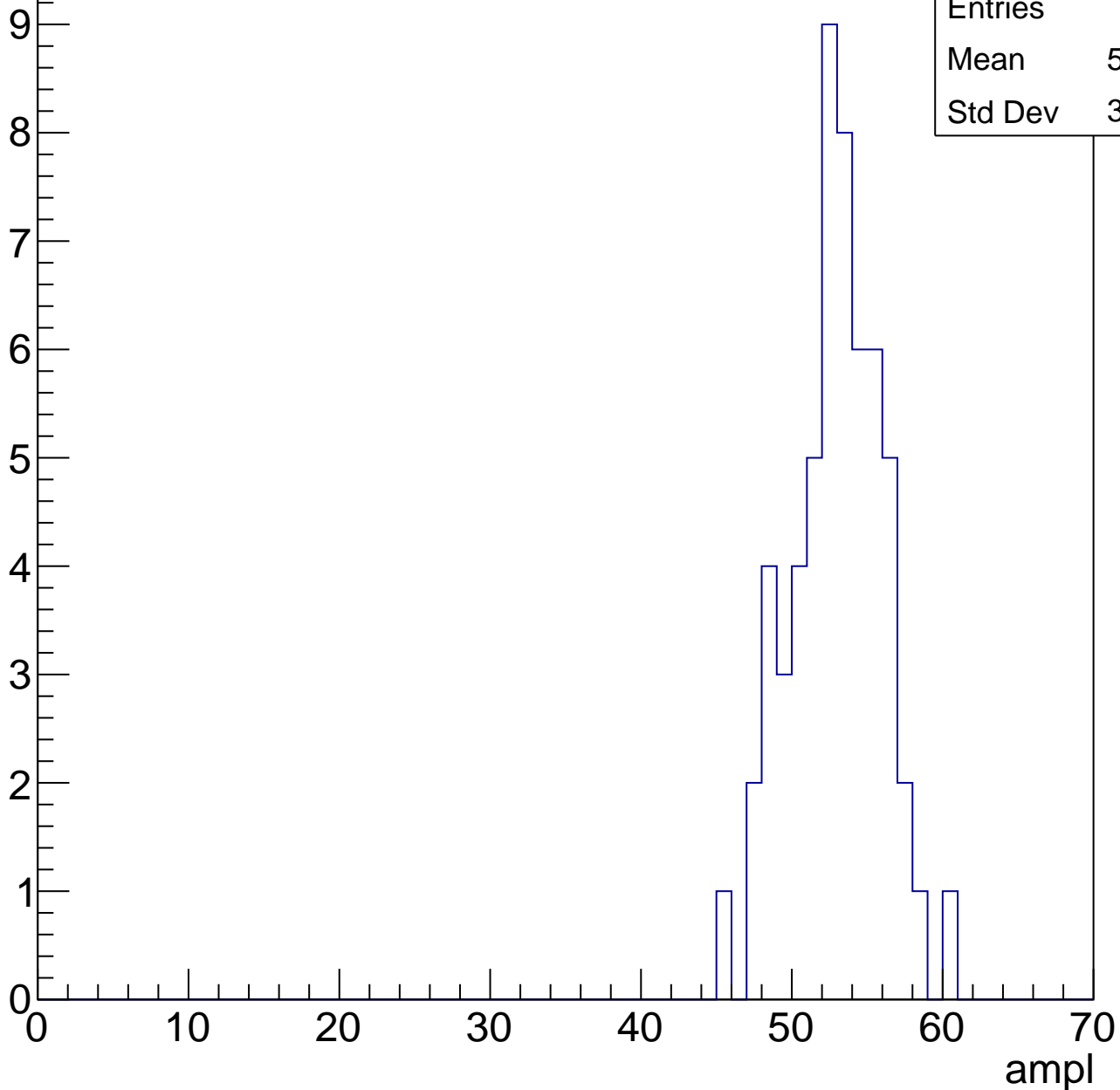


# B1L002S, U26-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.47
Std Dev	3.004



# B1L002S, U26-ch98, adc5

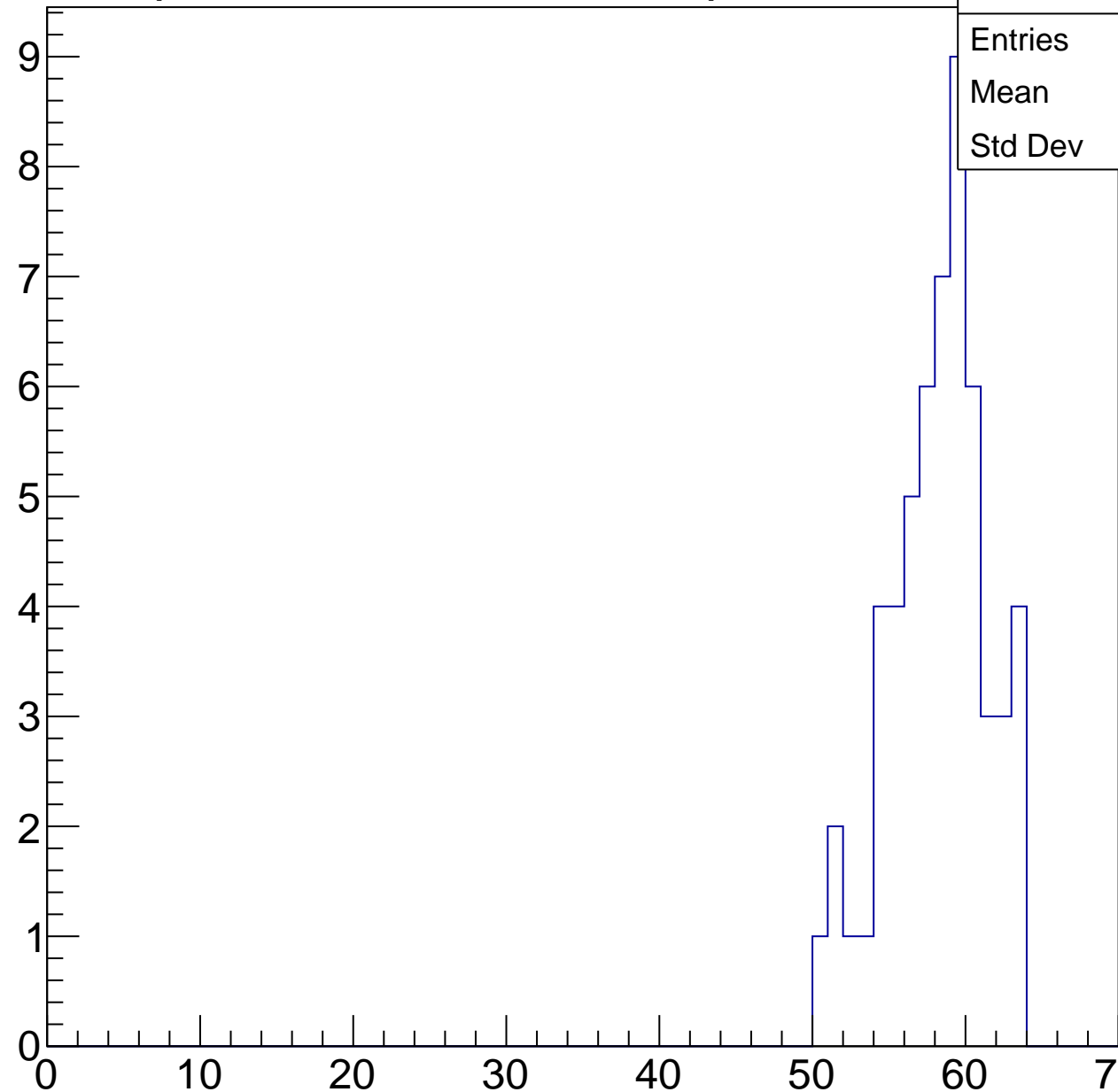
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	56
Mean	57.73
Std Dev	3.142

ampl

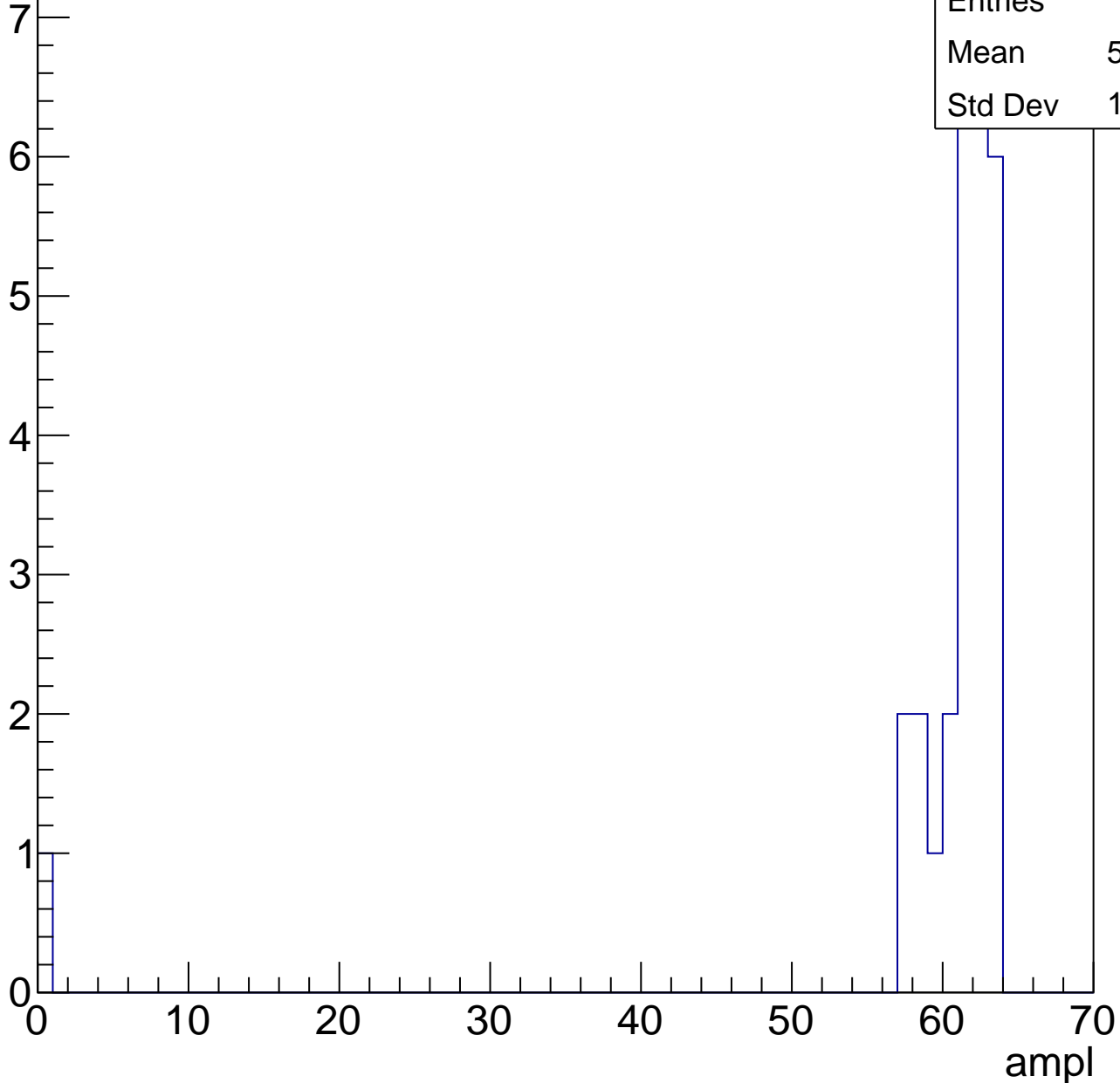


# B1L002S, U26-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.86
Std Dev	11.46

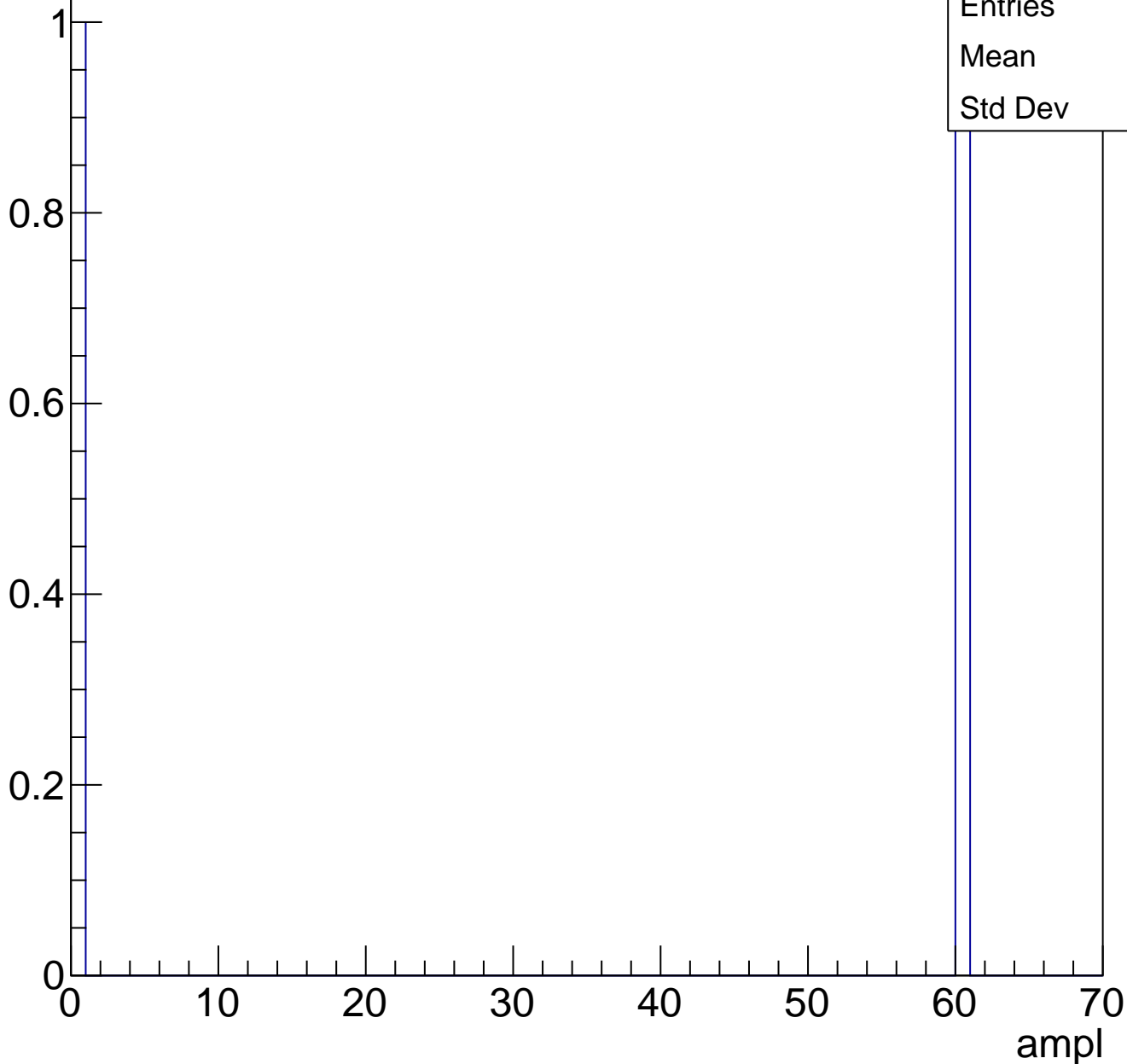




# B1L002S, U26-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch99, adc0

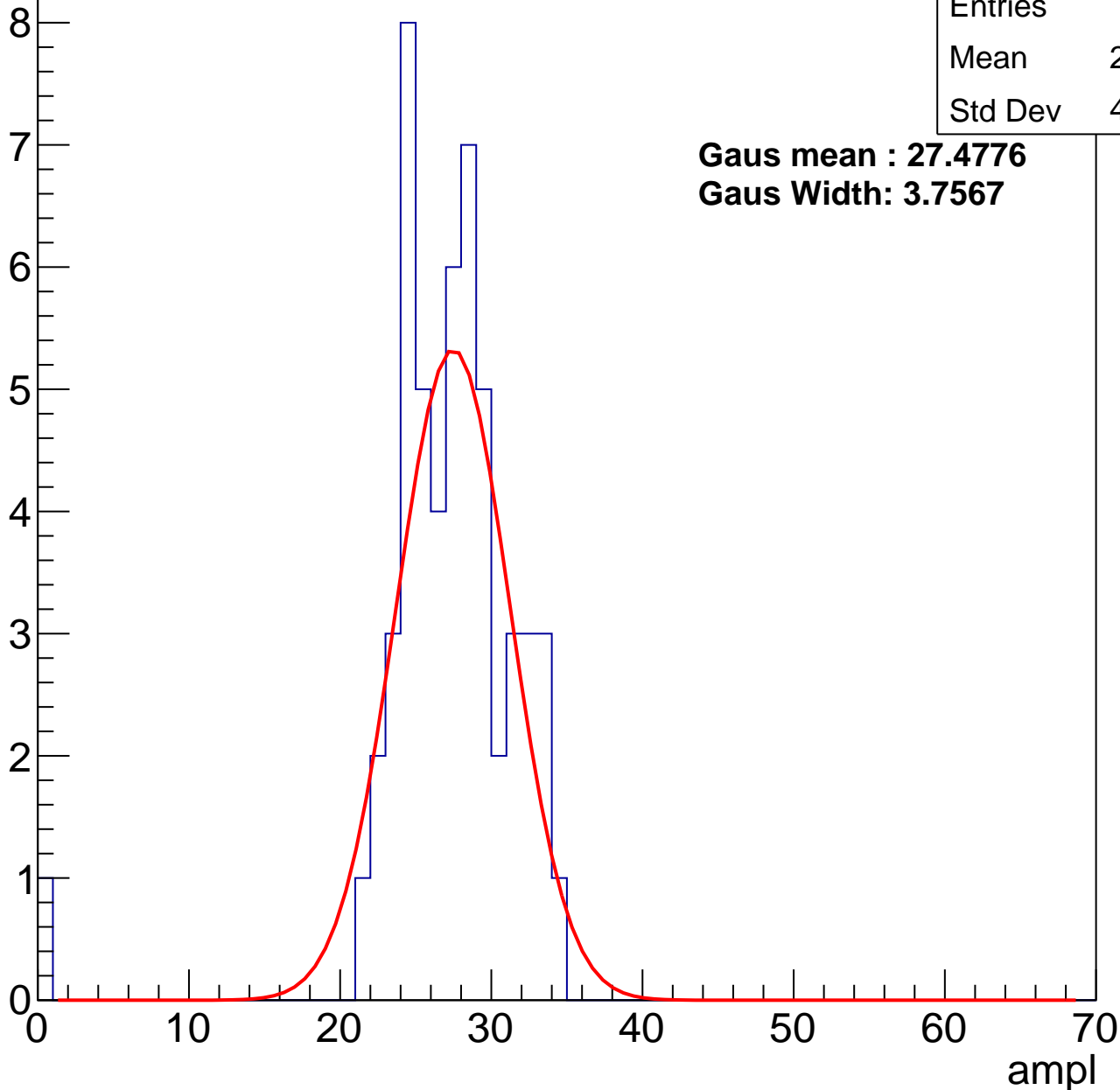
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	26.67
Std Dev	4.865

**Gaus mean : 27.4776**

**Gaus Width: 3.7567**



# B1L002S, U26-ch99, adc1

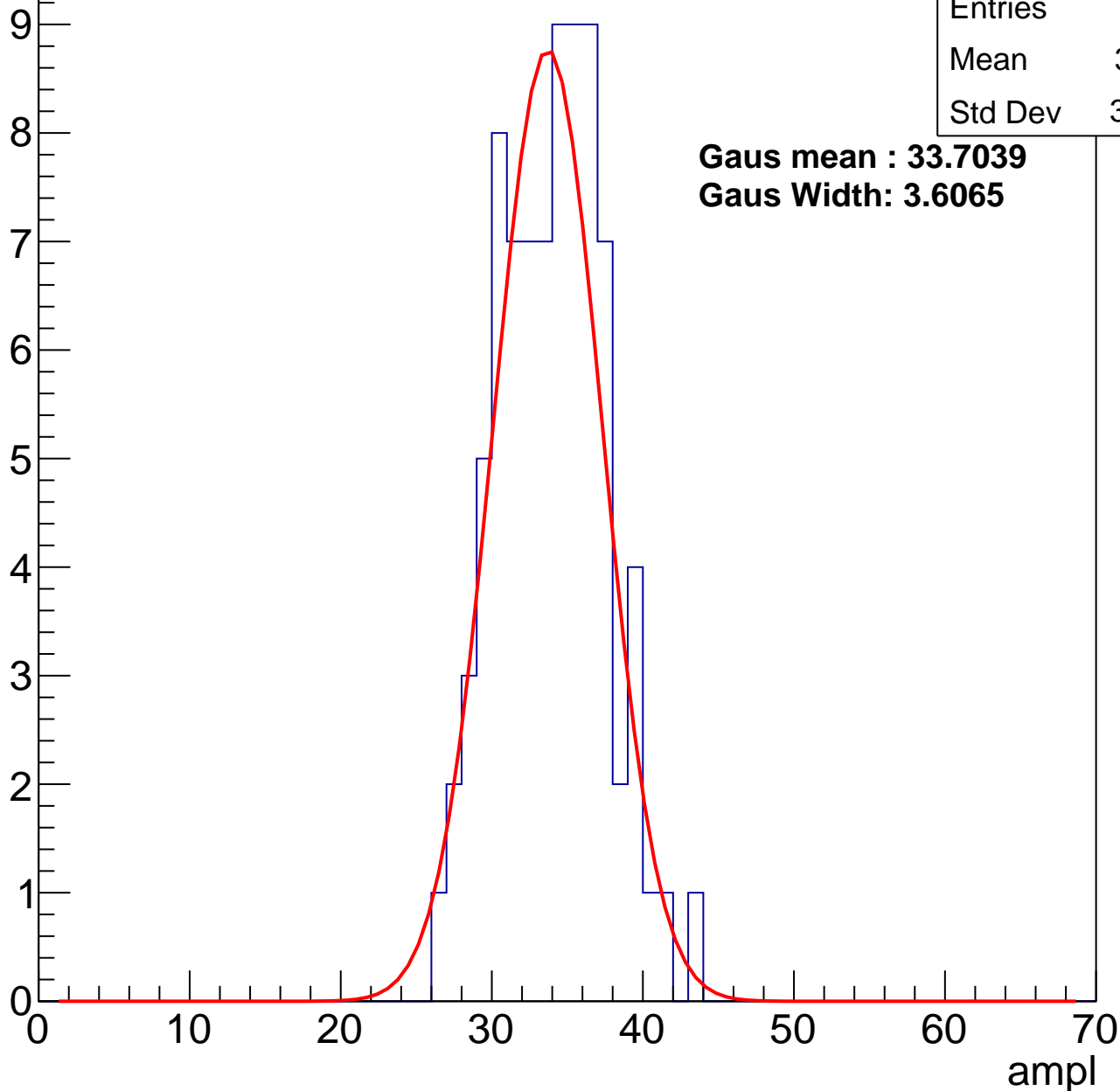
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	83
Mean	33.51
Std Dev	3.483

**Gaus mean : 33.7039**

**Gaus Width: 3.6065**



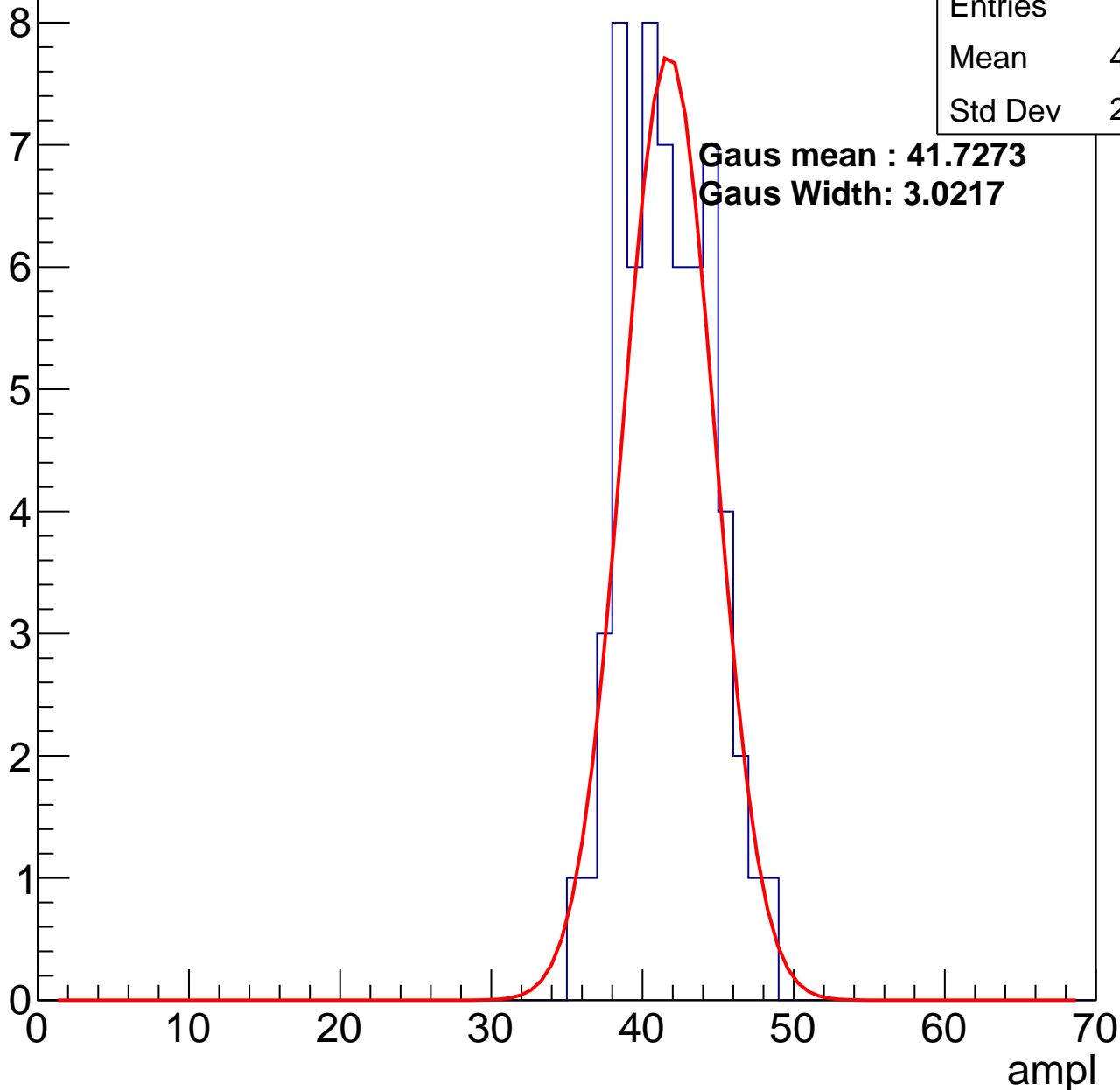
# B1L002S, U26-ch99, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.18
Std Dev	2.872

**Gaus mean : 41.7273**  
**Gaus Width: 3.0217**

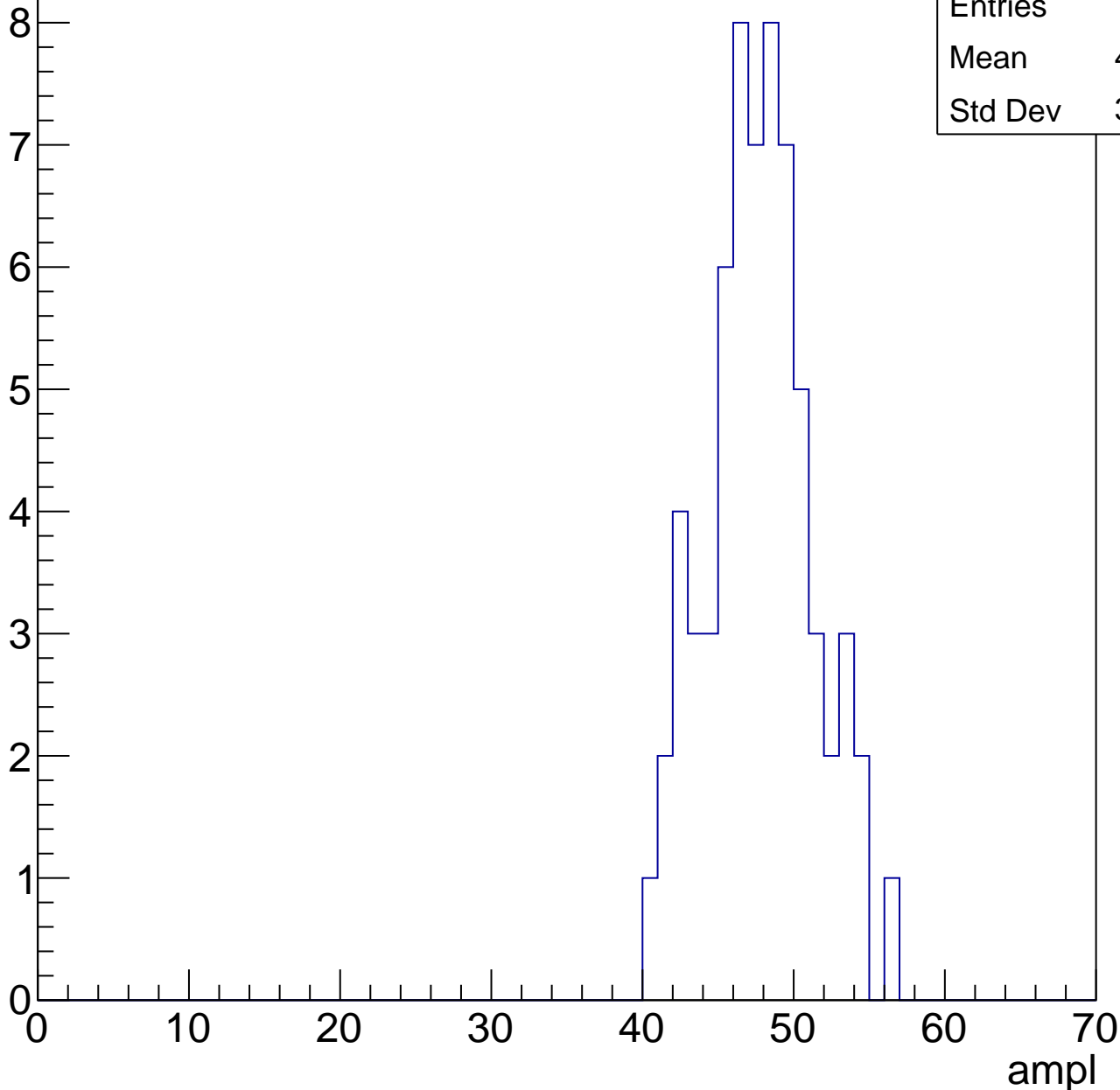


# B1L002S, U26-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

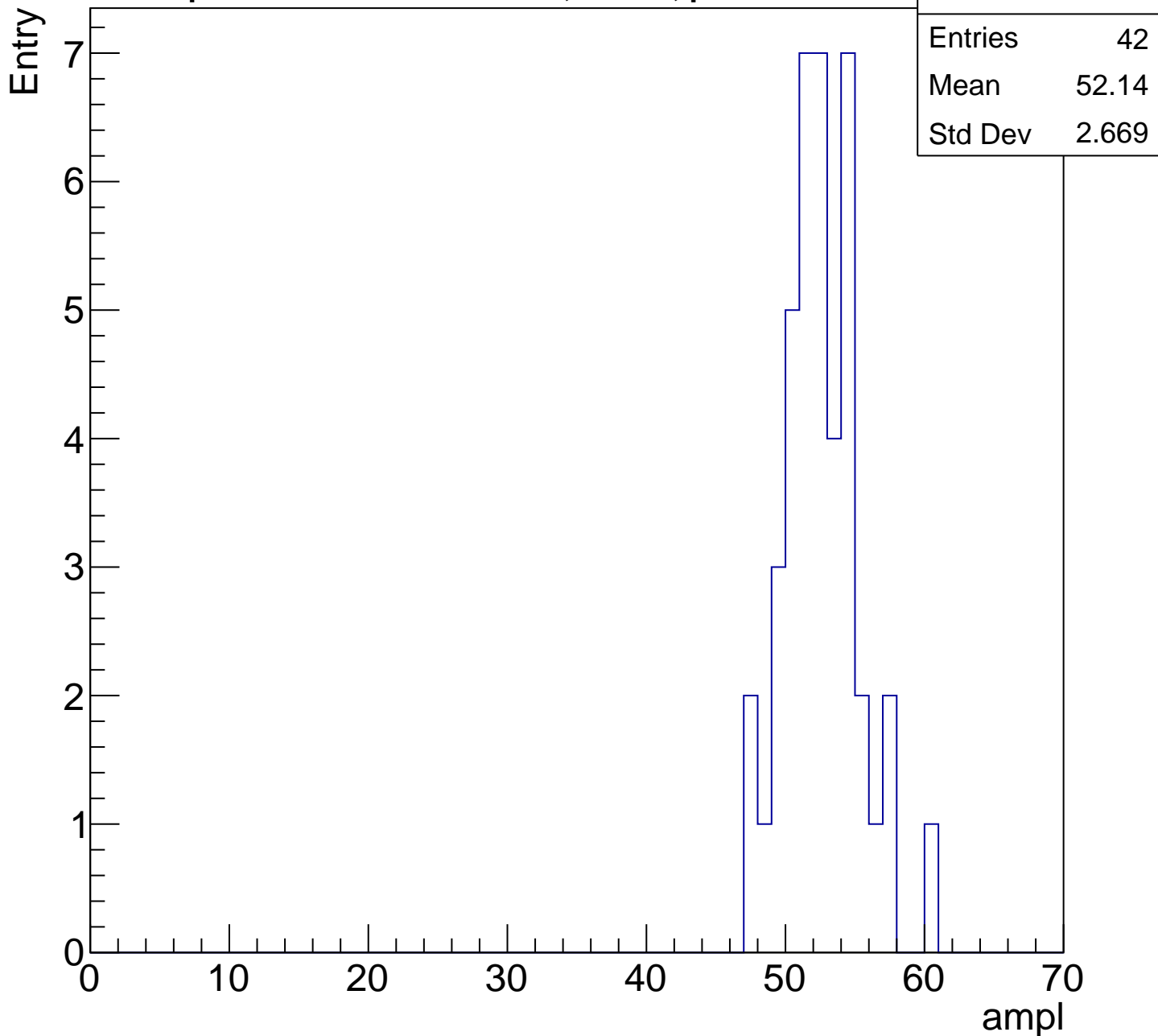
Entry

Entries	65
Mean	47.31
Std Dev	3.481



# B1L002S, U26-ch99, adc4

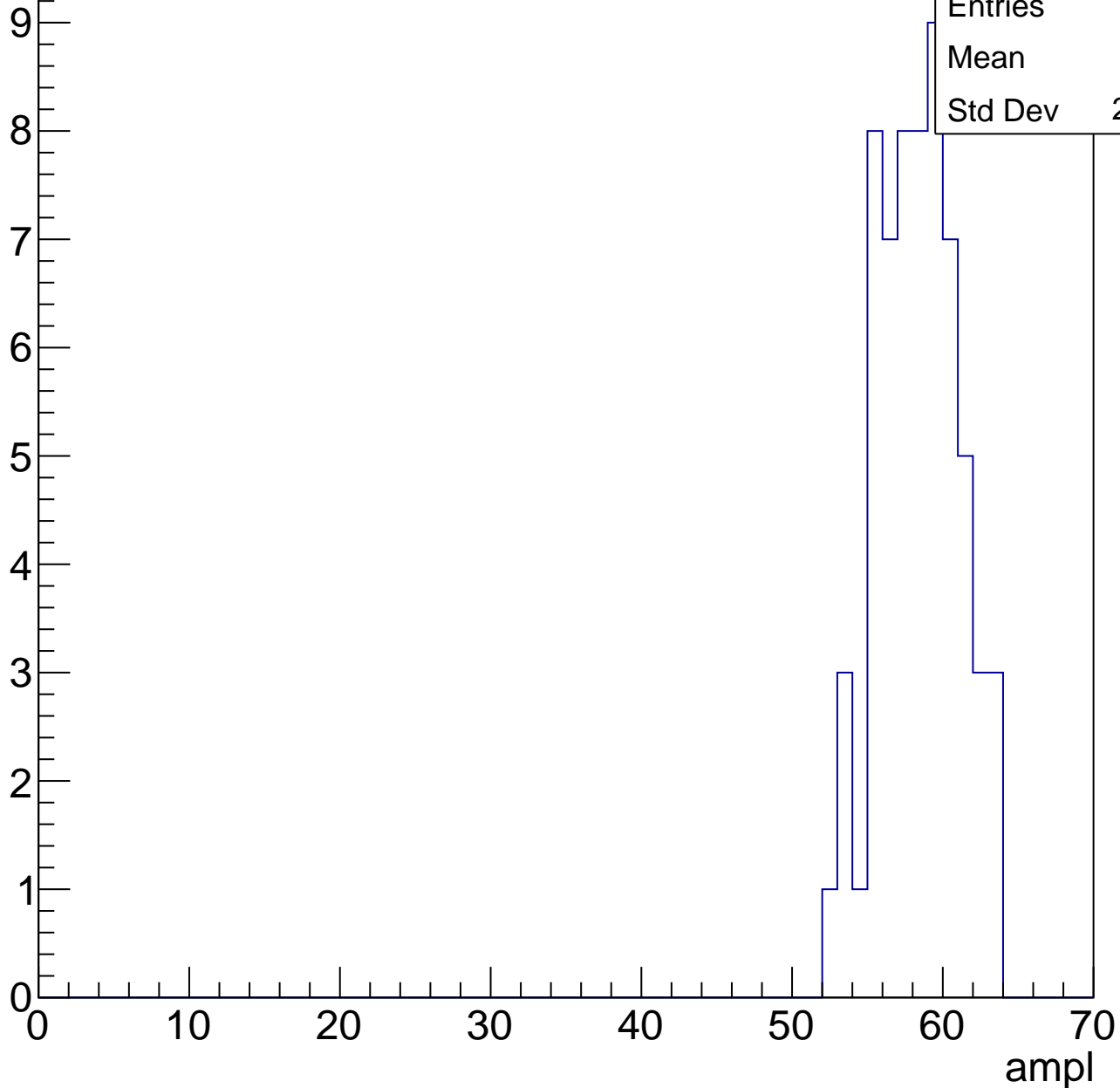
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U26-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

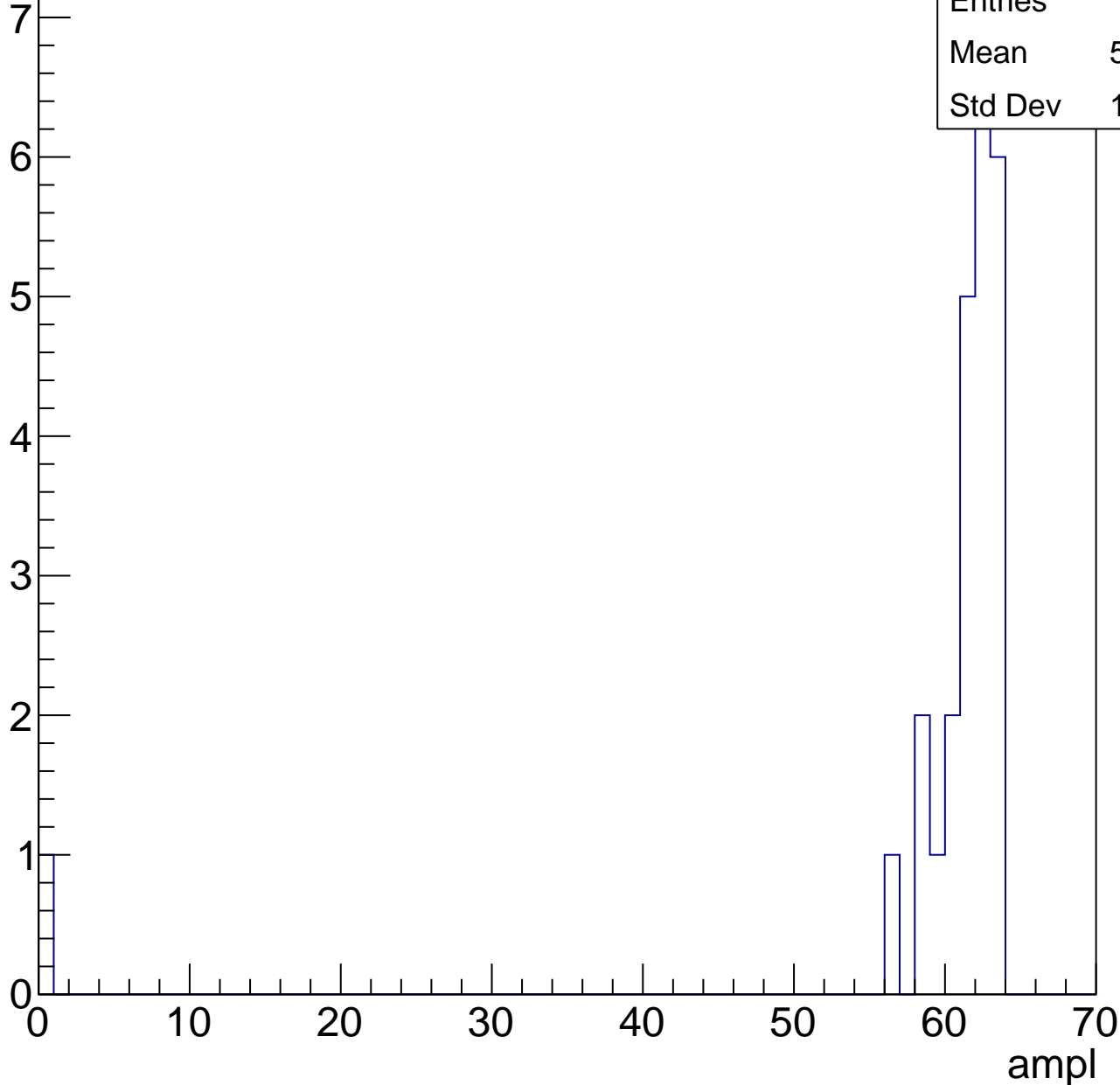


# B1L002S, U26-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.72
Std Dev	12.12





# B1L002S, U26-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch100, adc0

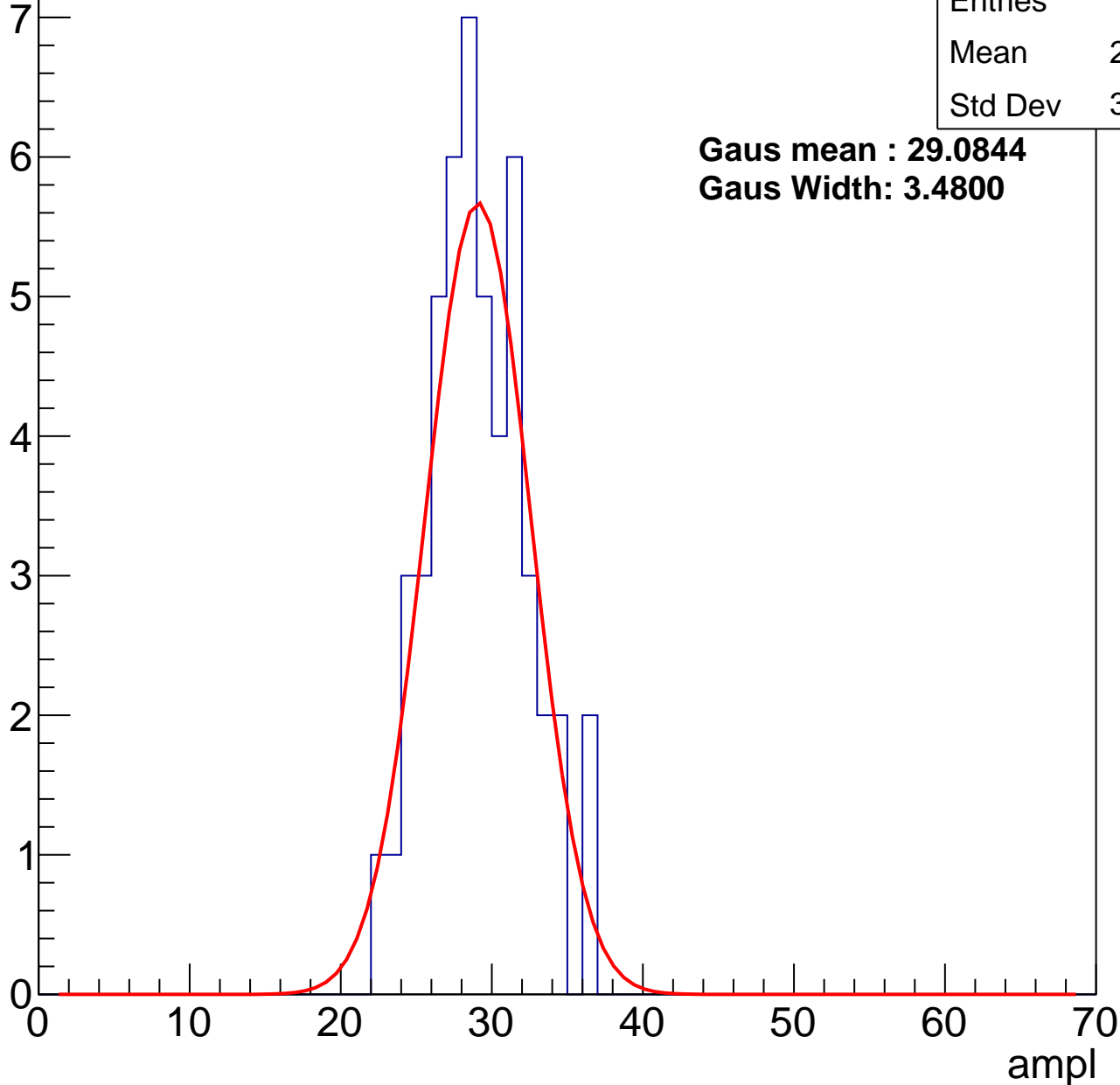
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	28.66
Std Dev	3.204

**Gaus mean : 29.0844**

**Gaus Width: 3.4800**



# B1L002S, U26-ch100, adc1

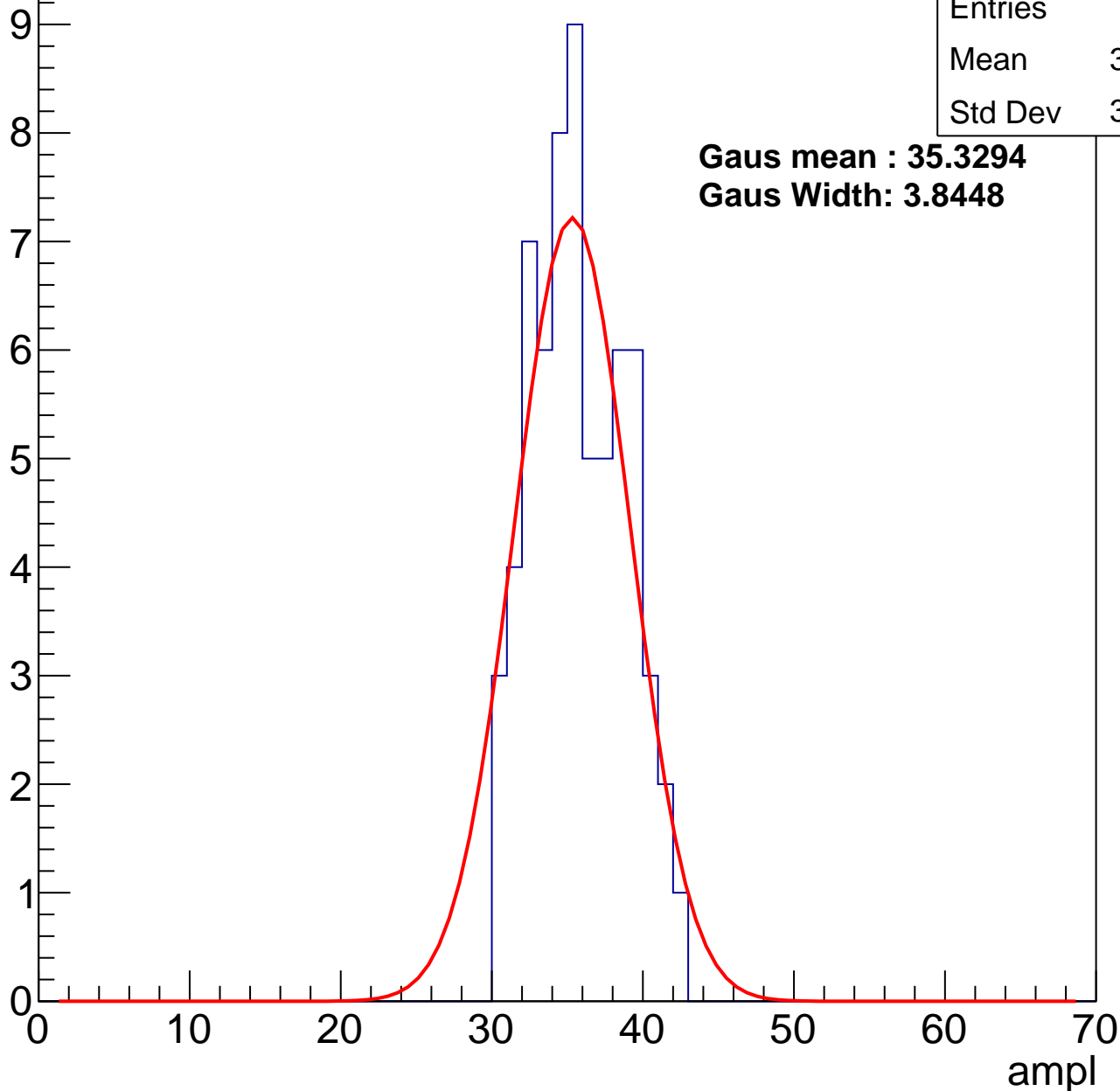
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	35.29
Std Dev	3.037

**Gaus mean : 35.3294**

**Gaus Width: 3.8448**



# B1L002S, U26-ch100, adc2

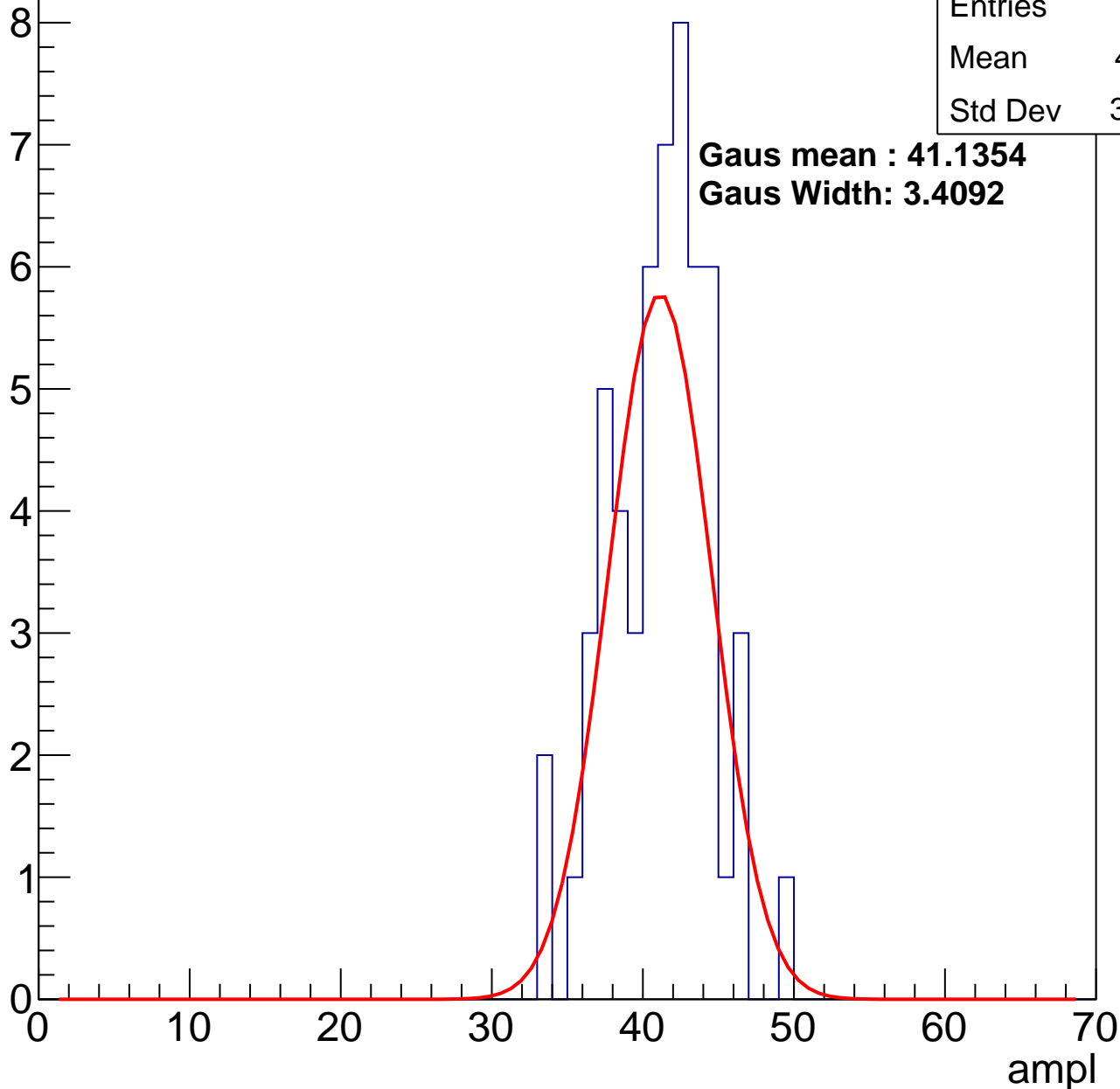
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.71
Std Dev	3.299

**Gaus mean : 41.1354**

**Gaus Width: 3.4092**

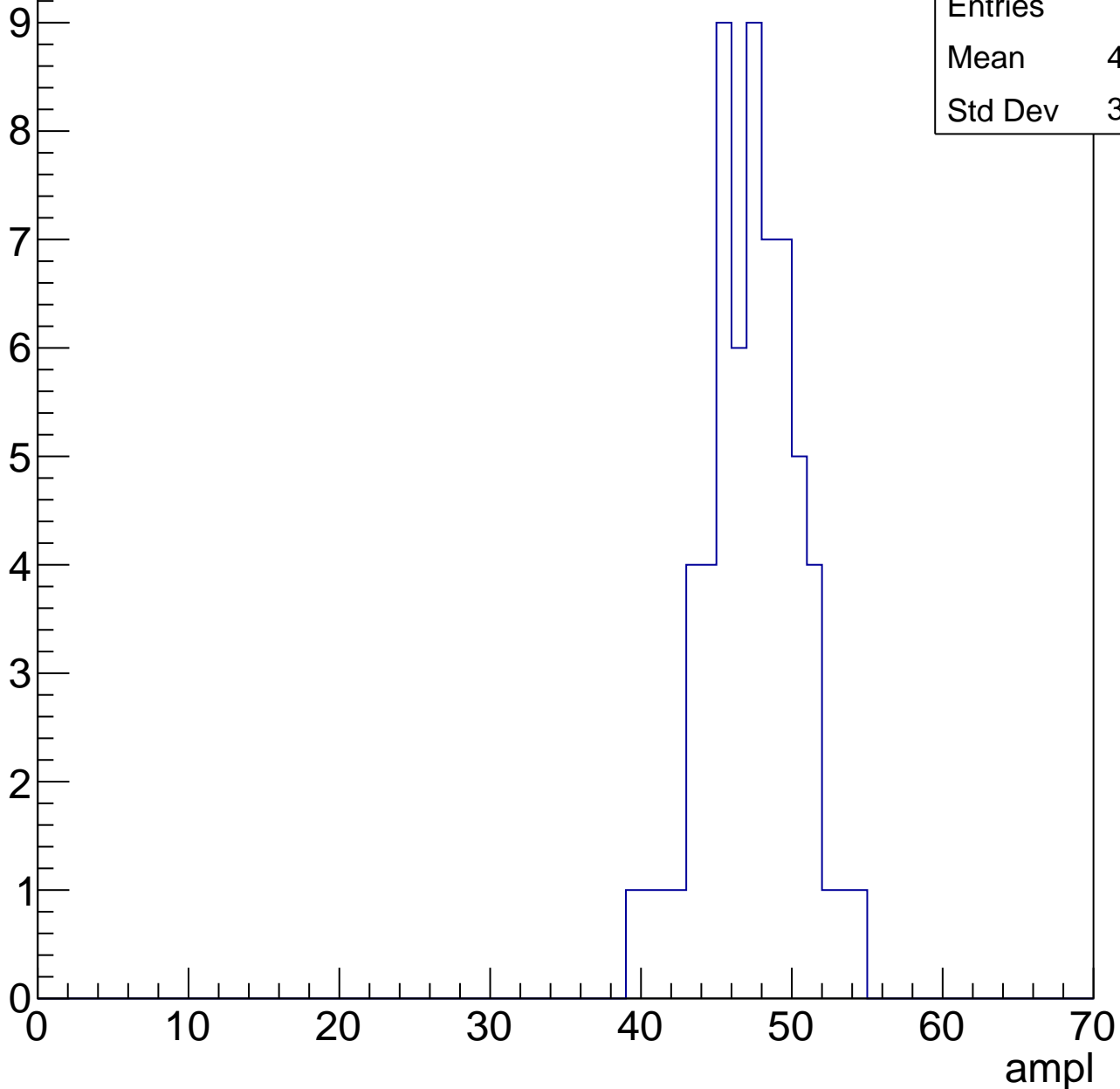


# B1L002S, U26-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.87
Std Dev	3.029

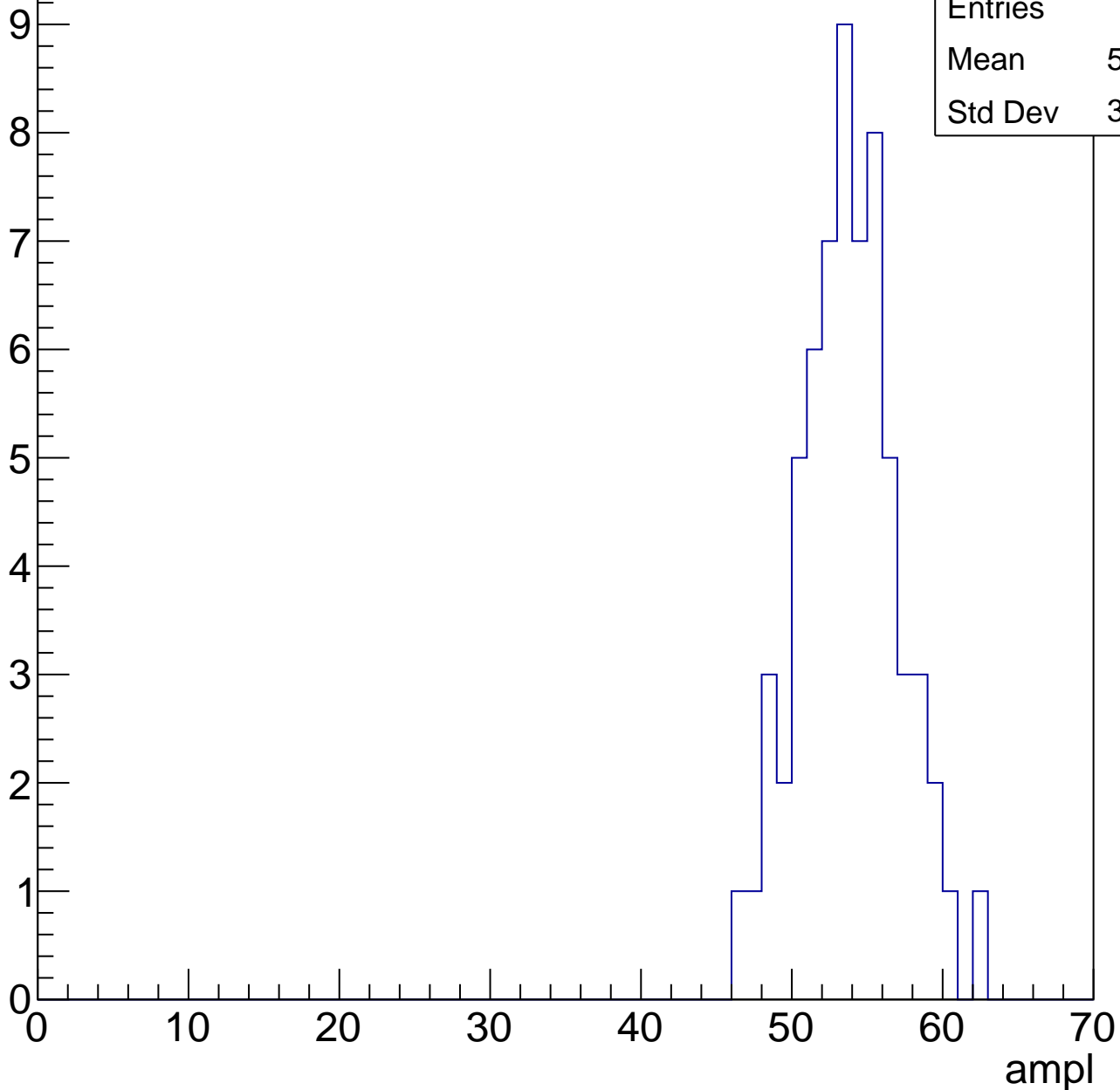


# B1L002S, U26-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	53.36
Std Dev	3.232

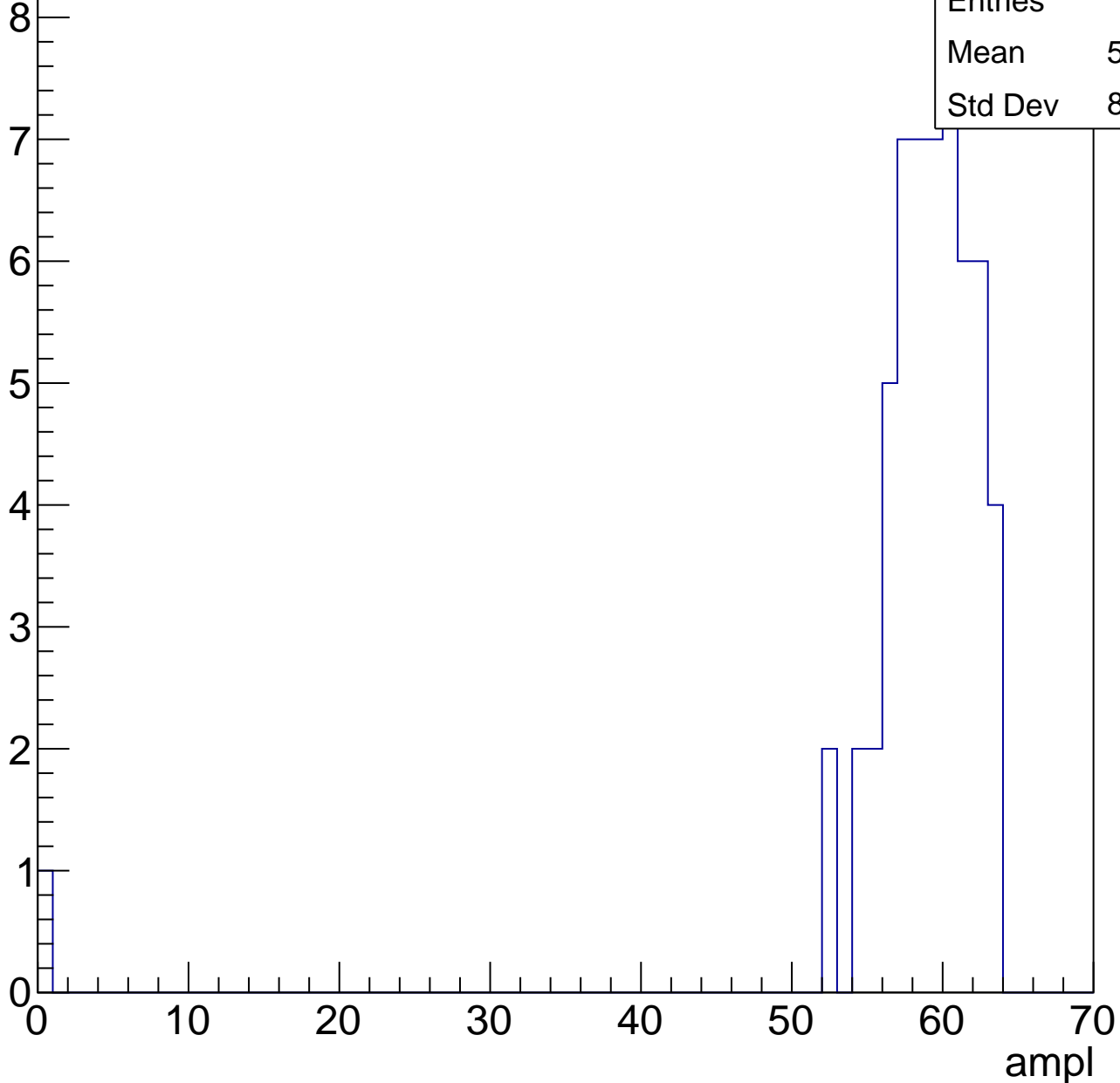


# B1L002S, U26-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

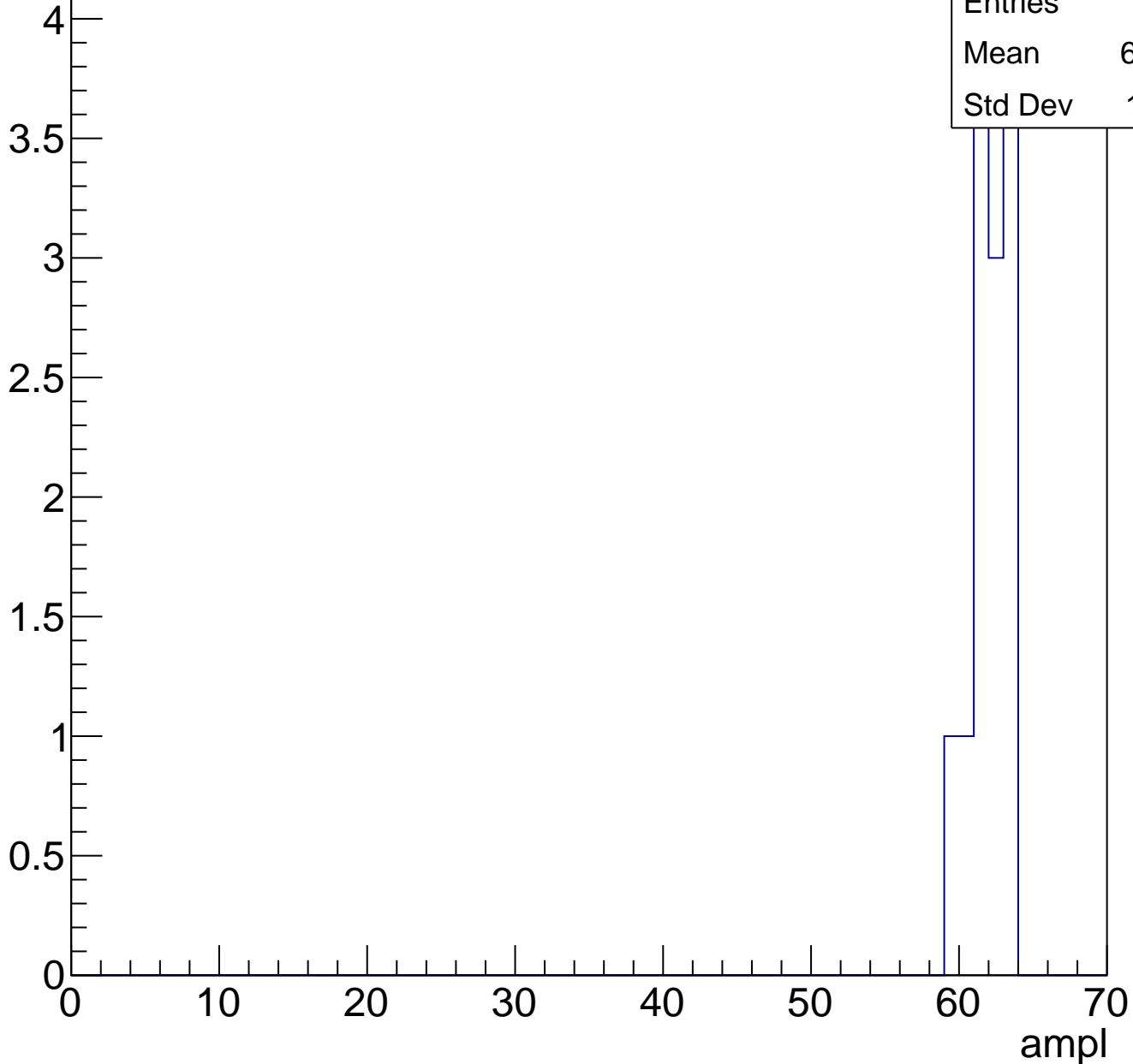
Entries	57
Mean	57.72
Std Dev	8.162



# B1L002S, U26-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch101, adc0

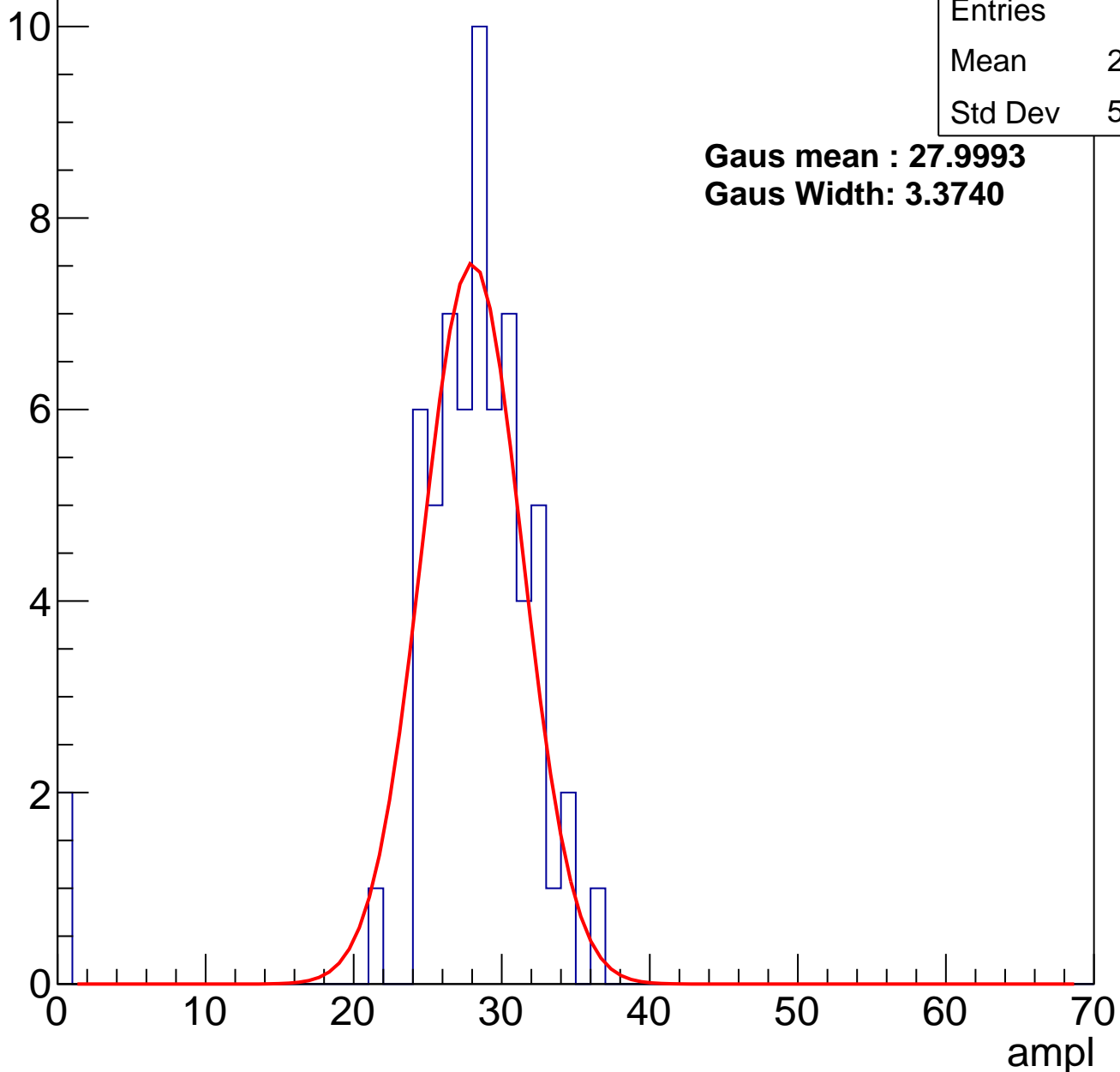
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	27.29
Std Dev	5.733

**Gaus mean : 27.9993**

**Gaus Width: 3.3740**

Entry



# B1L002S, U26-ch101, adc1

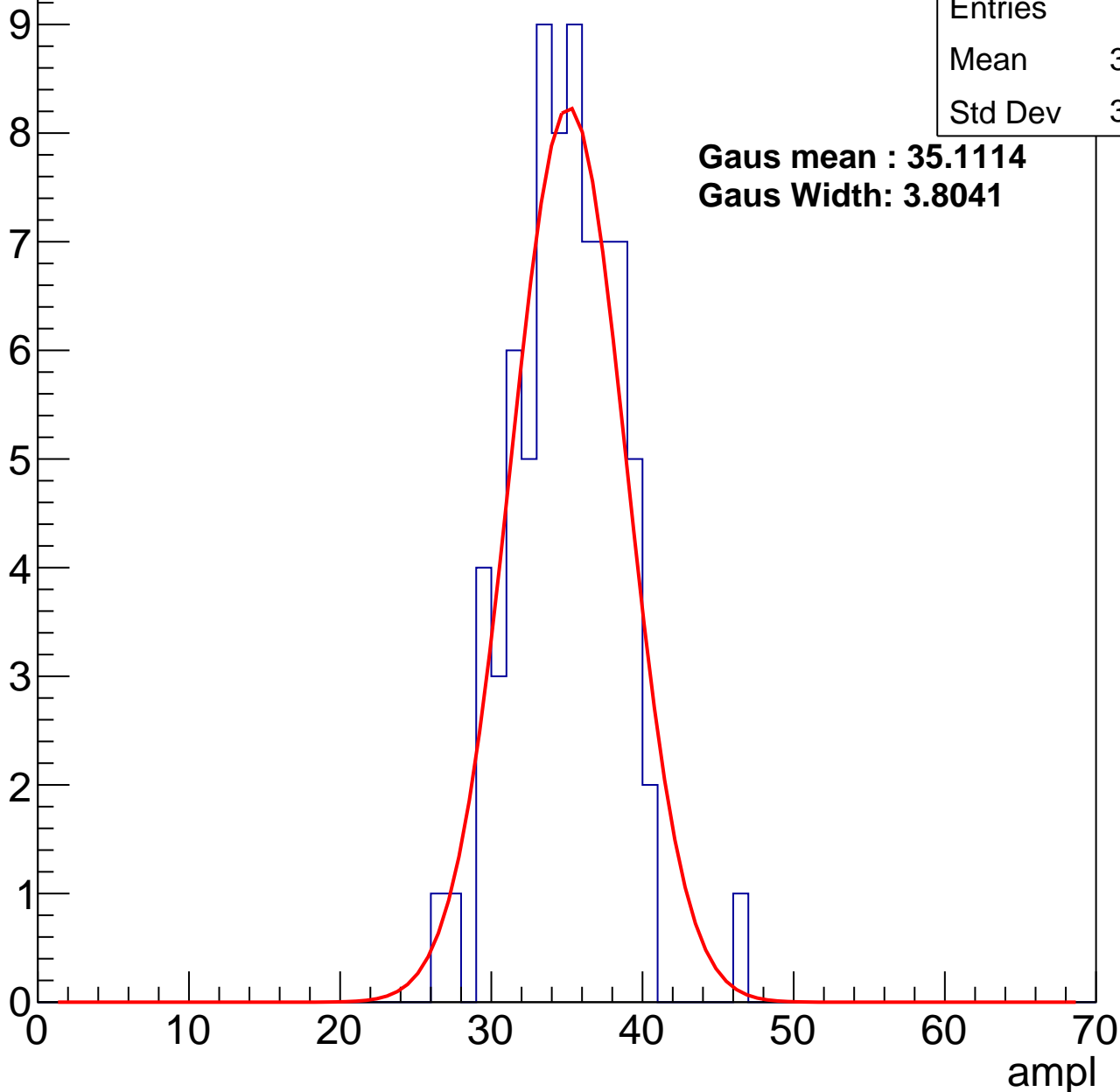
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	34.49
Std Dev	3.423

**Gaus mean : 35.1114**

**Gaus Width: 3.8041**



# B1L002S, U26-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	42
Std Dev	3.009

**Gaus mean : 42.5120**

**Gaus Width: 3.1576**

10

8

6

4

2

0

0

10

20

30

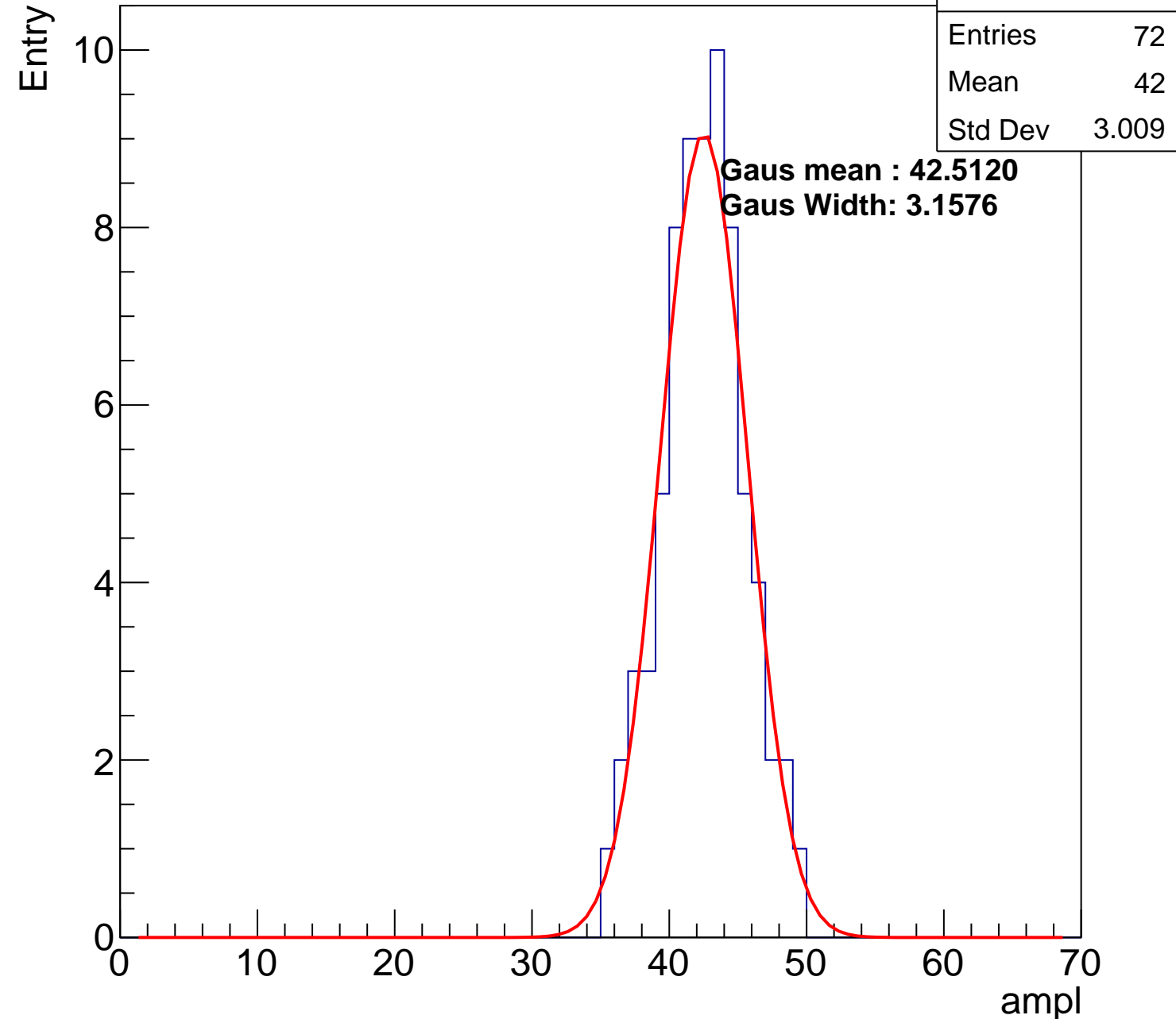
40

50

60

70

ampl

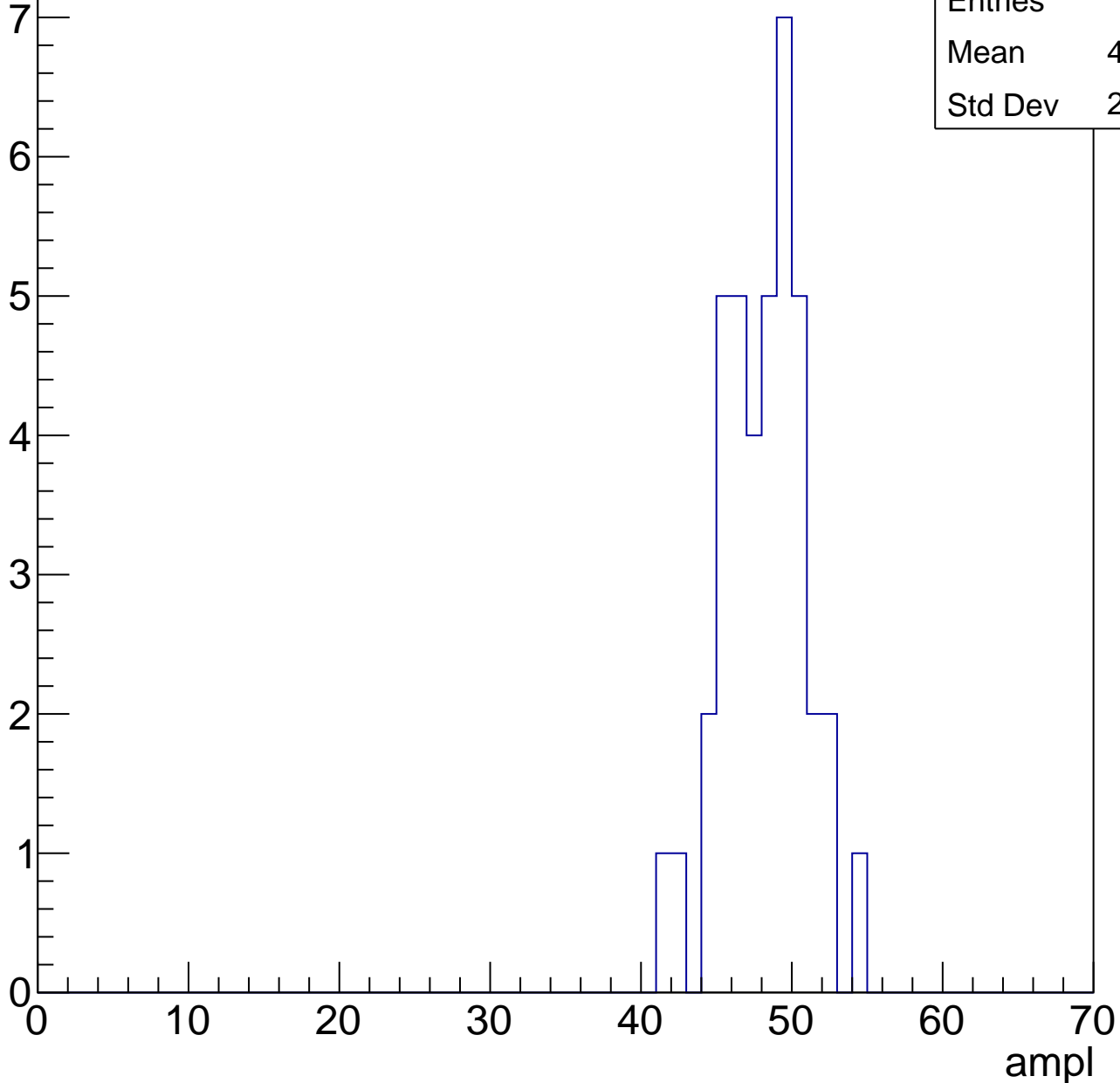


# B1L002S, U26-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	47.67
Std Dev	2.715

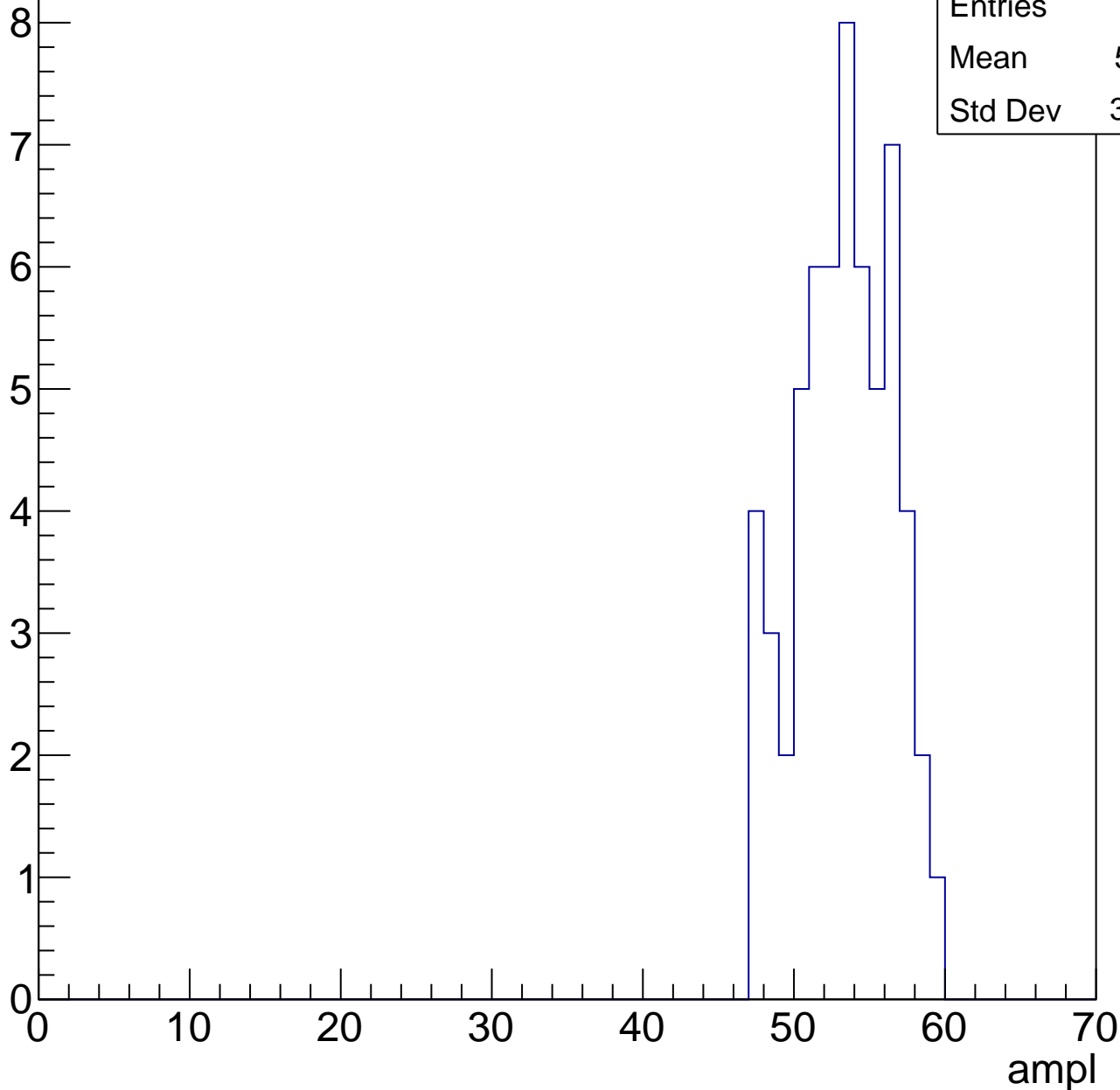


# B1L002S, U26-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	52.81
Std Dev	3.089

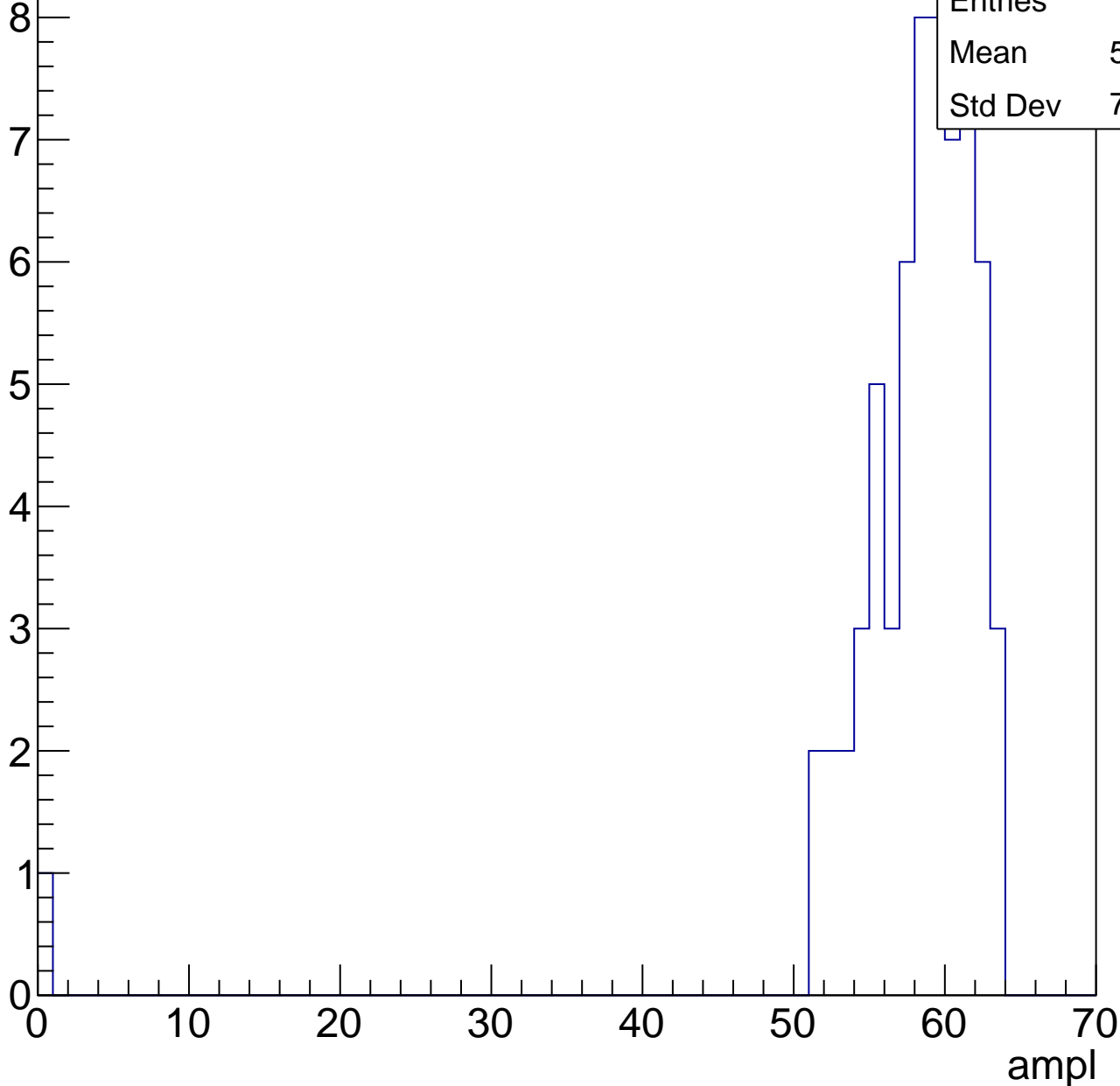


# B1L002S, U26-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.25
Std Dev	7.844

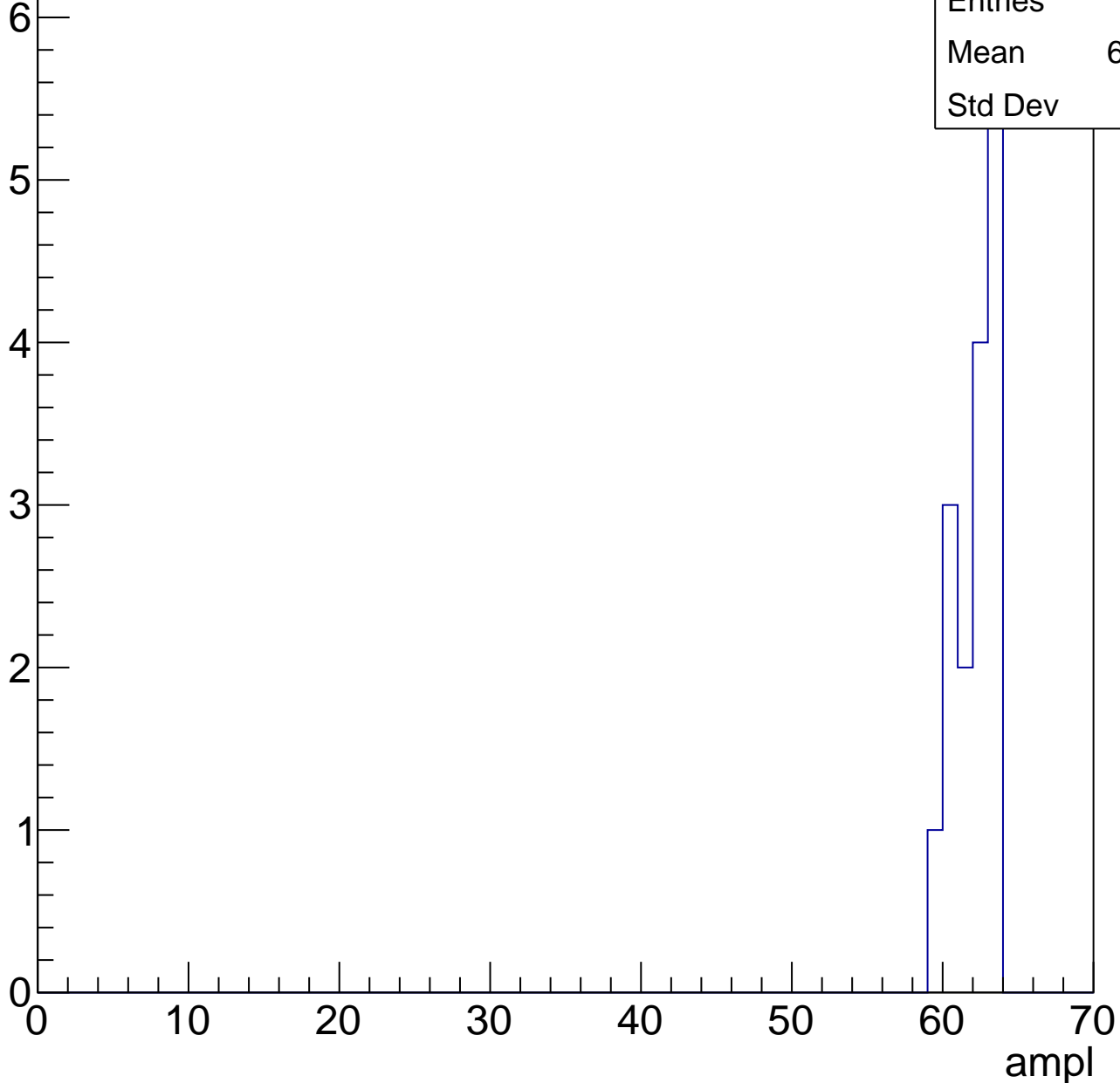


# B1L002S, U26-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	61.69
Std Dev	1.31

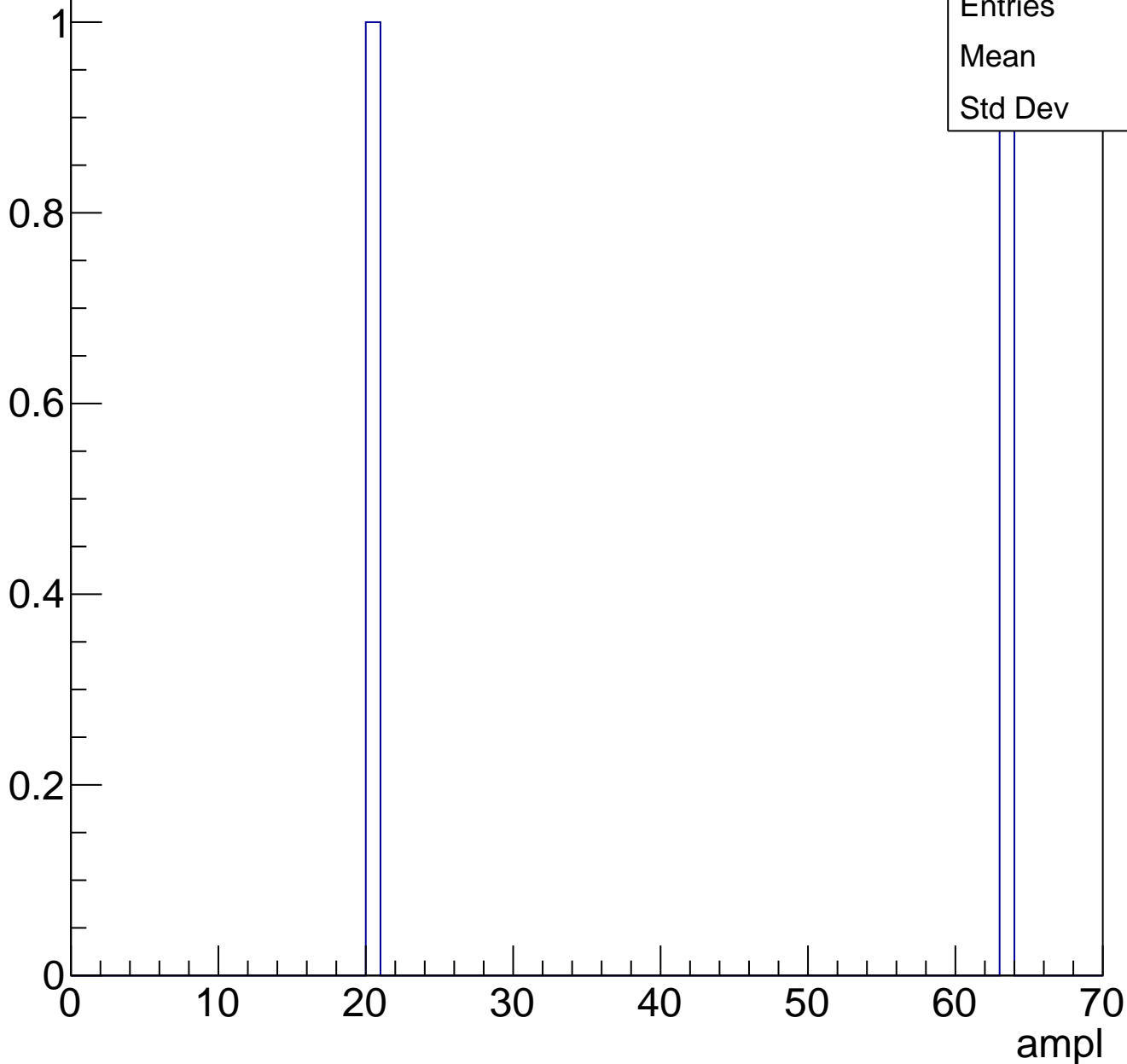




# B1L002S, U26-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch102, adc0

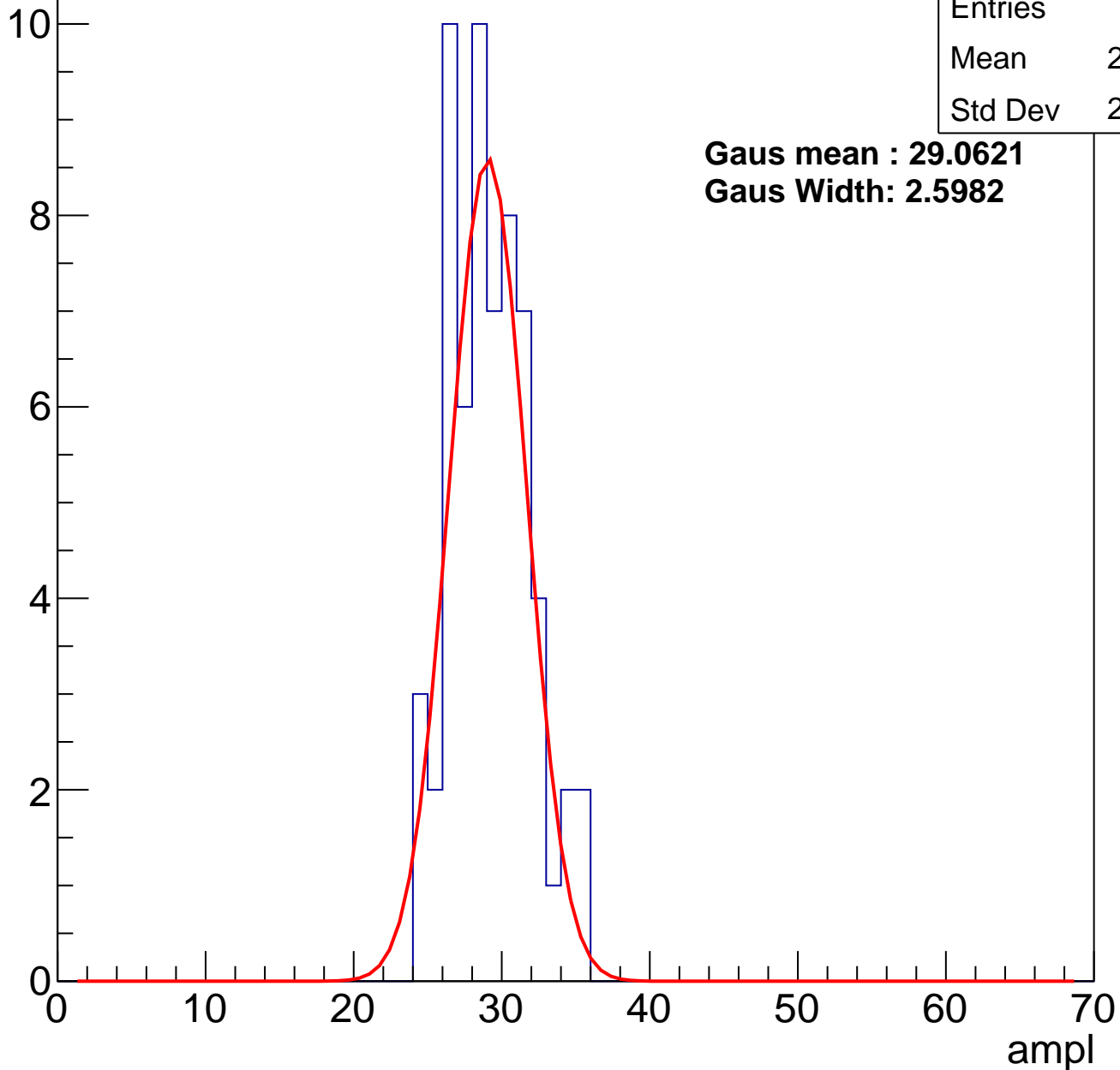
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	28.76
Std Dev	2.656

**Gaus mean : 29.0621**

**Gaus Width: 2.5982**

Entry



# B1L002S, U26-ch102, adc1

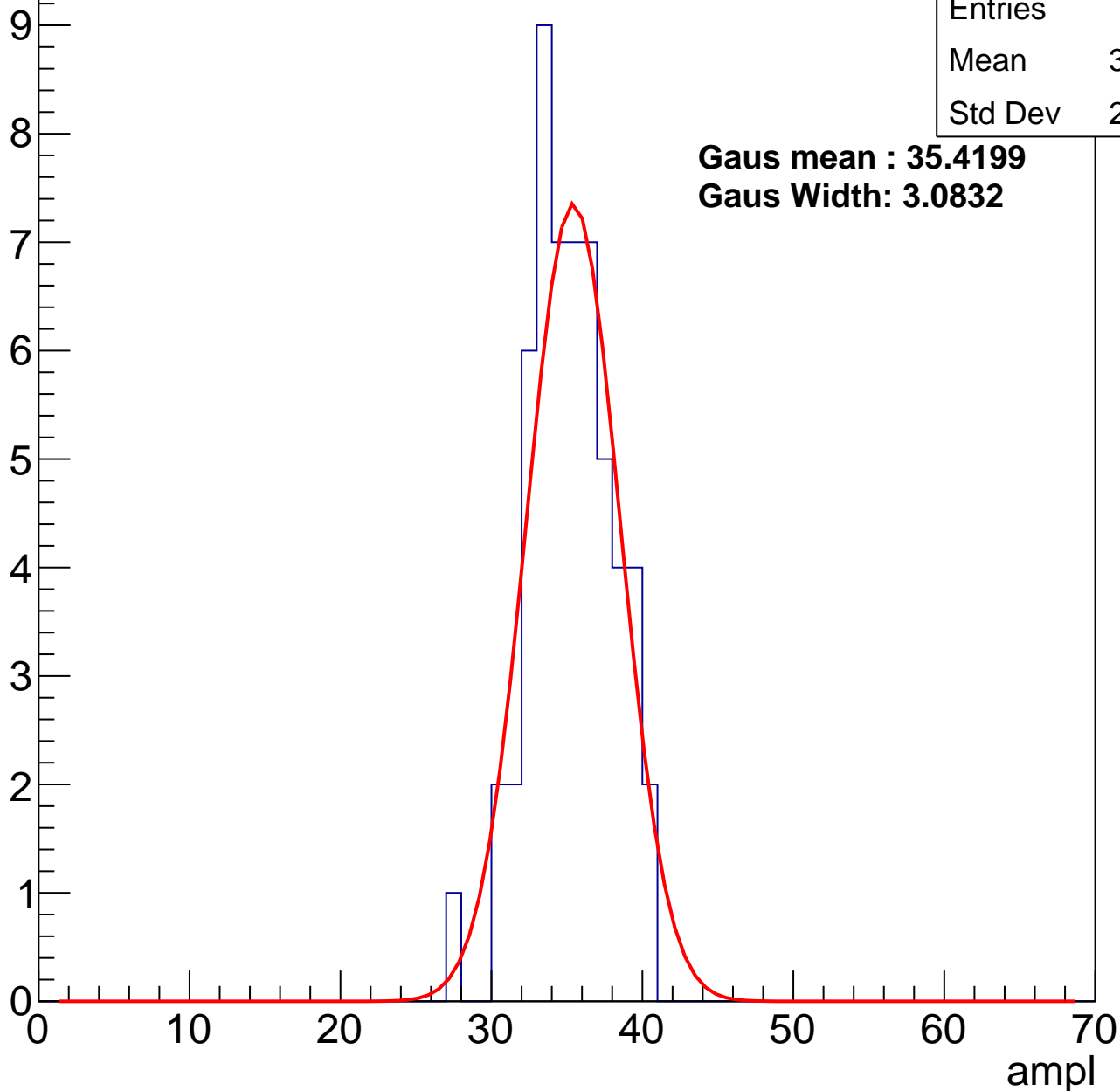
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.75
Std Dev	2.727

**Gaus mean : 35.4199**

**Gaus Width: 3.0832**



# B1L002S, U26-ch102, adc2

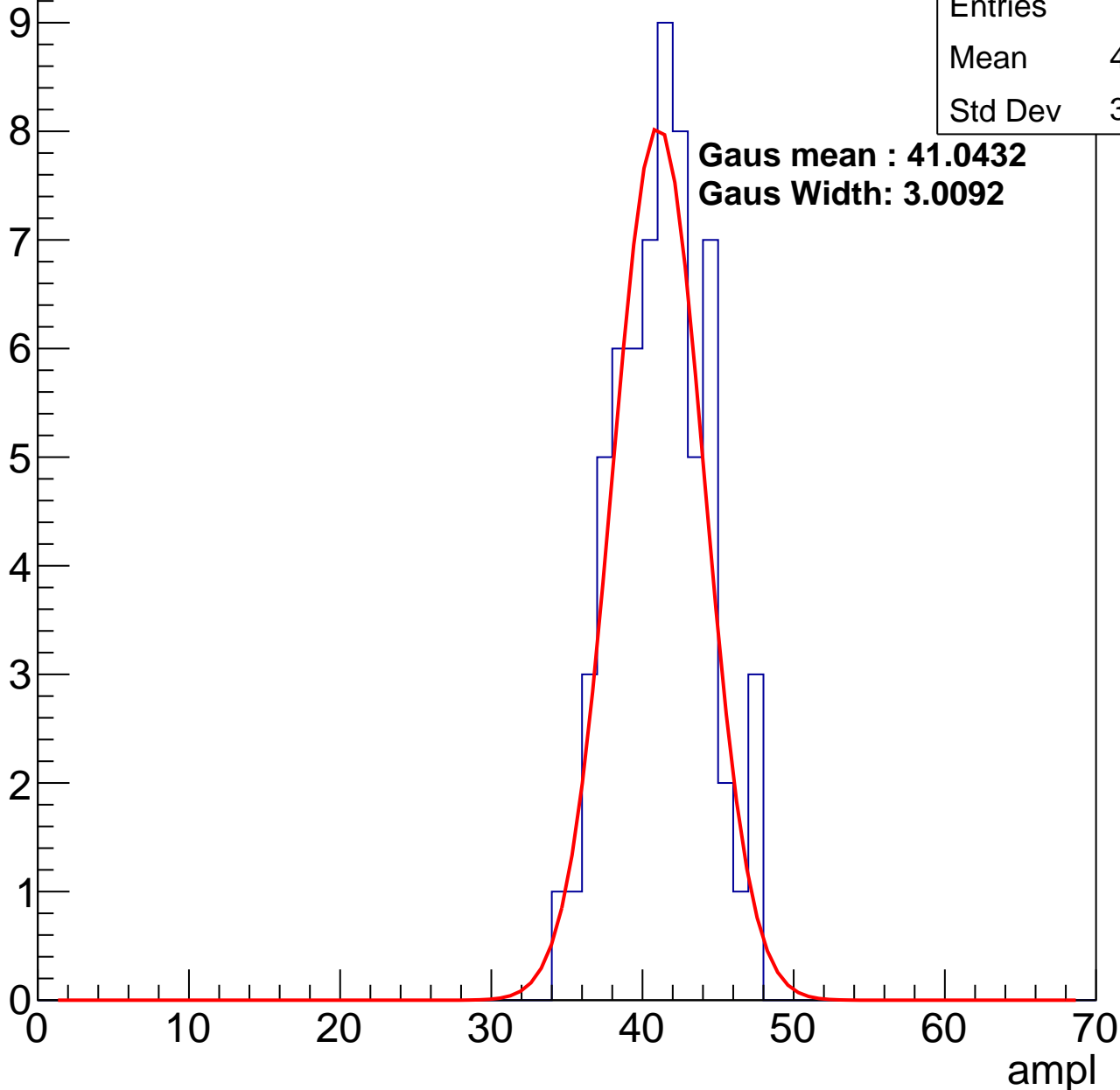
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	40.77
Std Dev	3.004

**Gaus mean : 41.0432**

**Gaus Width: 3.0092**

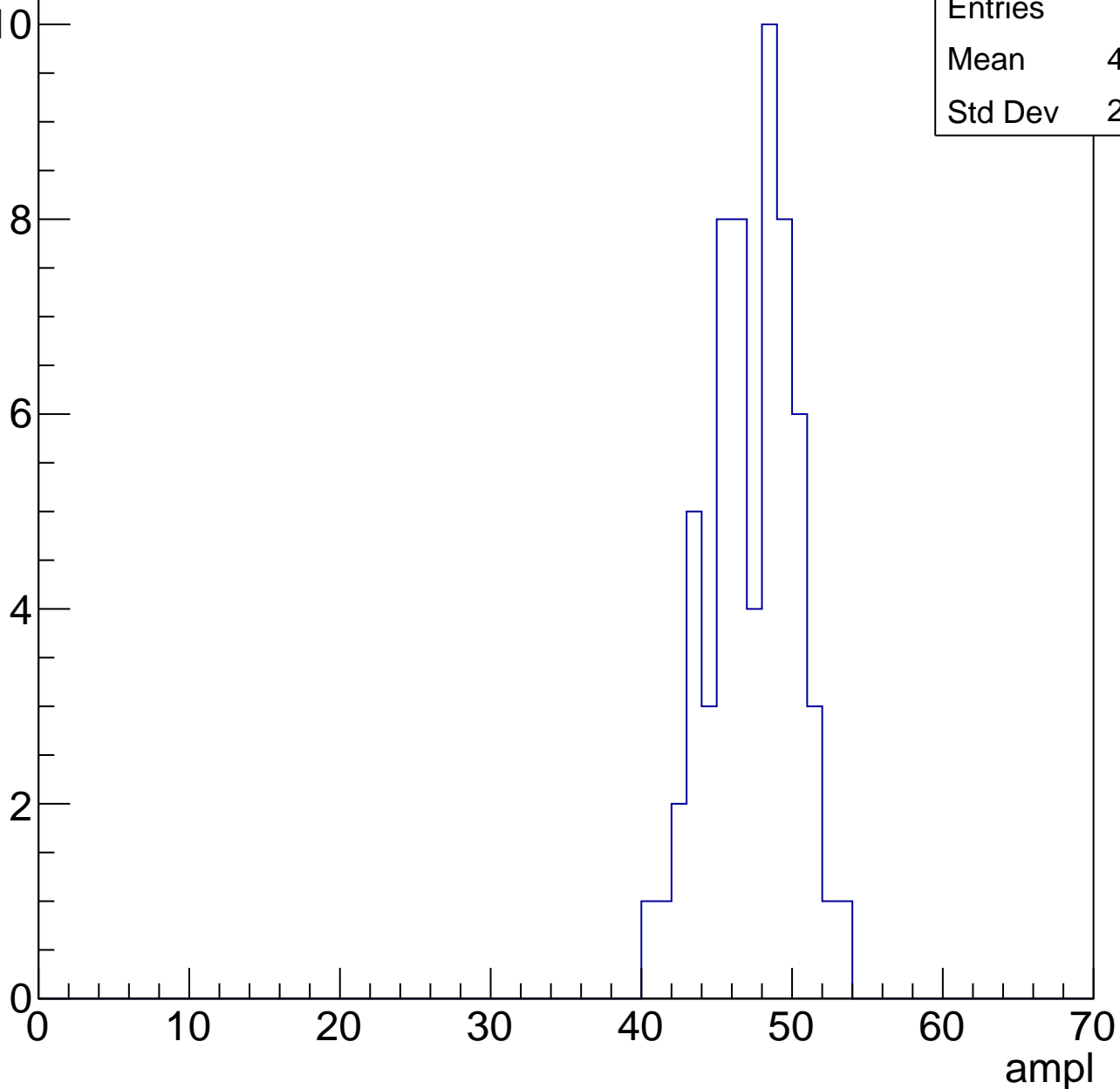


# B1L002S, U26-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.85
Std Dev	2.822

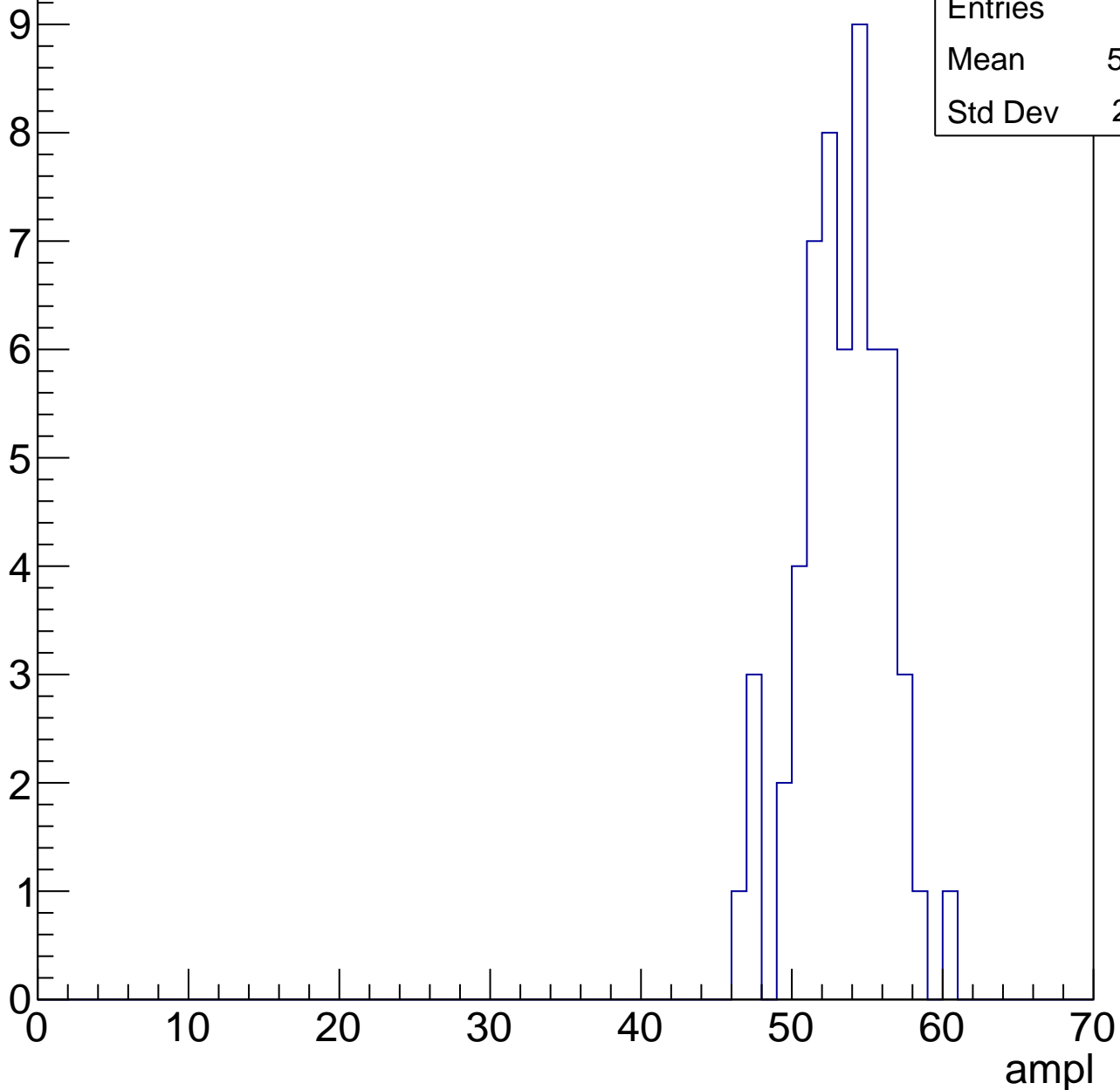


# B1L002S, U26-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	52.93
Std Dev	2.871



# B1L002S, U26-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	55
Mean	58.2
Std Dev	2.617

Entry

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4  
2  
0

0 10 20 30 40 50 60 70

ampl

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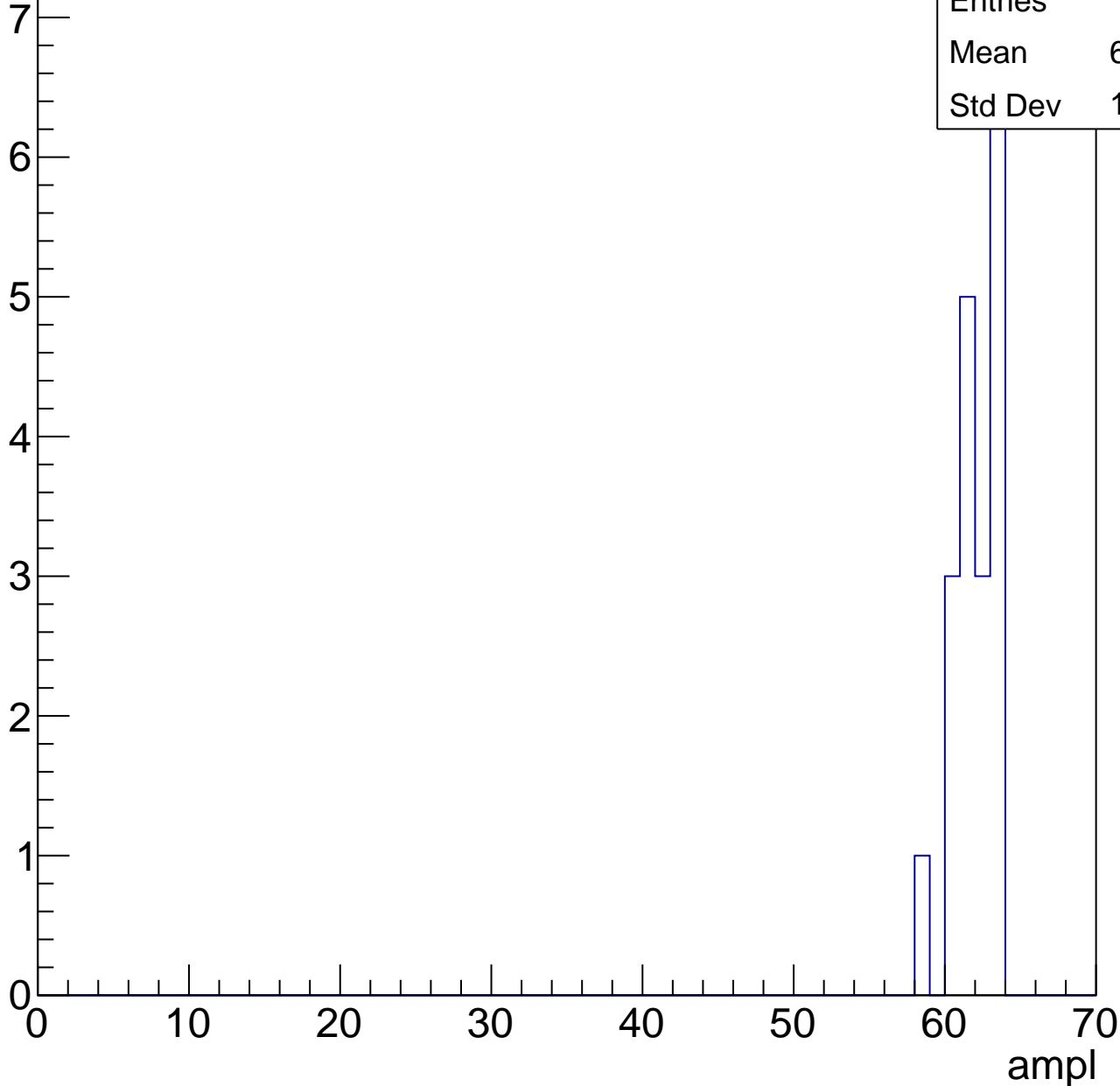
3

# B1L002S, U26-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.58
Std Dev	1.389

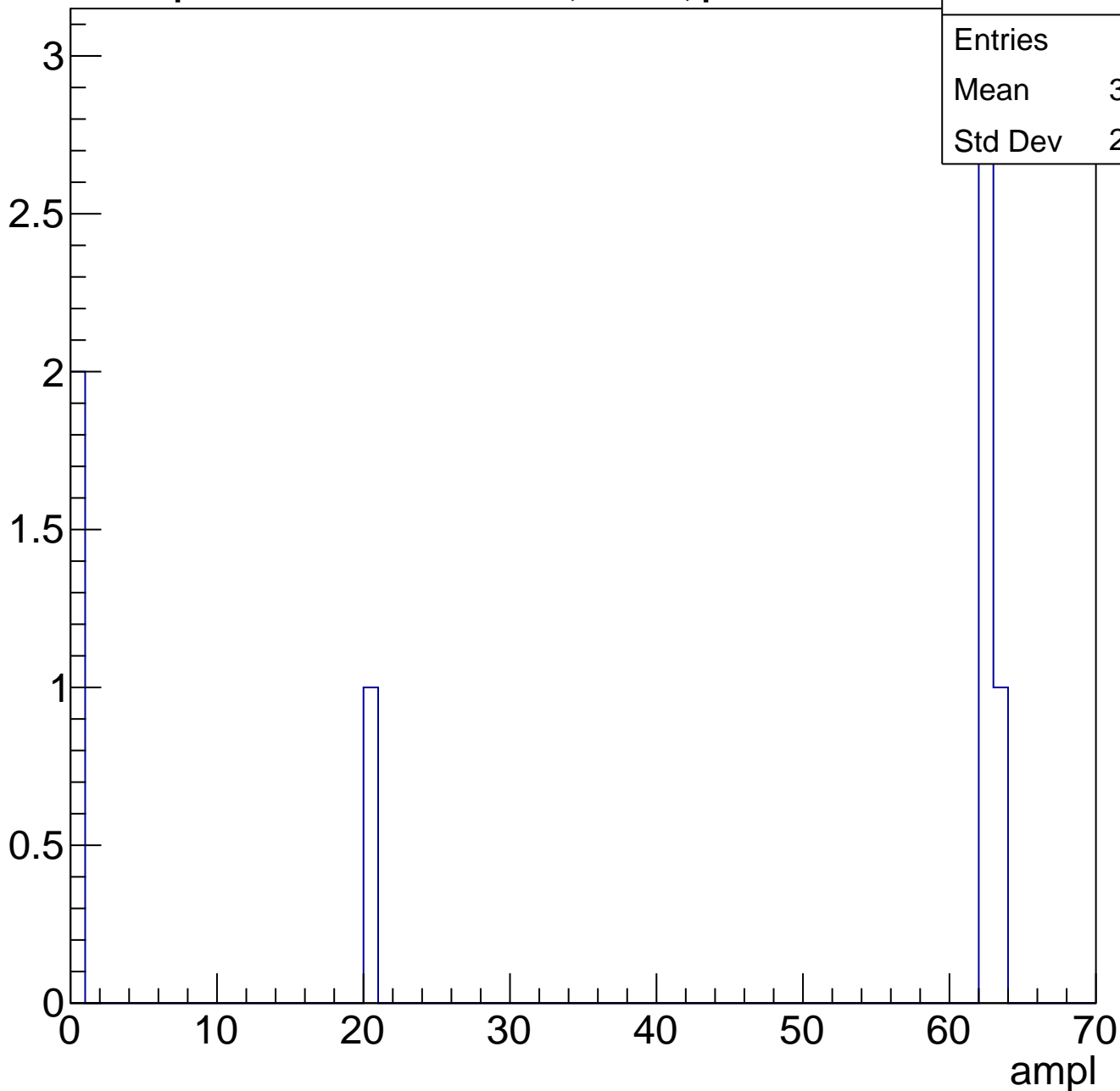




# B1L002S, U26-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch103, adc0

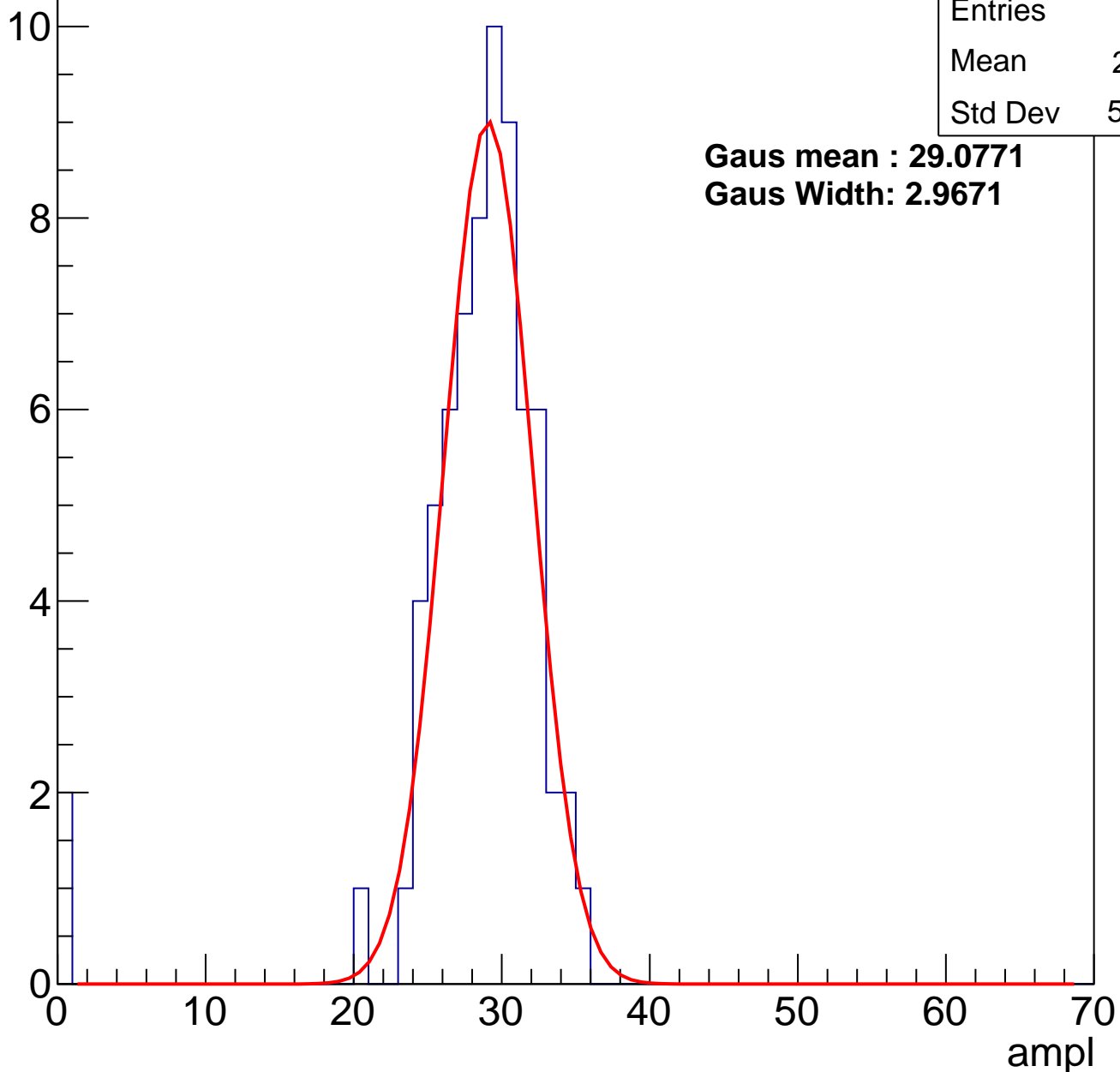
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	27.71
Std Dev	5.553

**Gaus mean : 29.0771**

**Gaus Width: 2.9671**

Entry



# B1L002S, U26-ch103, adc1

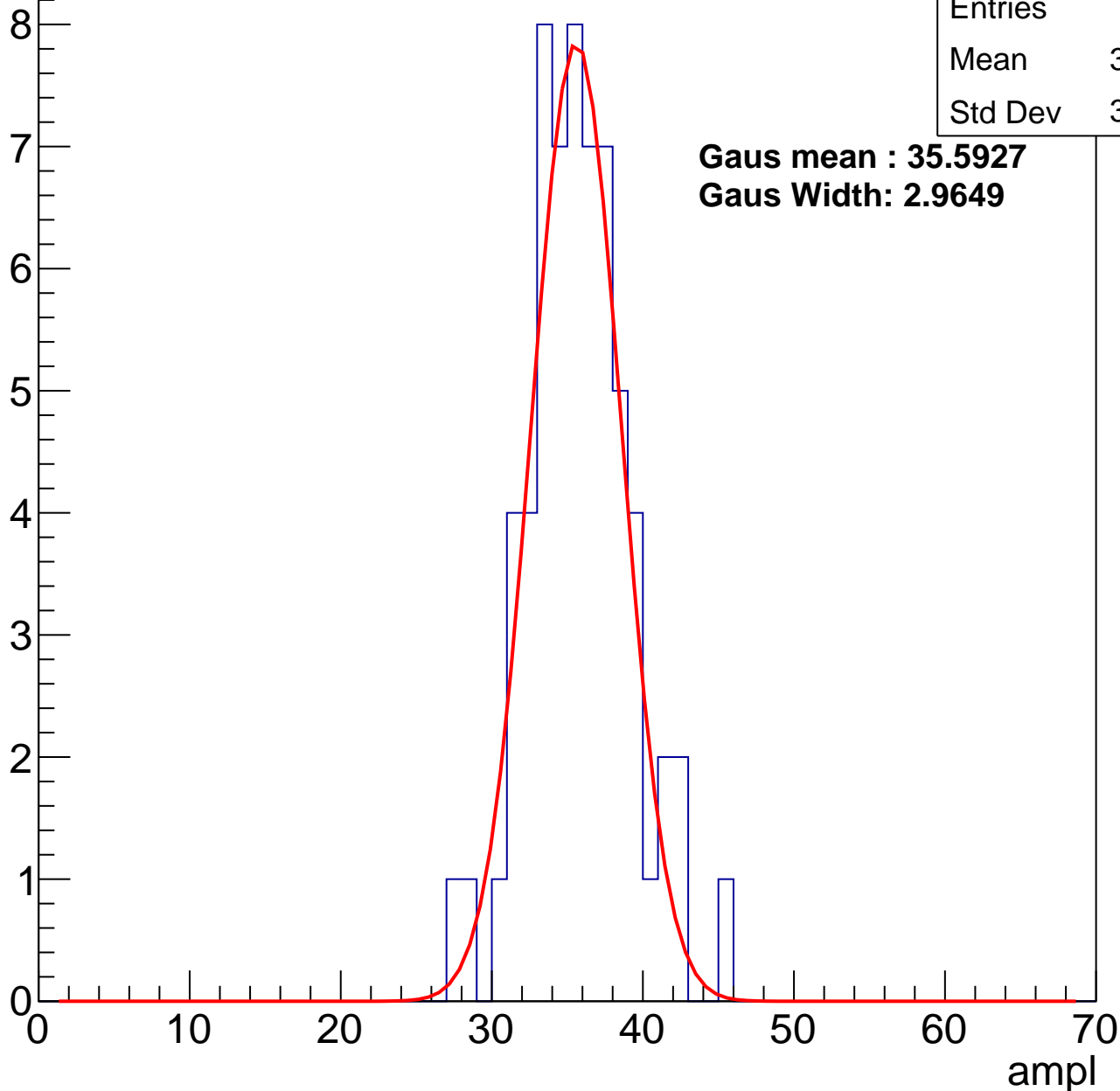
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.35
Std Dev	3.353

**Gaus mean : 35.5927**

**Gaus Width: 2.9649**



# B1L002S, U26-ch103, adc2

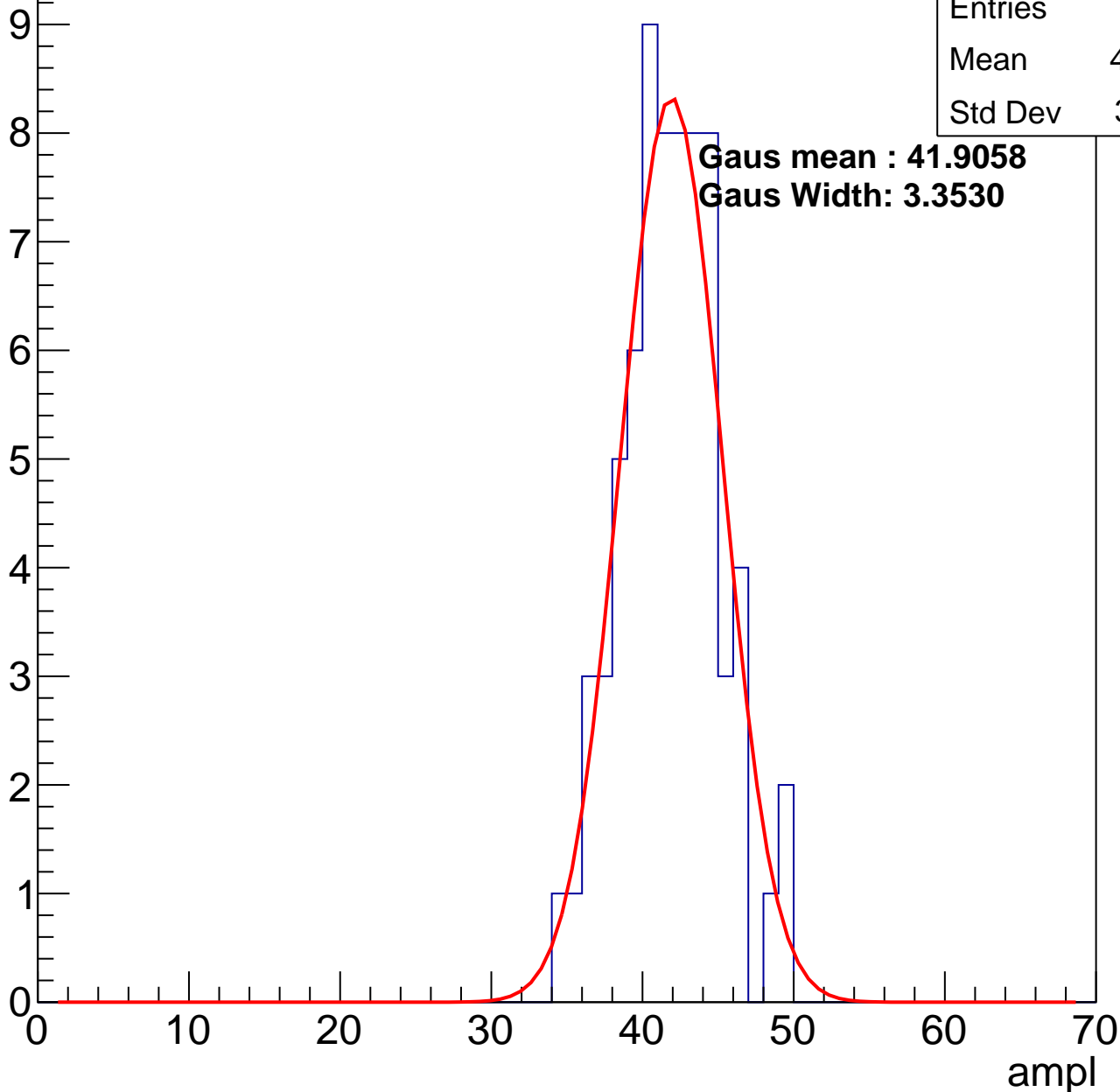
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	41.39
Std Dev	3.191

**Gaus mean : 41.9058**

**Gaus Width: 3.3530**

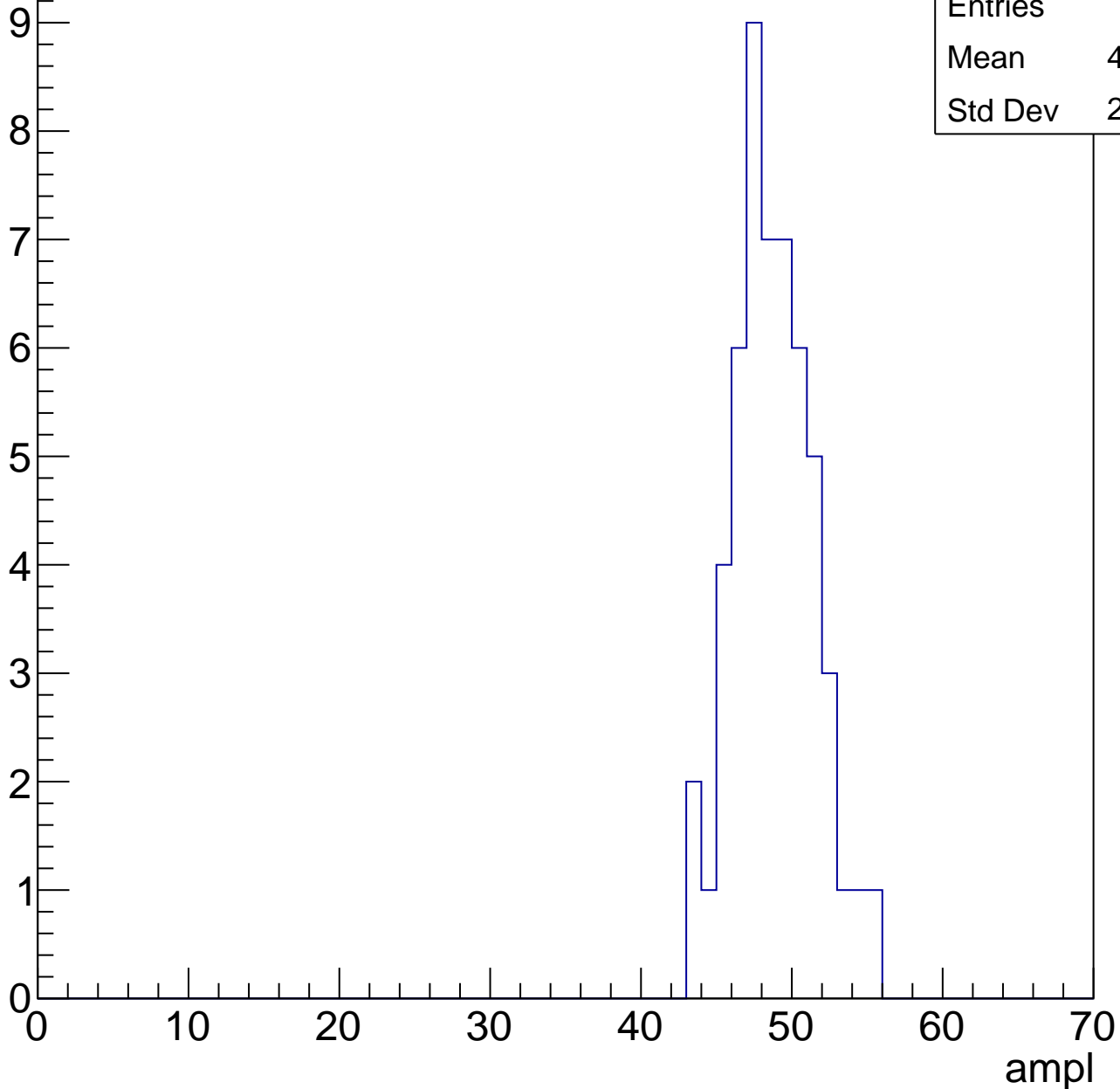


# B1L002S, U26-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	48.32
Std Dev	2.619

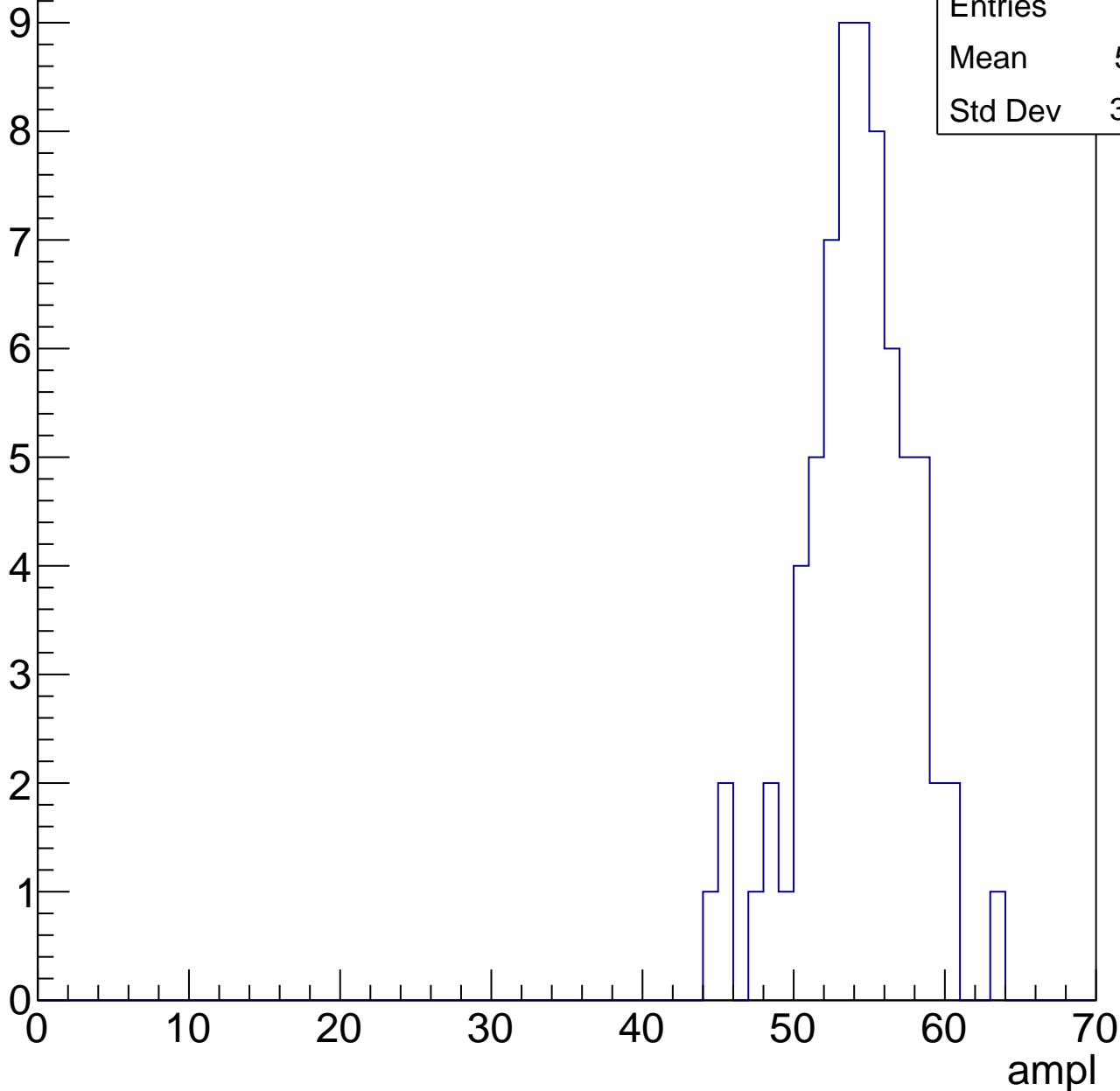


# B1L002S, U26-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	53.71
Std Dev	3.602

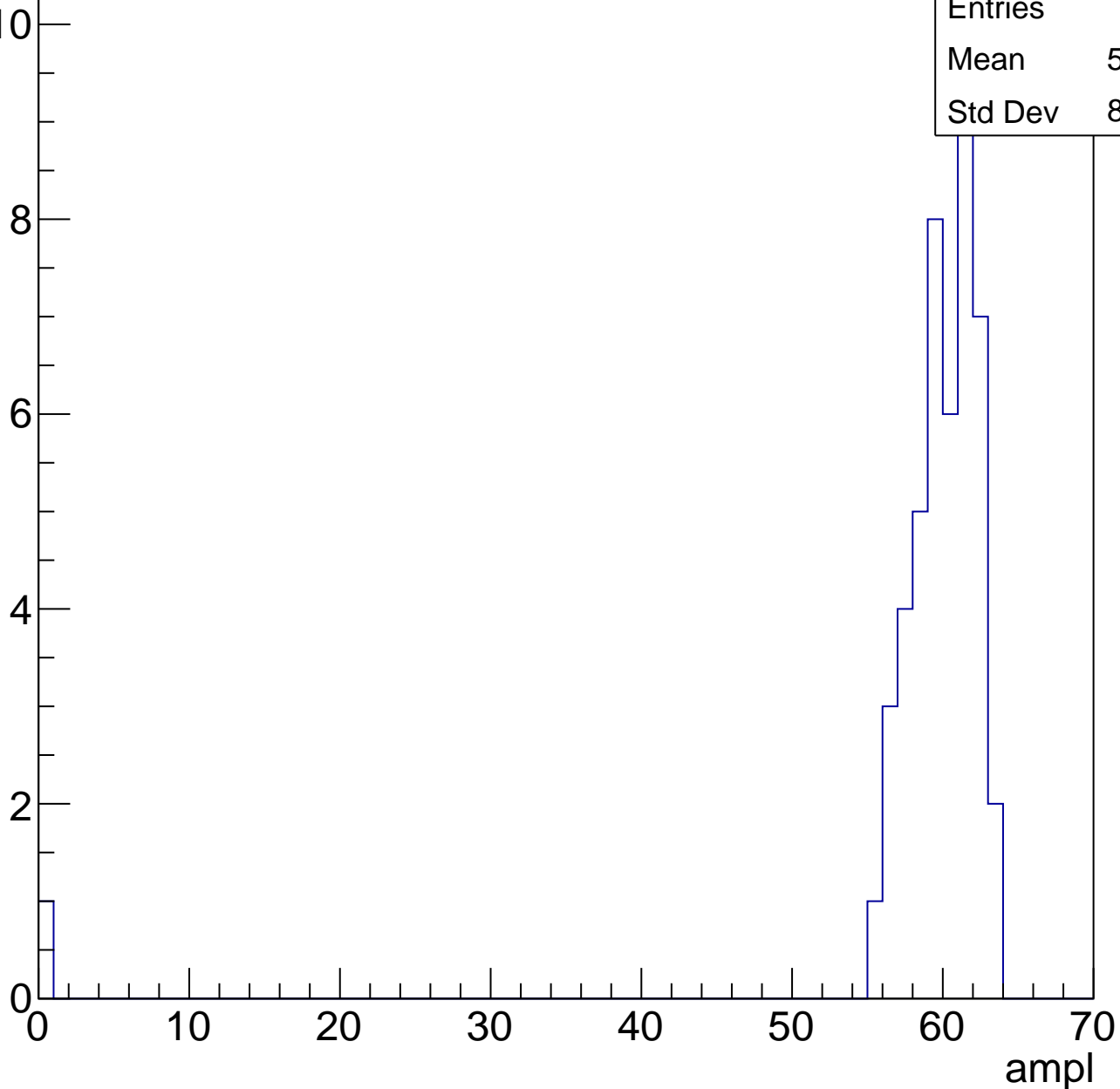


# B1L002S, U26-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	58.36
Std Dev	8.833

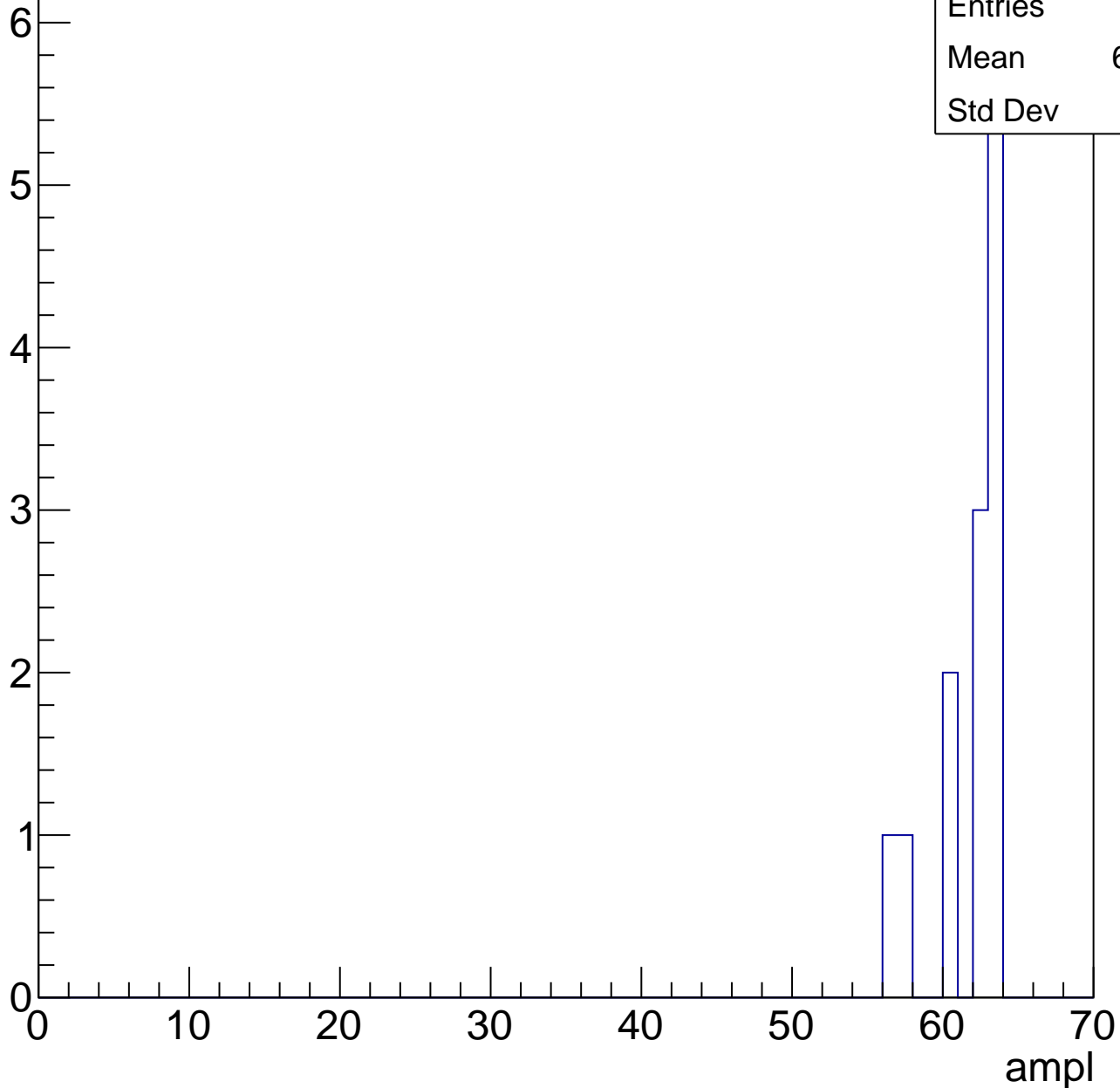


# B1L002S, U26-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.31
Std Dev	2.3





# B1L002S, U26-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch104, adc0

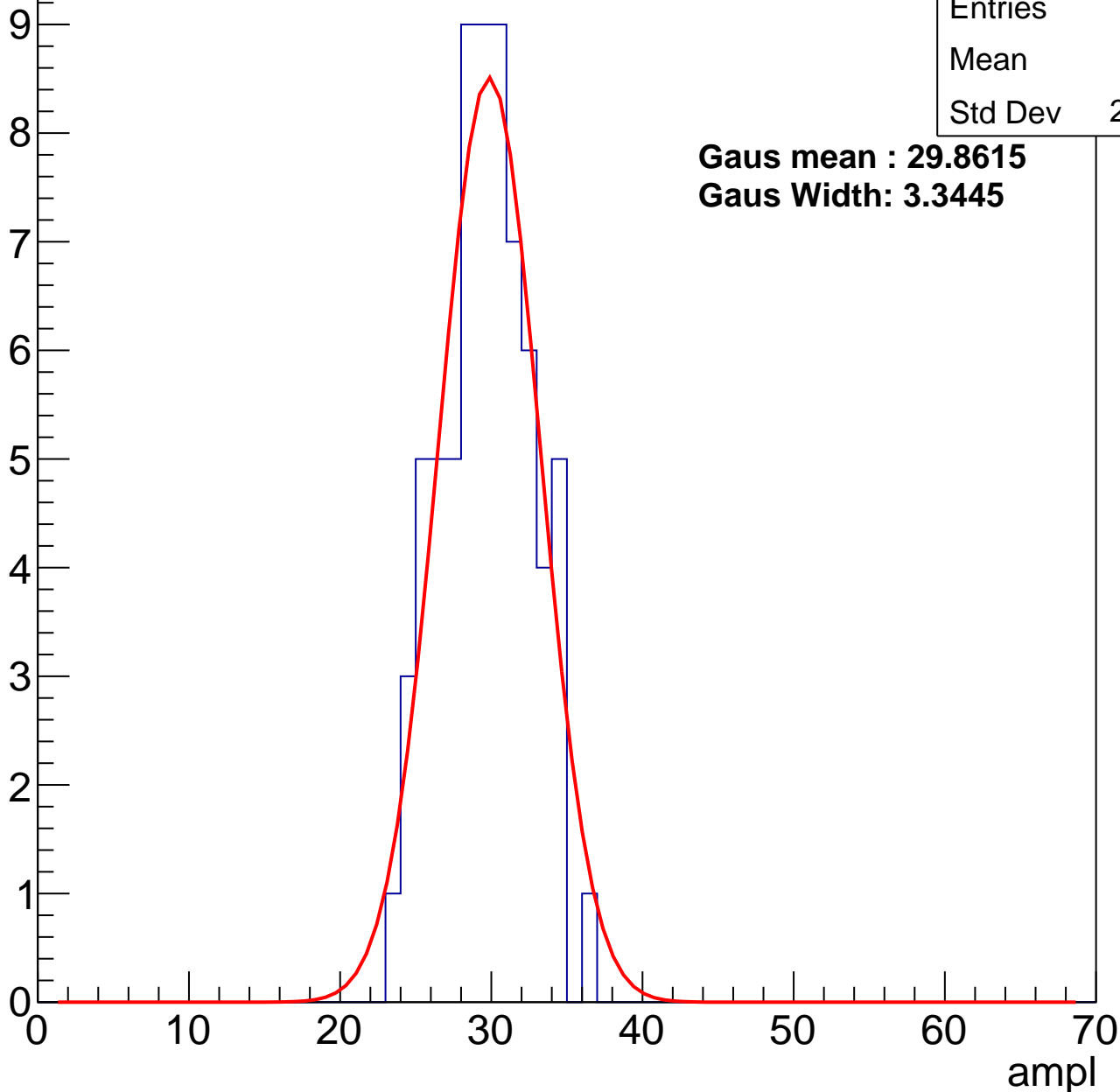
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	29.2
Std Dev	2.927

**Gaus mean : 29.8615**

**Gaus Width: 3.3445**



# B1L002S, U26-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	35.99
Std Dev	3.027

**Gaus mean : 36.3467**

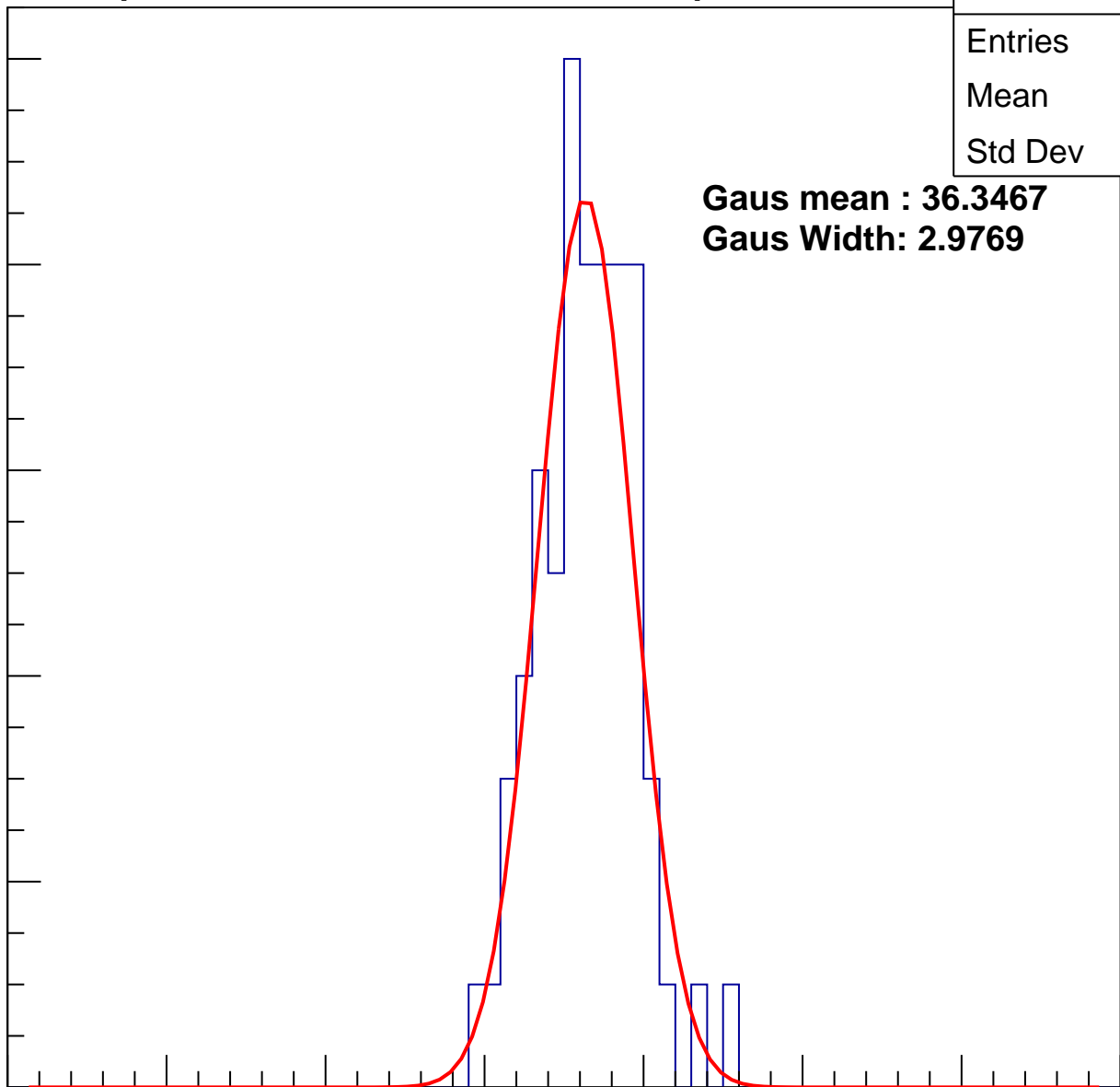
**Gaus Width: 2.9769**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U26-ch104, adc2

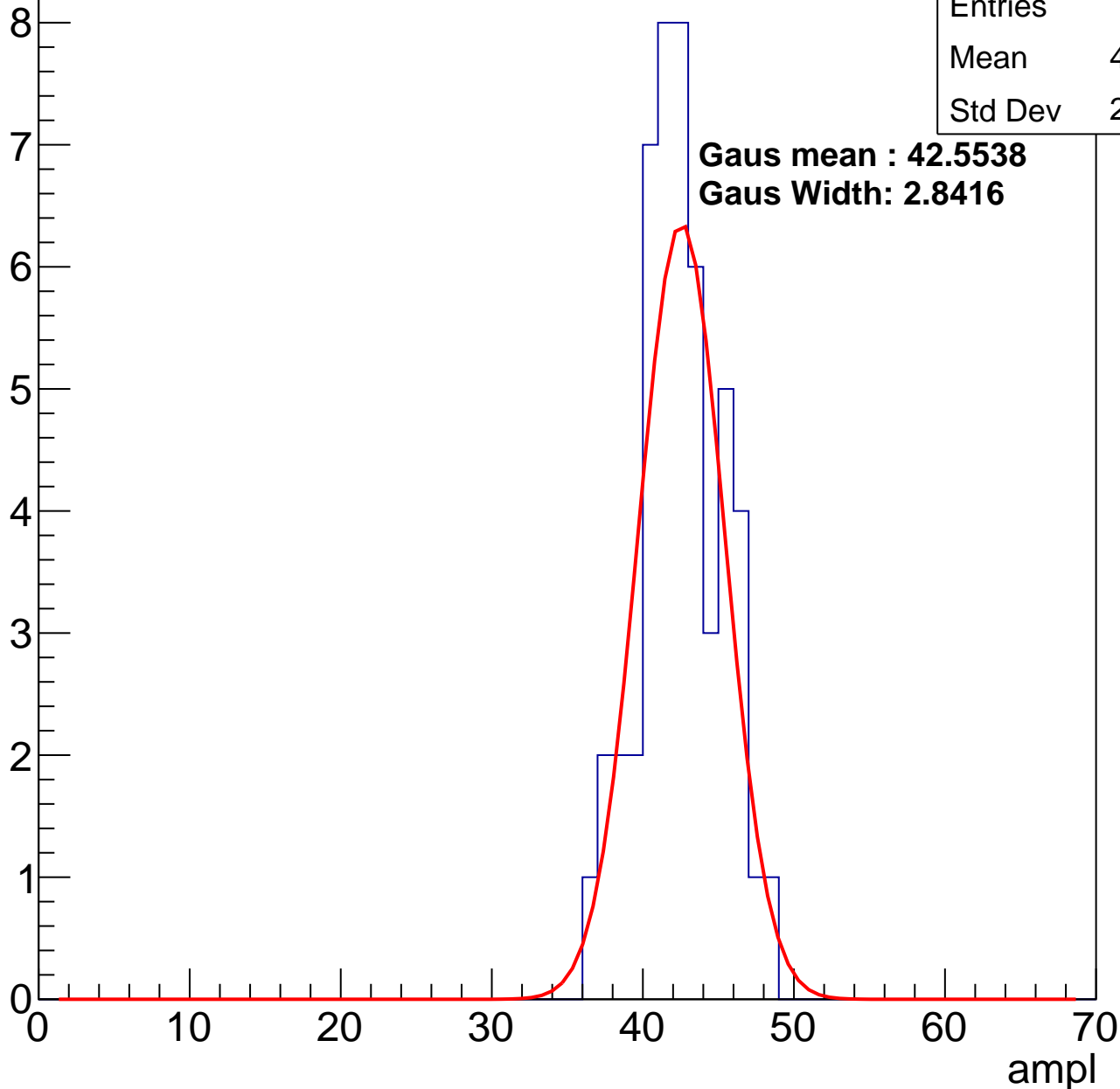
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	42.04
Std Dev	2.683

**Gaus mean : 42.5538**

**Gaus Width: 2.8416**

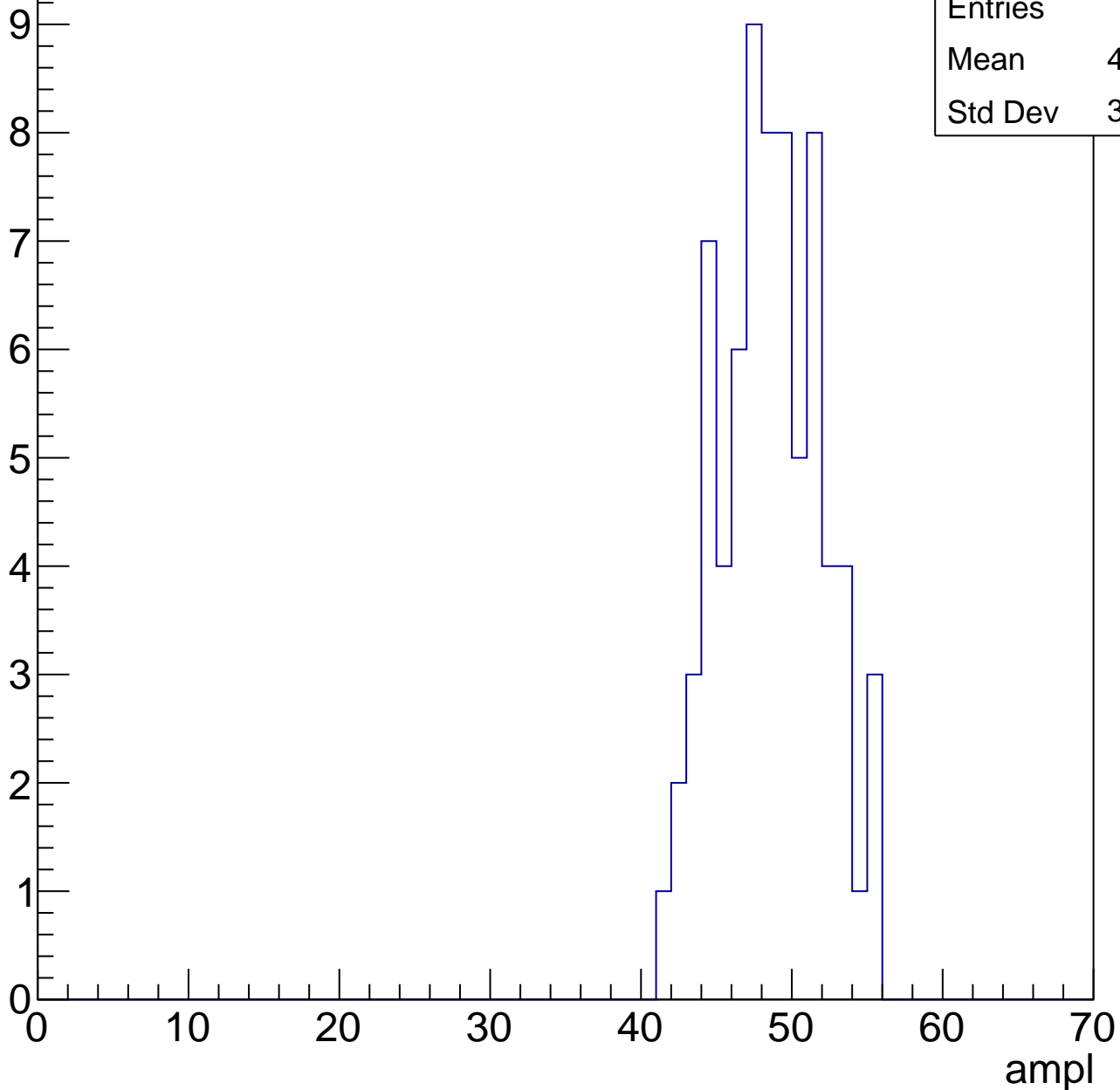


# B1L002S, U26-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	48.14
Std Dev	3.357



# B1L002S, U26-ch104, adc4

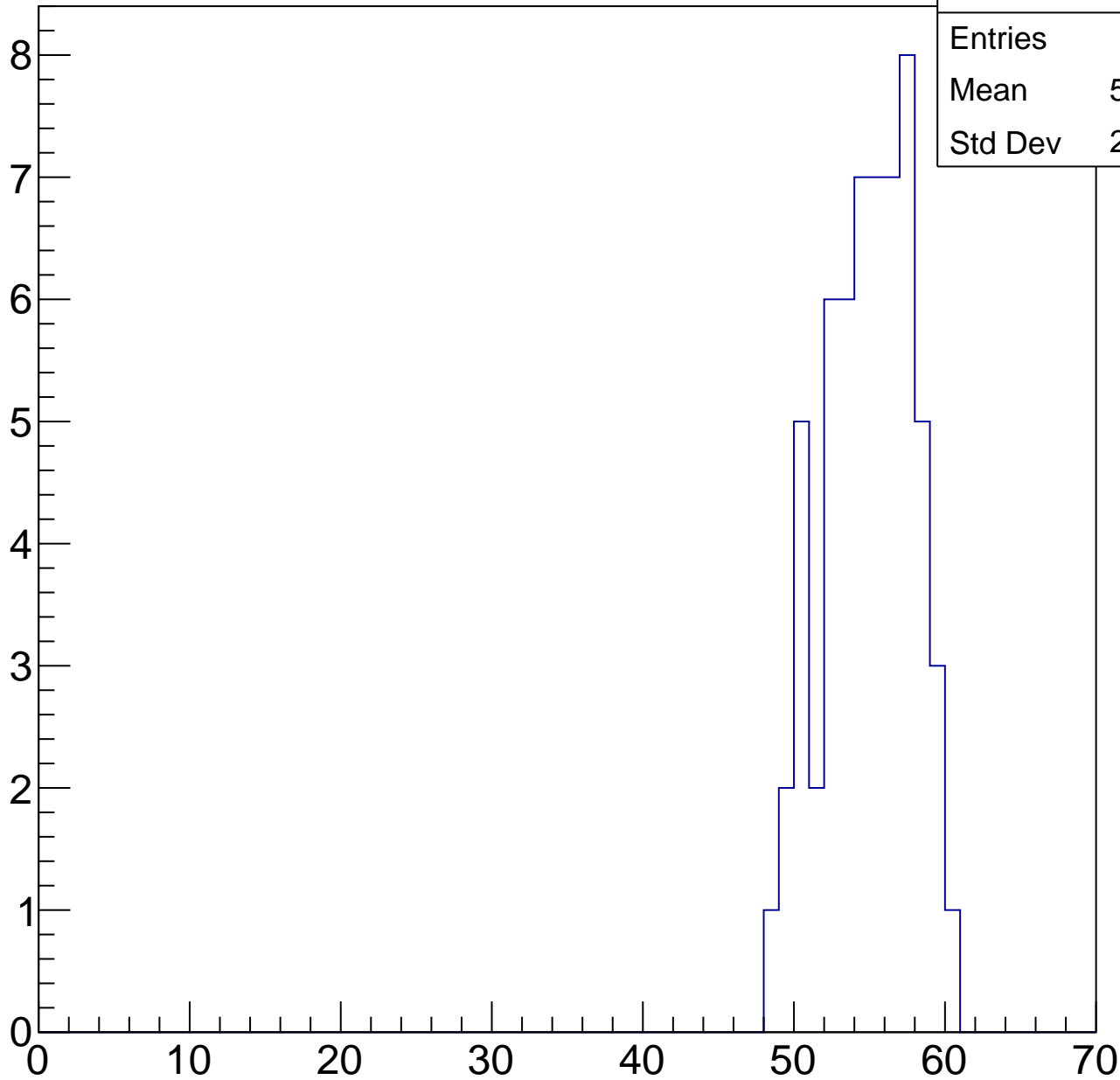
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	54.43
Std Dev	2.889

ampl

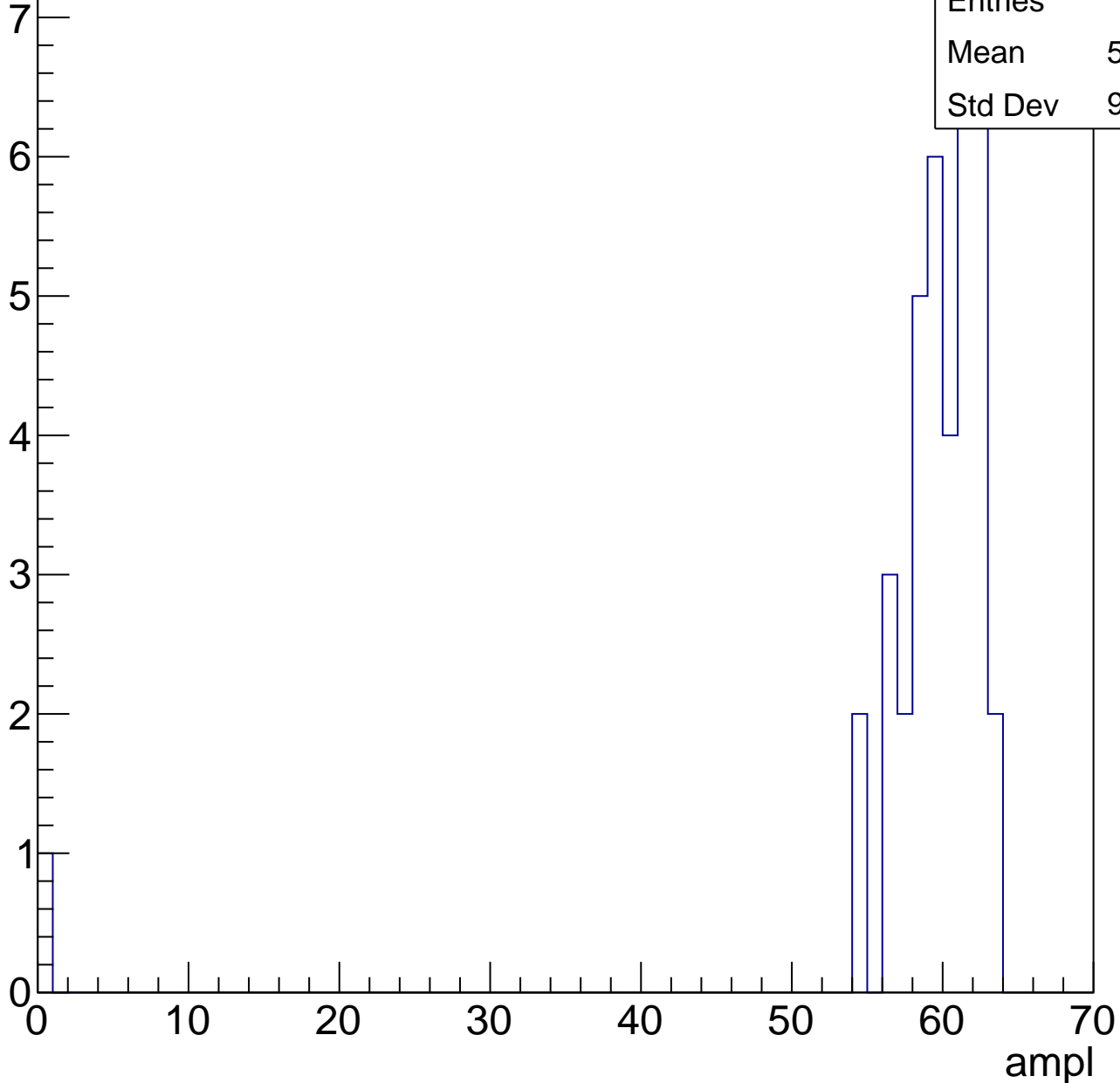


# B1L002S, U26-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	57.97
Std Dev	9.683

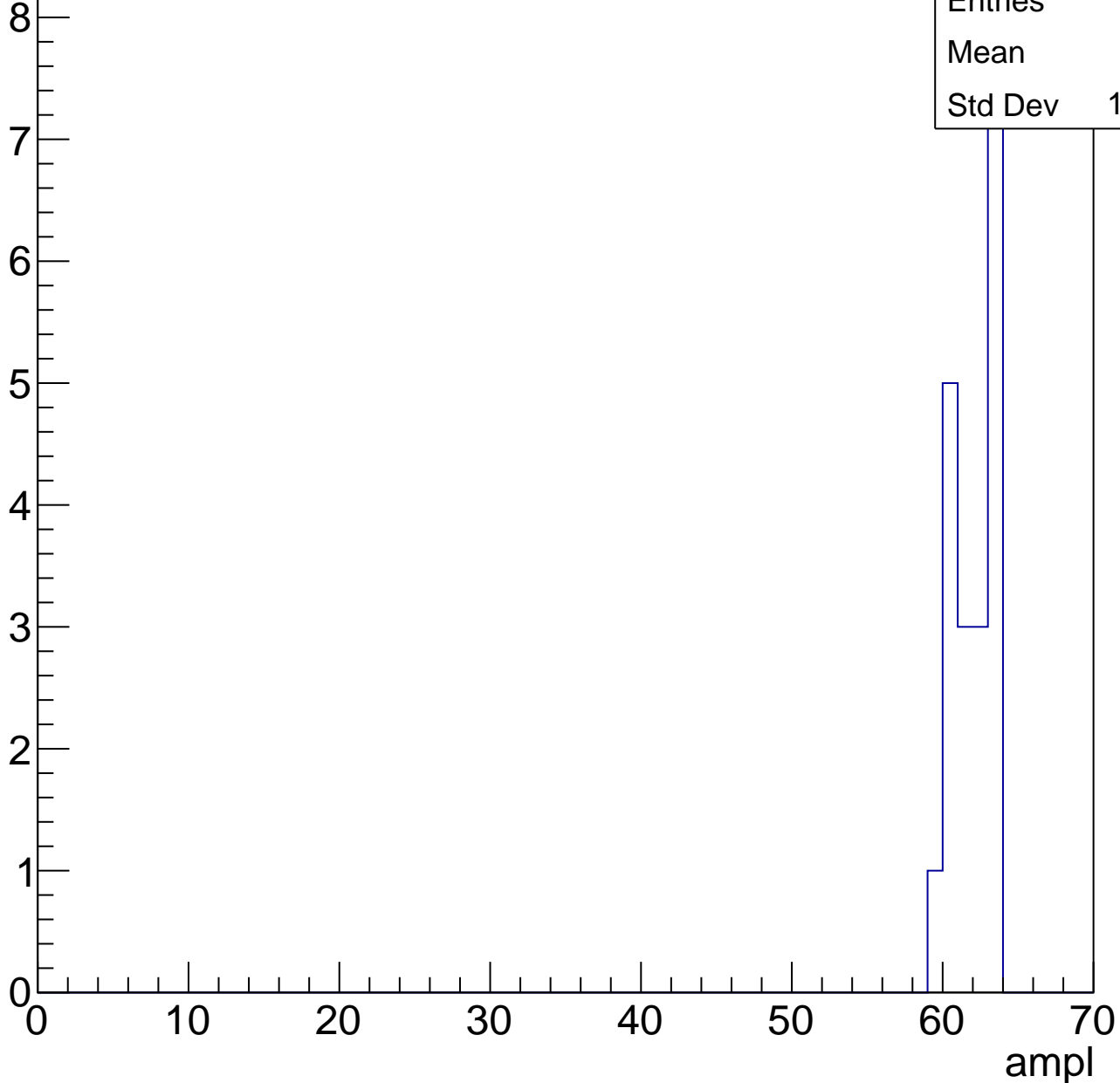


# B1L002S, U26-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.6
Std Dev	1.356





# B1L002S, U26-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	28.99
Std Dev	3.068

**Gaus mean : 29.2483**

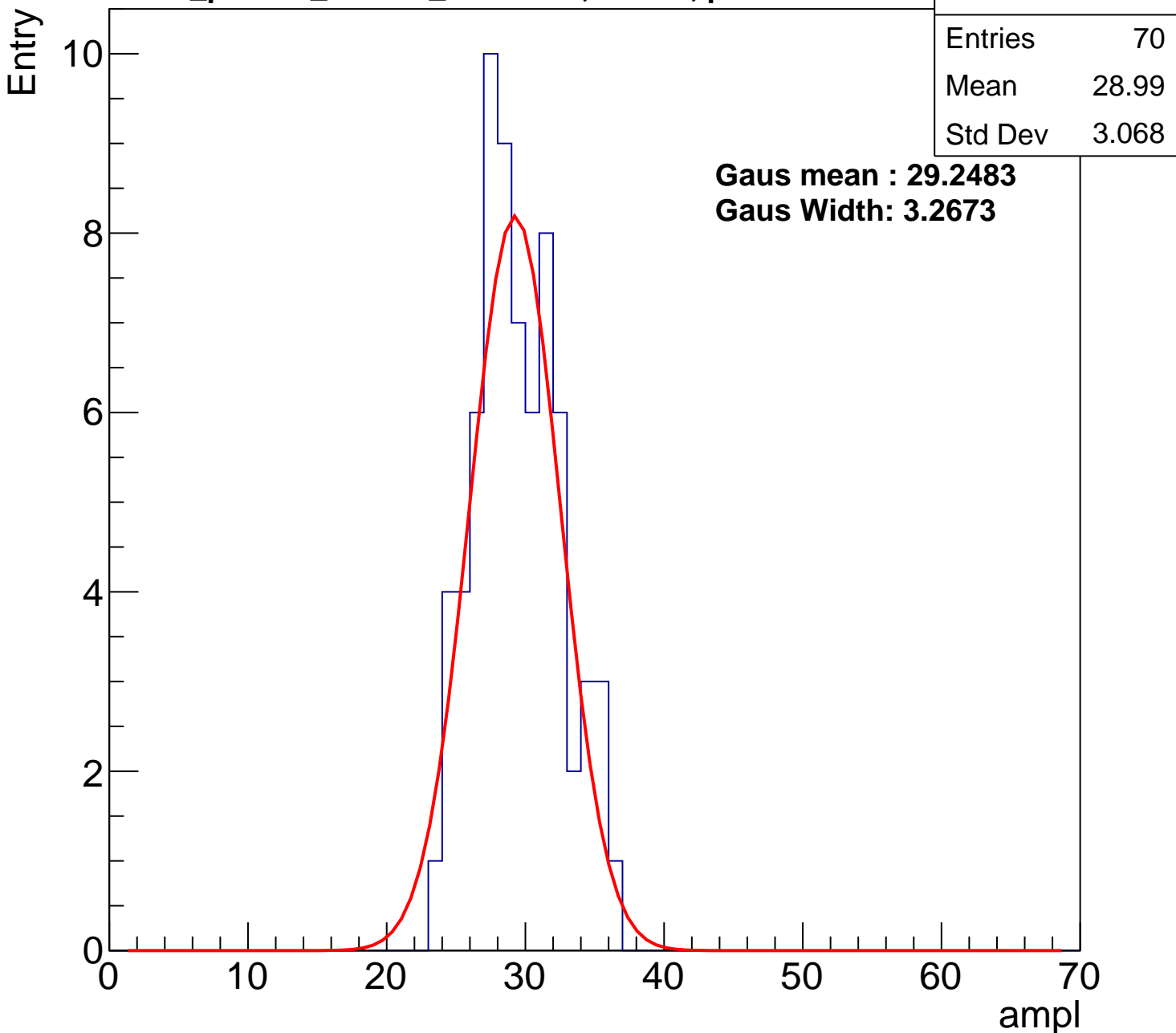
**Gaus Width: 3.2673**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch105, adc1

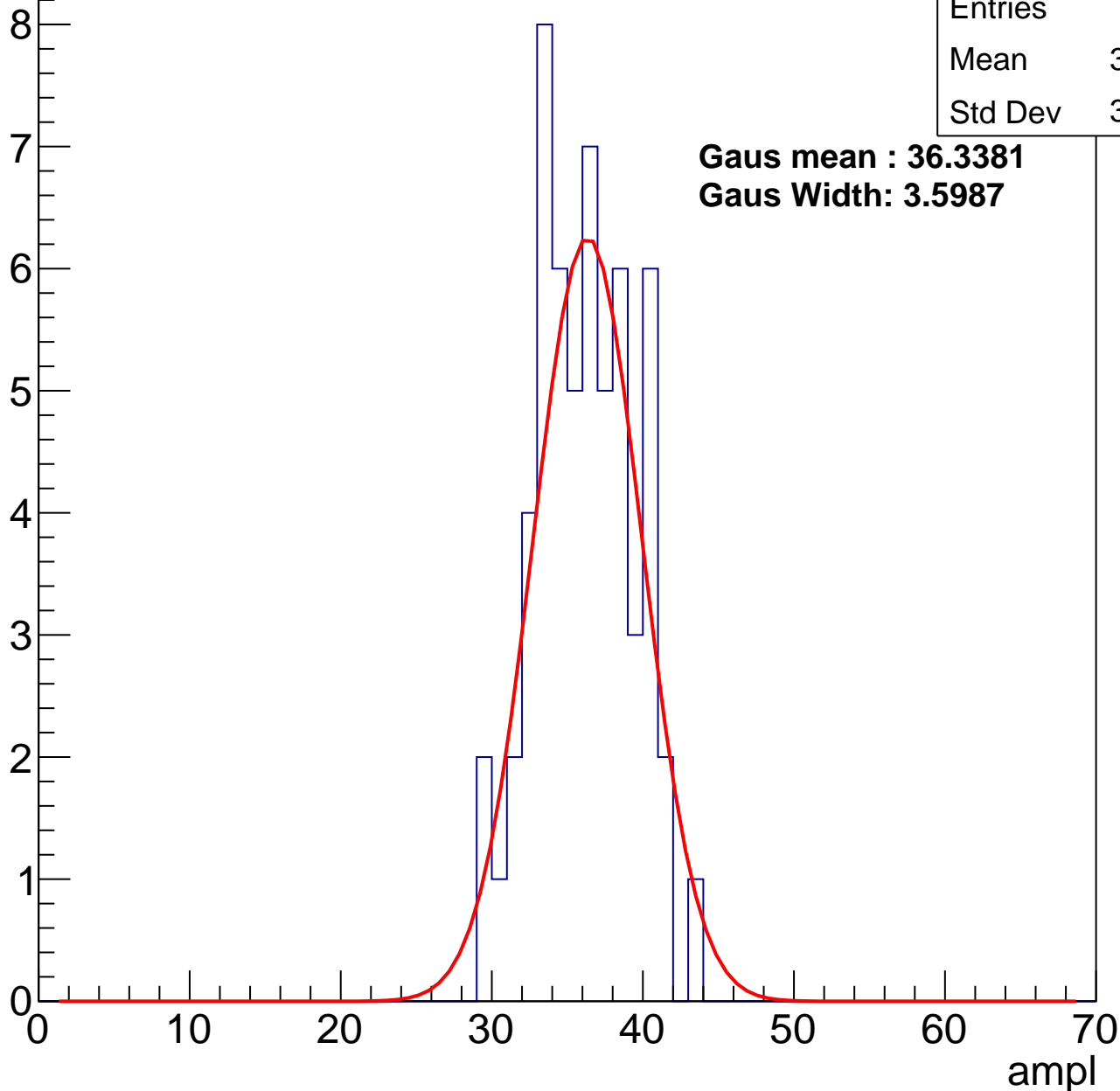
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.66
Std Dev	3.198

**Gaus mean : 36.3381**

**Gaus Width: 3.5987**



# B1L002S, U26-ch105, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	41.96
Std Dev	3.661

**Gaus mean : 41.9847**

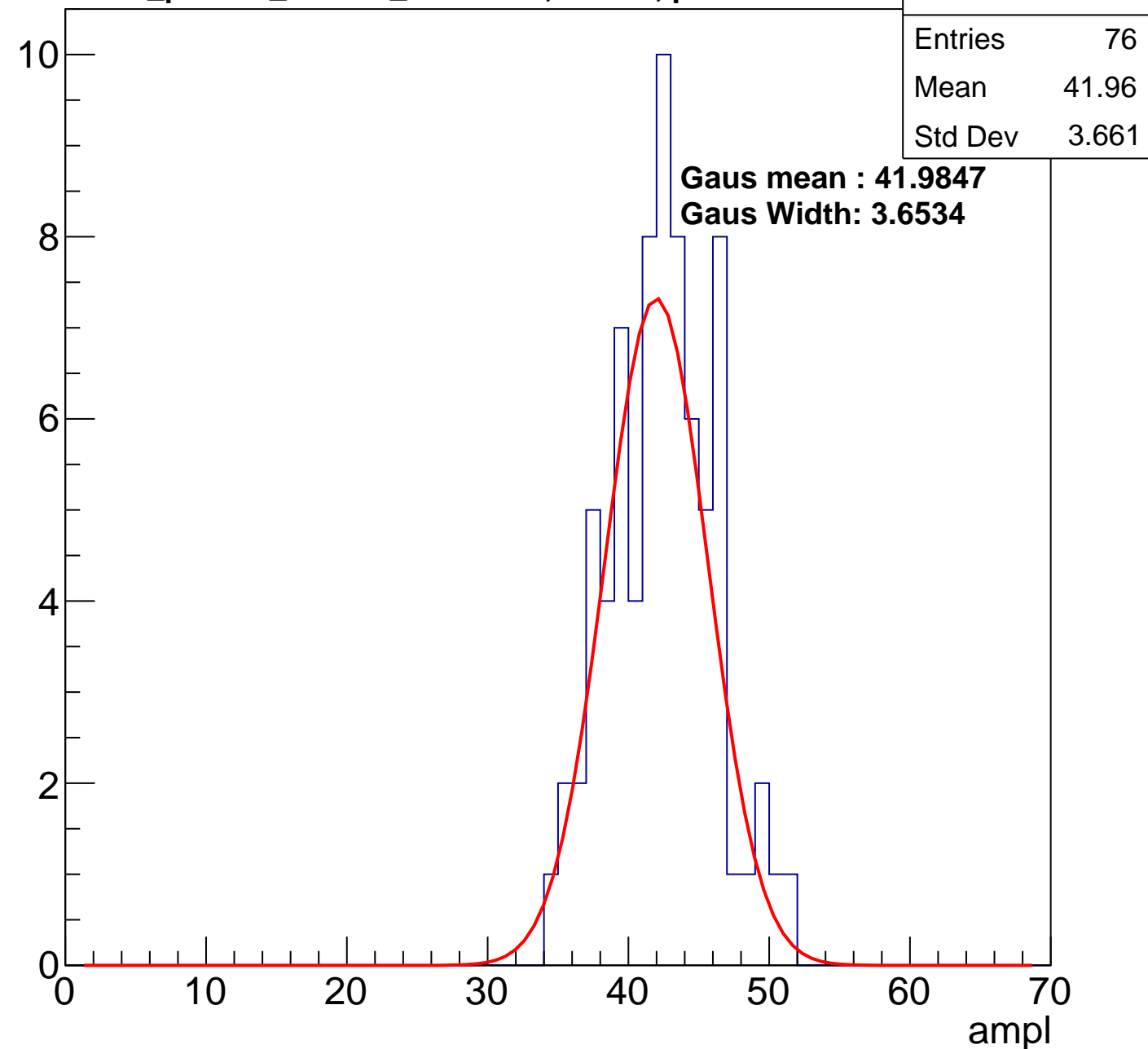
**Gaus Width: 3.6534**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

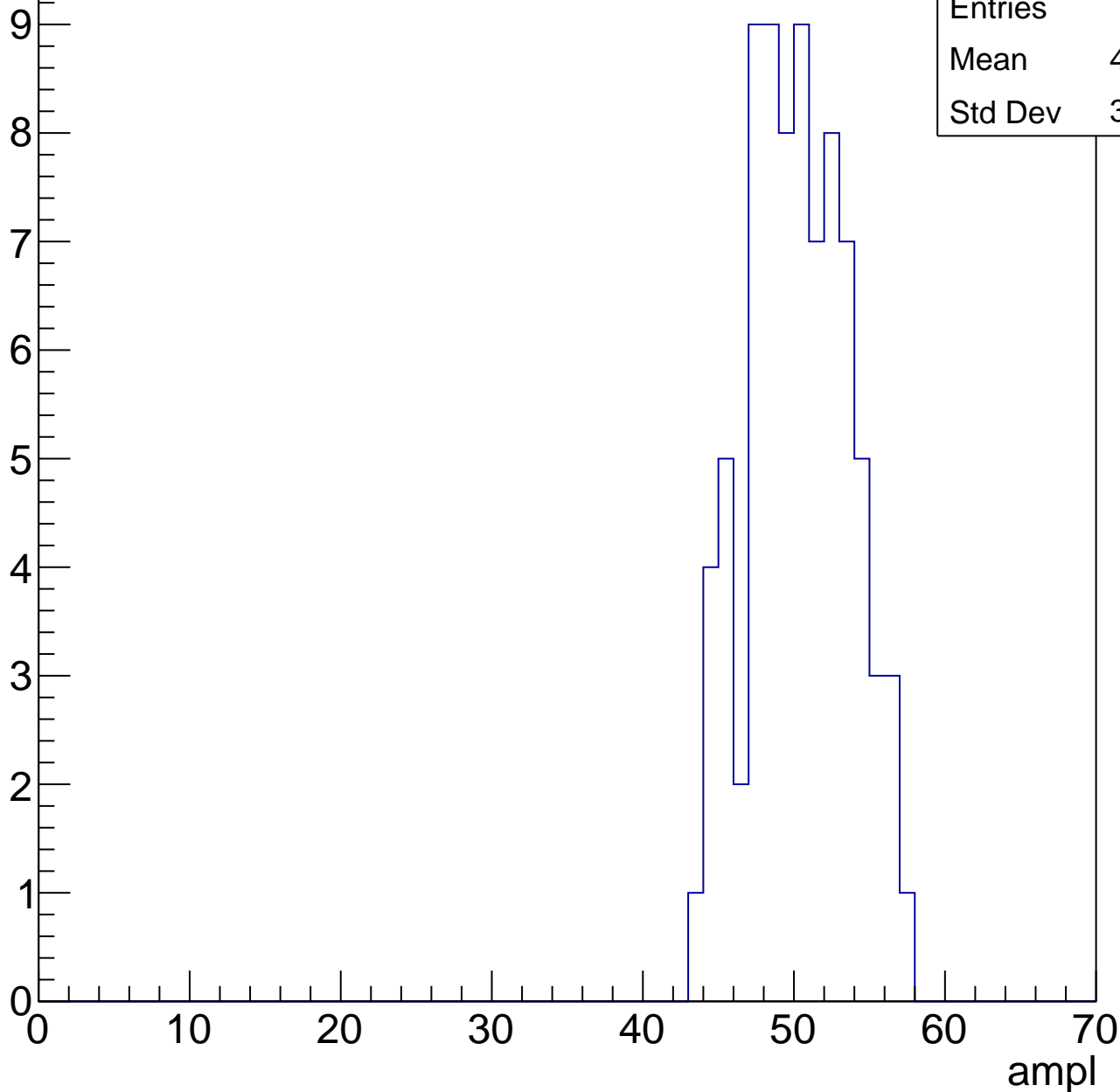


# B1L002S, U26-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	49.84
Std Dev	3.309

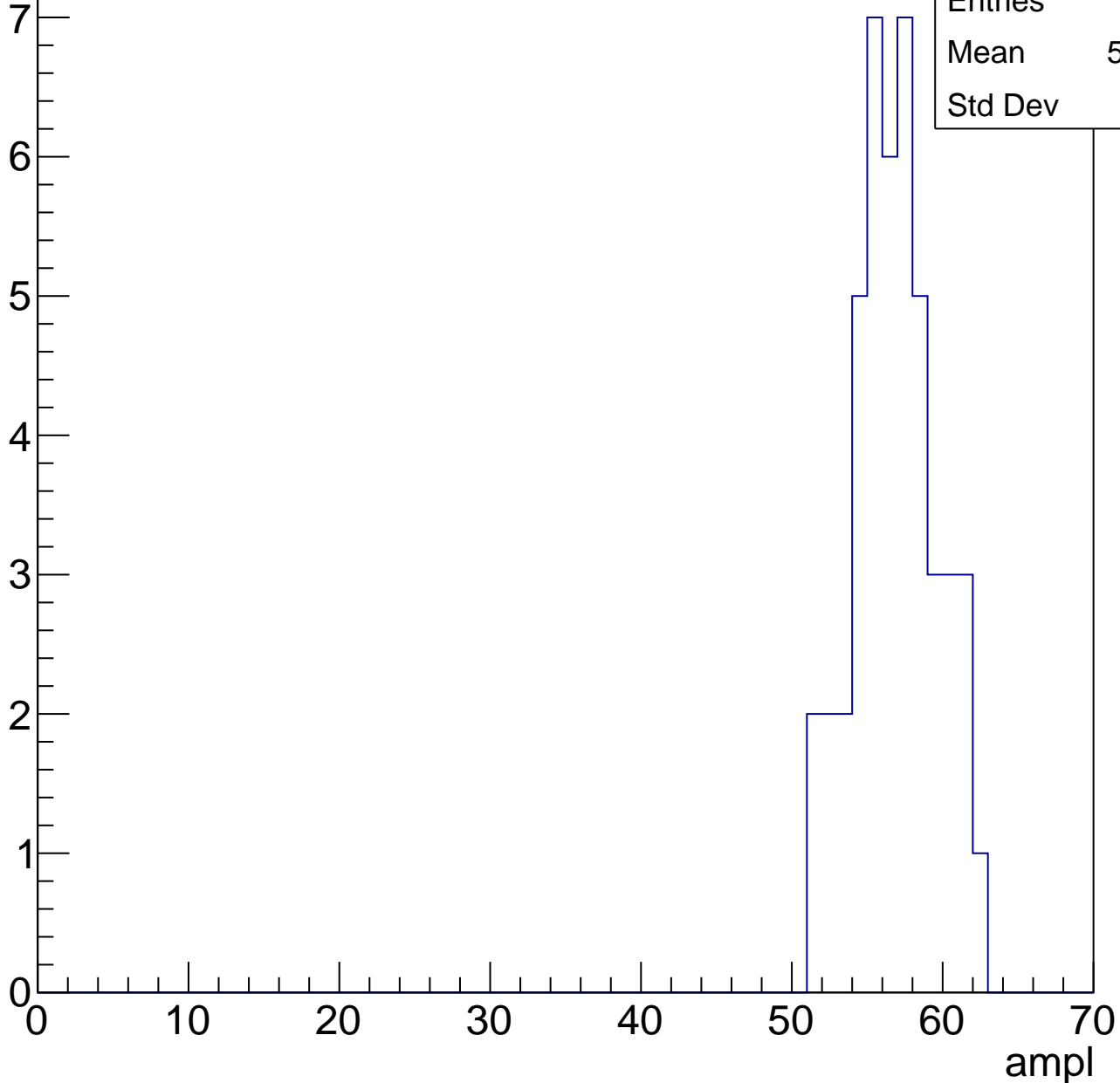


# B1L002S, U26-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	56.39
Std Dev	2.69

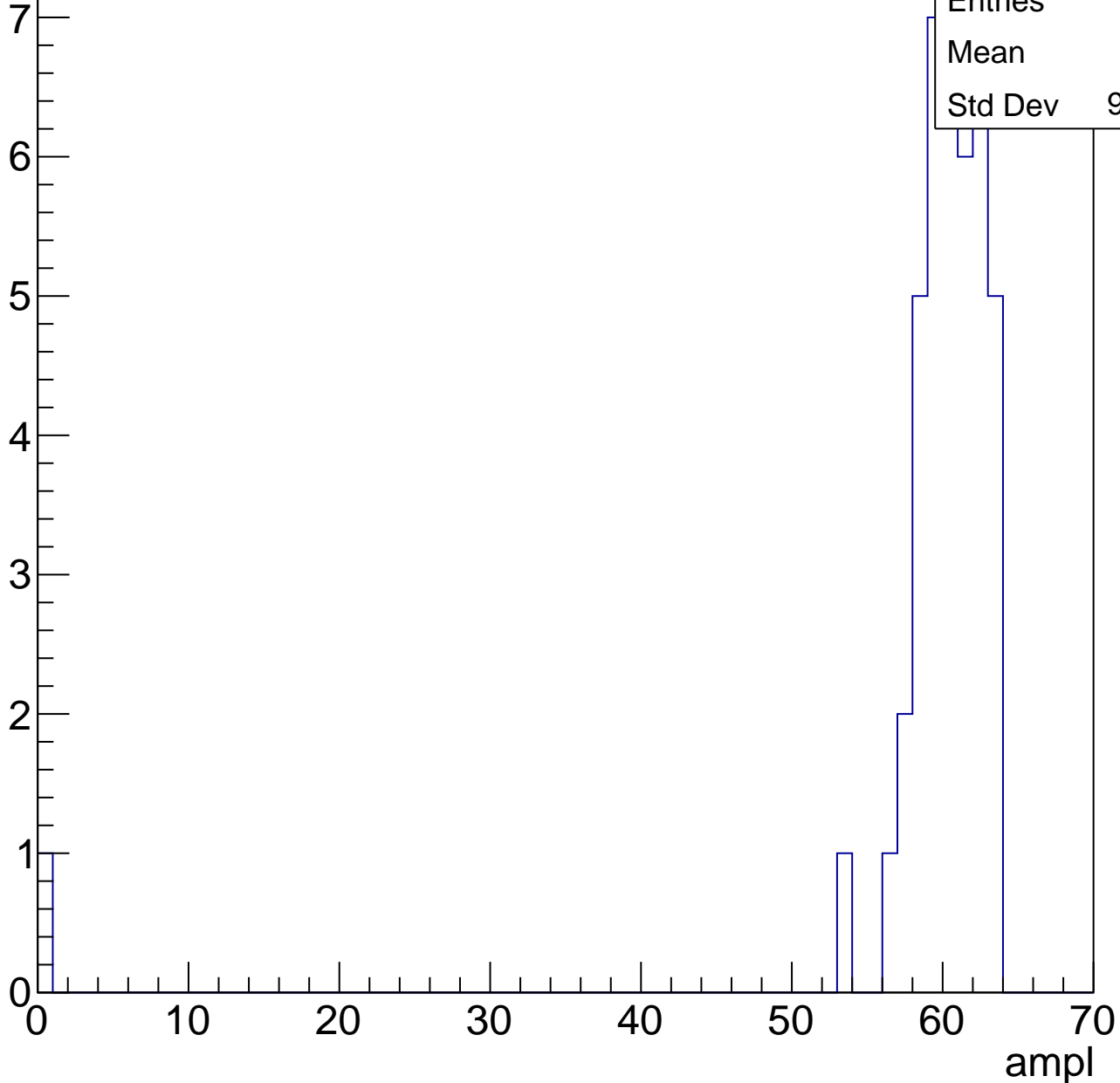


# B1L002S, U26-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	58.6
Std Dev	9.394

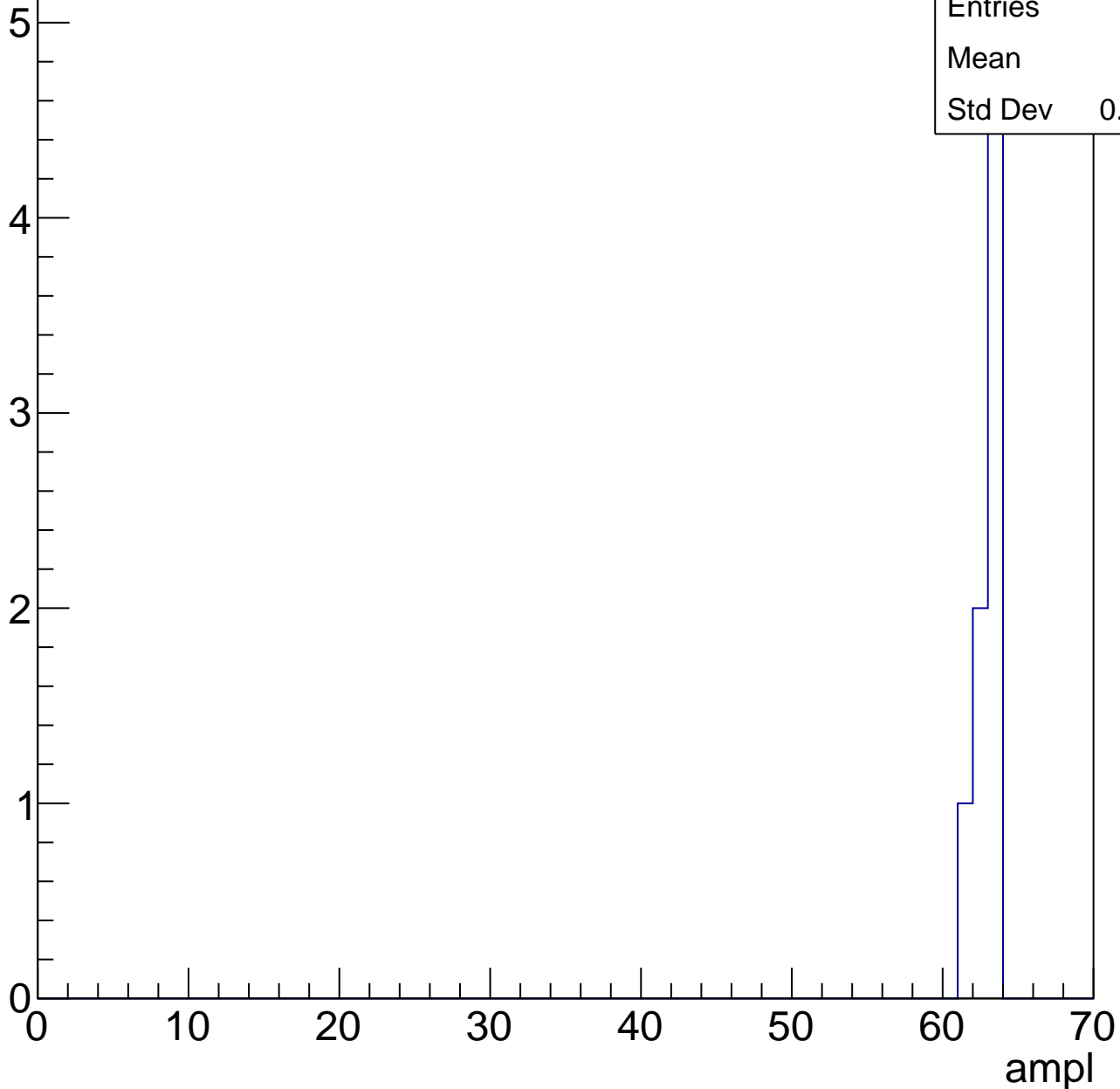


# B1L002S, U26-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	8
Mean	62.5
Std Dev	0.7071





# B1L002S, U26-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch106, adc0

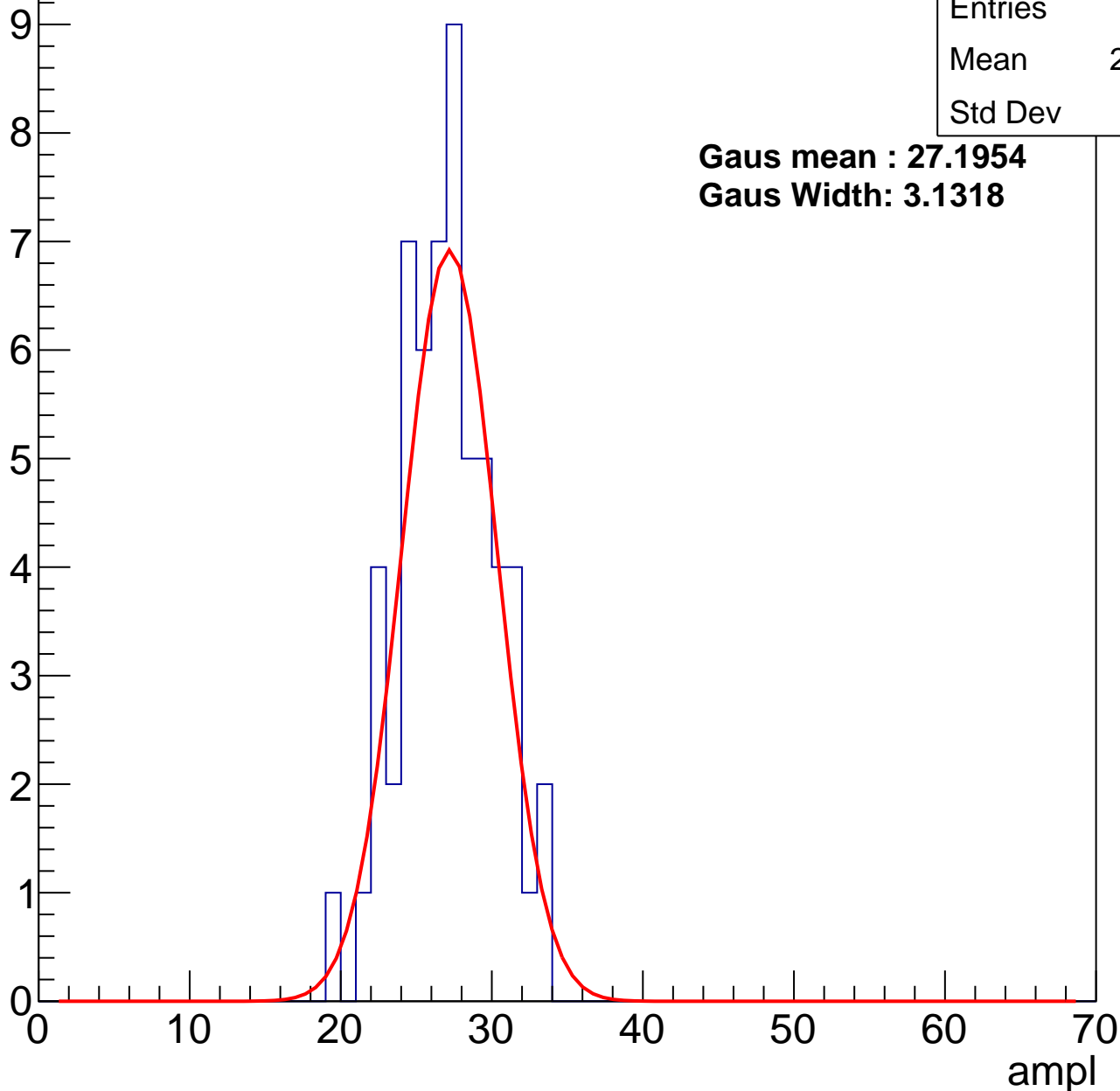
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	26.62
Std Dev	3.05

**Gaus mean : 27.1954**

**Gaus Width: 3.1318**



# B1L002S, U26-ch106, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	33.03
Std Dev	3.259

**Gaus mean : 33.8798**

**Gaus Width: 3.6061**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B1L002S, U26-ch106, adc2

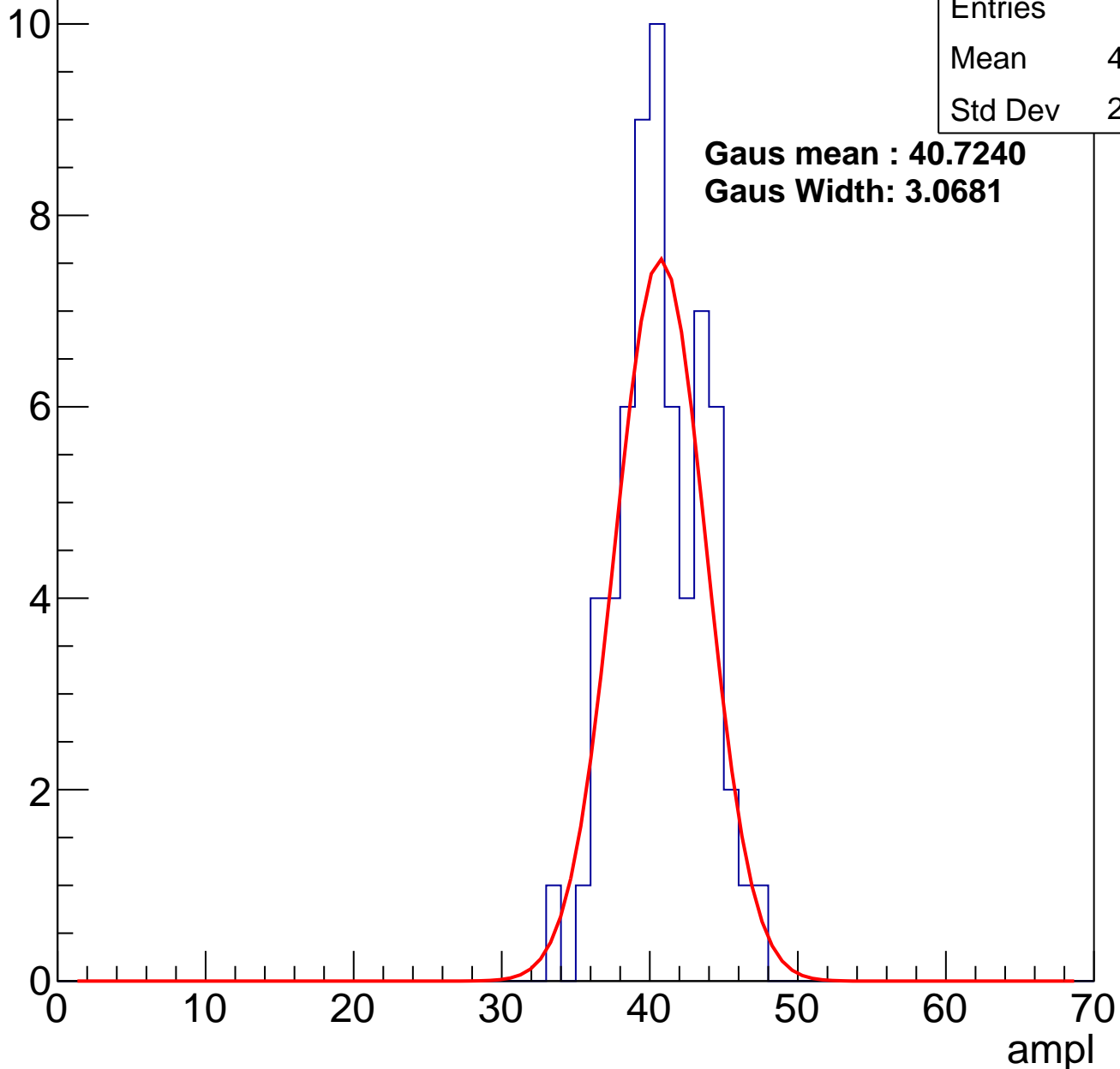
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	40.34
Std Dev	2.885

**Gaus mean : 40.7240**

**Gaus Width: 3.0681**

Entry

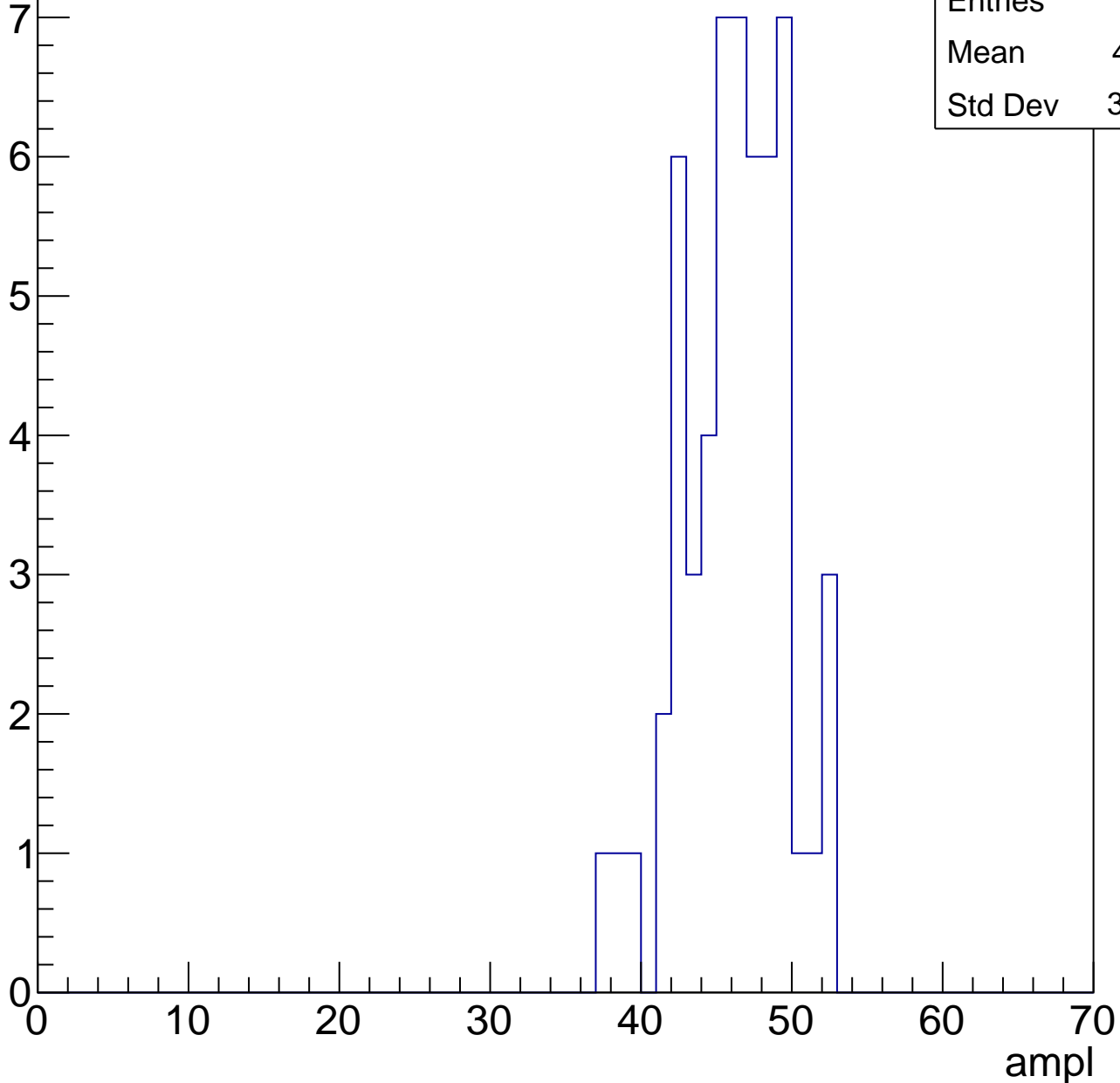


# B1L002S, U26-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	45.71
Std Dev	3.347

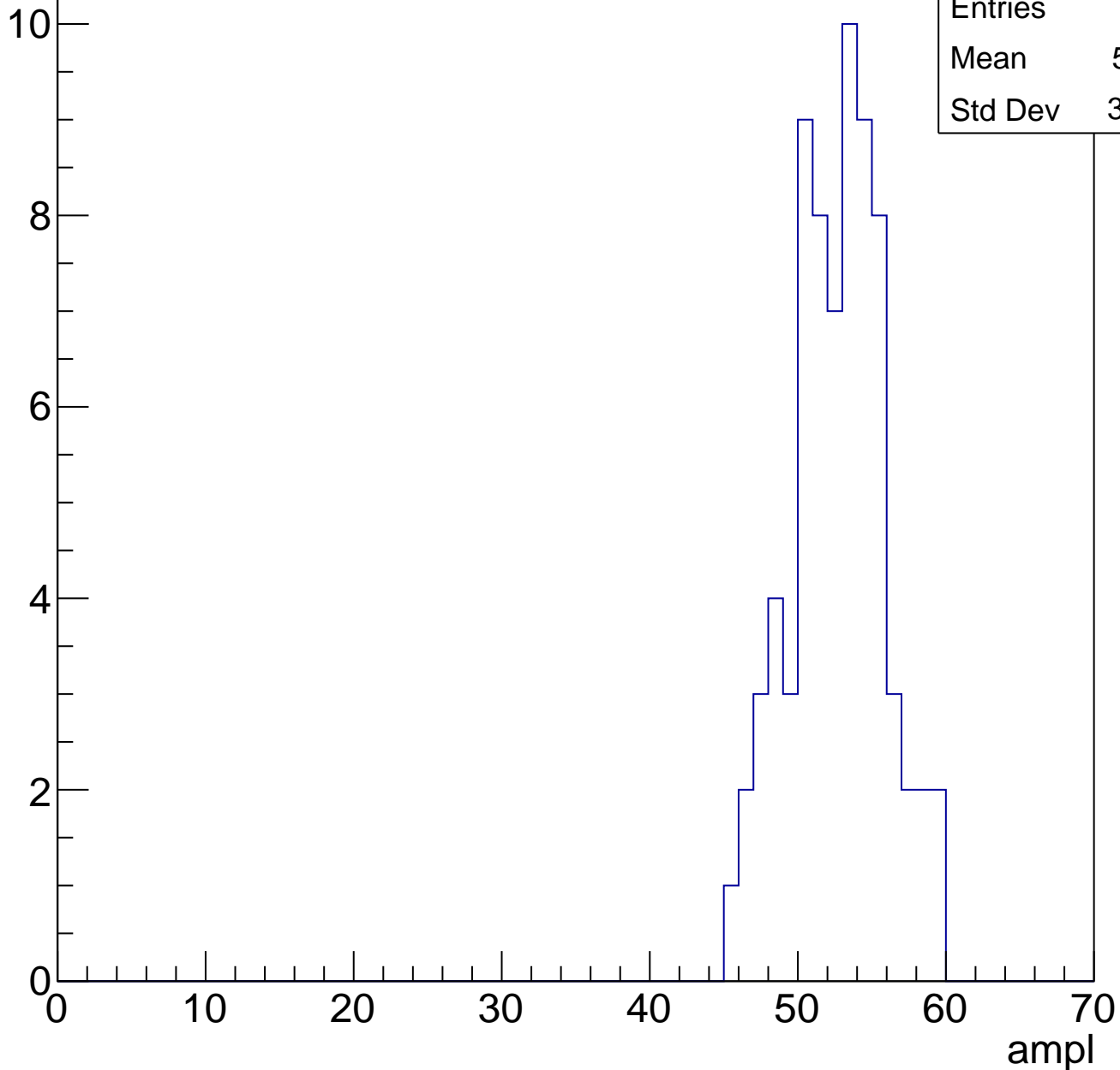


# B1L002S, U26-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	52.21
Std Dev	3.127

Entry

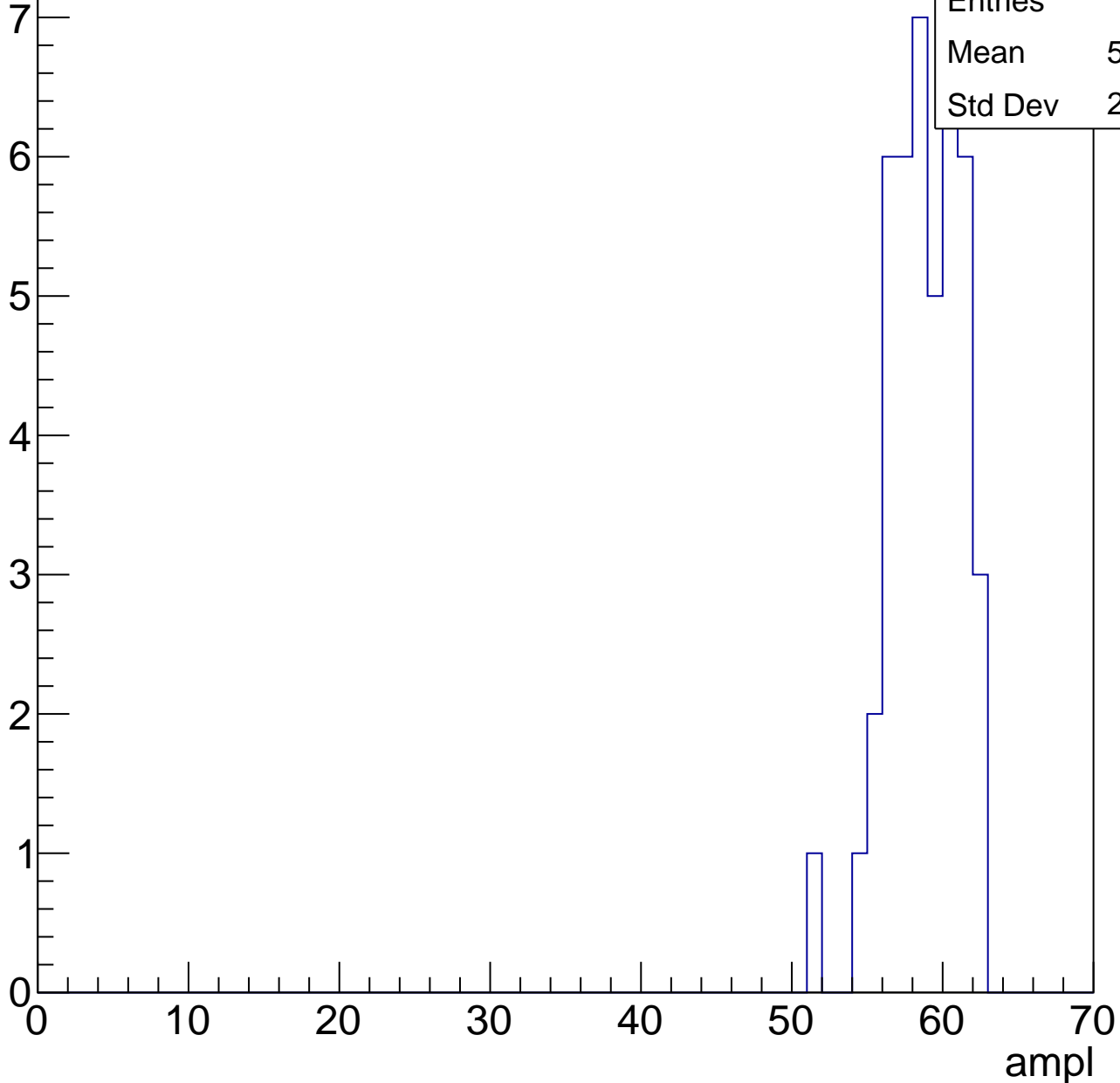


# B1L002S, U26-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.32
Std Dev	2.353

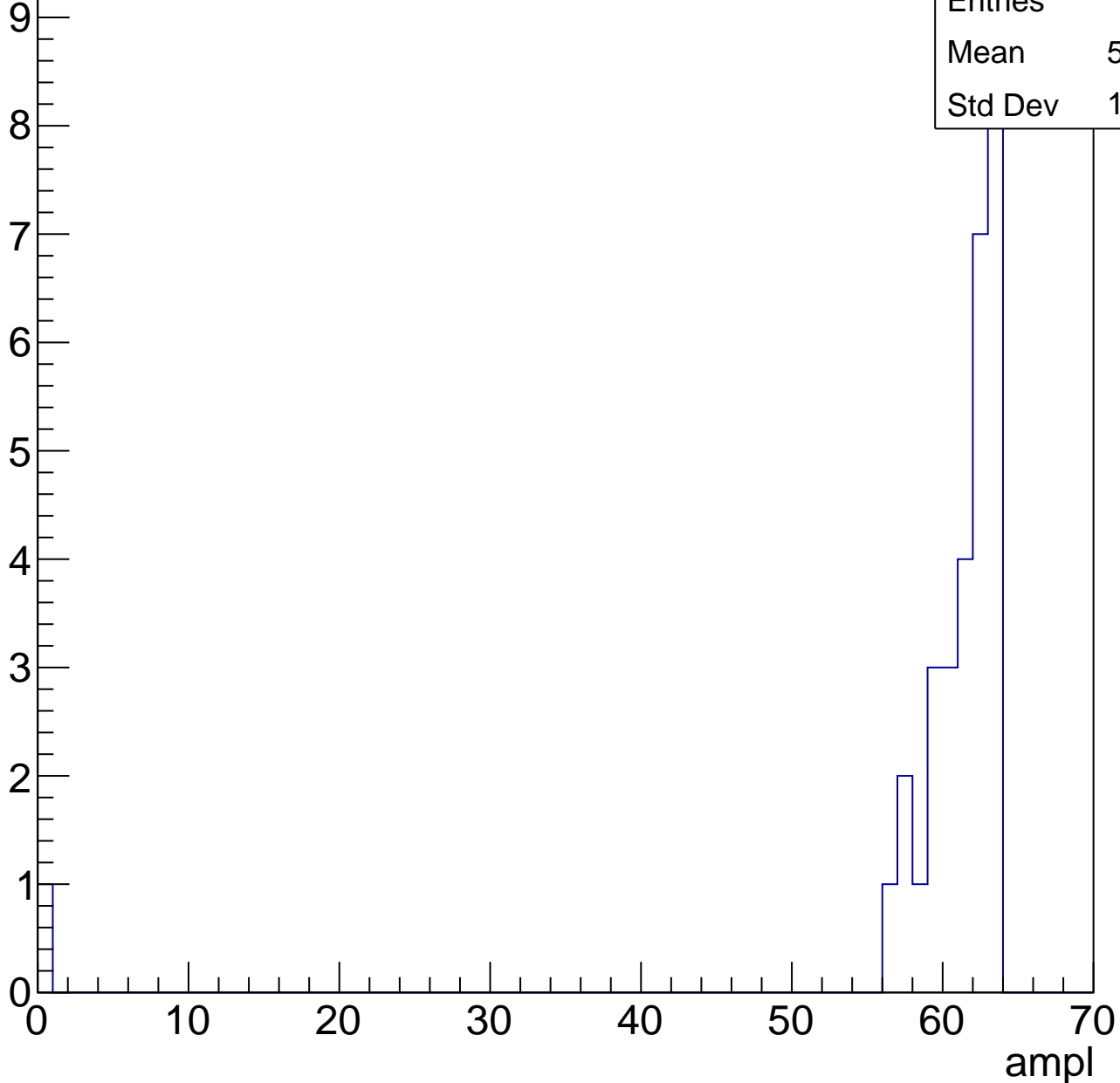


# B1L002S, U26-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	31
Mean	59.03
Std Dev	10.96

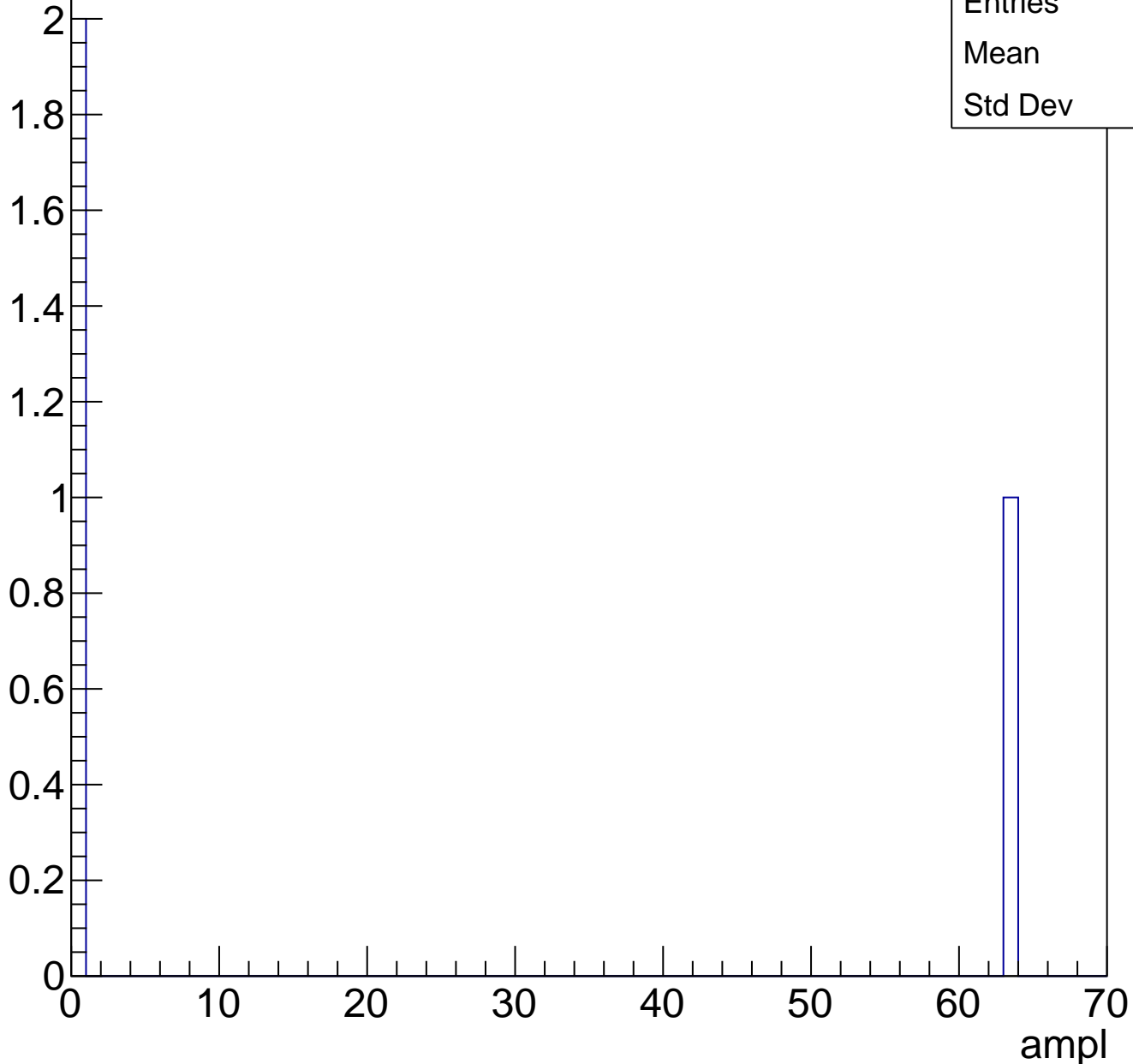




# B1L002S, U26-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U26-ch107, adc0

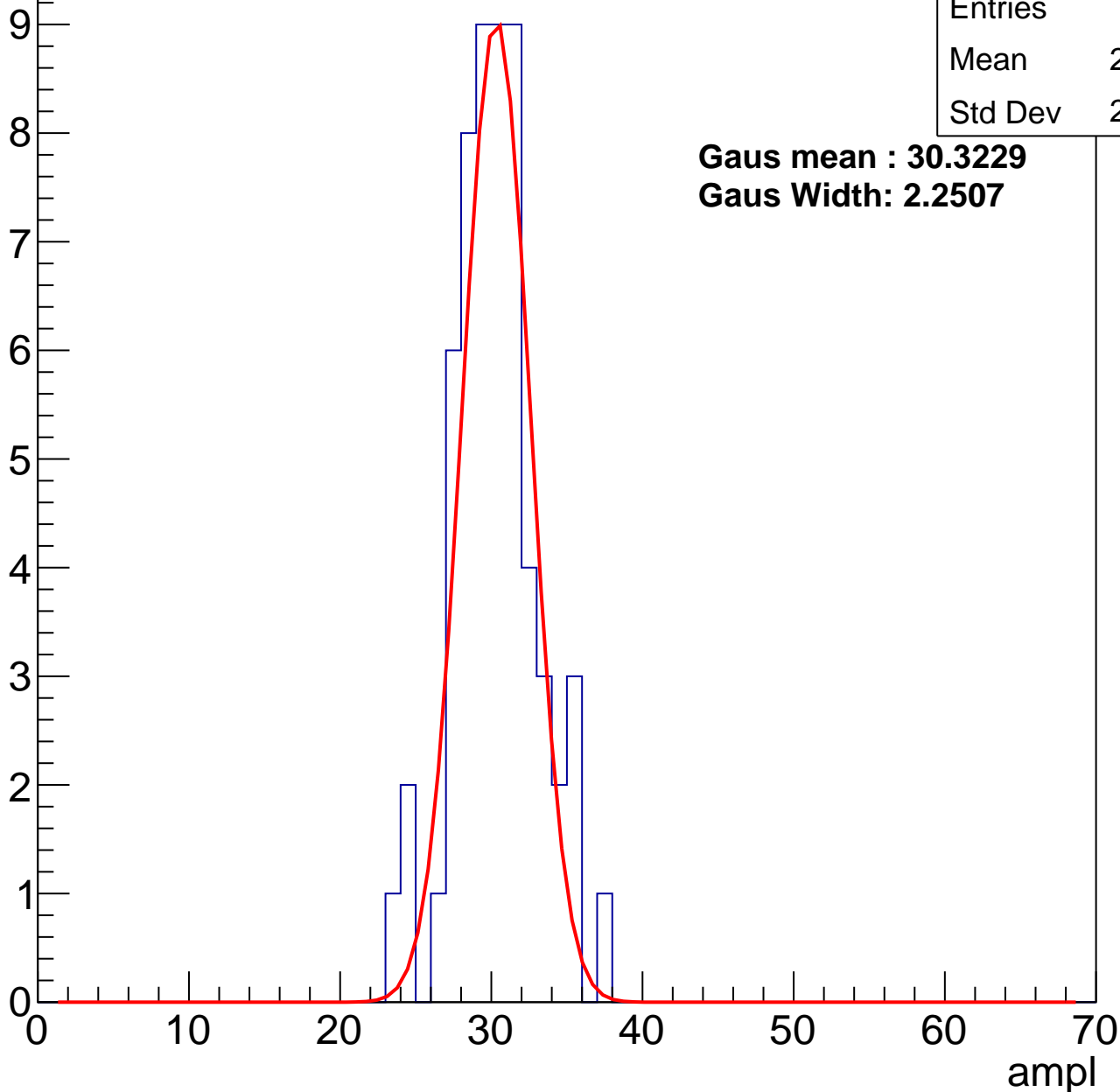
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	29.83
Std Dev	2.749

**Gaus mean : 30.3229**

**Gaus Width: 2.2507**



# B1L002S, U26-ch107, adc1

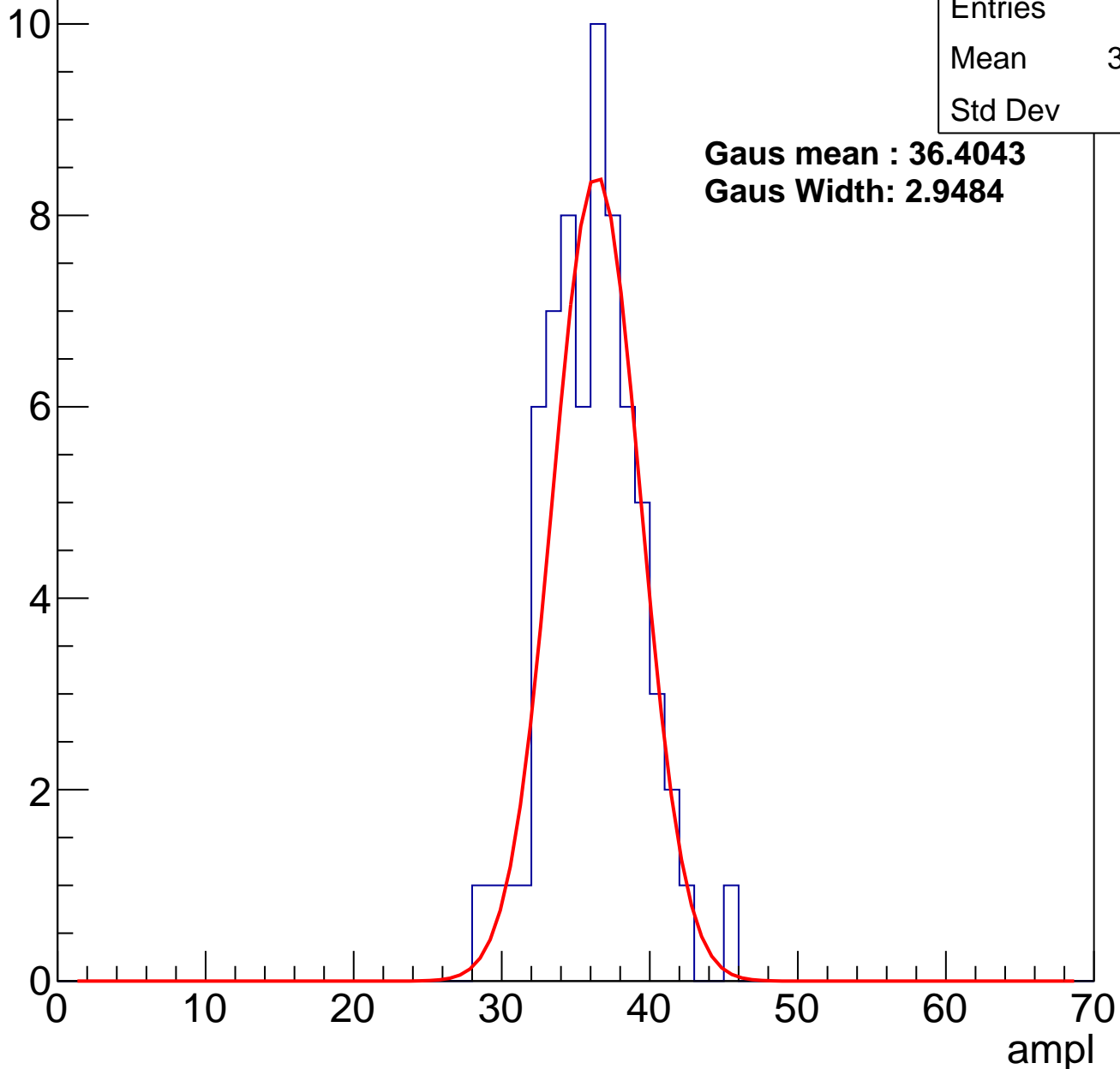
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	35.69
Std Dev	3.13

**Gaus mean : 36.4043**

**Gaus Width: 2.9484**

Entry



# B1L002S, U26-ch107, adc2

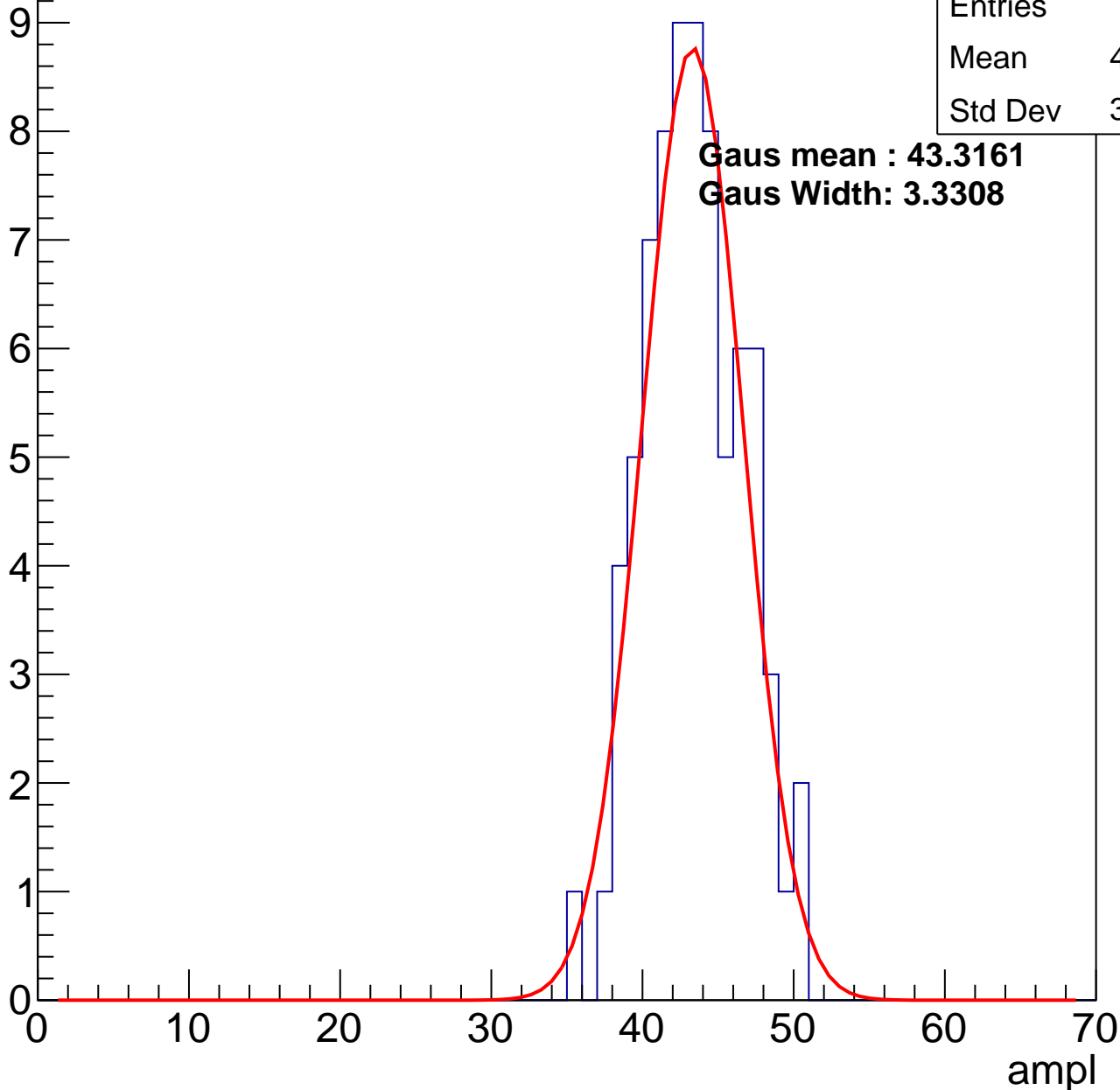
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	42.93
Std Dev	3.206

**Gaus mean : 43.3161**

**Gaus Width: 3.3308**

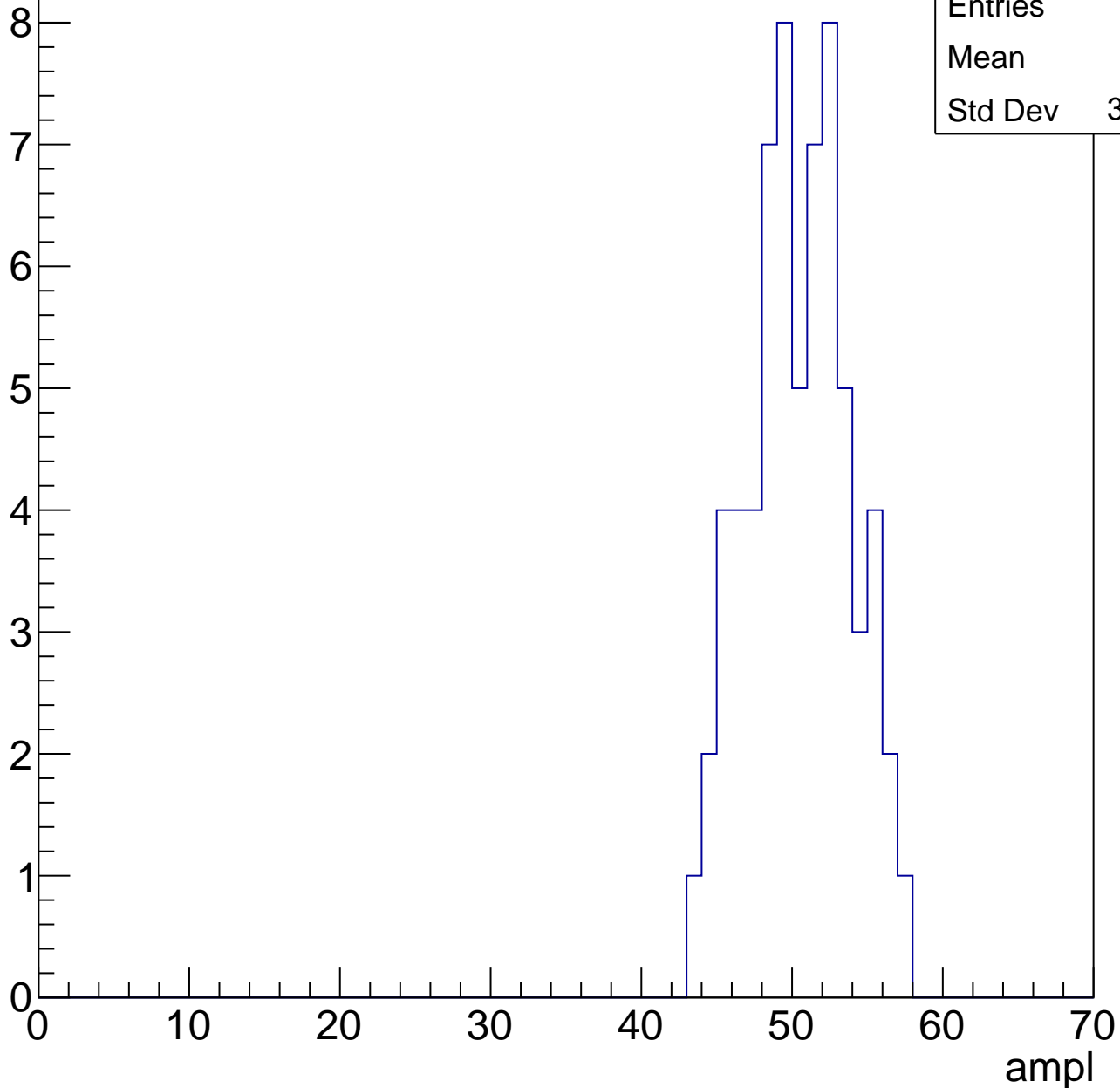


# B1L002S, U26-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	50
Std Dev	3.305

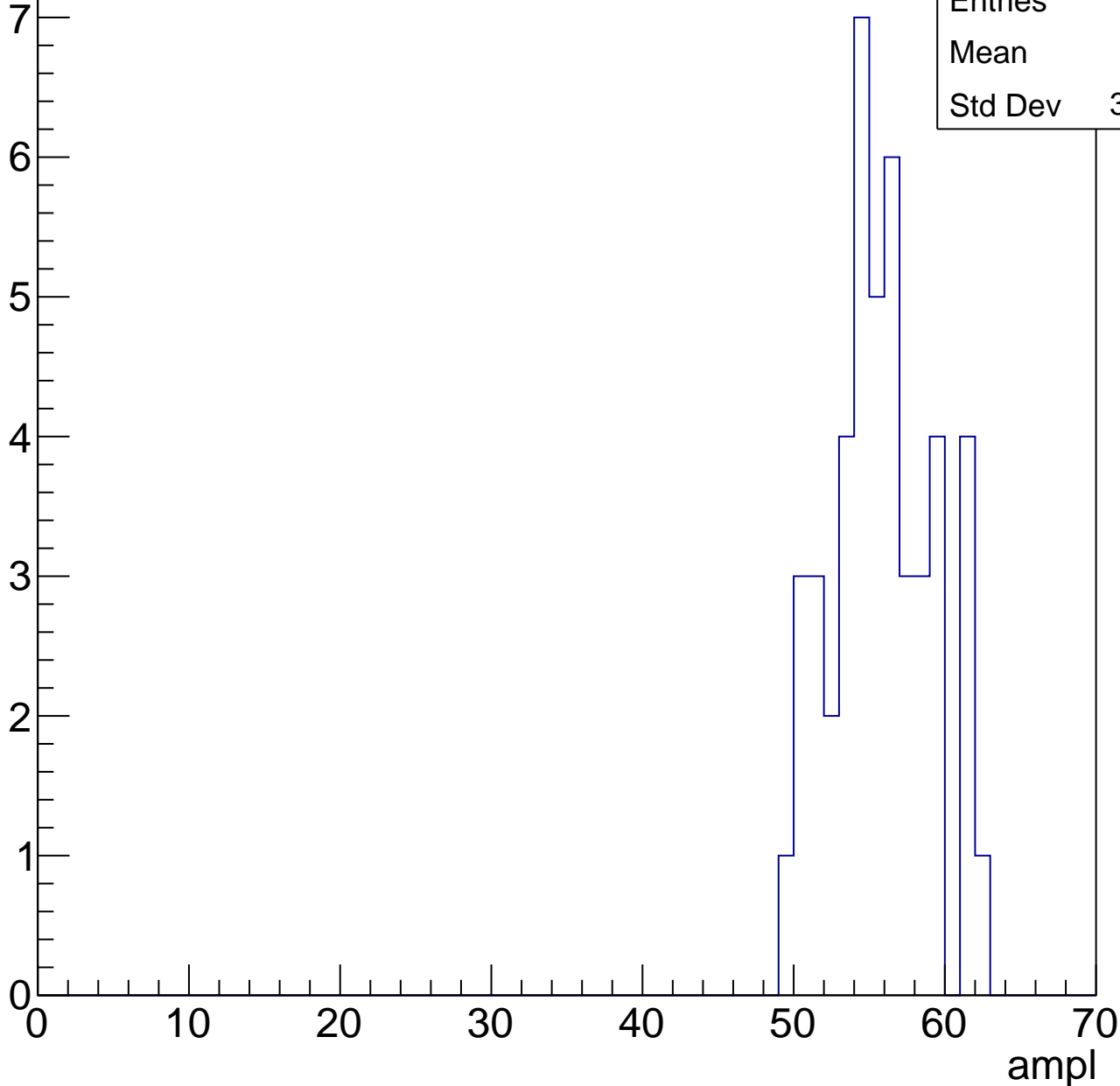


# B1L002S, U26-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	55.3
Std Dev	3.289

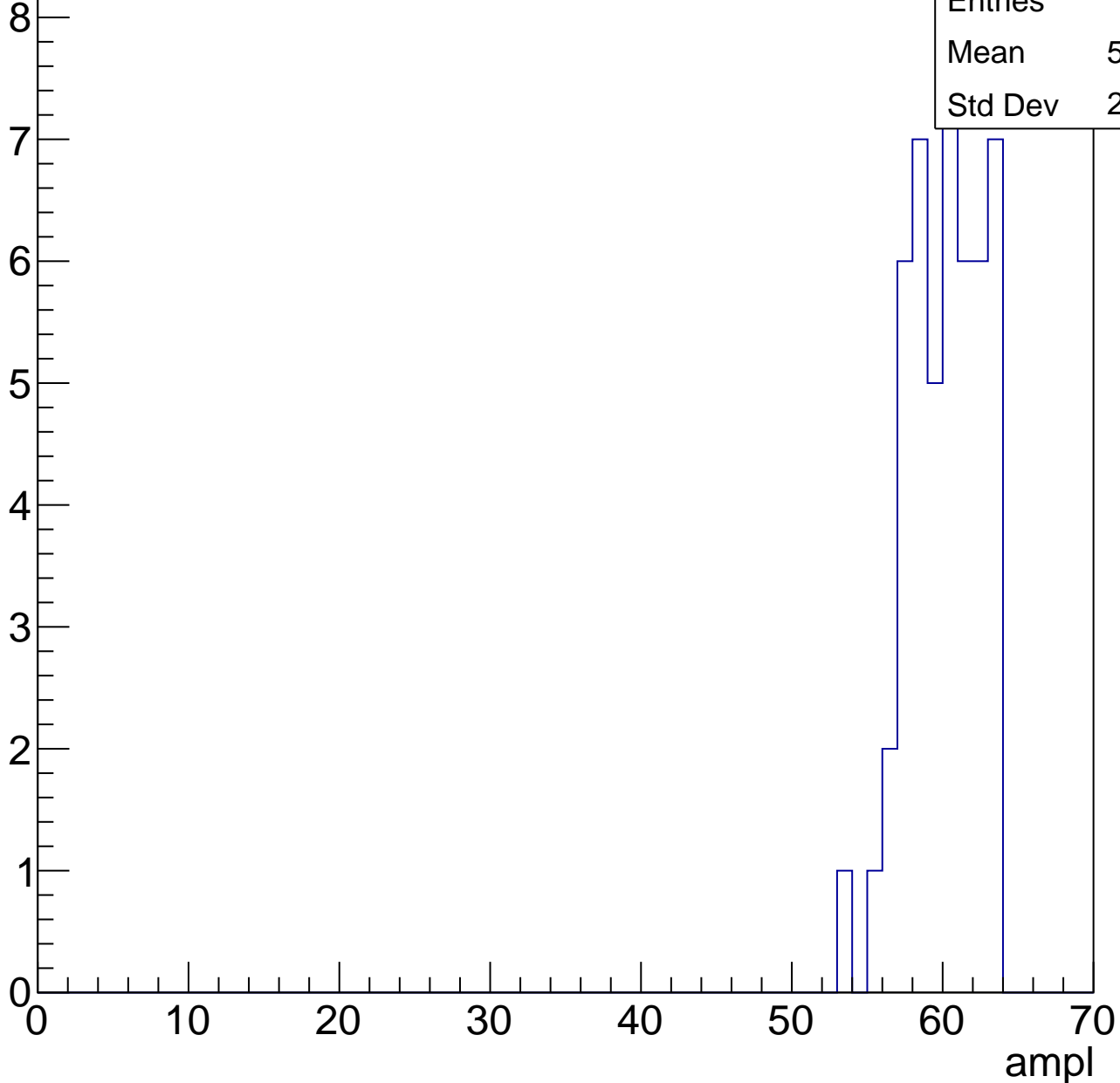


# B1L002S, U26-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

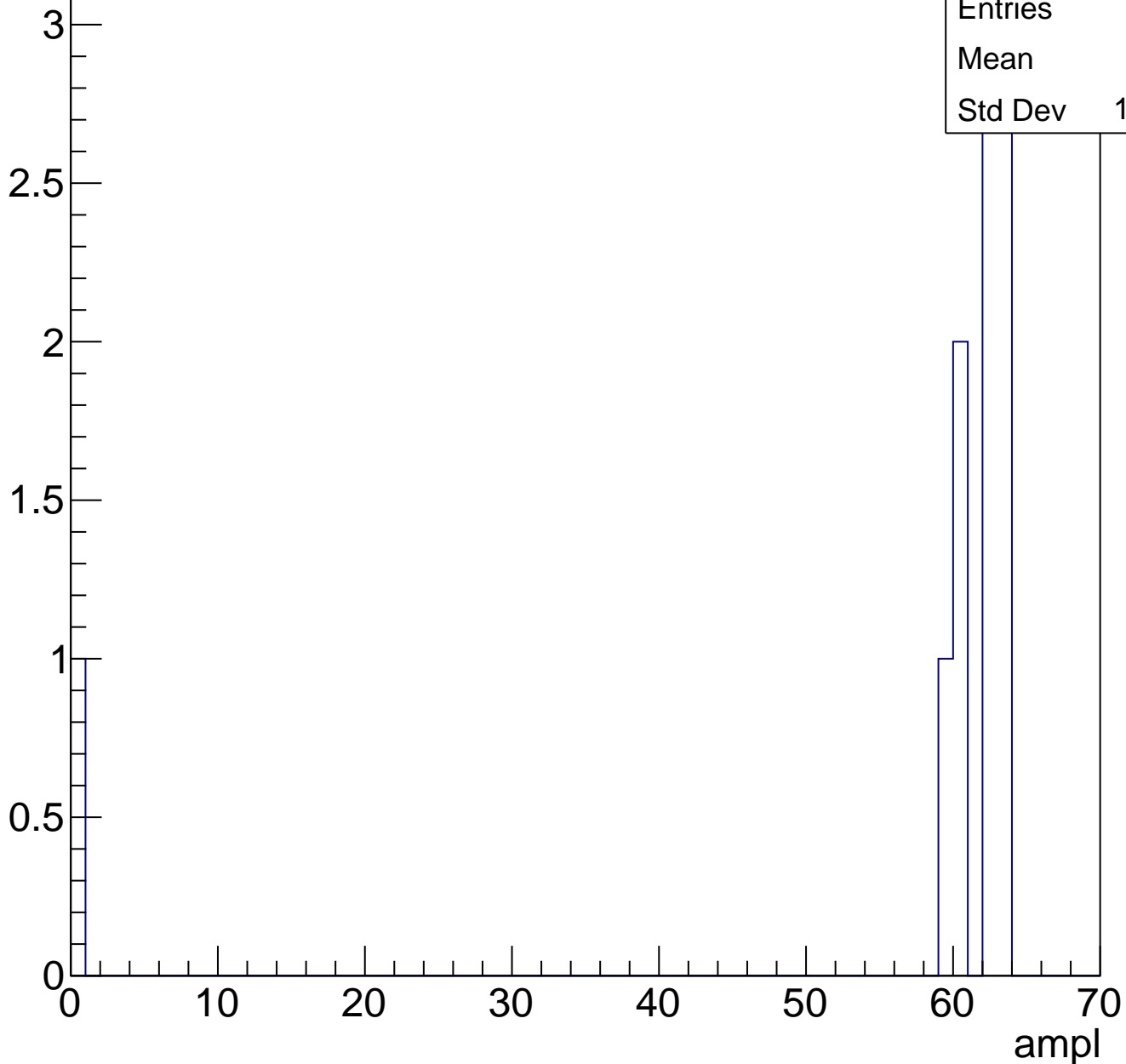
Entries	49
Mean	59.63
Std Dev	2.388



# B1L002S, U26-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B1L002S, U26-ch108, adc0

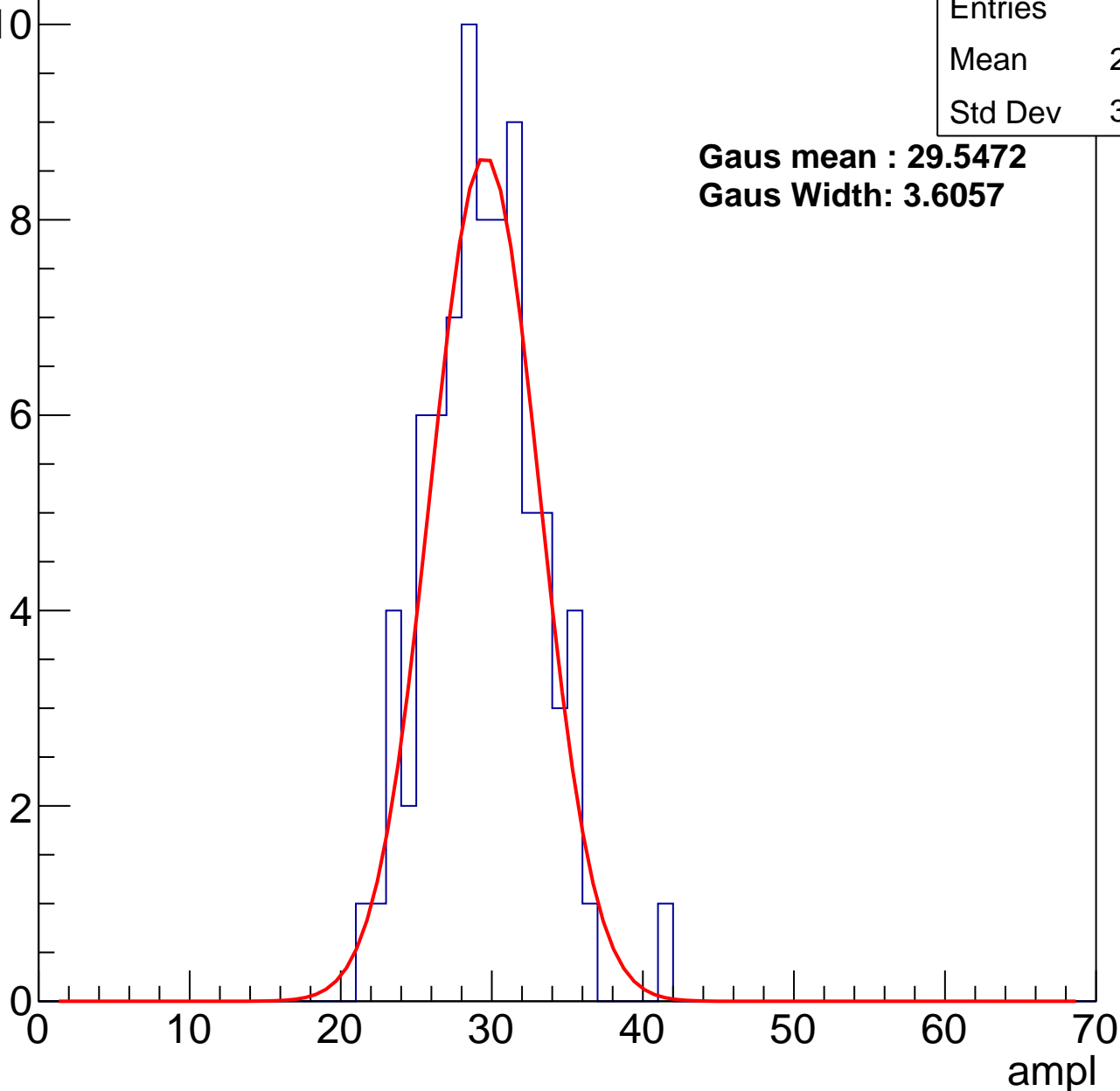
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	29.05
Std Dev	3.644

**Gaus mean : 29.5472**

**Gaus Width: 3.6057**



# B1L002S, U26-ch108, adc1

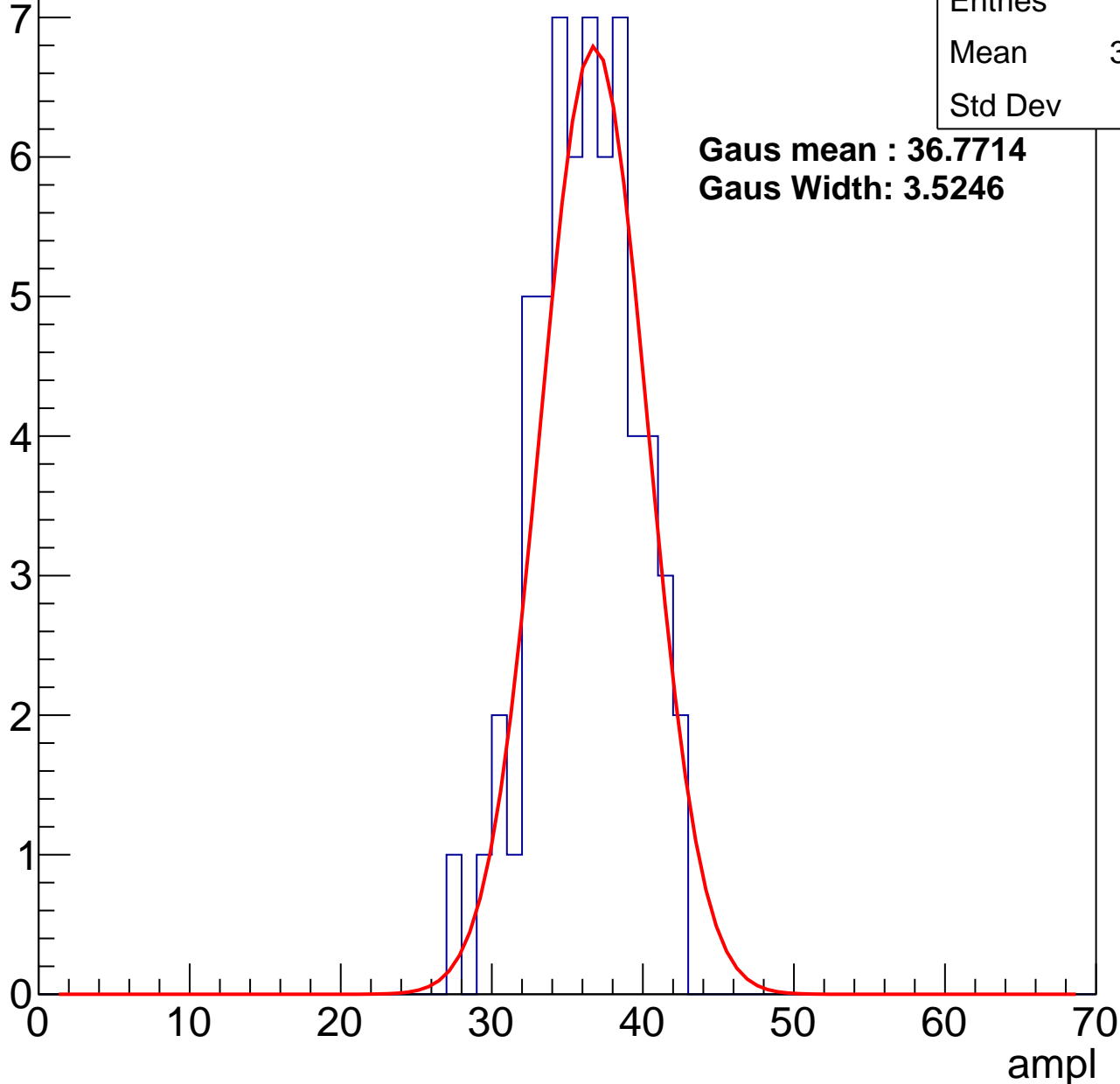
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.79
Std Dev	3.3

**Gaus mean : 36.7714**

**Gaus Width: 3.5246**



# B1L002S, U26-ch108, adc2

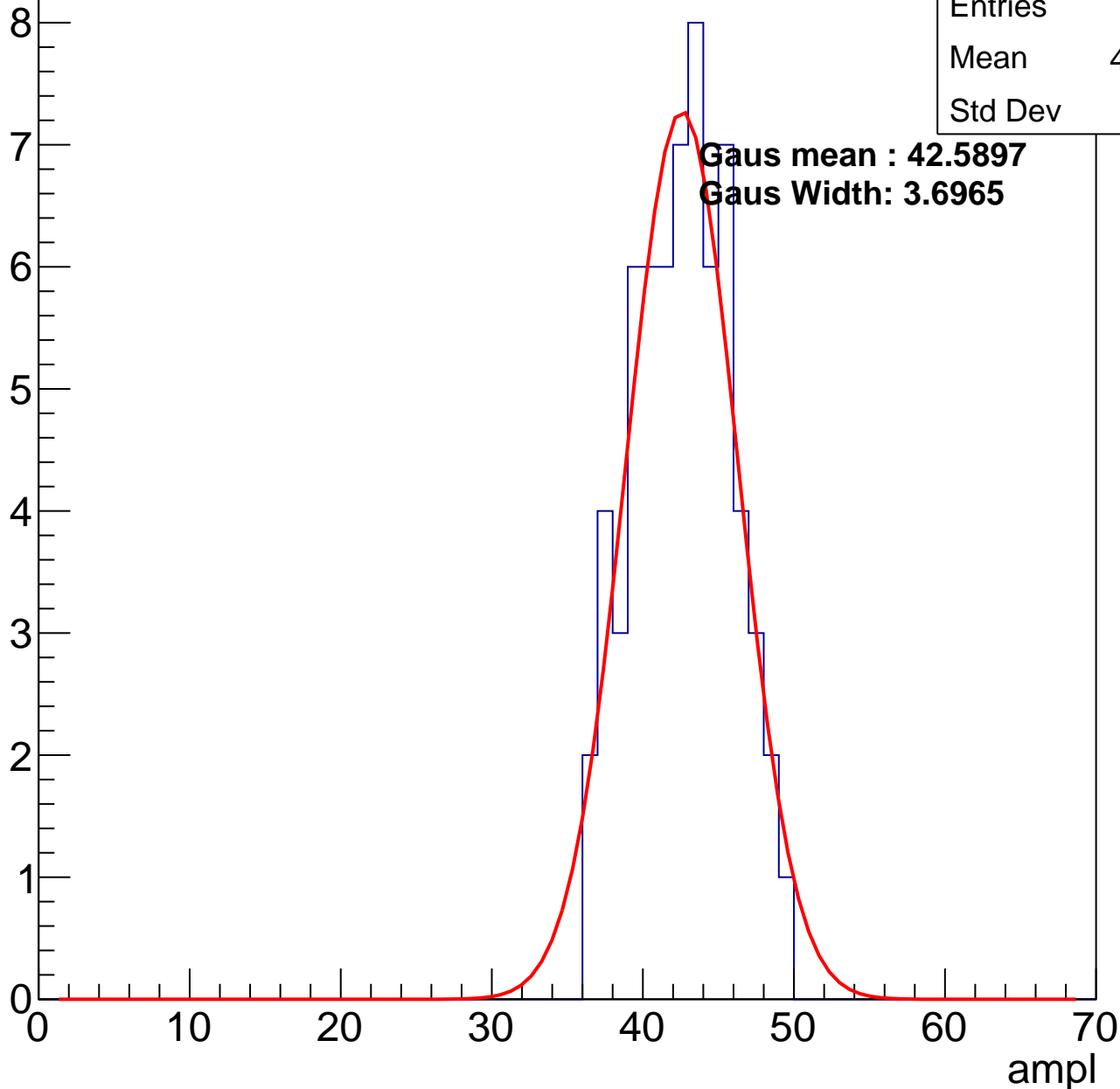
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	42.17
Std Dev	3.18

**Gaus mean : 42.5897**

**Gaus Width: 3.6965**

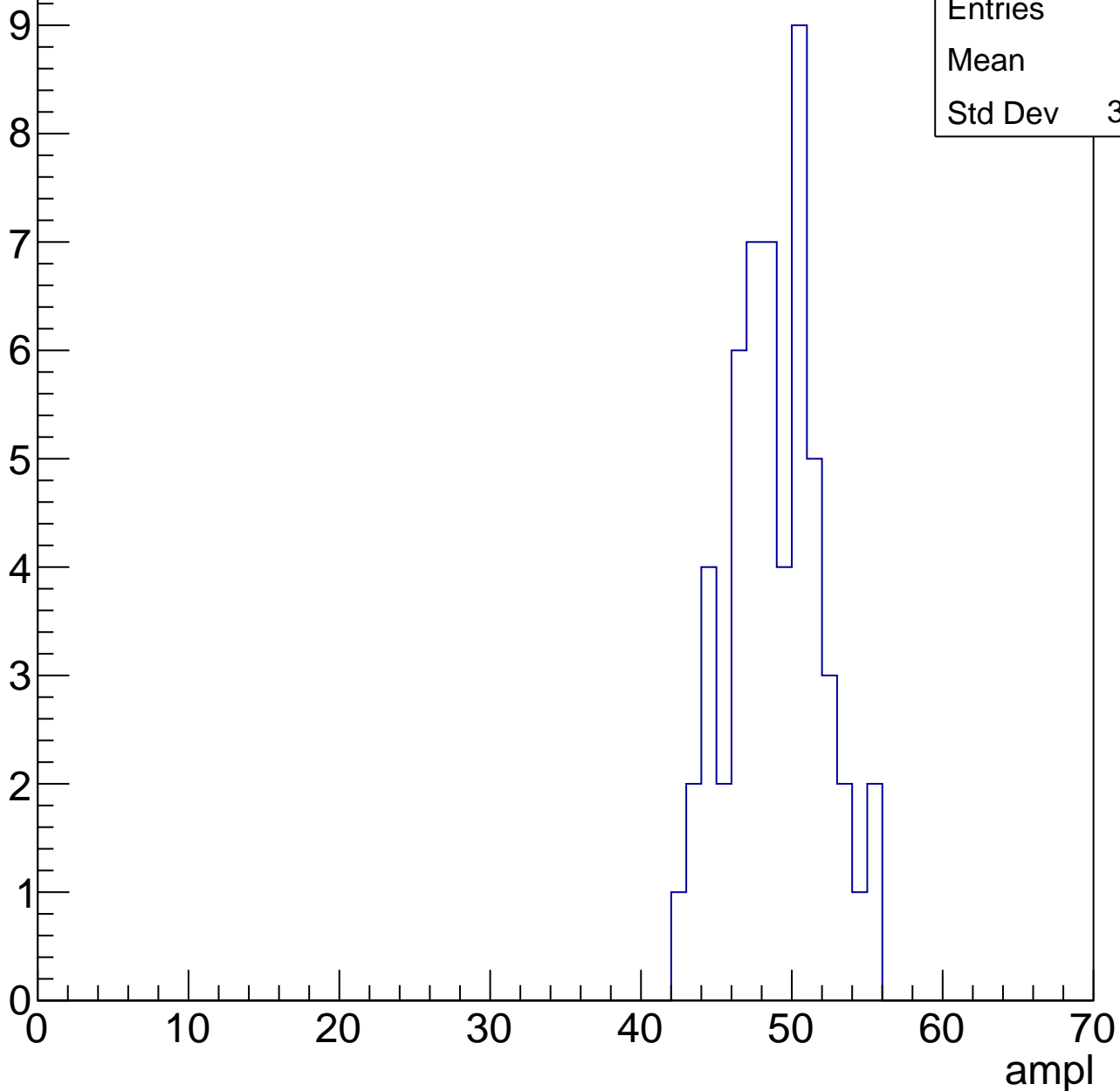


# B1L002S, U26-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	48.4
Std Dev	3.037

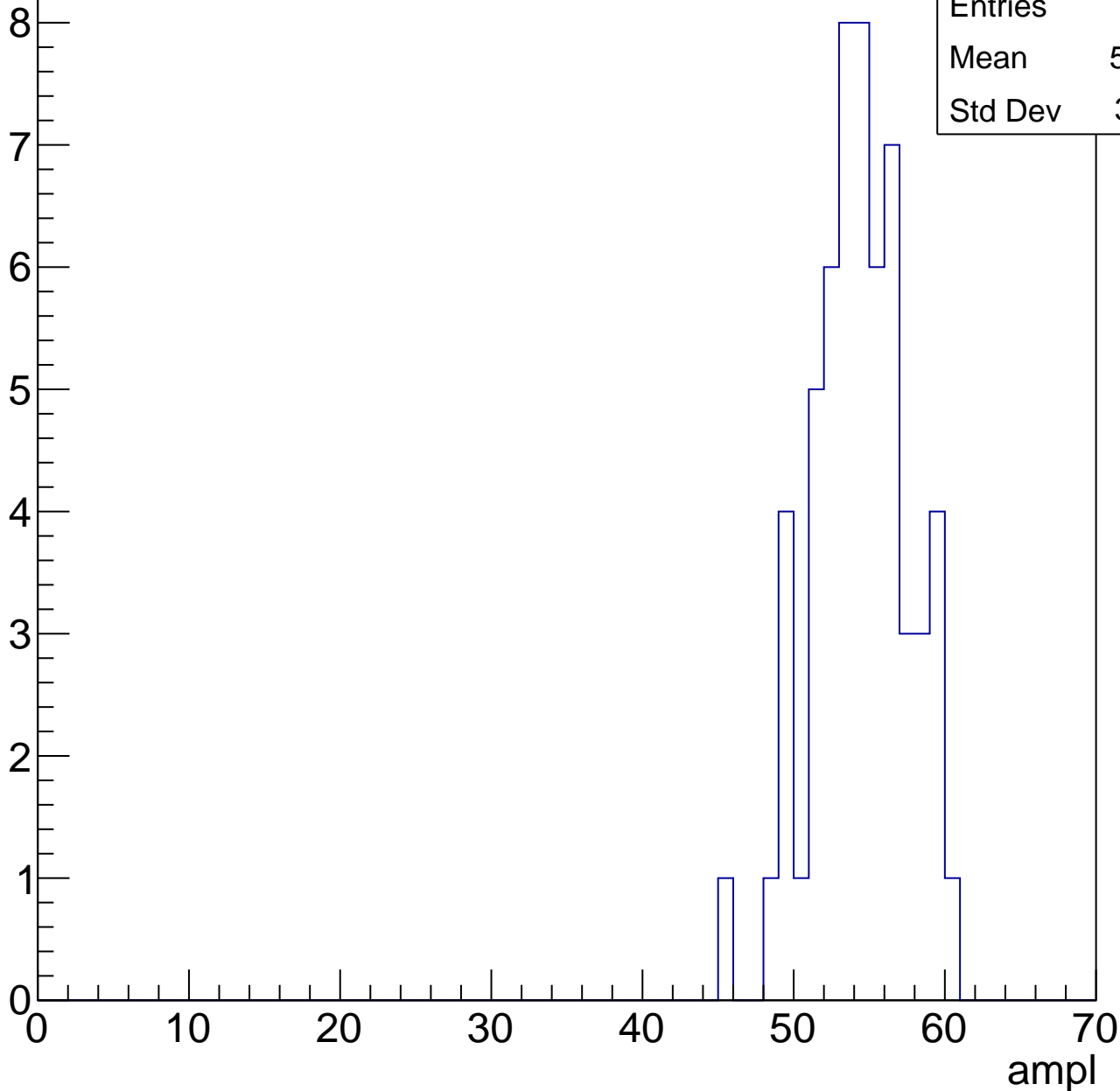


# B1L002S, U26-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	53.88
Std Dev	3.091

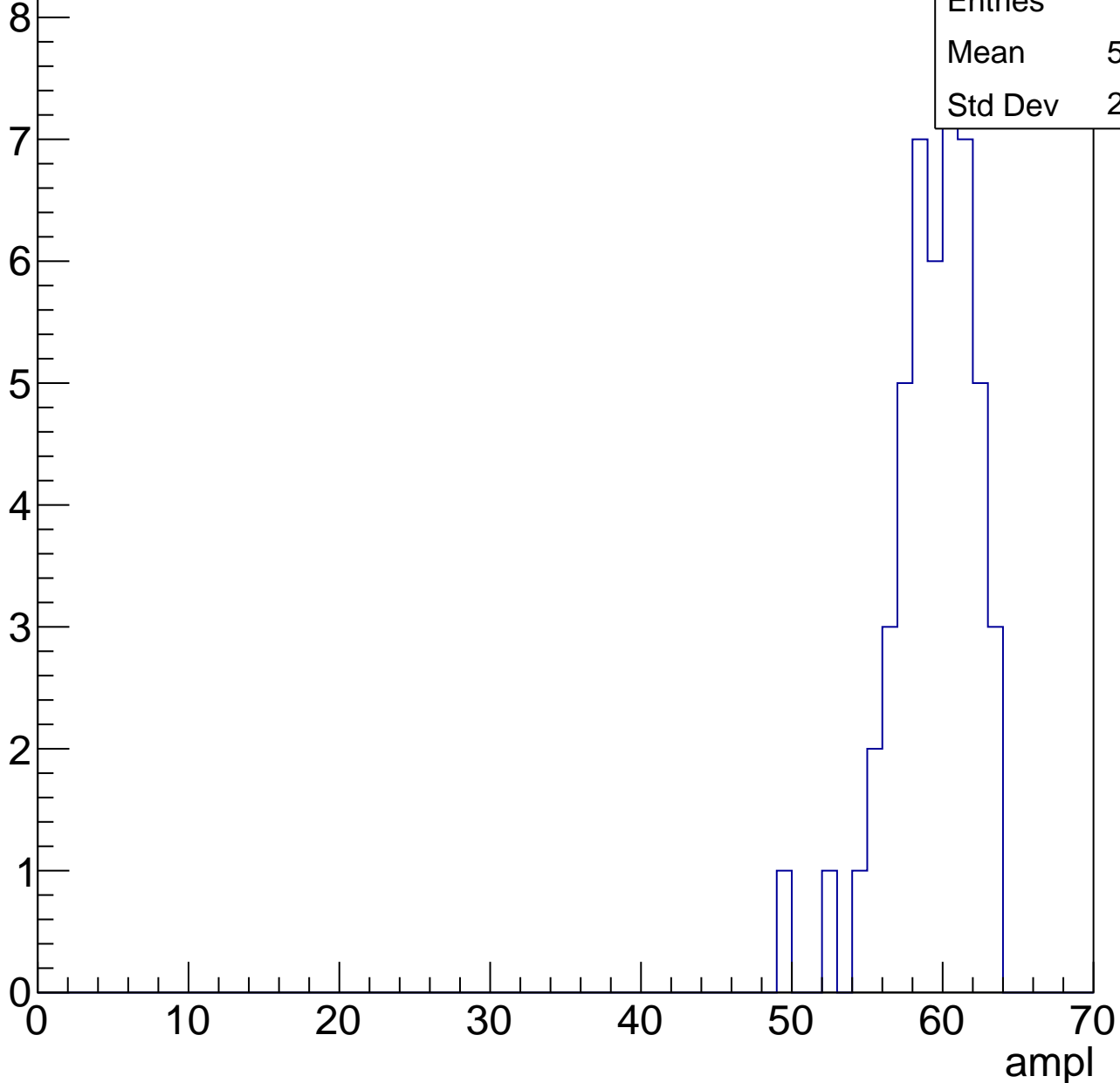


# B1L002S, U26-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	58.86
Std Dev	2.814

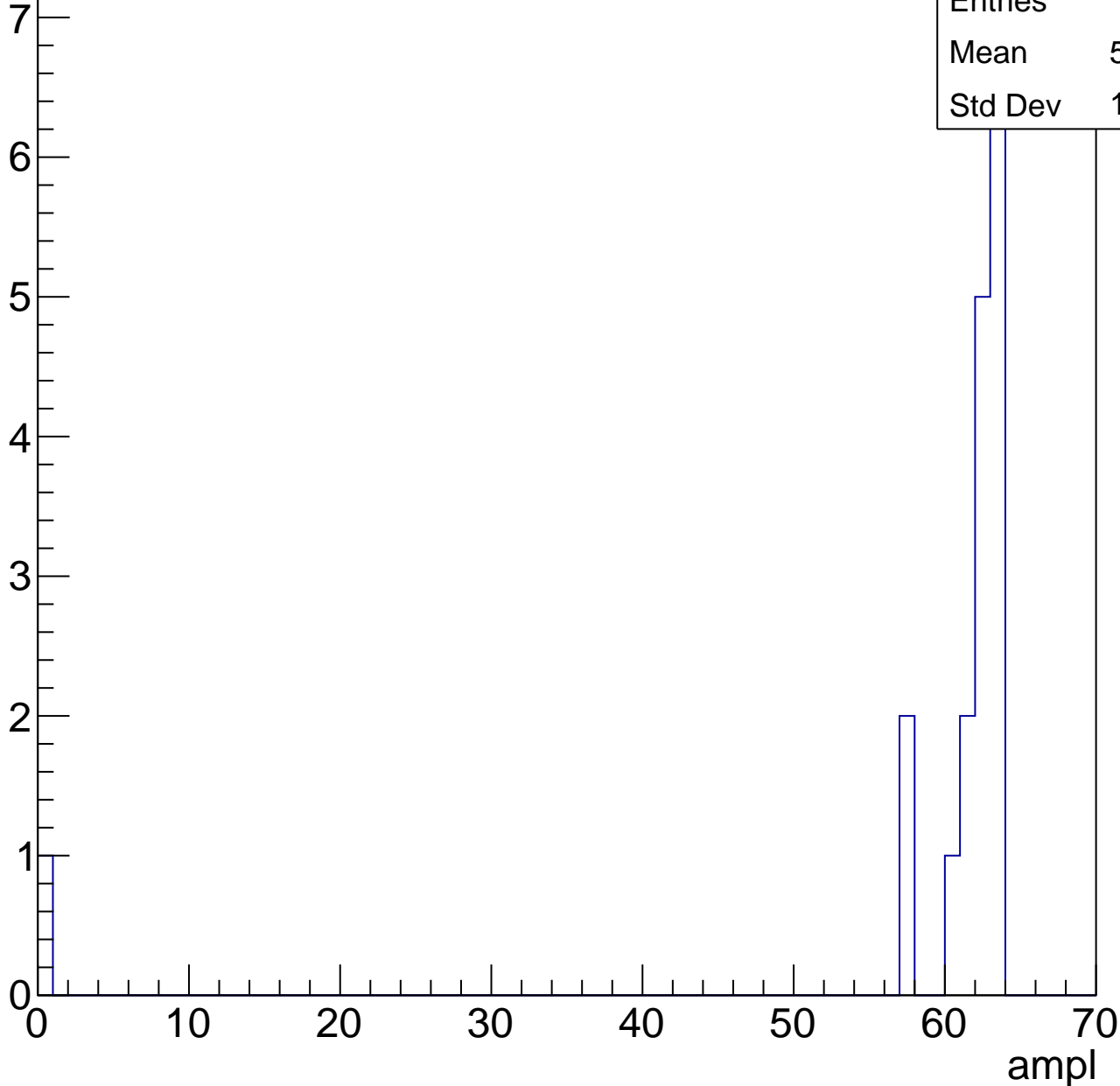


# B1L002S, U26-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.17
Std Dev	14.23

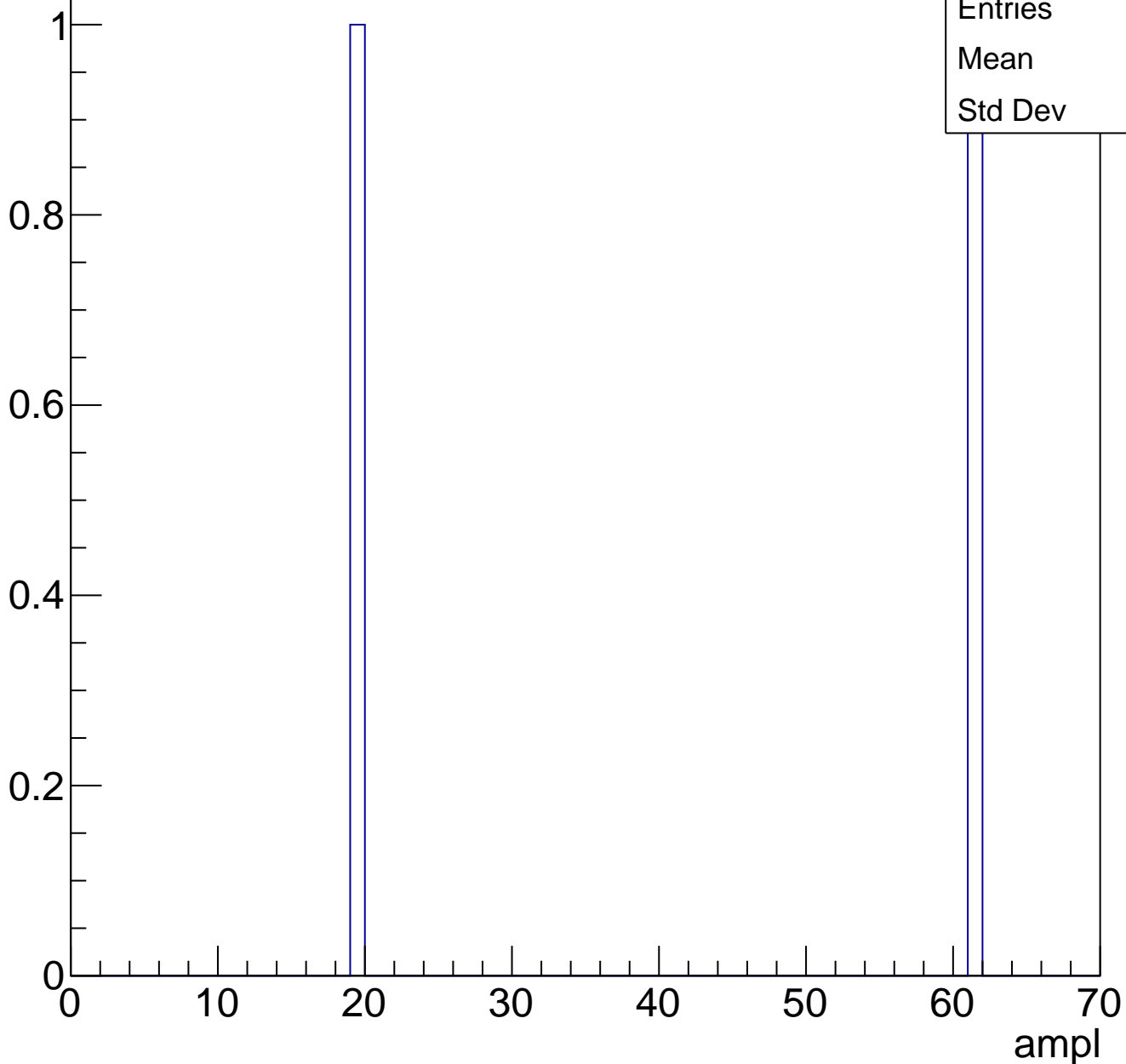




# B1L002S, U26-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch109, adc0

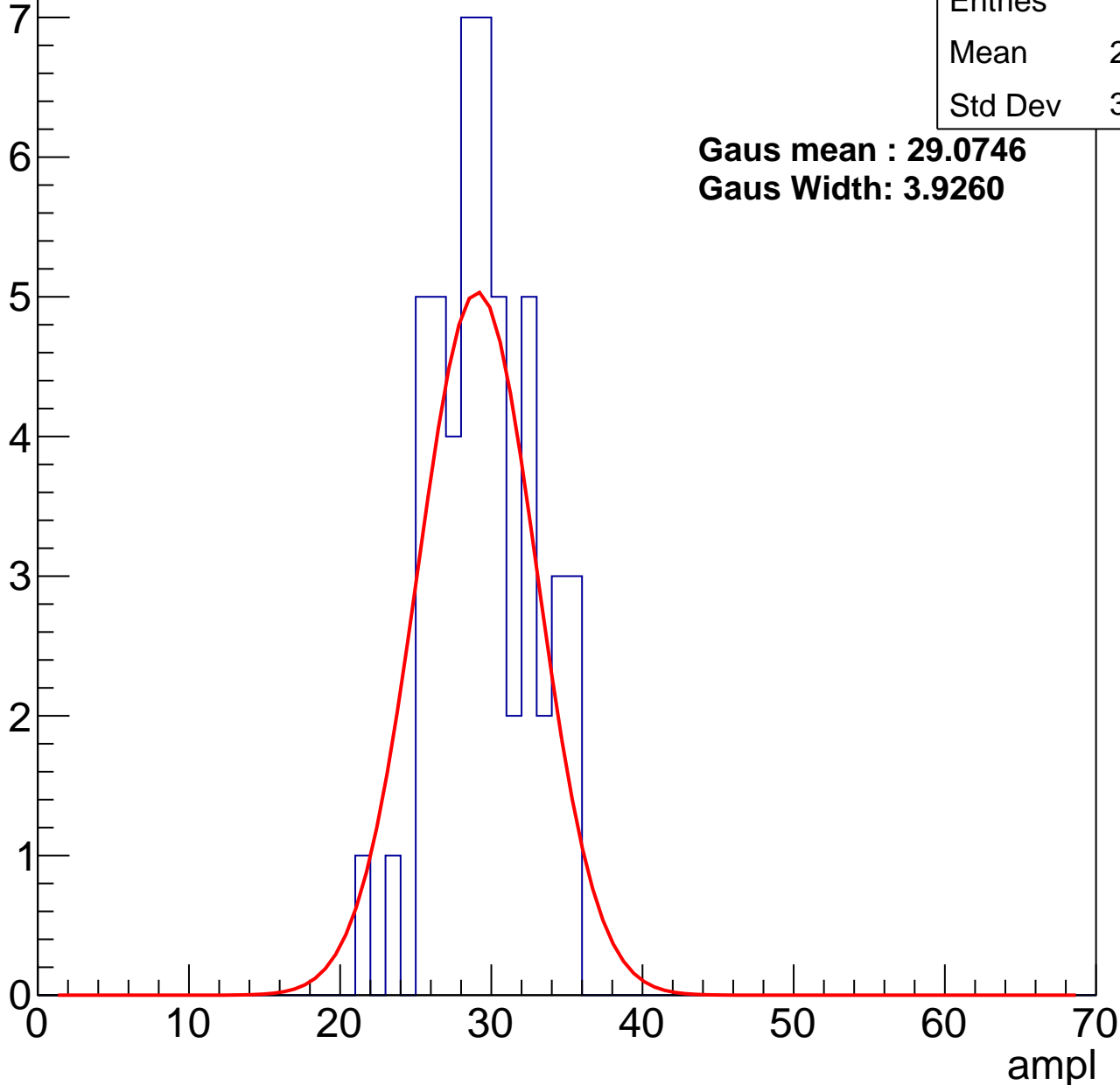
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	29.02
Std Dev	3.228

**Gaus mean : 29.0746**

**Gaus Width: 3.9260**



# B1L002S, U26-ch109, adc1

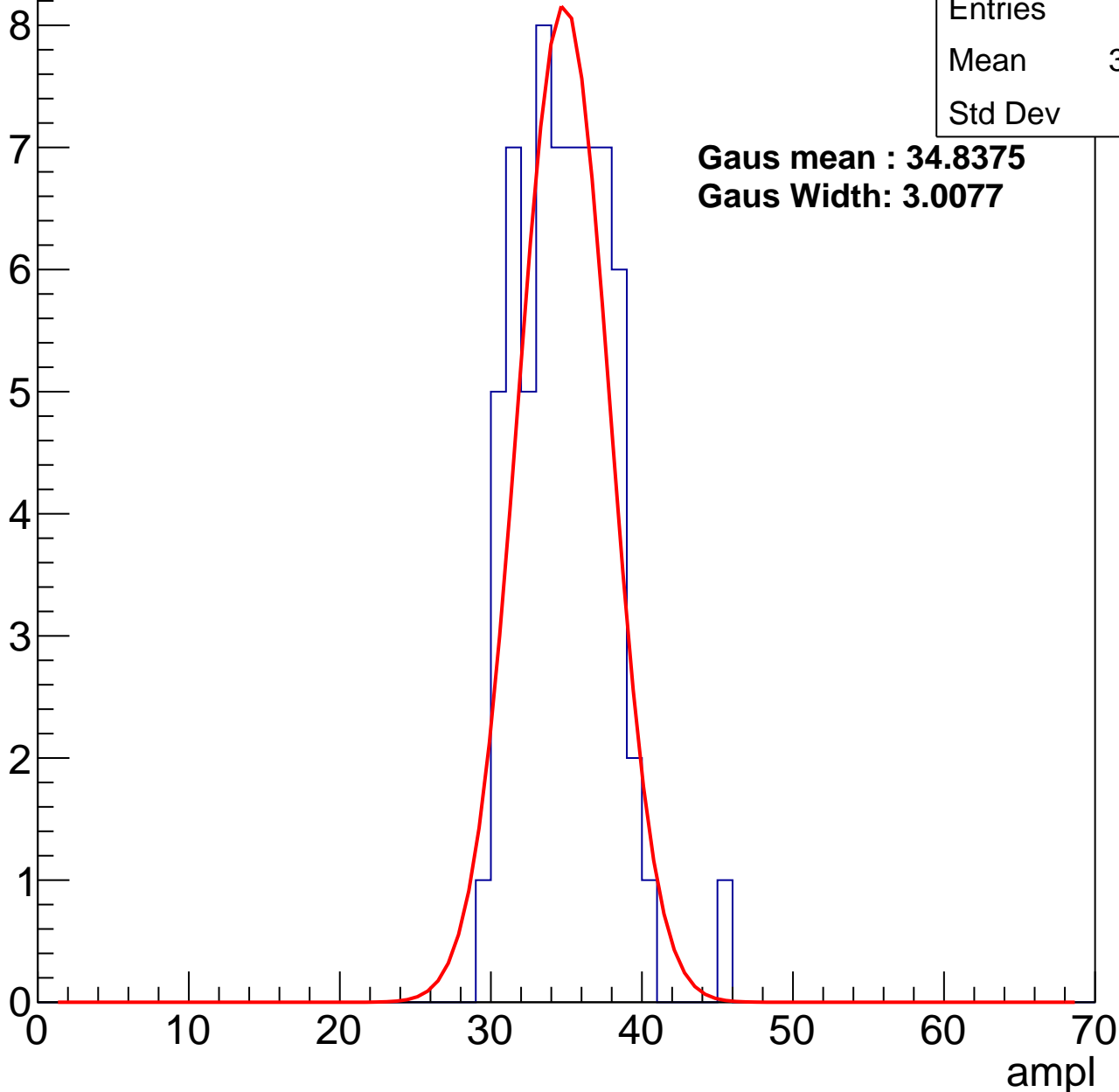
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.45
Std Dev	3.02

**Gaus mean : 34.8375**

**Gaus Width: 3.0077**



# B1L002S, U26-ch109, adc2

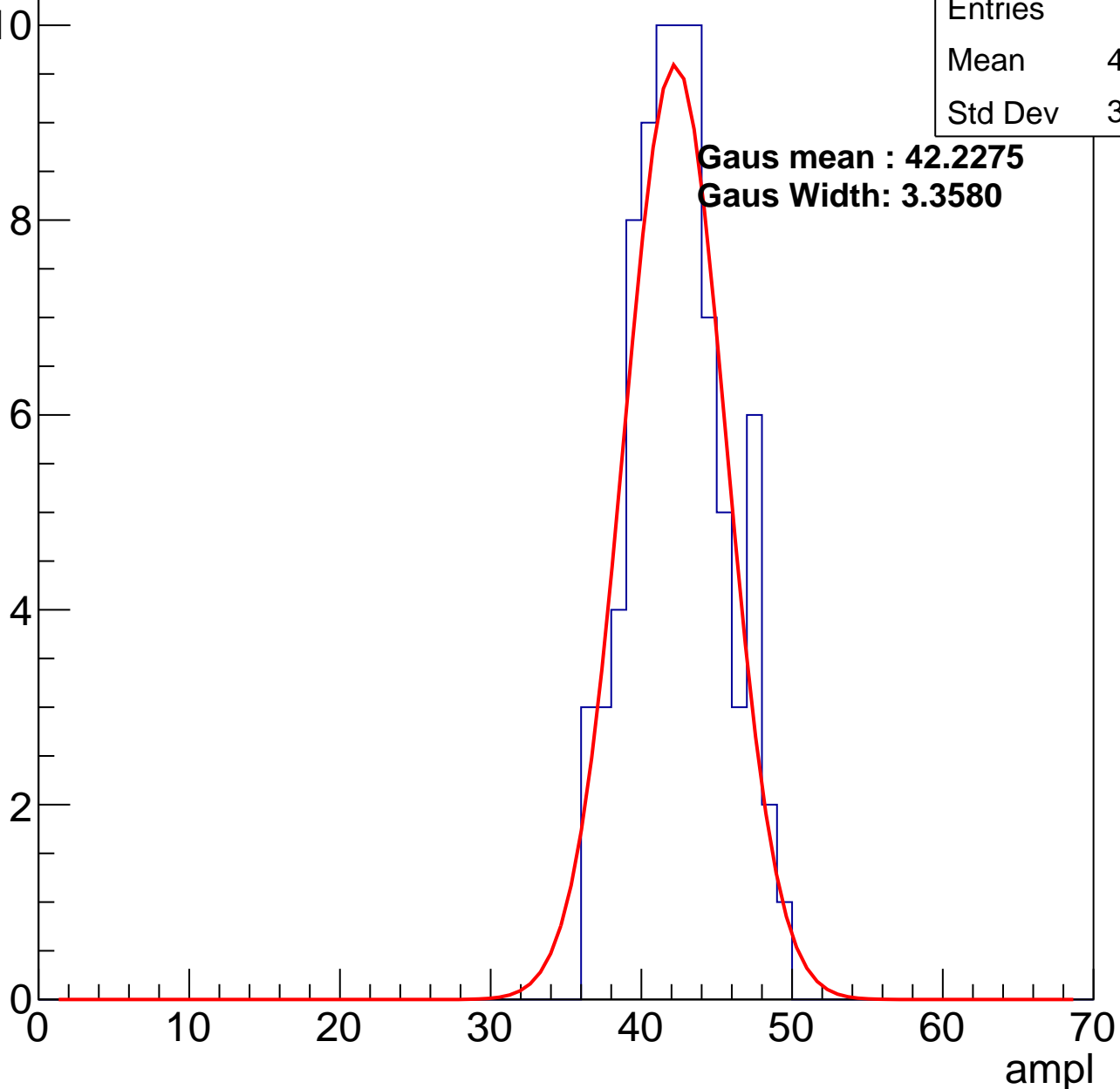
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	41.99
Std Dev	3.077

**Gaus mean : 42.2275**

**Gaus Width: 3.3580**

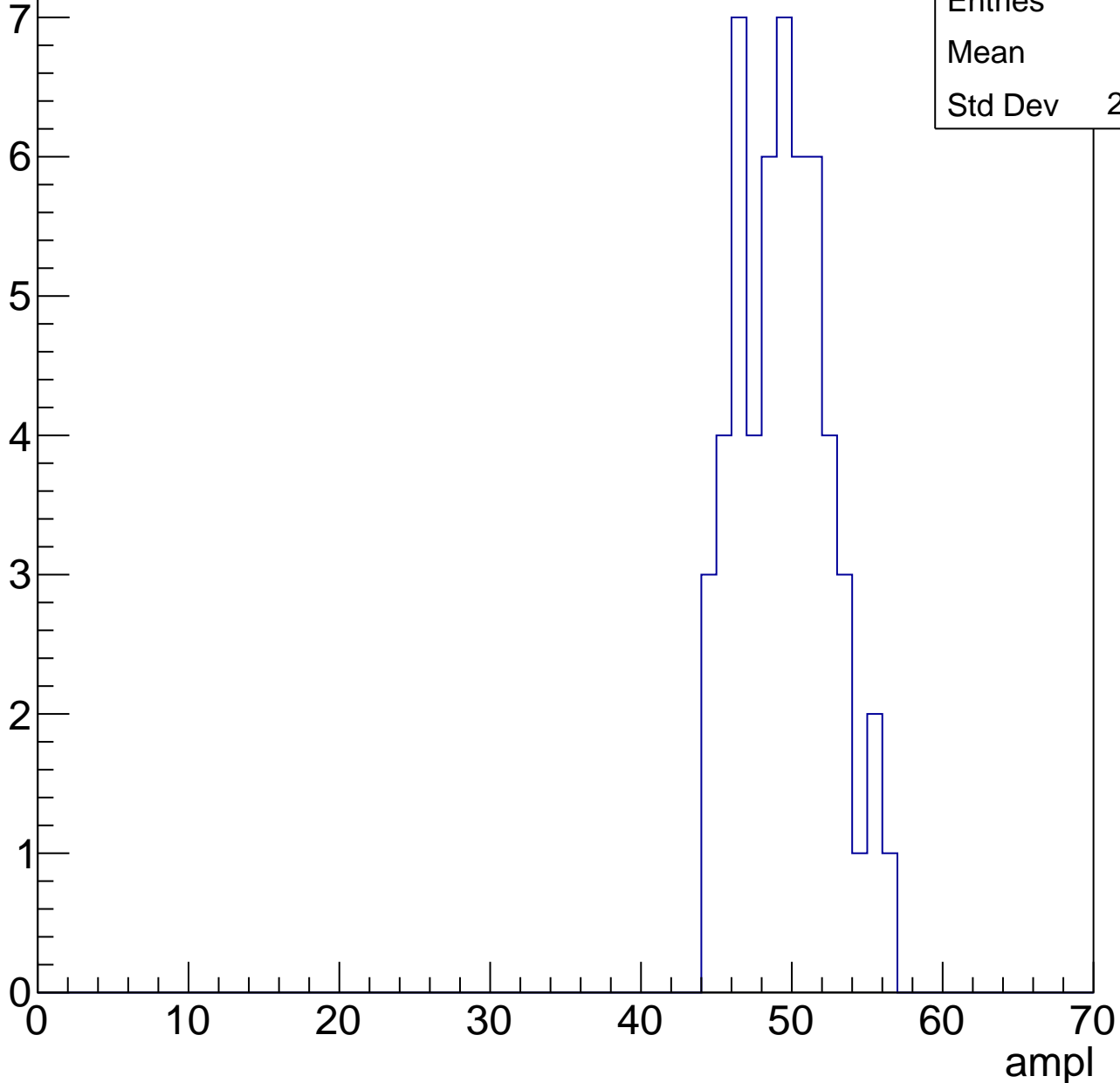


# B1L002S, U26-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	49
Std Dev	2.994

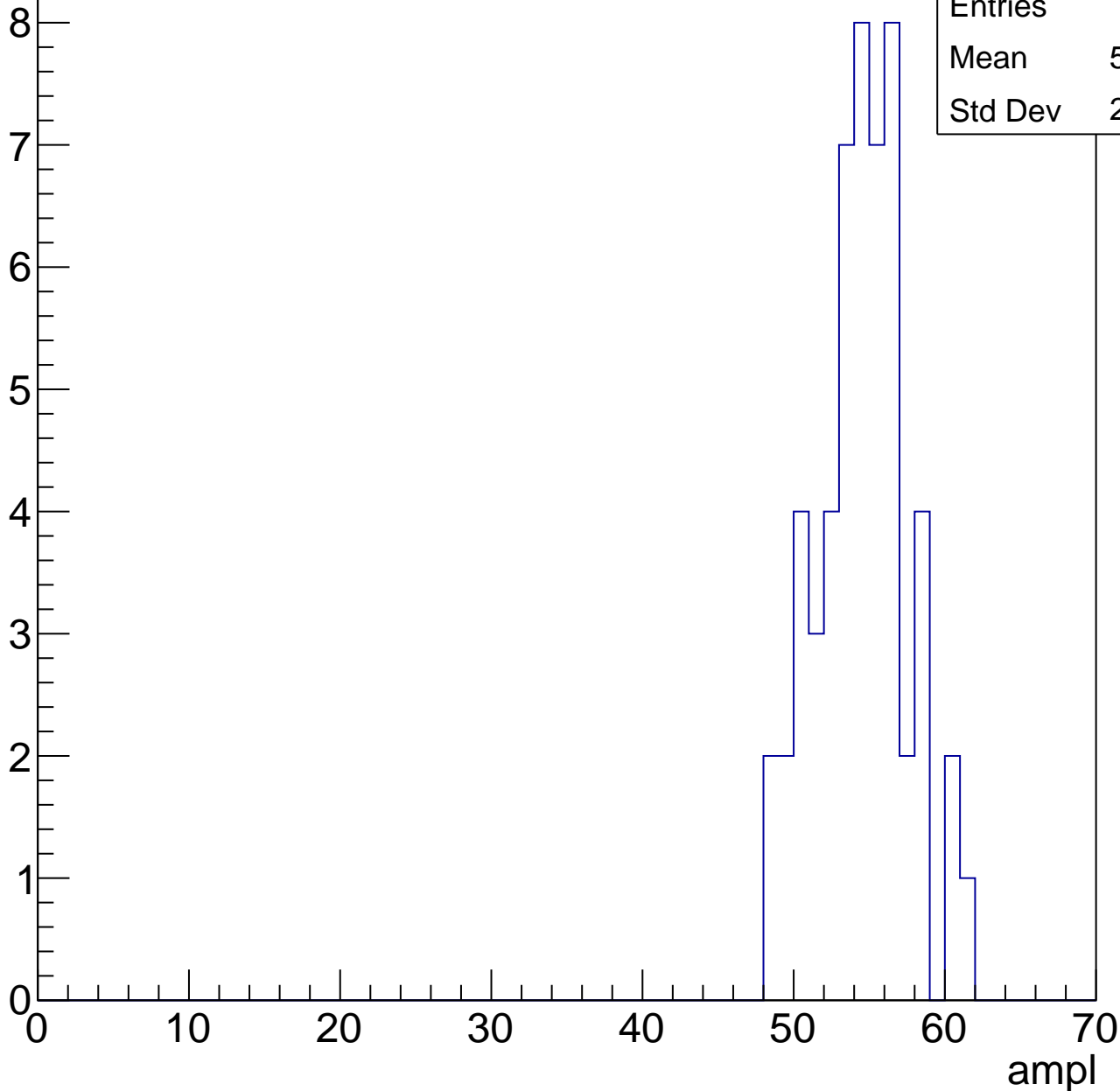


# B1L002S, U26-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	54.04
Std Dev	2.975

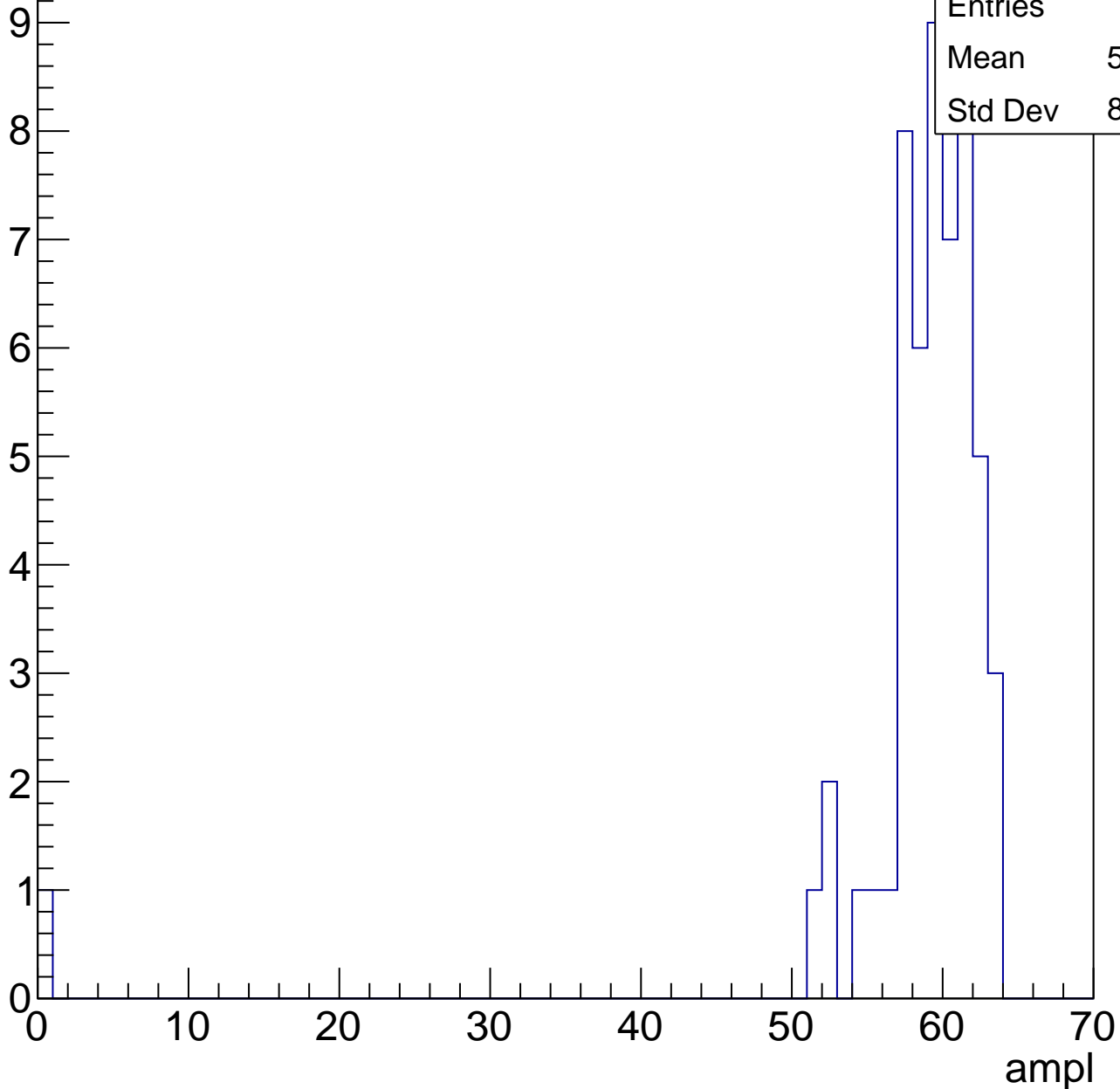


# B1L002S, U26-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.83
Std Dev	8.382

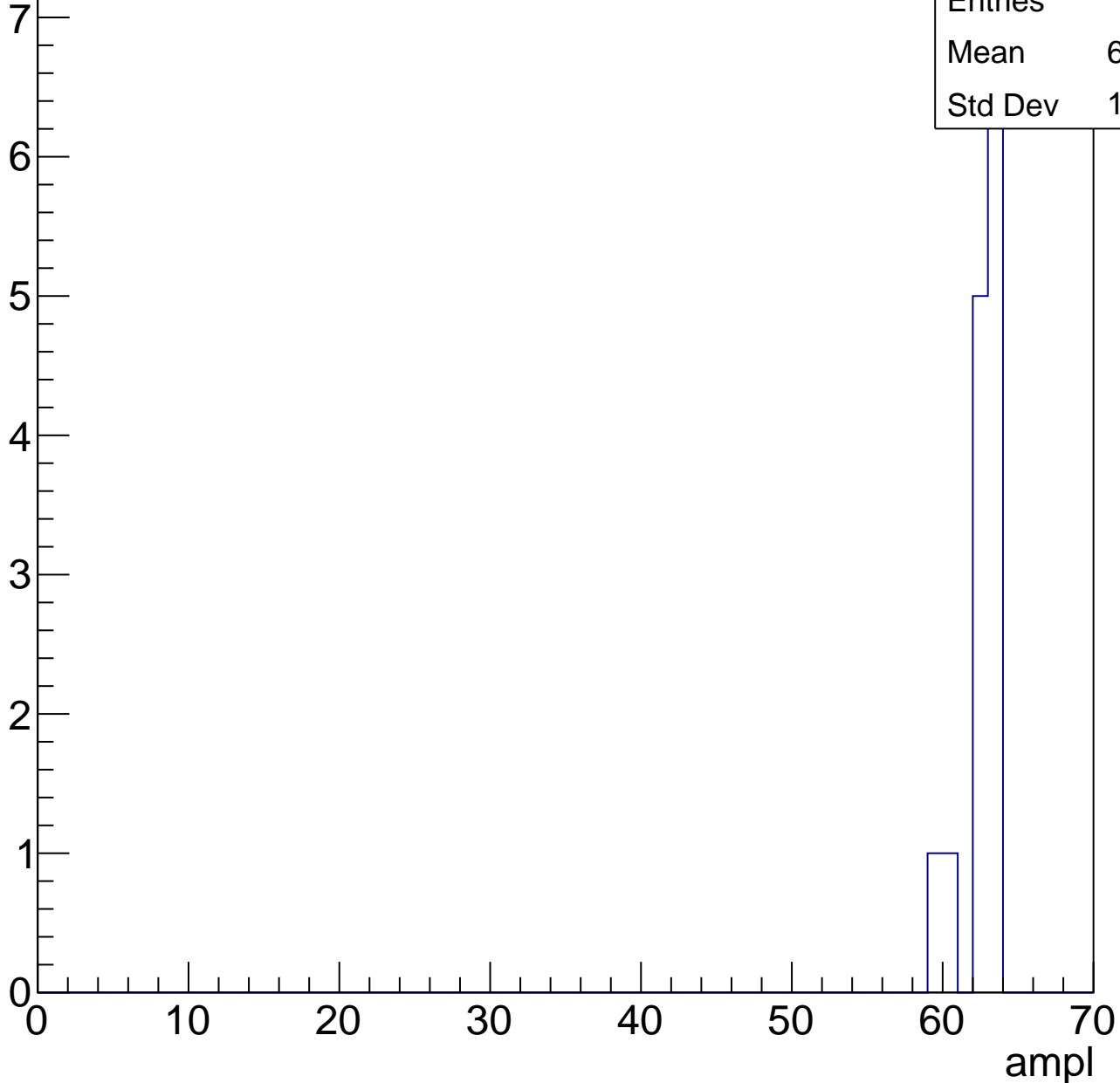


# B1L002S, U26-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	62.14
Std Dev	1.187





# B1L002S, U26-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch110, adc0

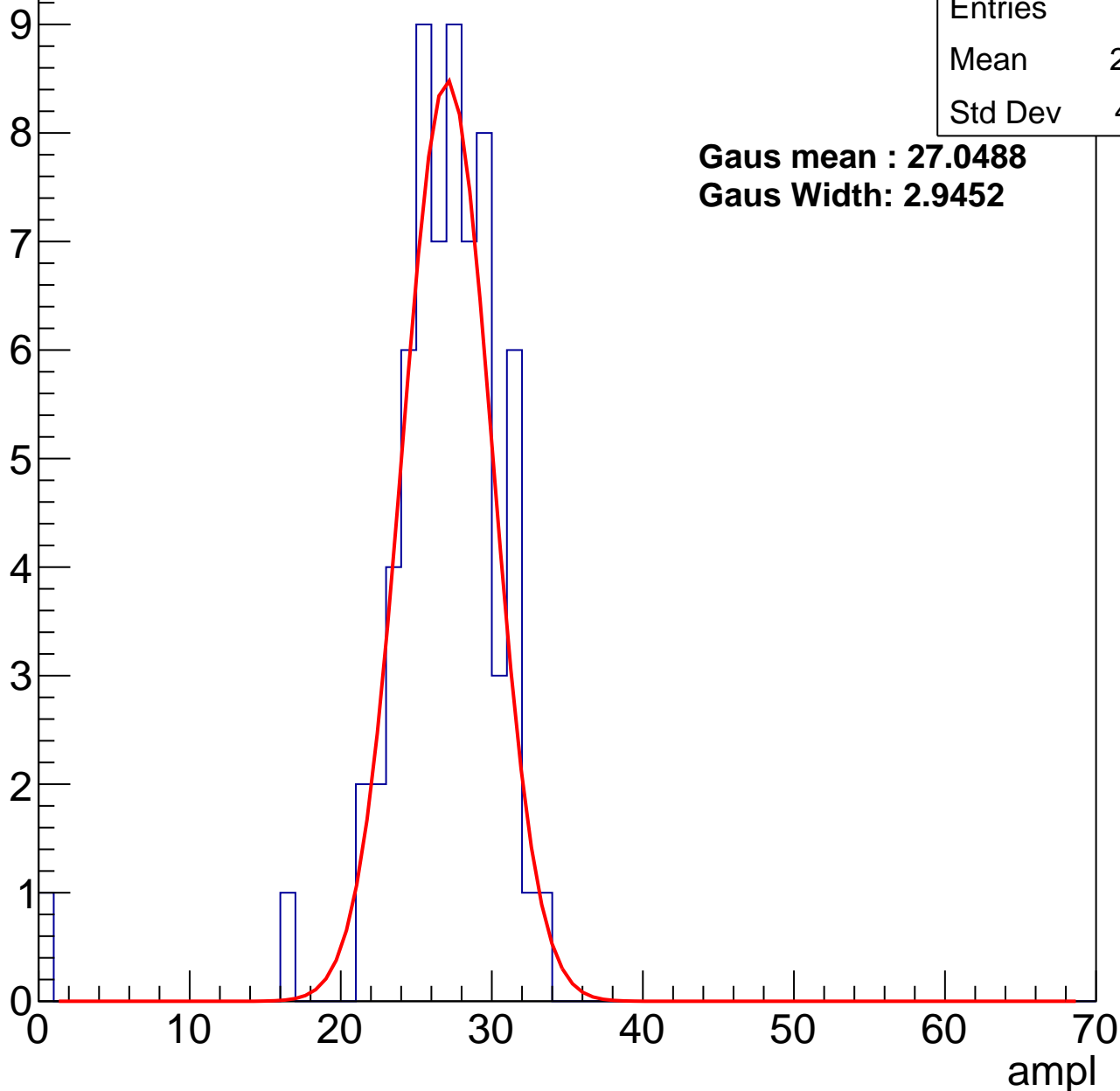
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	26.22
Std Dev	4.431

**Gaus mean : 27.0488**

**Gaus Width: 2.9452**



# B1L002S, U26-ch110, adc1

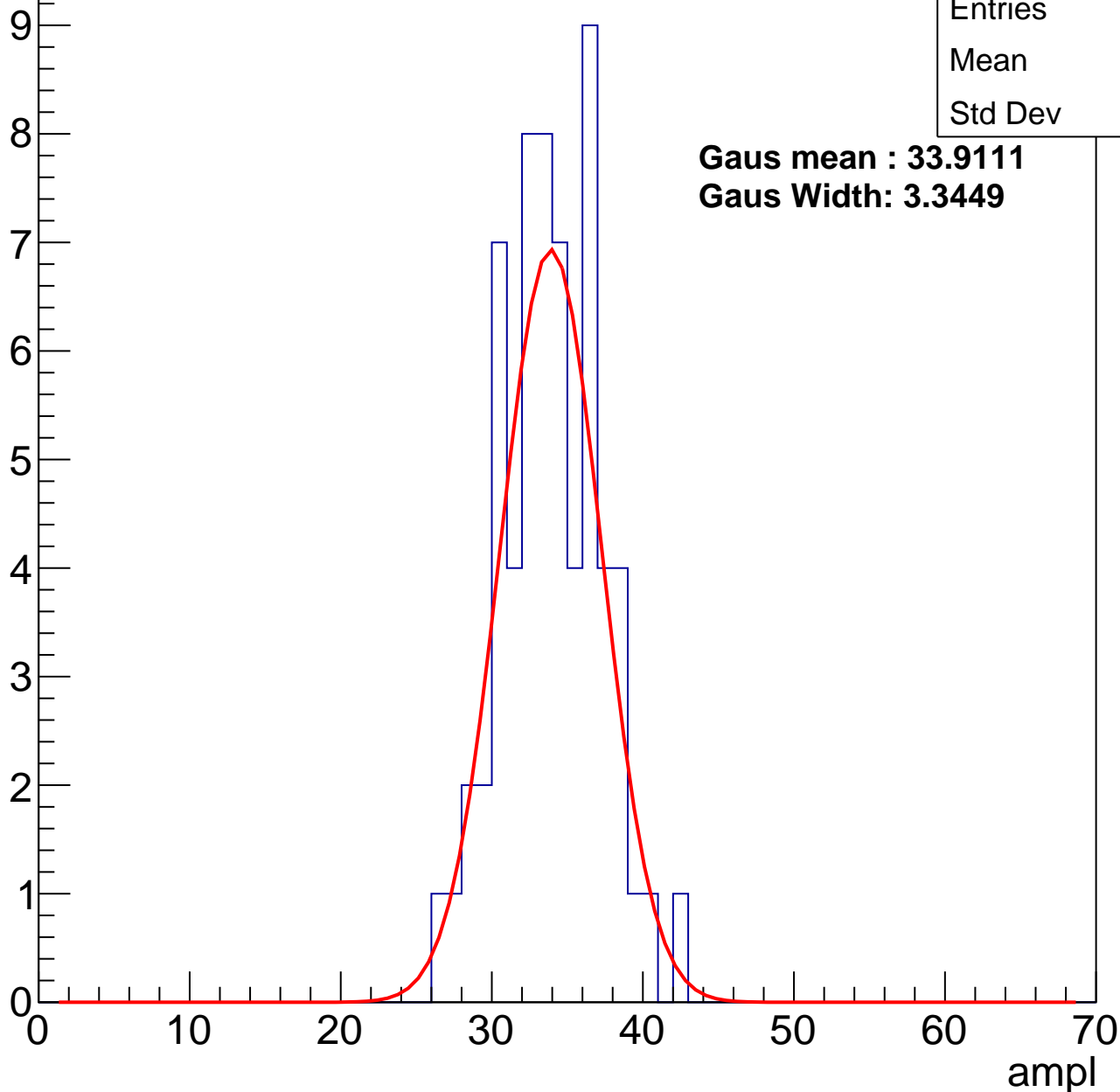
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.5
Std Dev	3.24

**Gaus mean : 33.9111**

**Gaus Width: 3.3449**



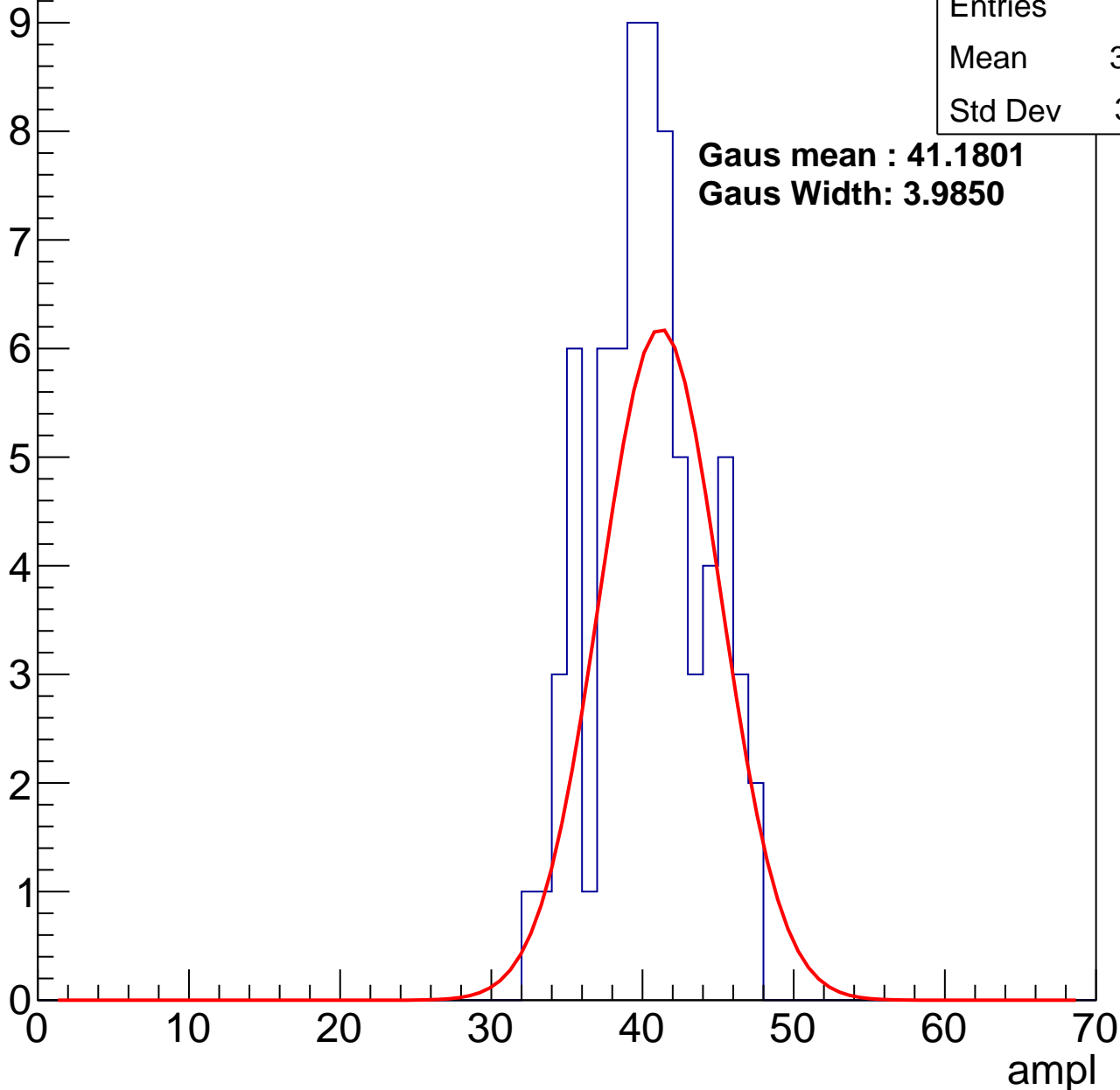
# B1L002S, U26-ch110, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	39.92
Std Dev	3.581

**Gaus mean : 41.1801**  
**Gaus Width: 3.9850**

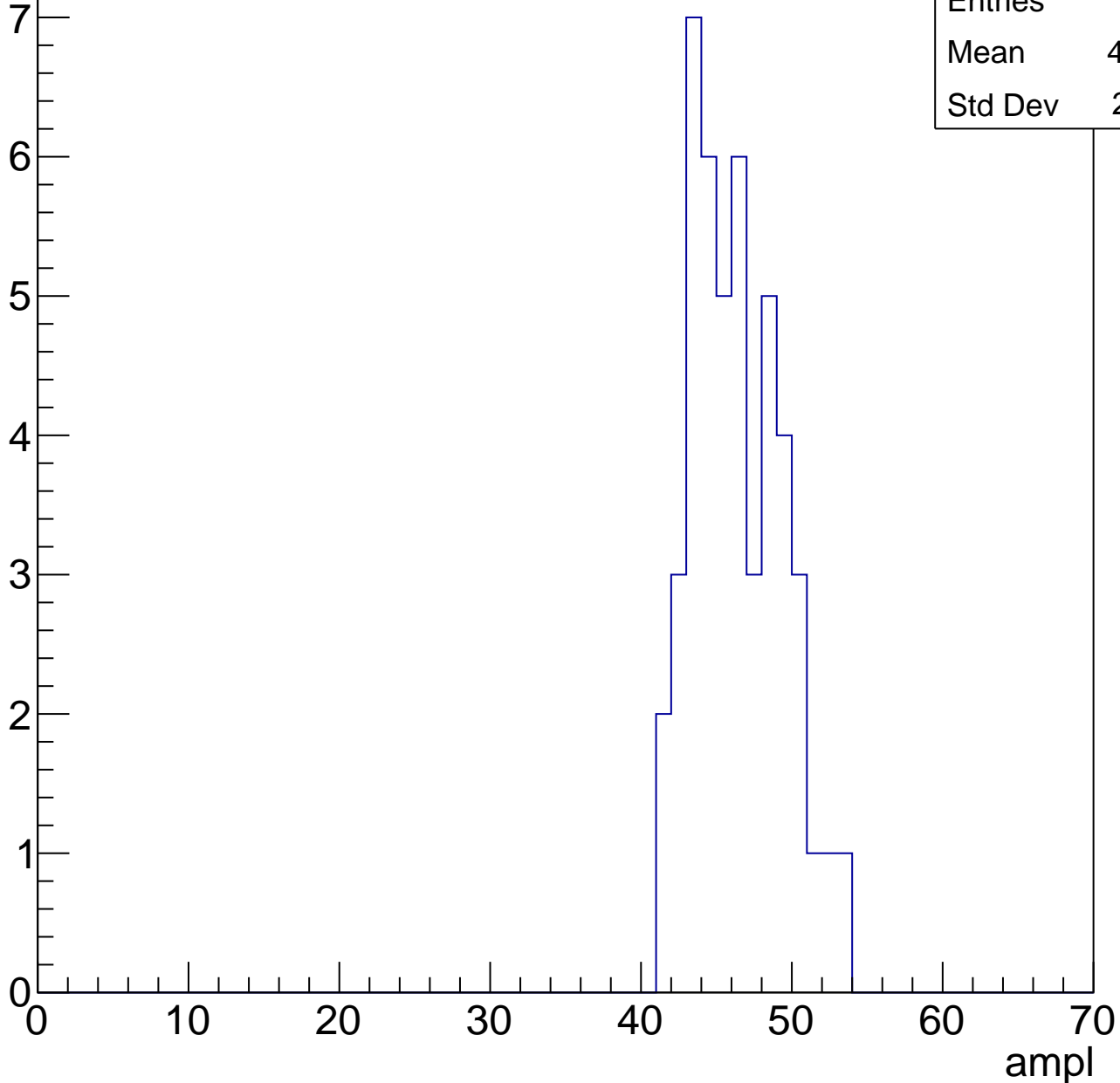


# B1L002S, U26-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	45.89
Std Dev	2.941

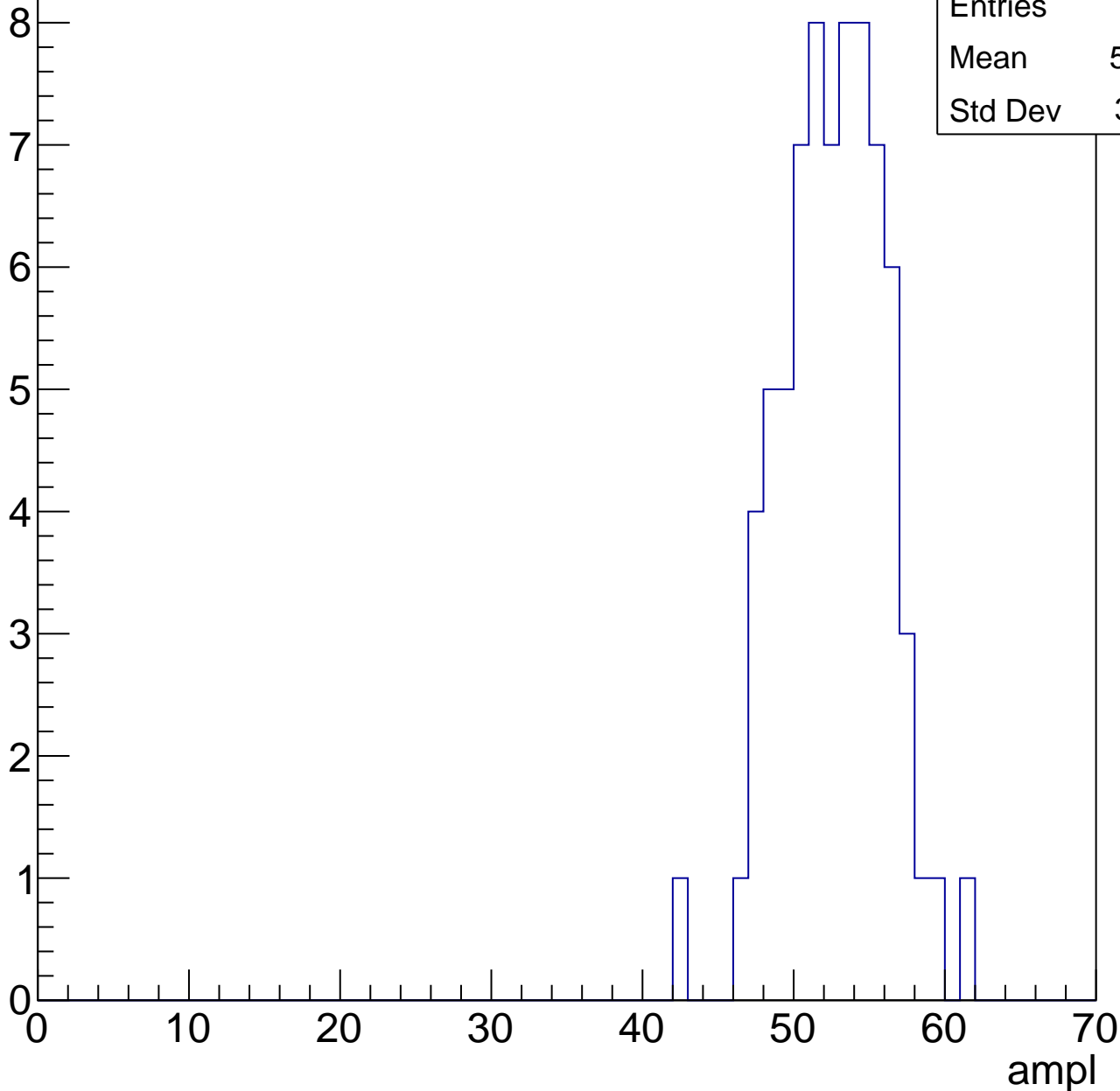


# B1L002S, U26-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	52.18
Std Dev	3.381

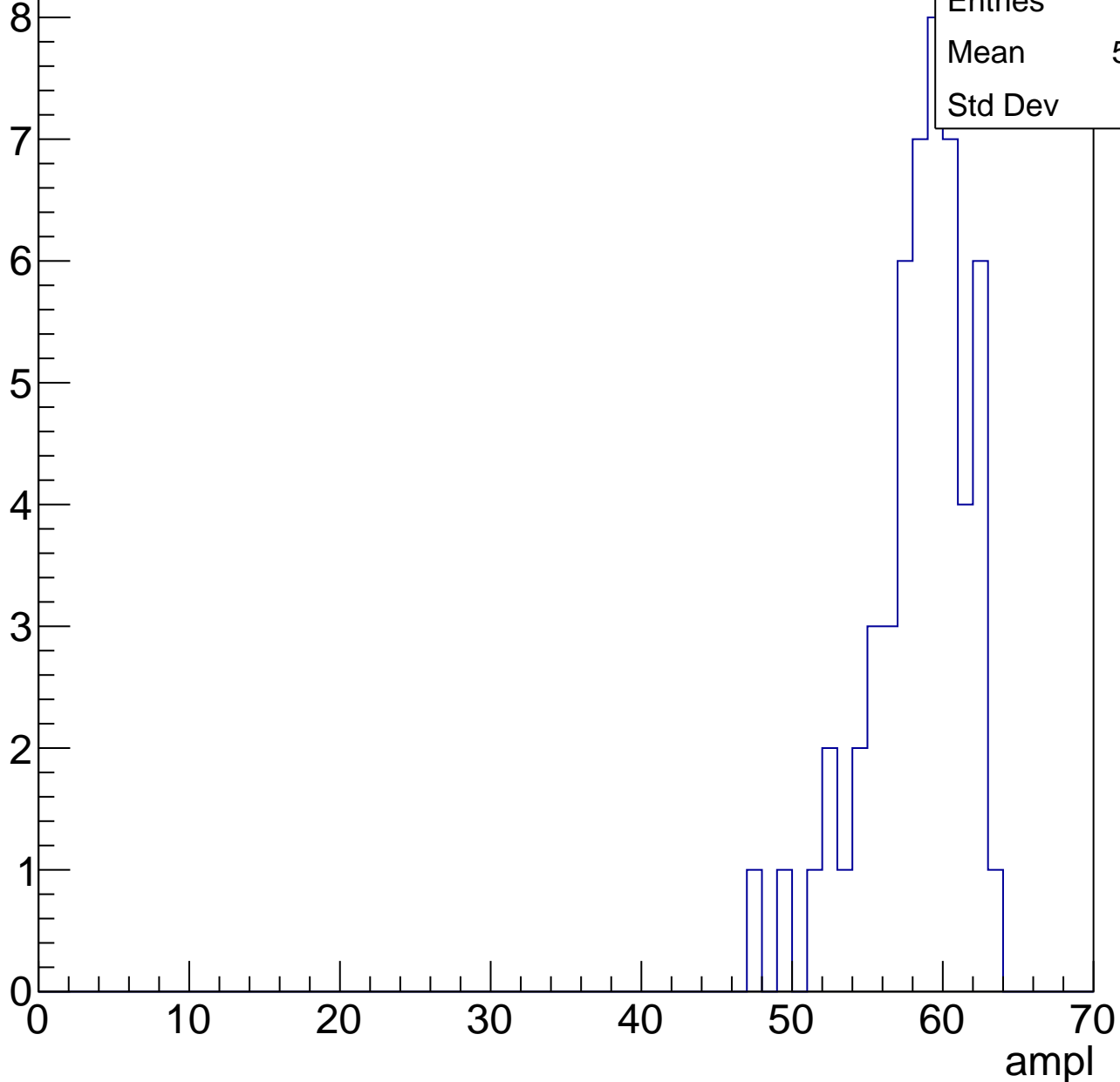


# B1L002S, U26-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.81
Std Dev	3.41

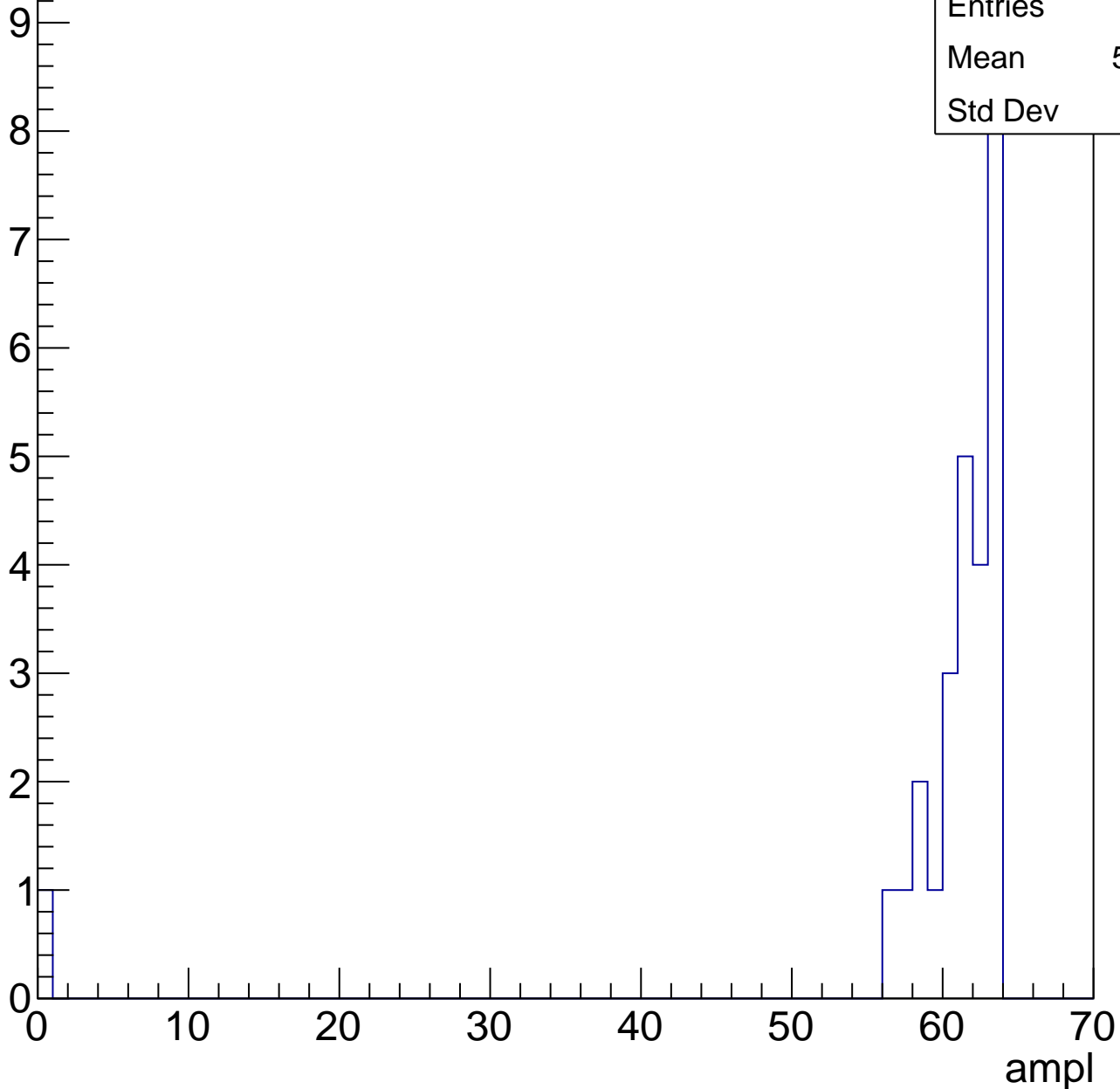


# B1L002S, U26-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.81
Std Dev	11.7





# B1L002S, U26-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U26-ch111, adc0

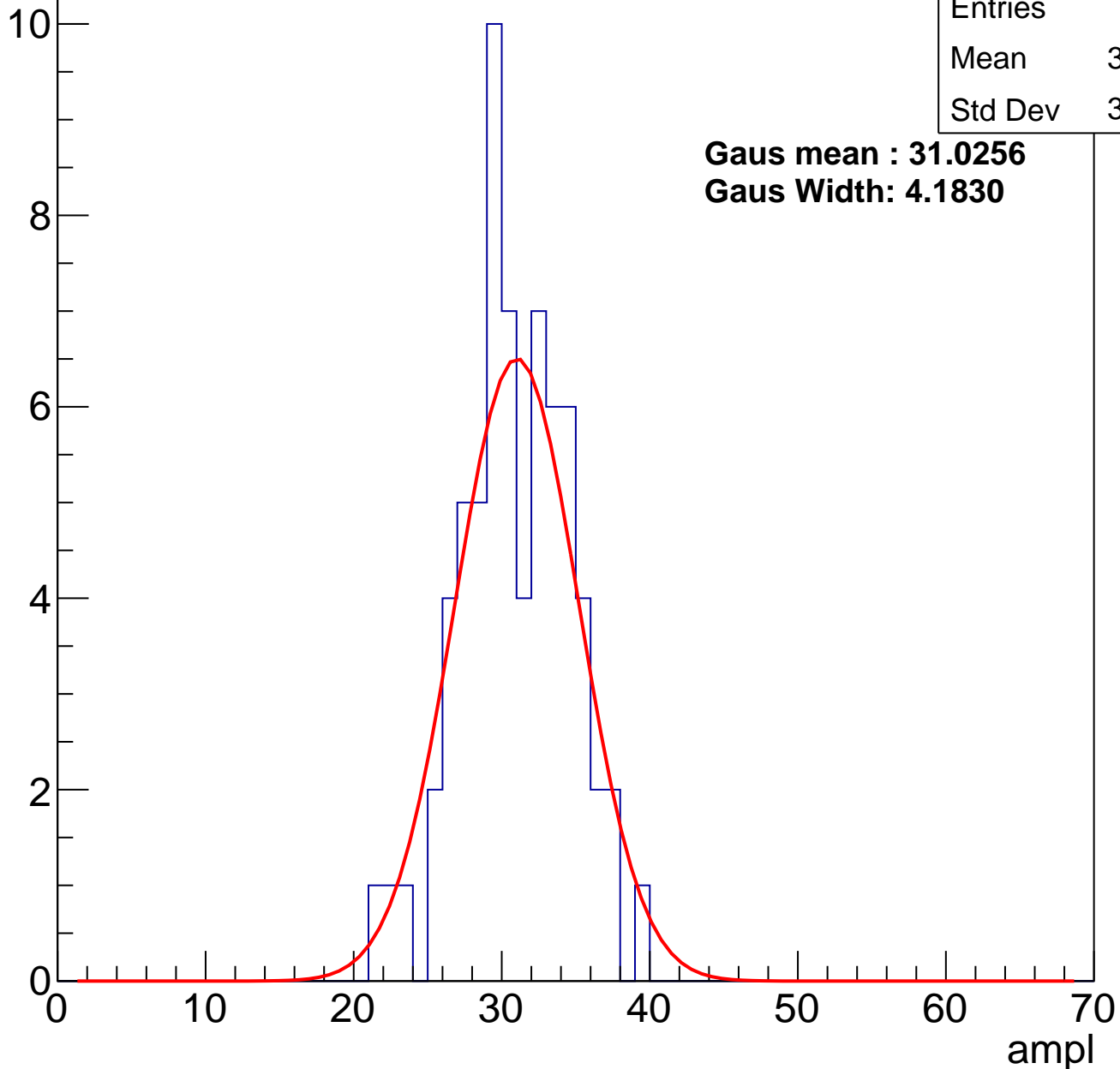
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	30.44
Std Dev	3.644

**Gaus mean : 31.0256**

**Gaus Width: 4.1830**

Entry



# B1L002S, U26-ch111, adc1

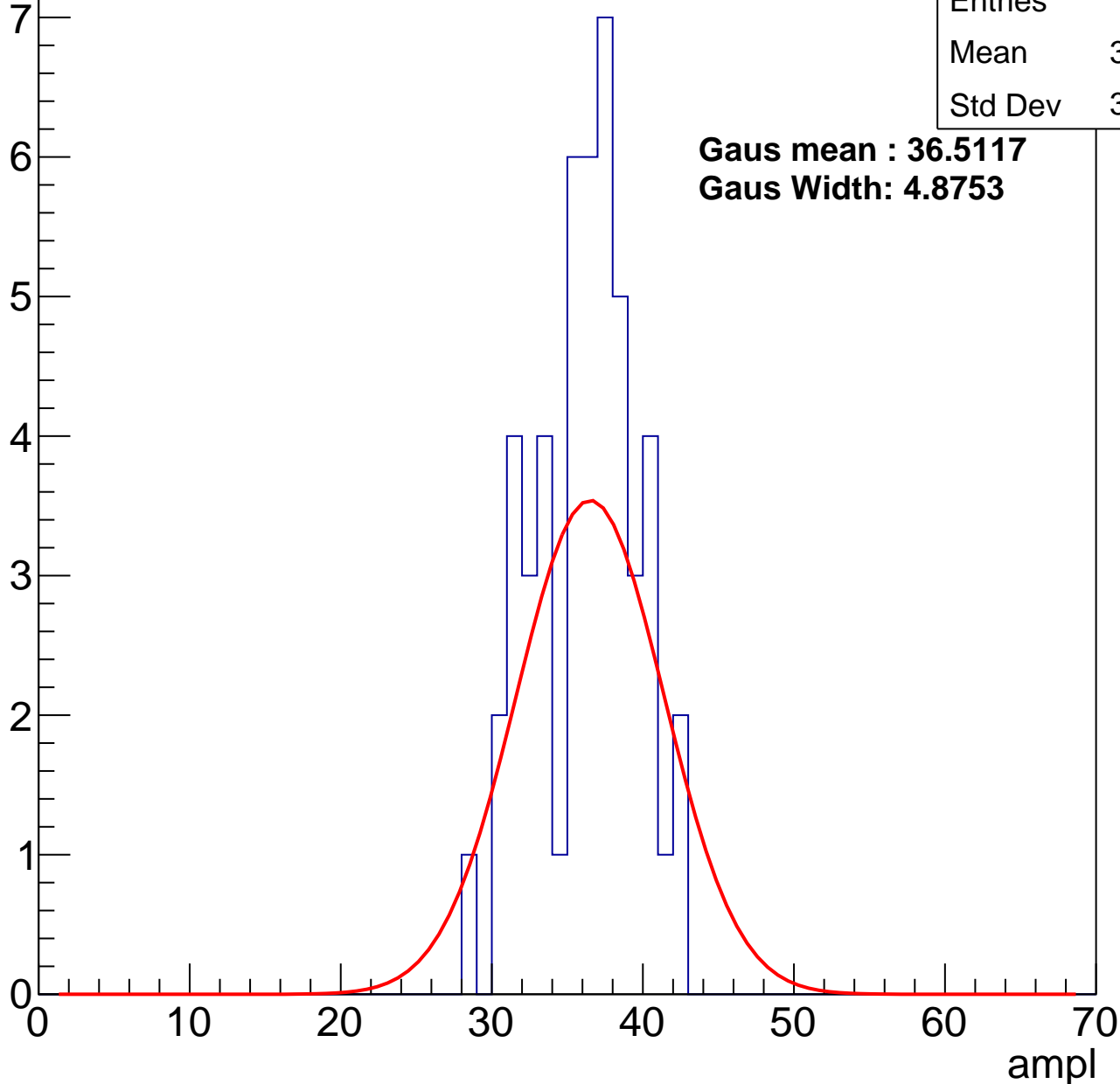
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	35.73
Std Dev	3.324

**Gaus mean : 36.5117**

**Gaus Width: 4.8753**



# B1L002S, U26-ch111, adc2

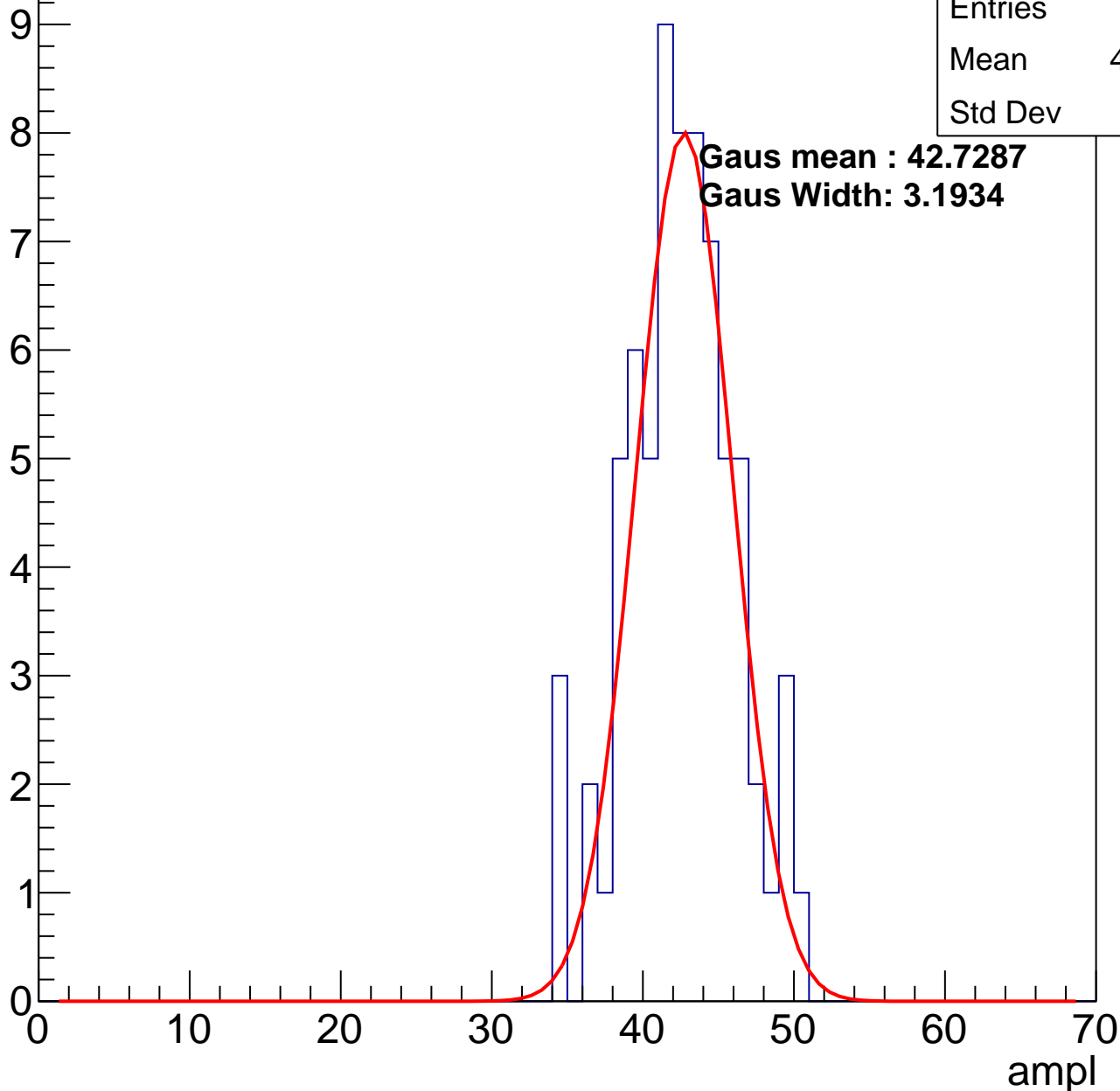
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	42.06
Std Dev	3.58

**Gaus mean : 42.7287**

**Gaus Width: 3.1934**

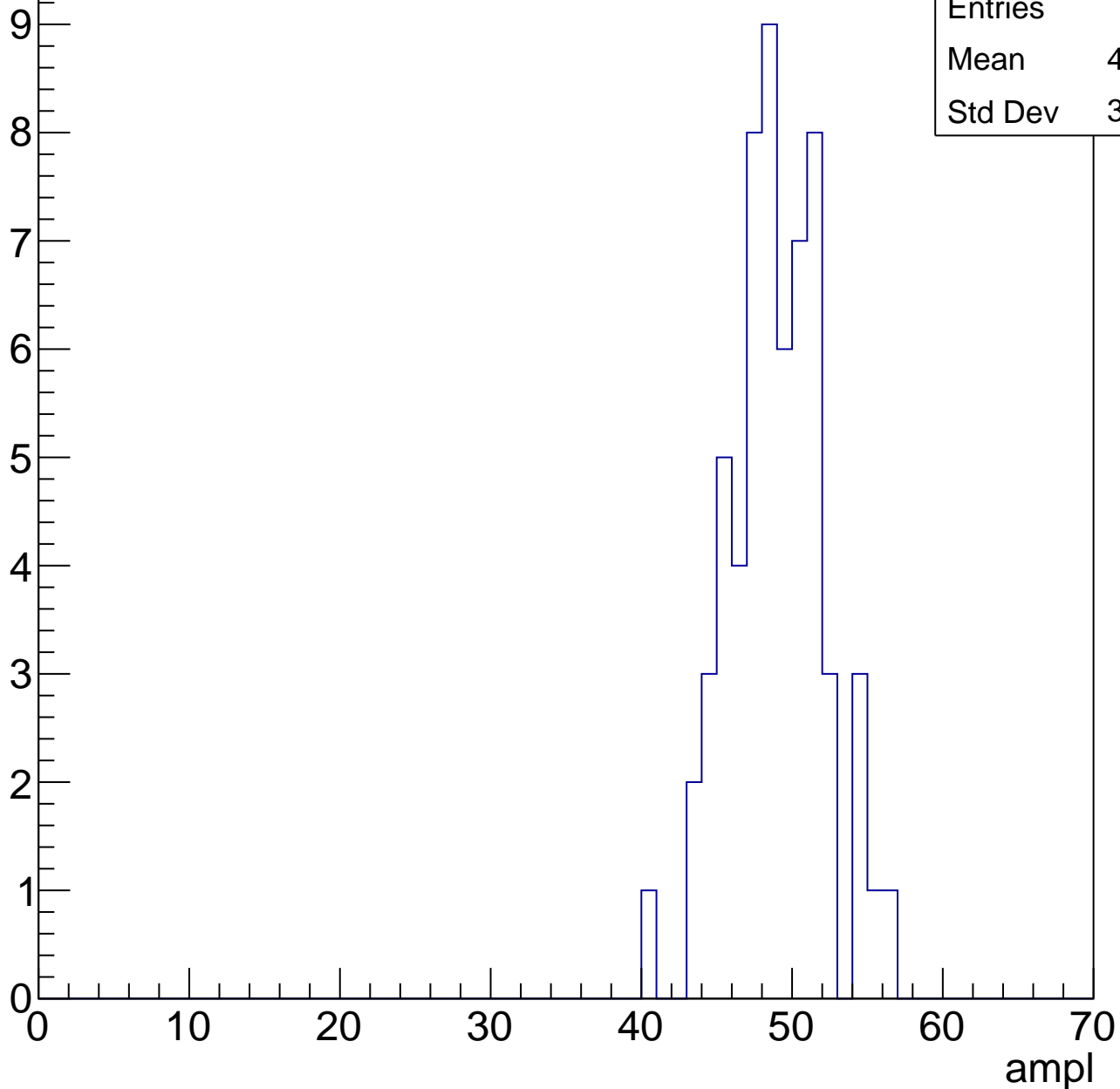


# B1L002S, U26-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	48.46
Std Dev	3.118

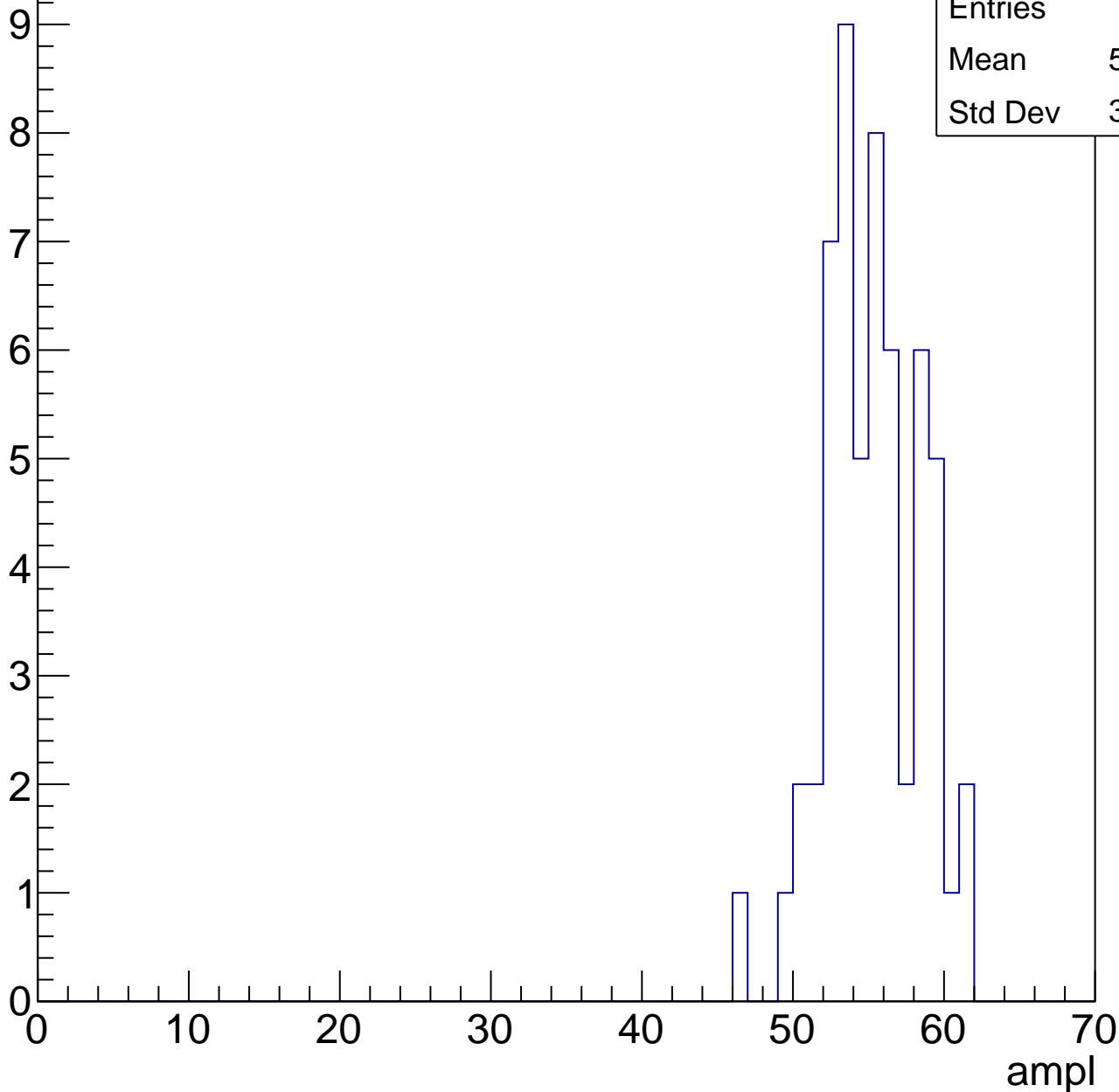


# B1L002S, U26-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

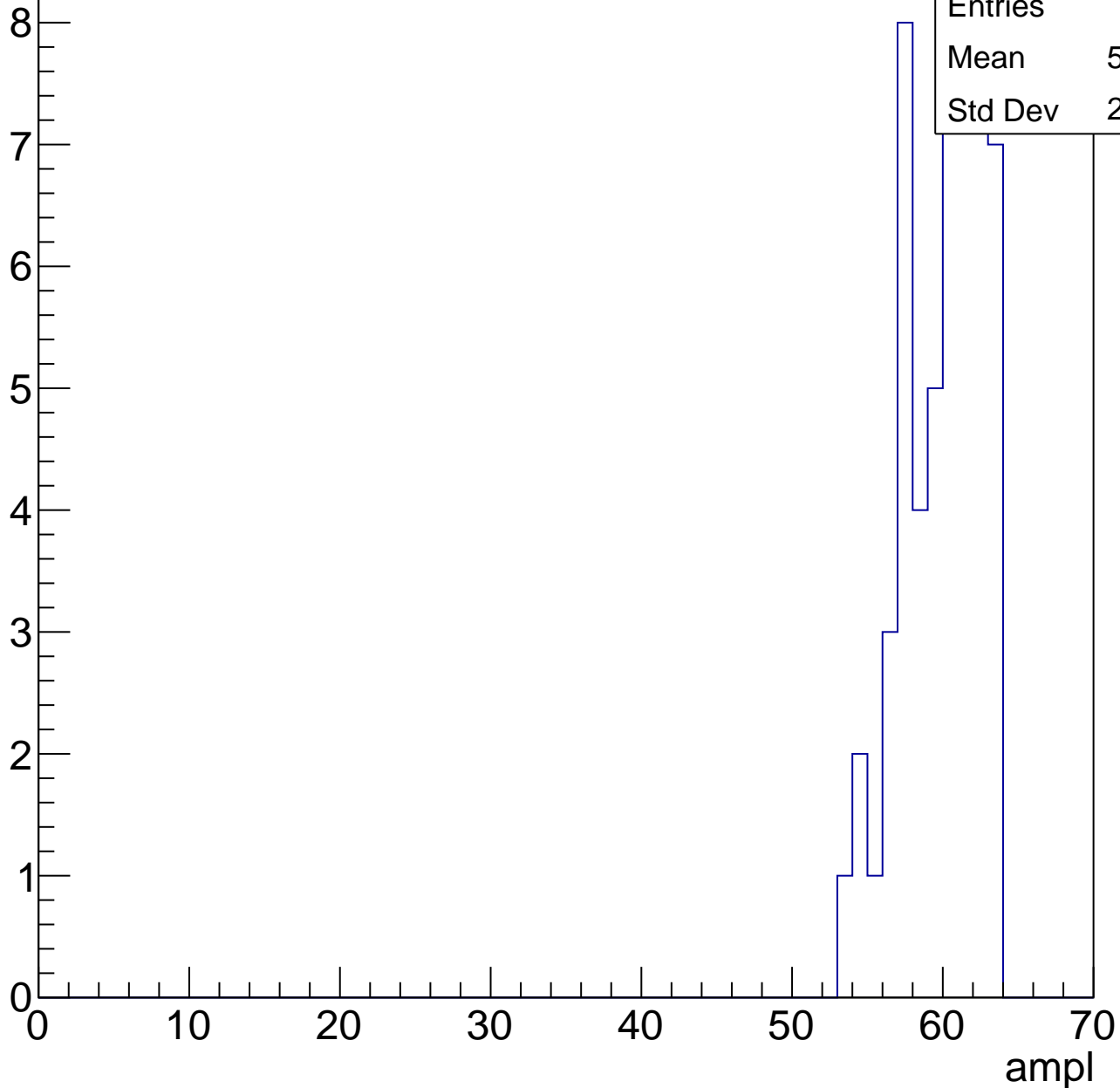
Entries	57
Mean	54.79
Std Dev	3.093



# B1L002S, U26-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

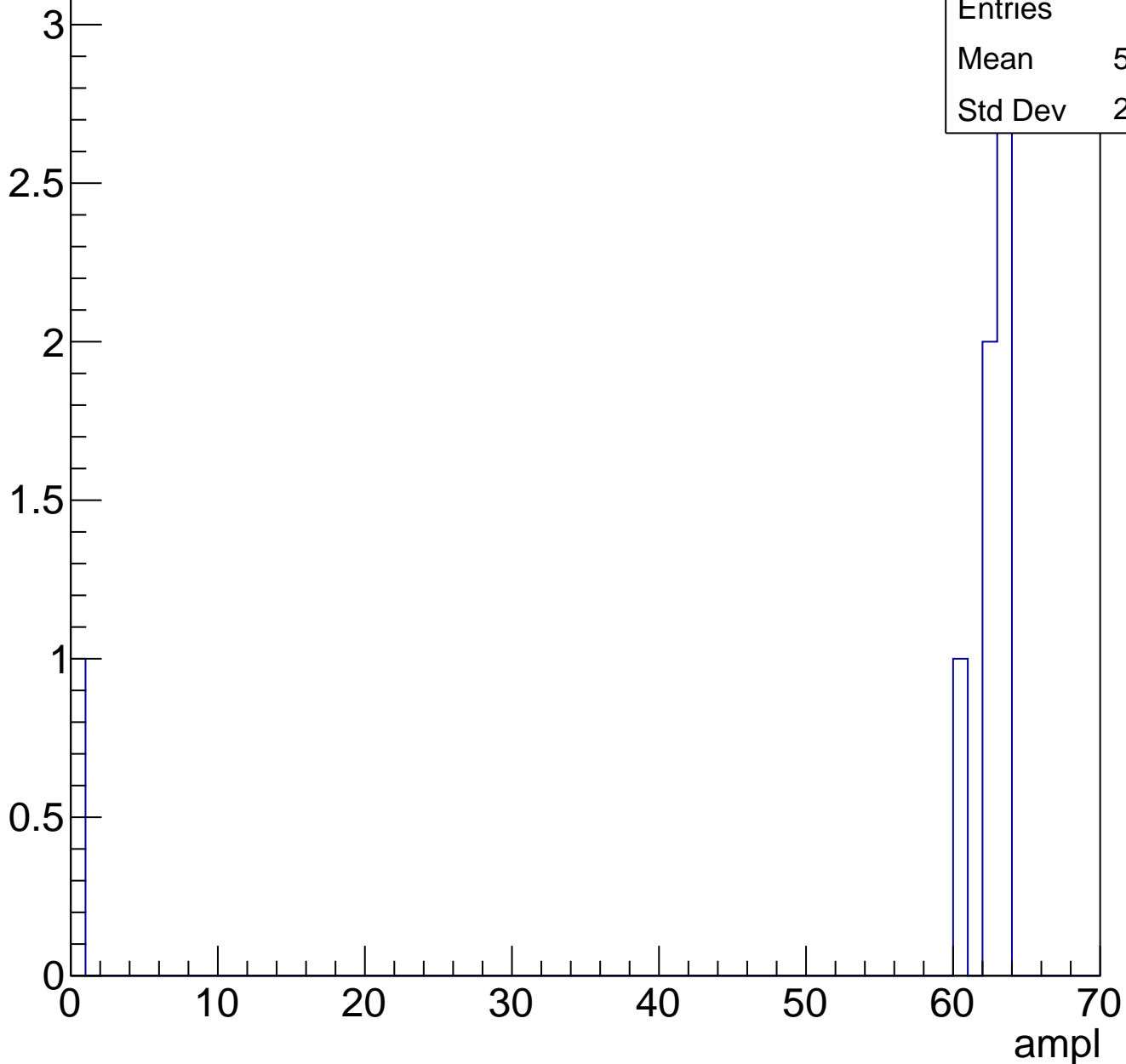


Entries	55
Mean	59.49
Std Dev	2.614

# B1L002S, U26-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch112, adc0

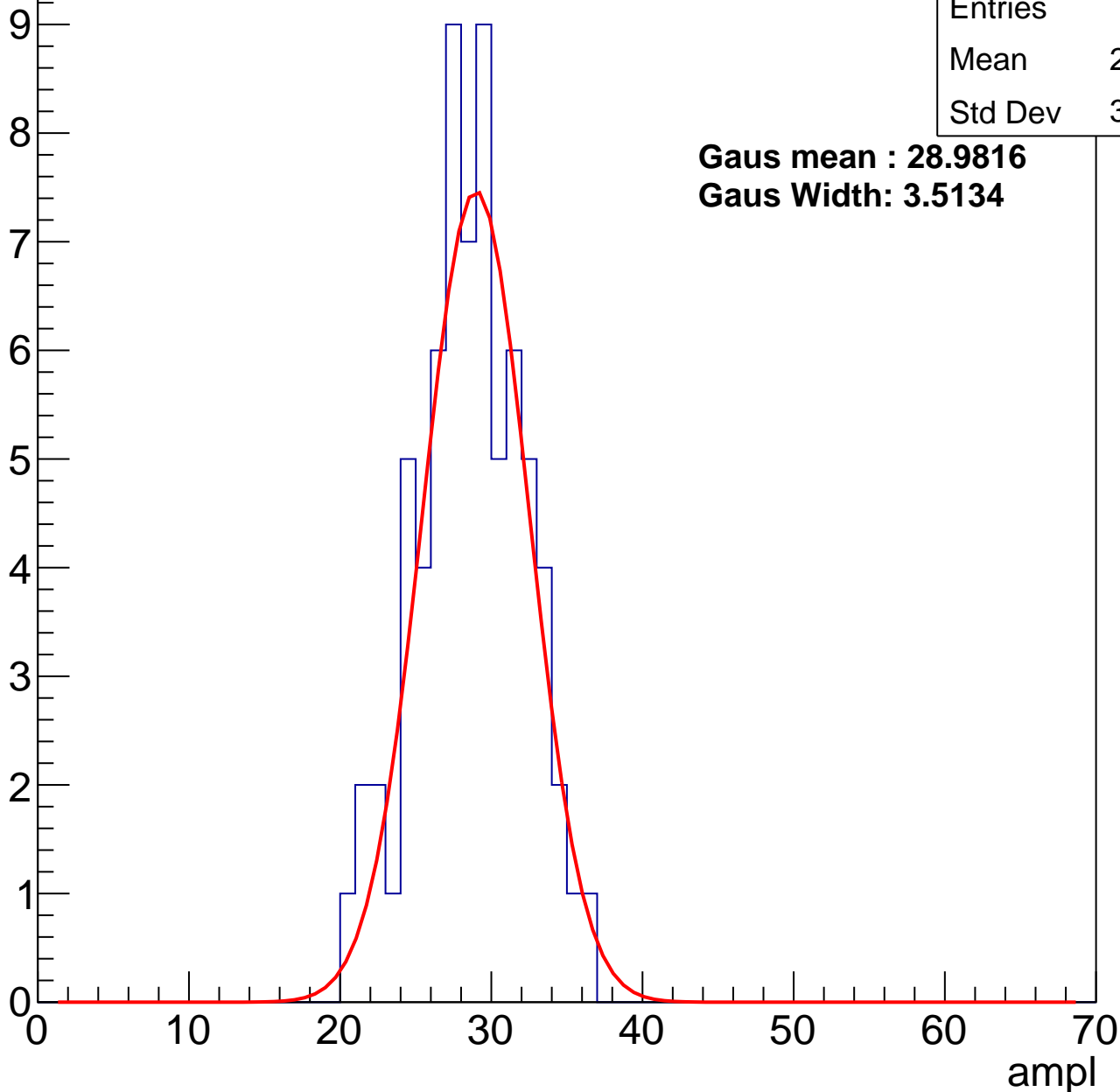
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.17
Std Dev	3.493

**Gaus mean : 28.9816**

**Gaus Width: 3.5134**



# B1L002S, U26-ch112, adc1

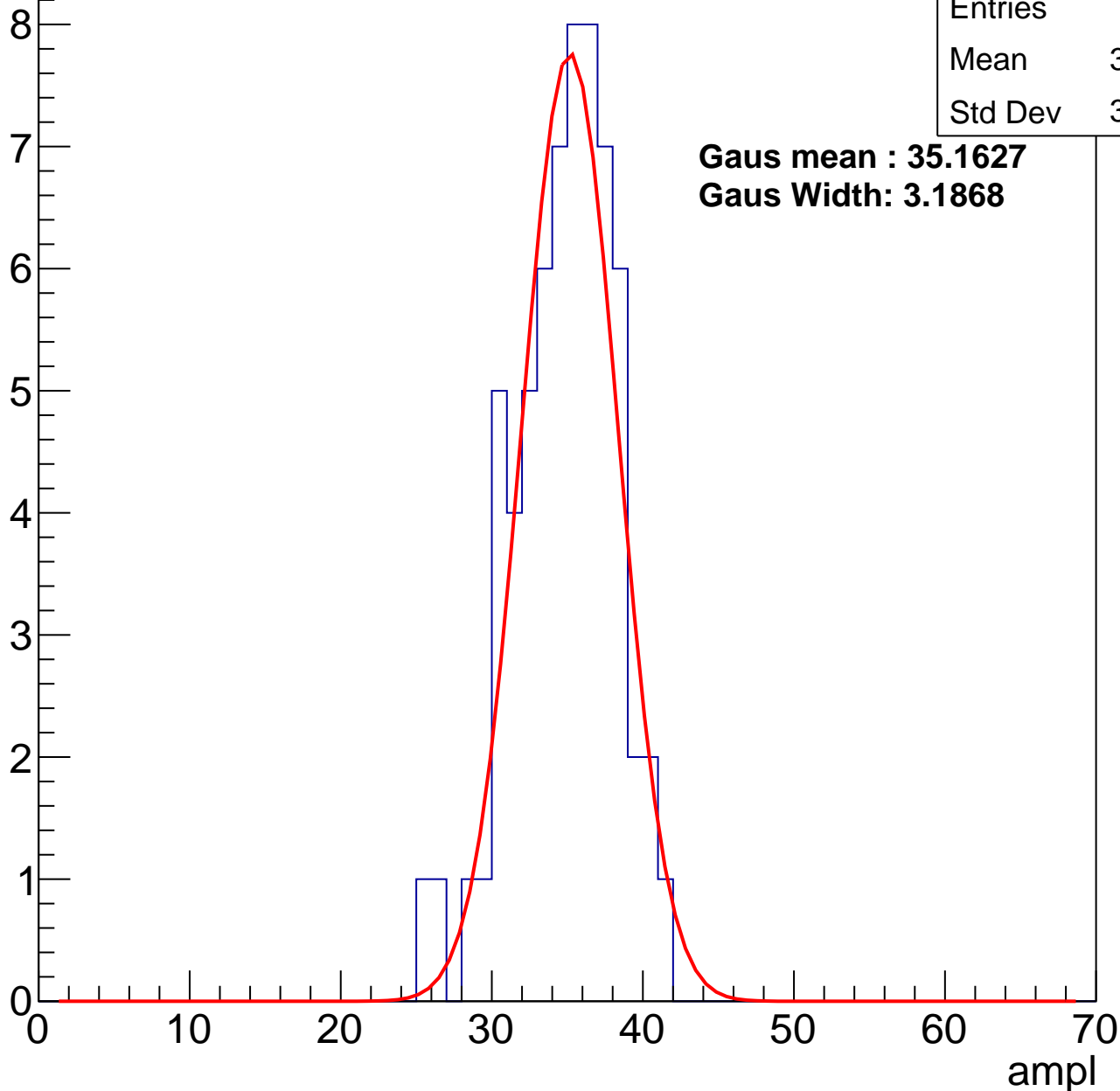
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.34
Std Dev	3.306

**Gaus mean : 35.1627**

**Gaus Width: 3.1868**



# B1L002S, U26-ch112, adc2

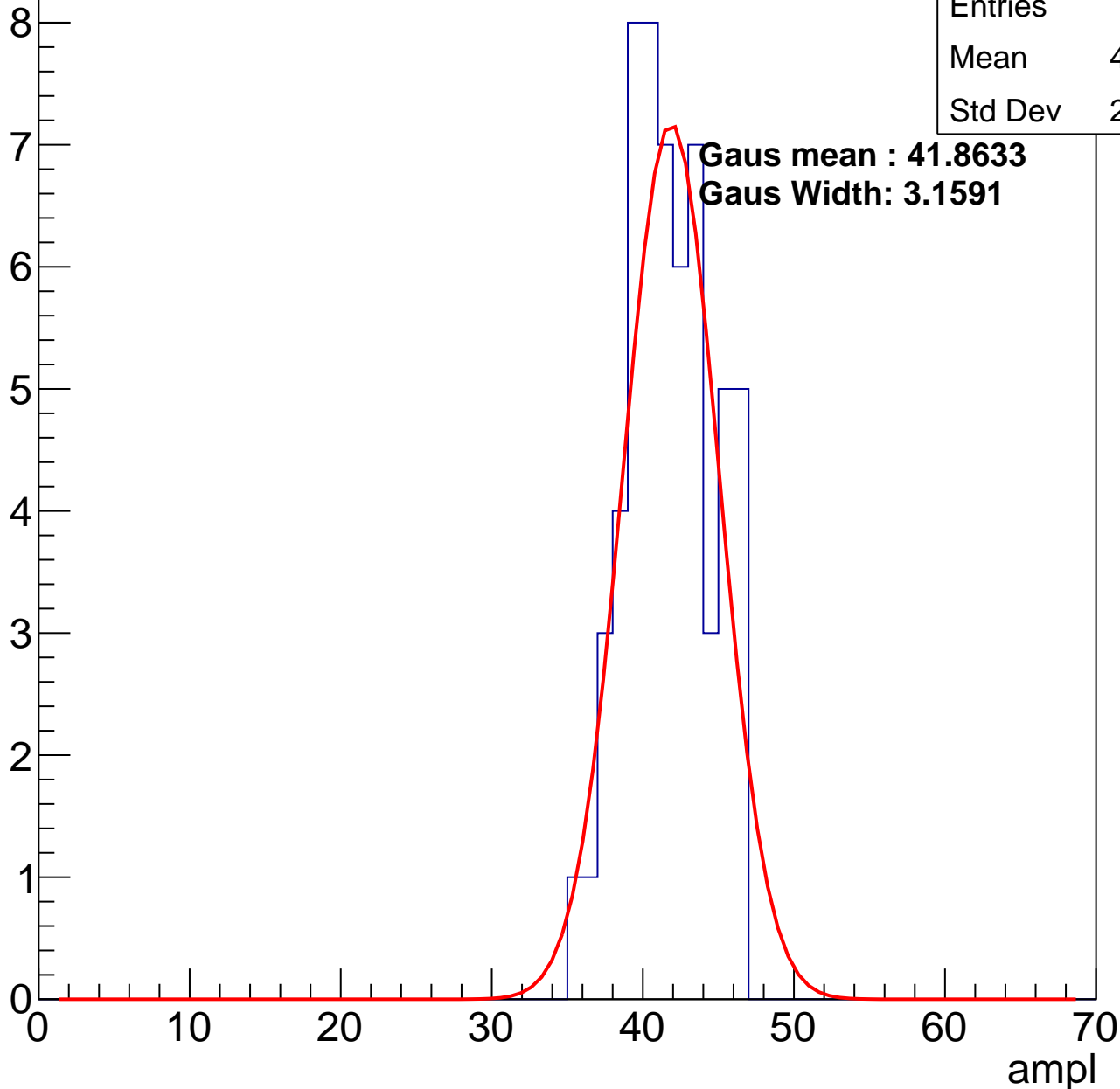
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.26
Std Dev	2.777

**Gaus mean : 41.8633**

**Gaus Width: 3.1591**

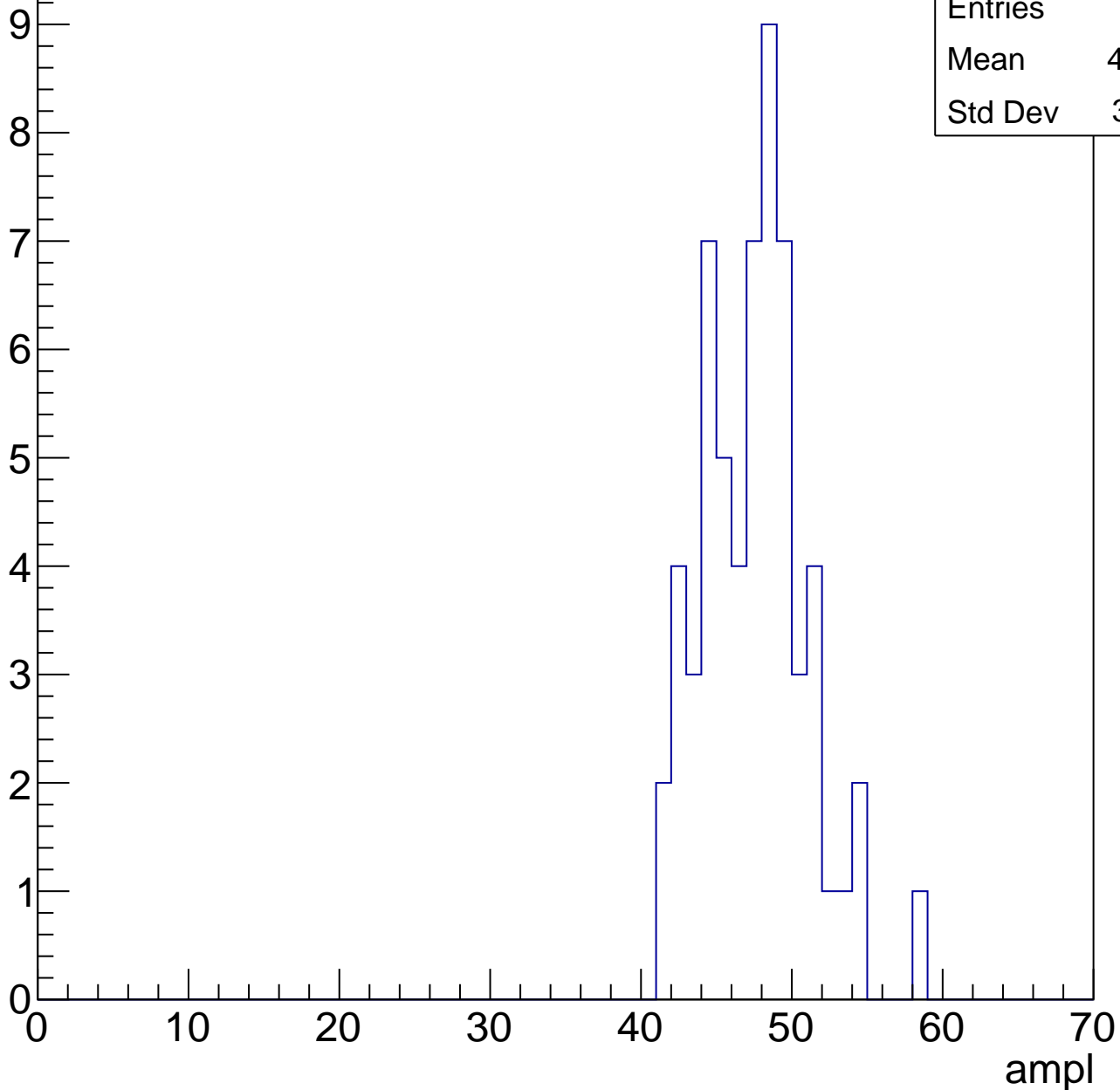


# B1L002S, U26-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	47.08
Std Dev	3.451

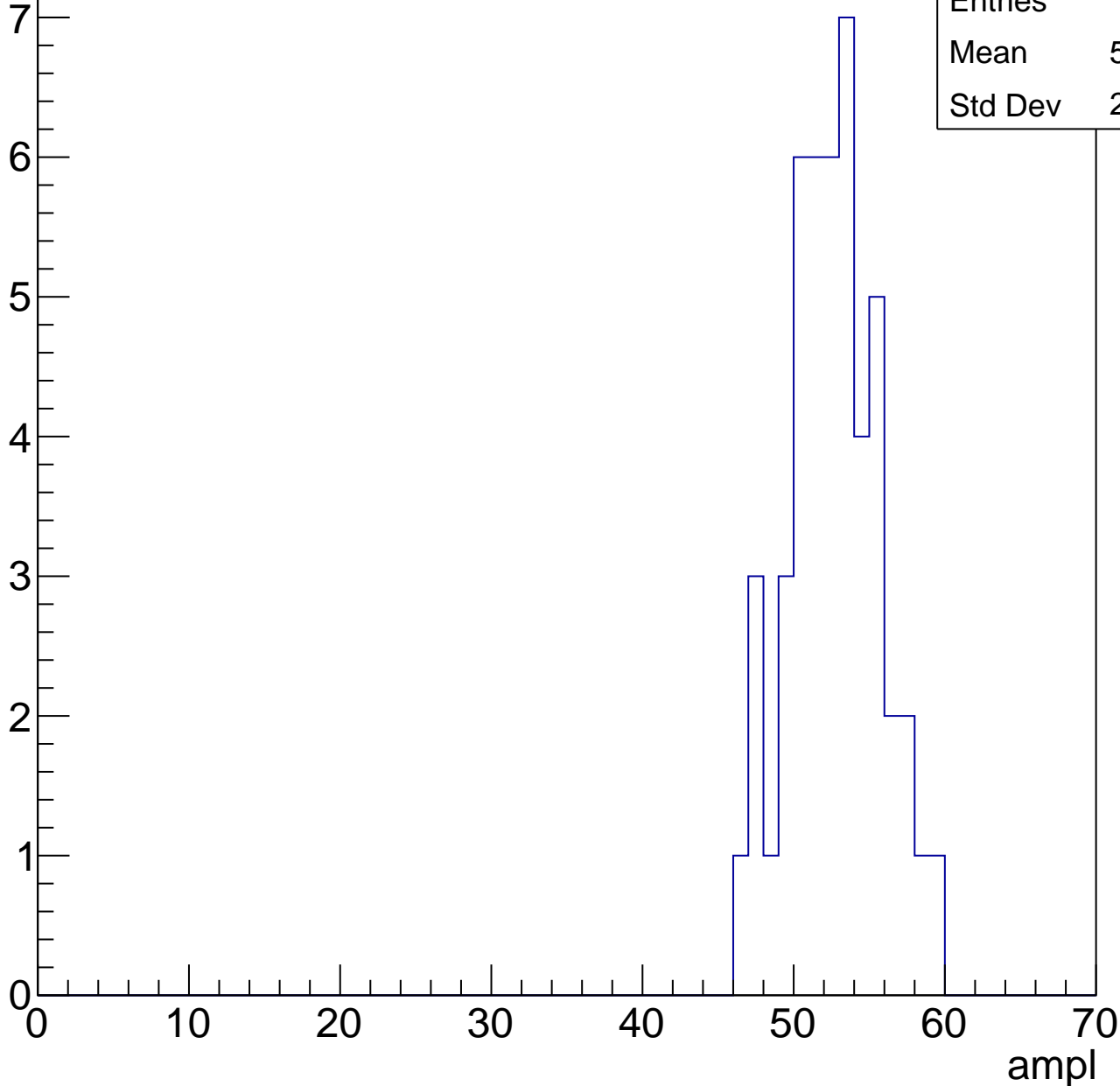


# B1L002S, U26-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	52.19
Std Dev	2.949

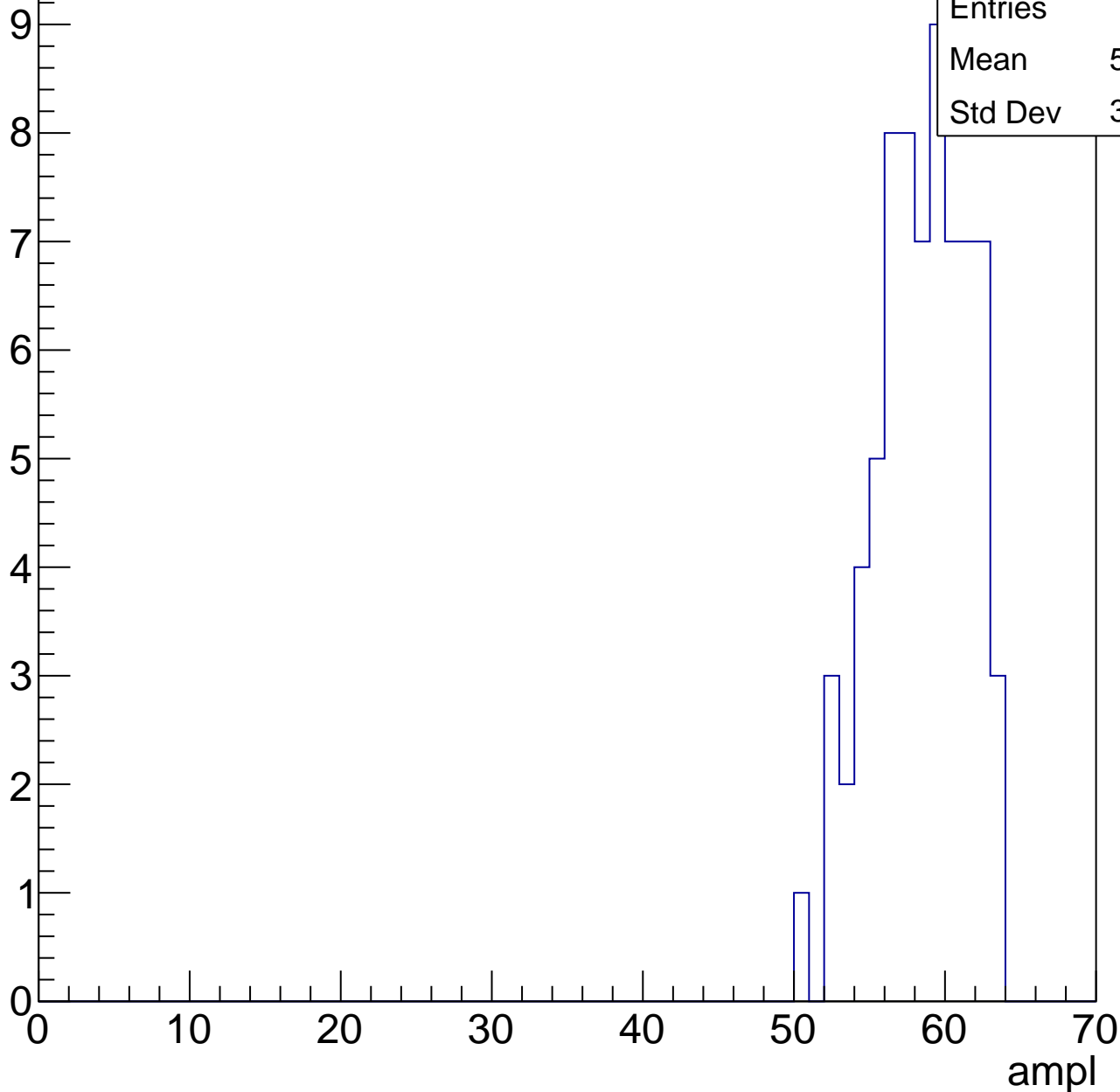


# B1L002S, U26-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	57.94
Std Dev	3.044

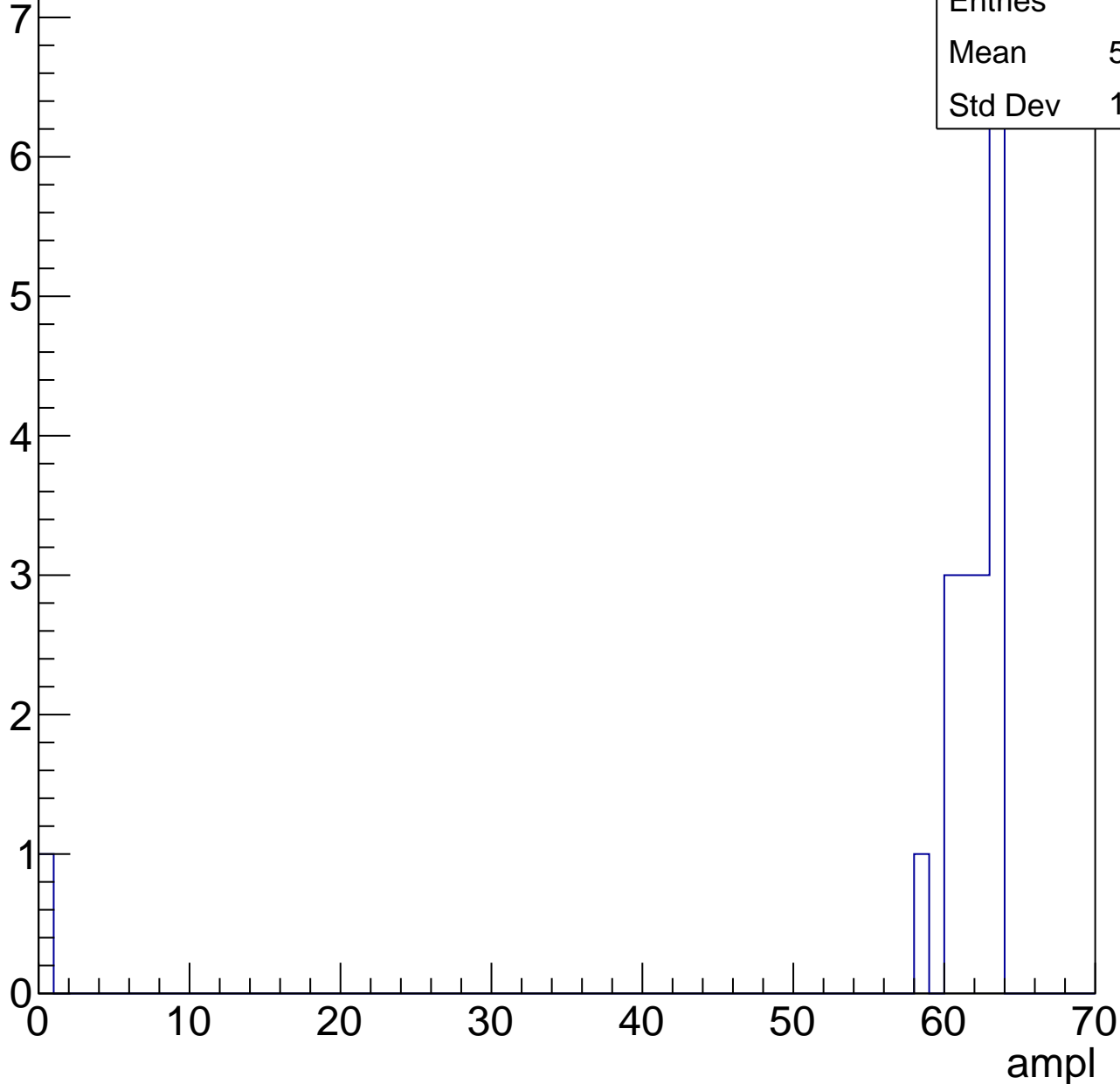


# B1L002S, U26-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.22
Std Dev	14.19





# B1L002S, U26-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U26-ch113, adc0

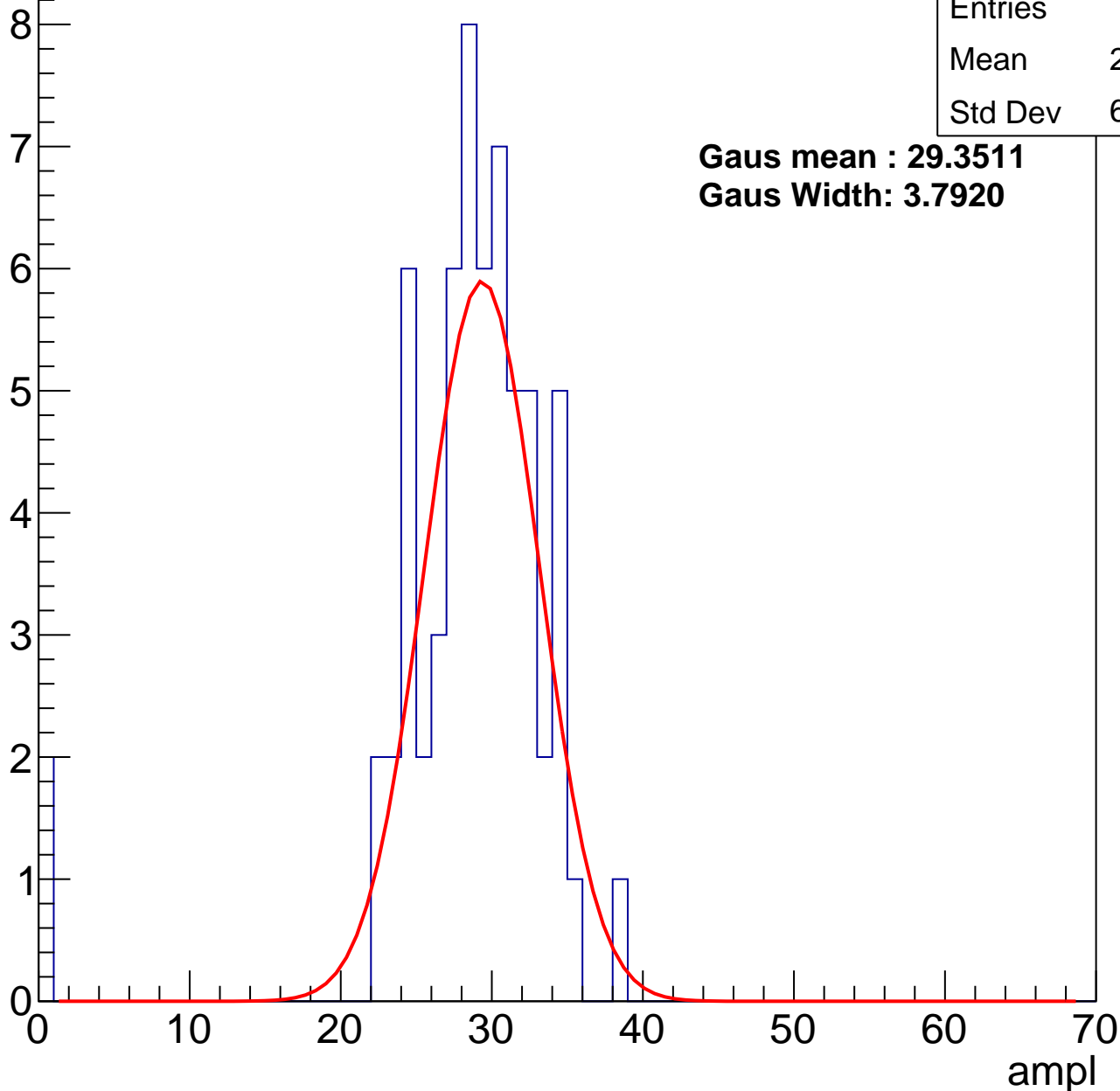
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.87
Std Dev	6.119

**Gaus mean : 29.3511**

**Gaus Width: 3.7920**



# B1L002S, U26-ch113, adc1

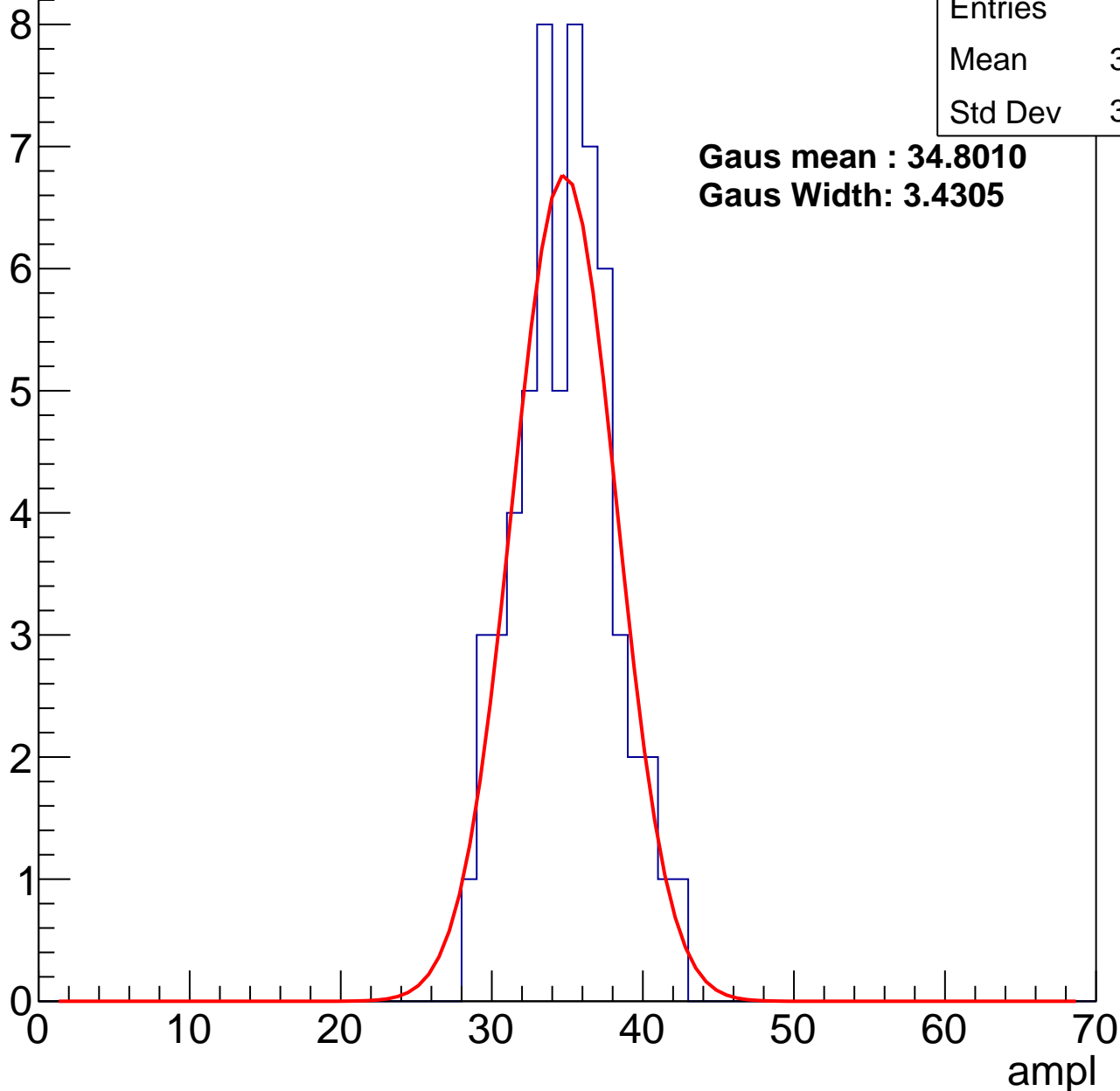
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.44
Std Dev	3.148

**Gaus mean : 34.8010**

**Gaus Width: 3.4305**



# B1L002S, U26-ch113, adc2

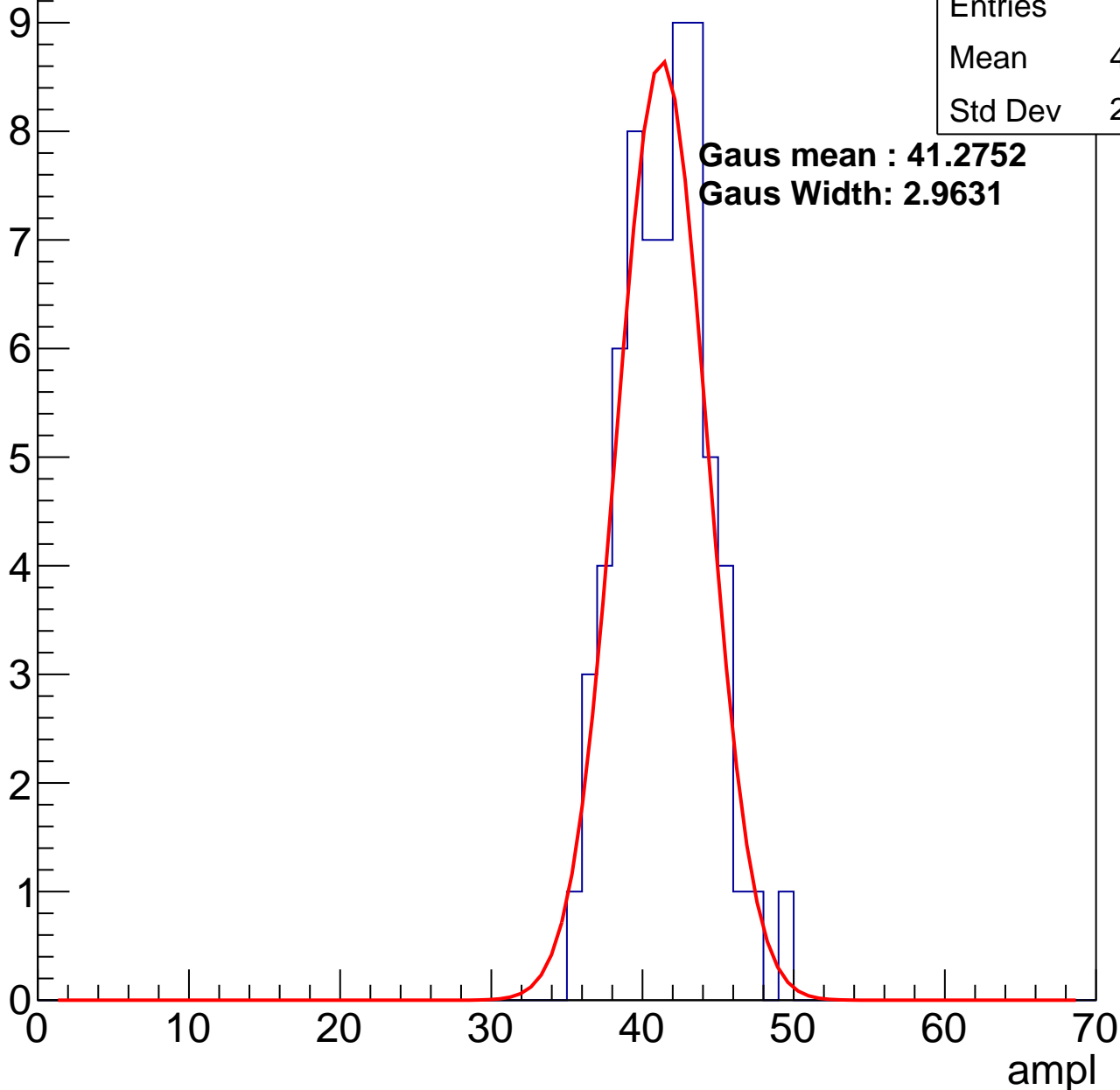
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.98
Std Dev	2.879

**Gaus mean : 41.2752**

**Gaus Width: 2.9631**

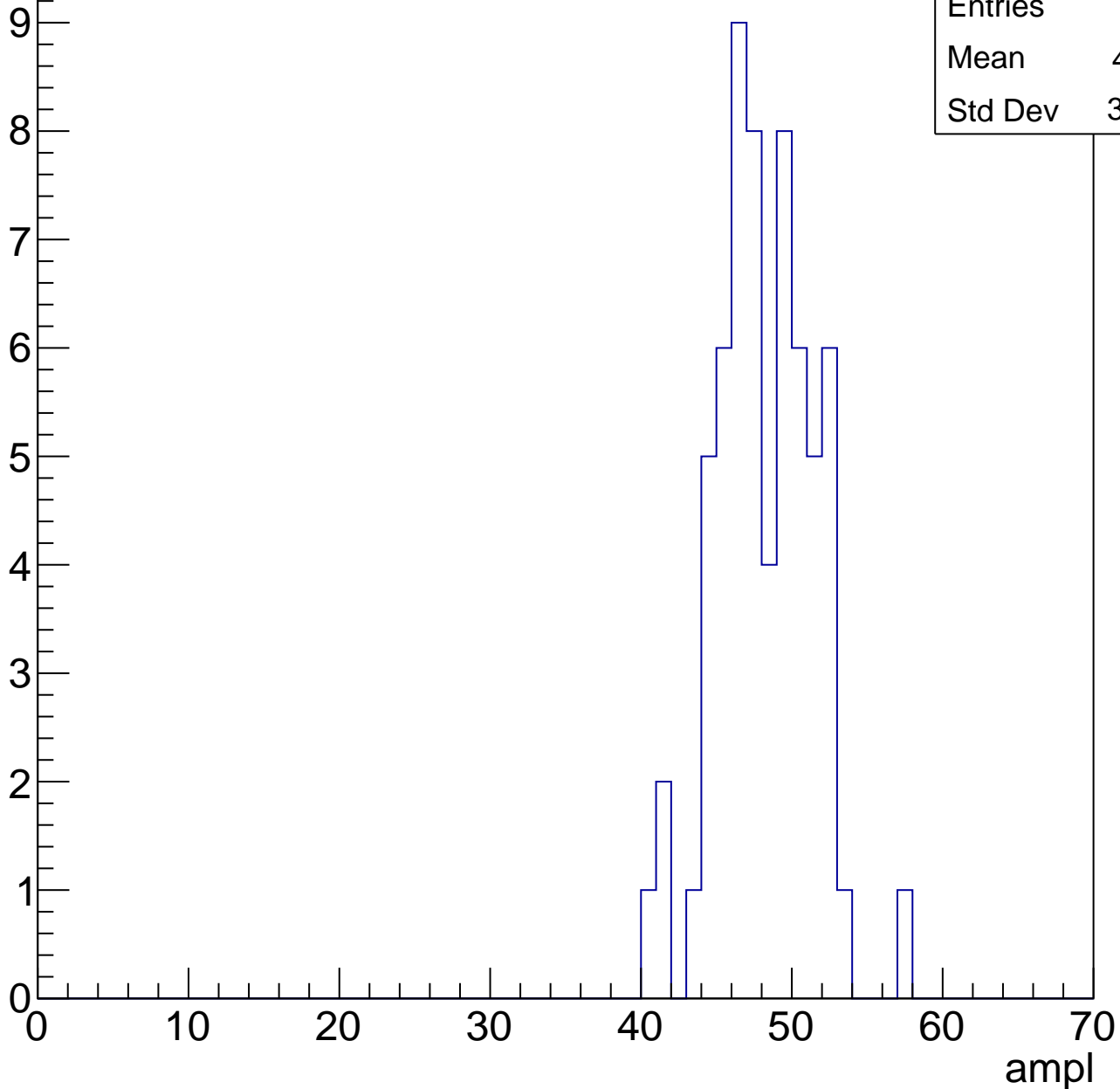


# B1L002S, U26-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	47.71
Std Dev	3.184

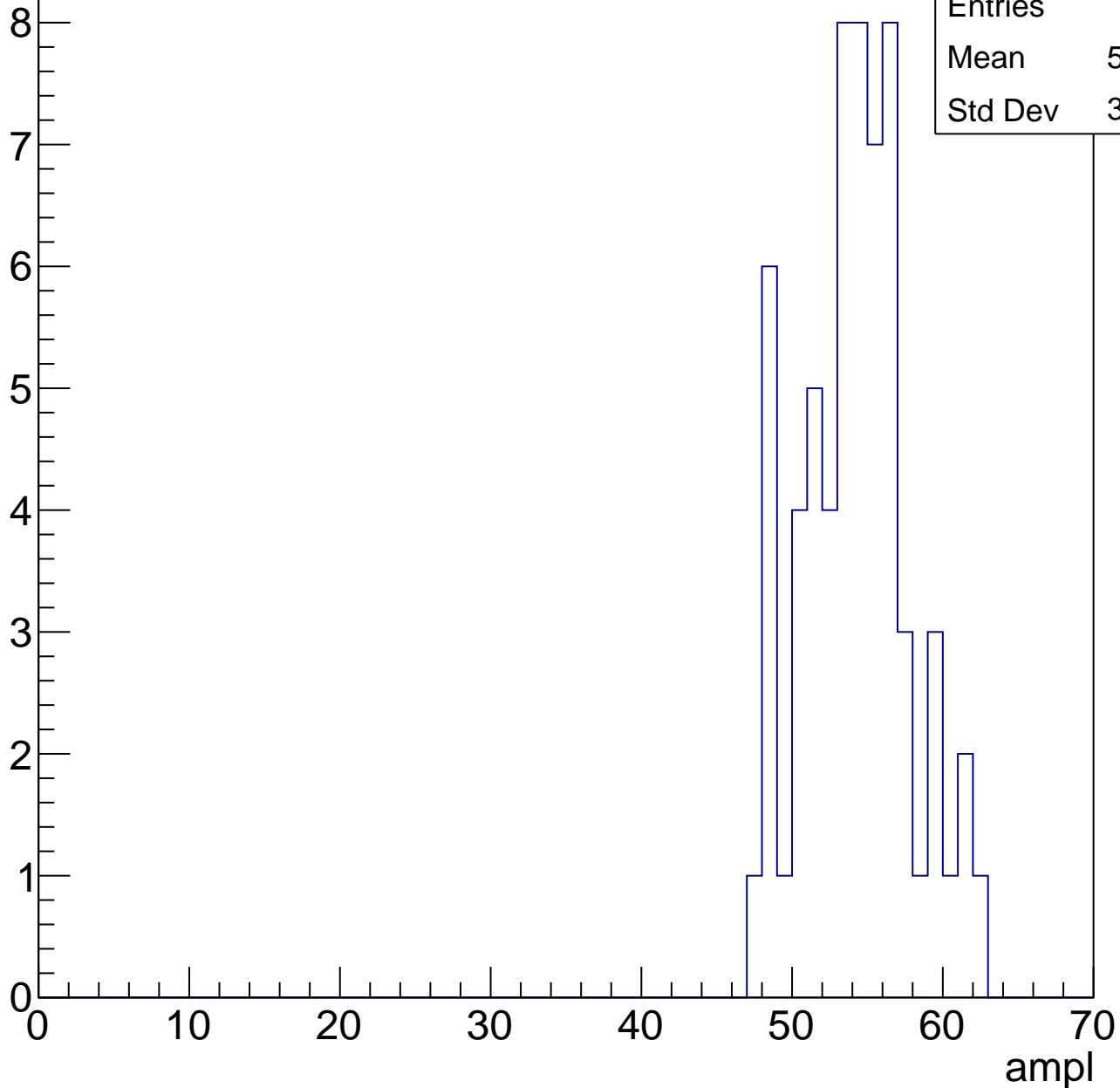


# B1L002S, U26-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	53.75
Std Dev	3.505

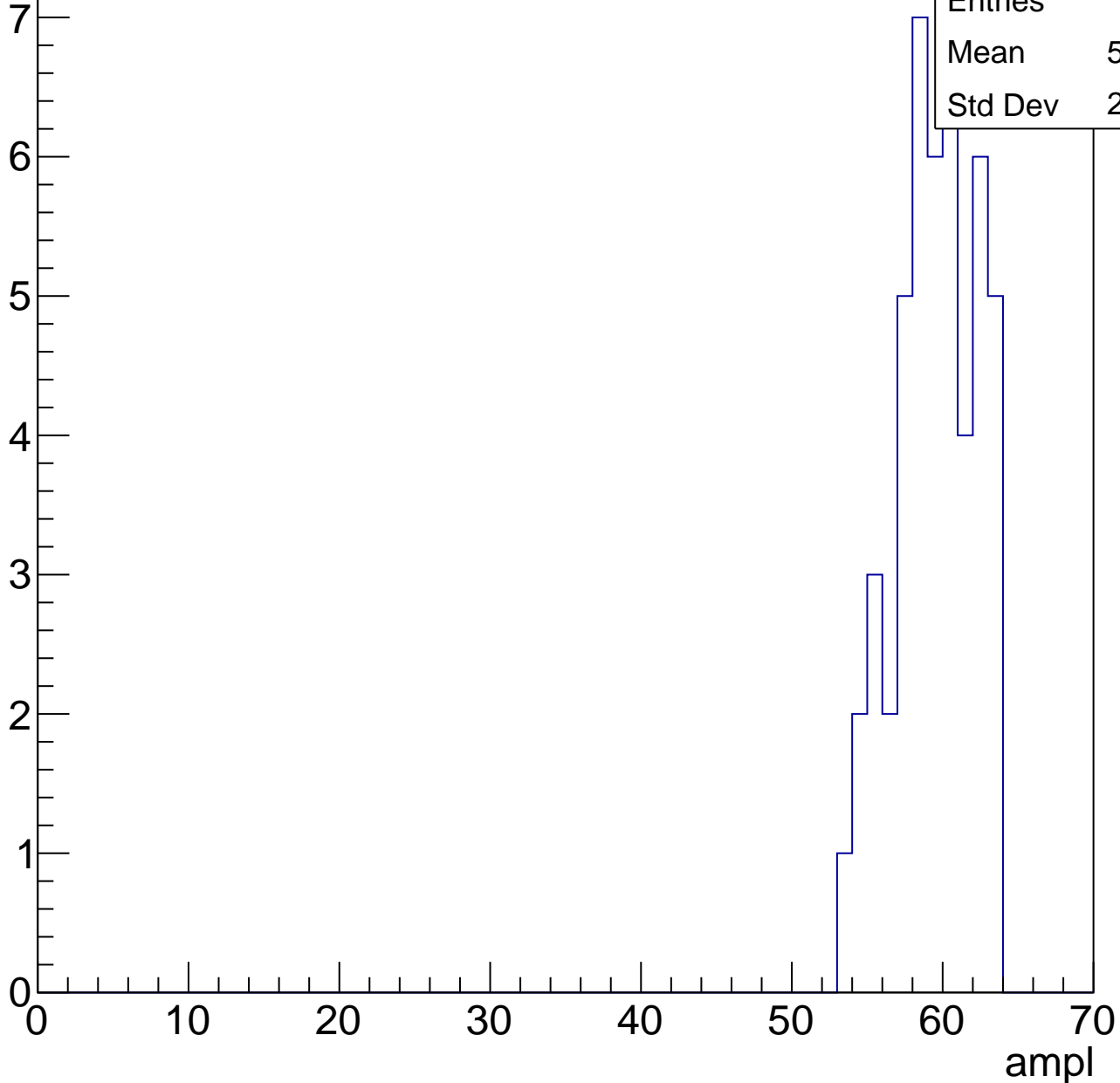


# B1L002S, U26-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	59.04
Std Dev	2.645

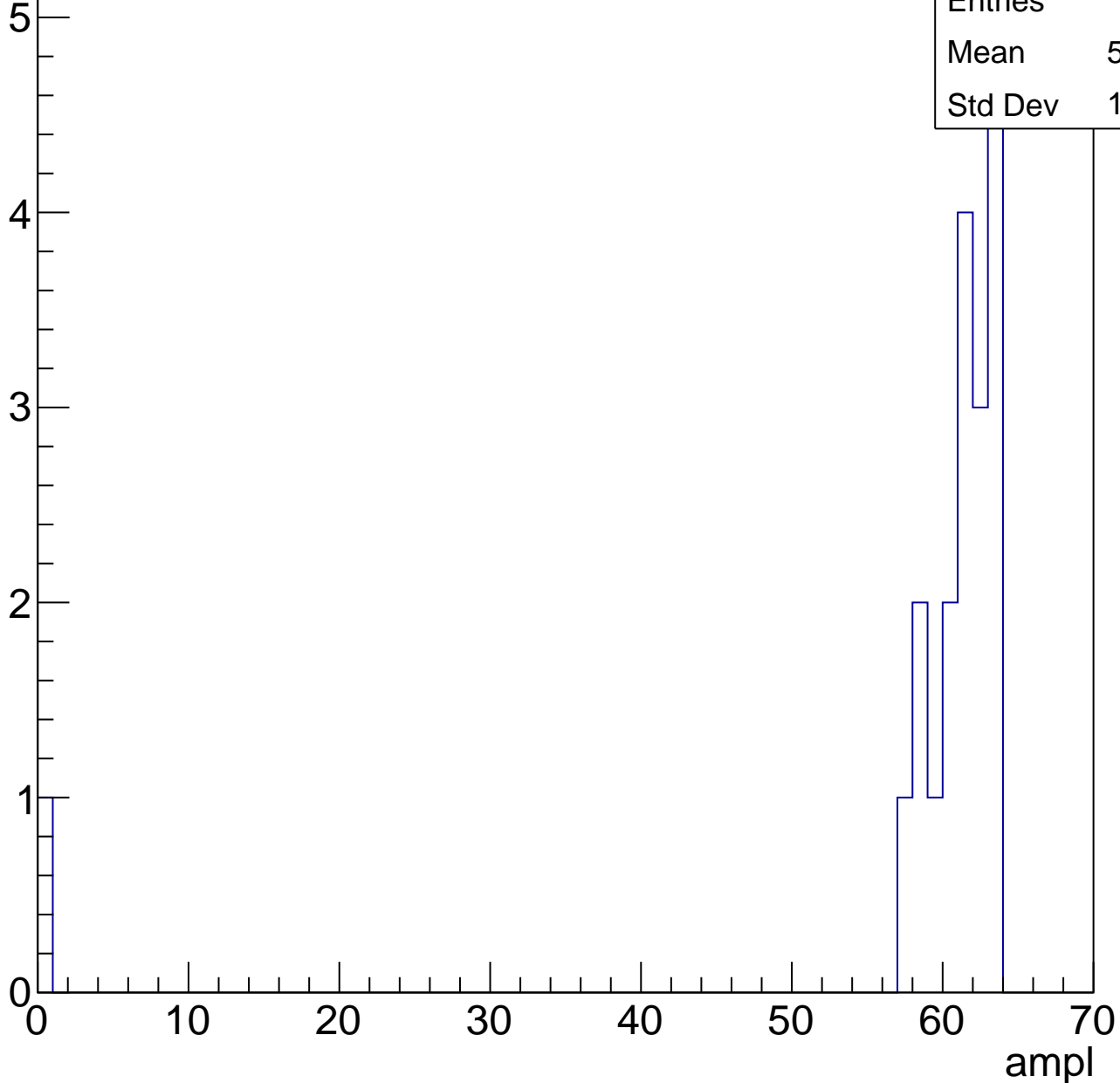


# B1L002S, U26-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	57.74
Std Dev	13.73

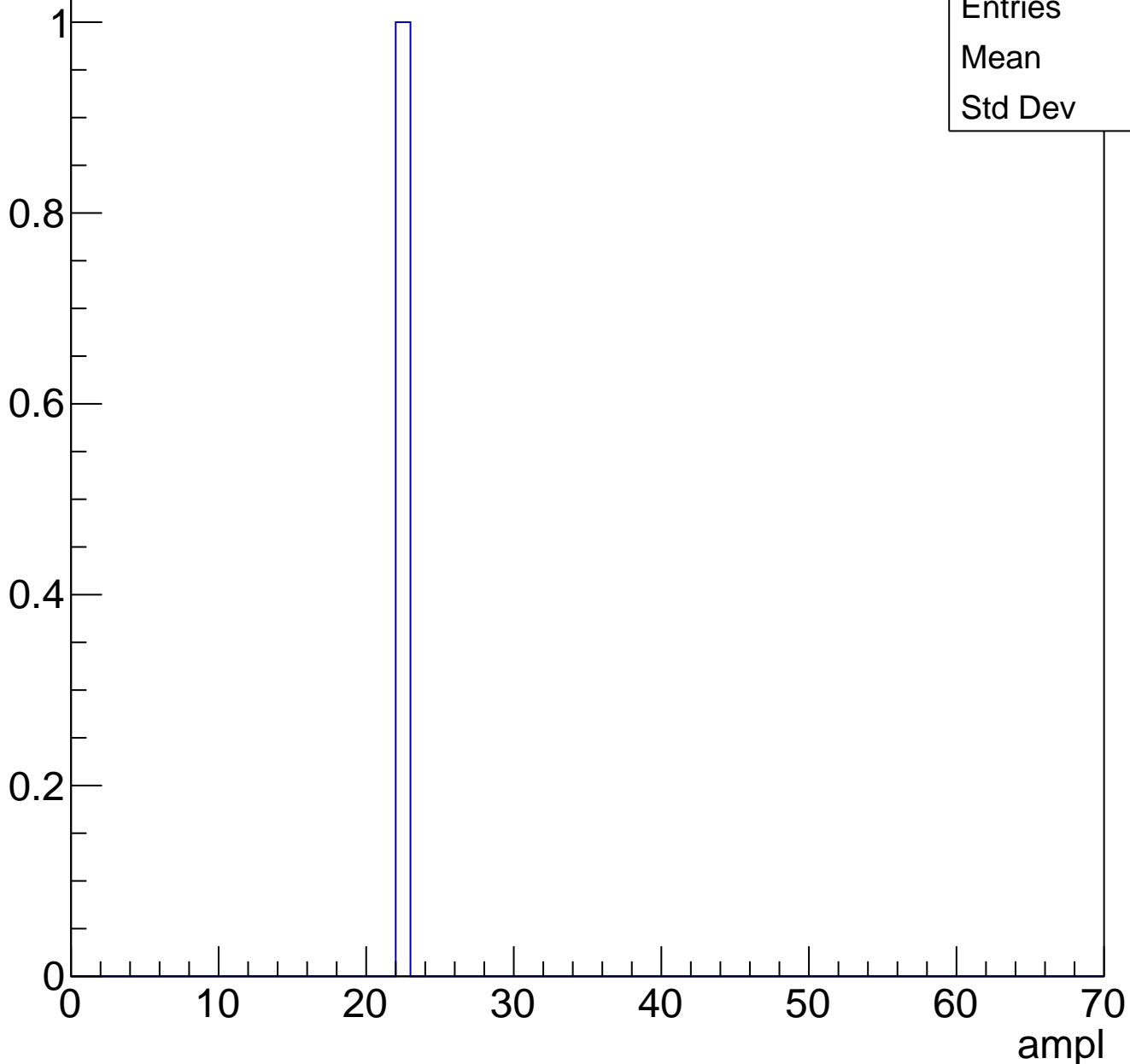




# B1L002S, U26-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	22
Std Dev	0

# B1L002S, U26-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	26.76
Std Dev	3.57

**Gaus mean : 27.6818**

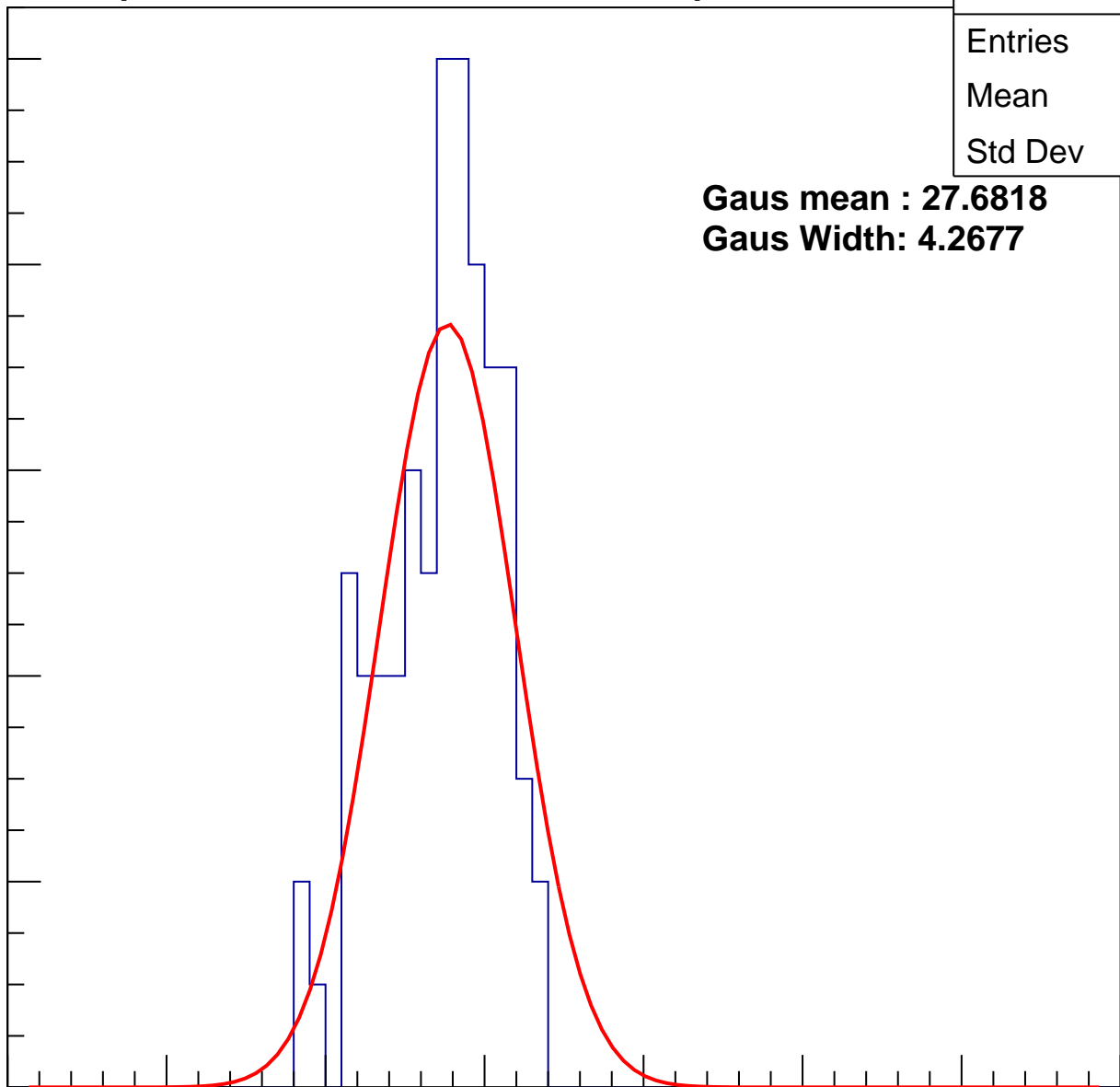
**Gaus Width: 4.2677**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch114, adc1

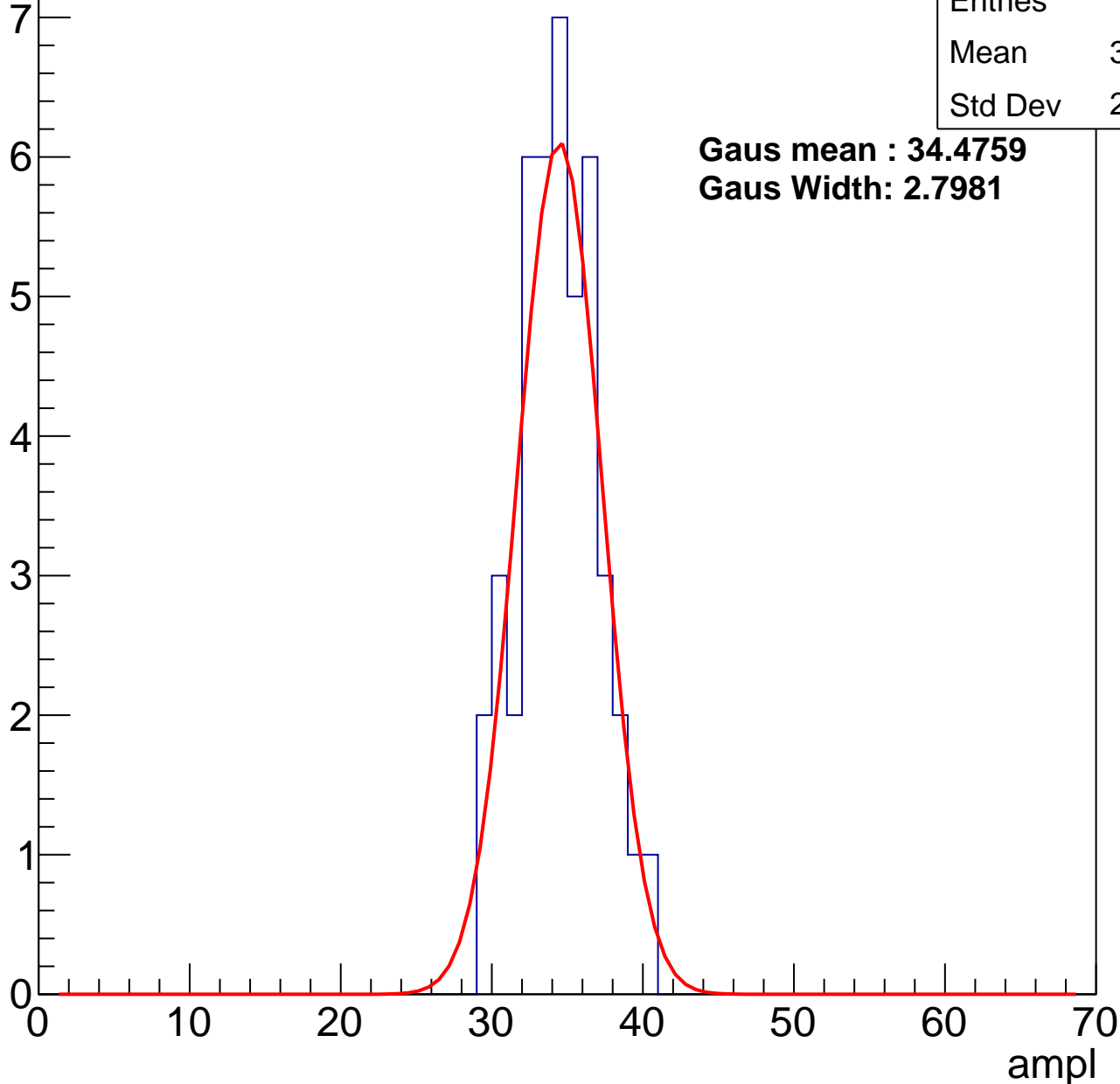
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	33.98
Std Dev	2.589

**Gaus mean : 34.4759**

**Gaus Width: 2.7981**



# B1L002S, U26-ch114, adc2

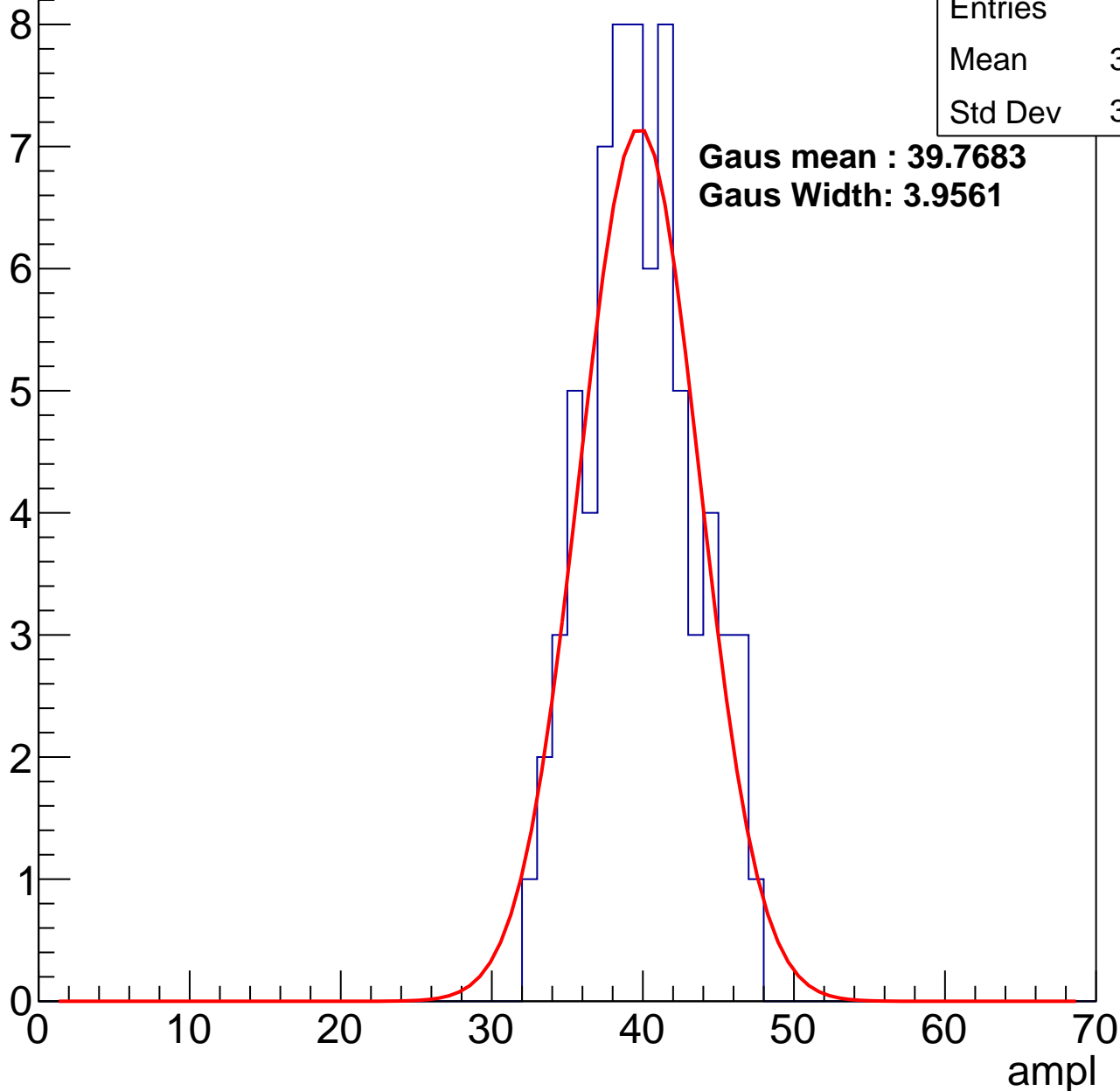
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	39.39
Std Dev	3.534

**Gaus mean : 39.7683**

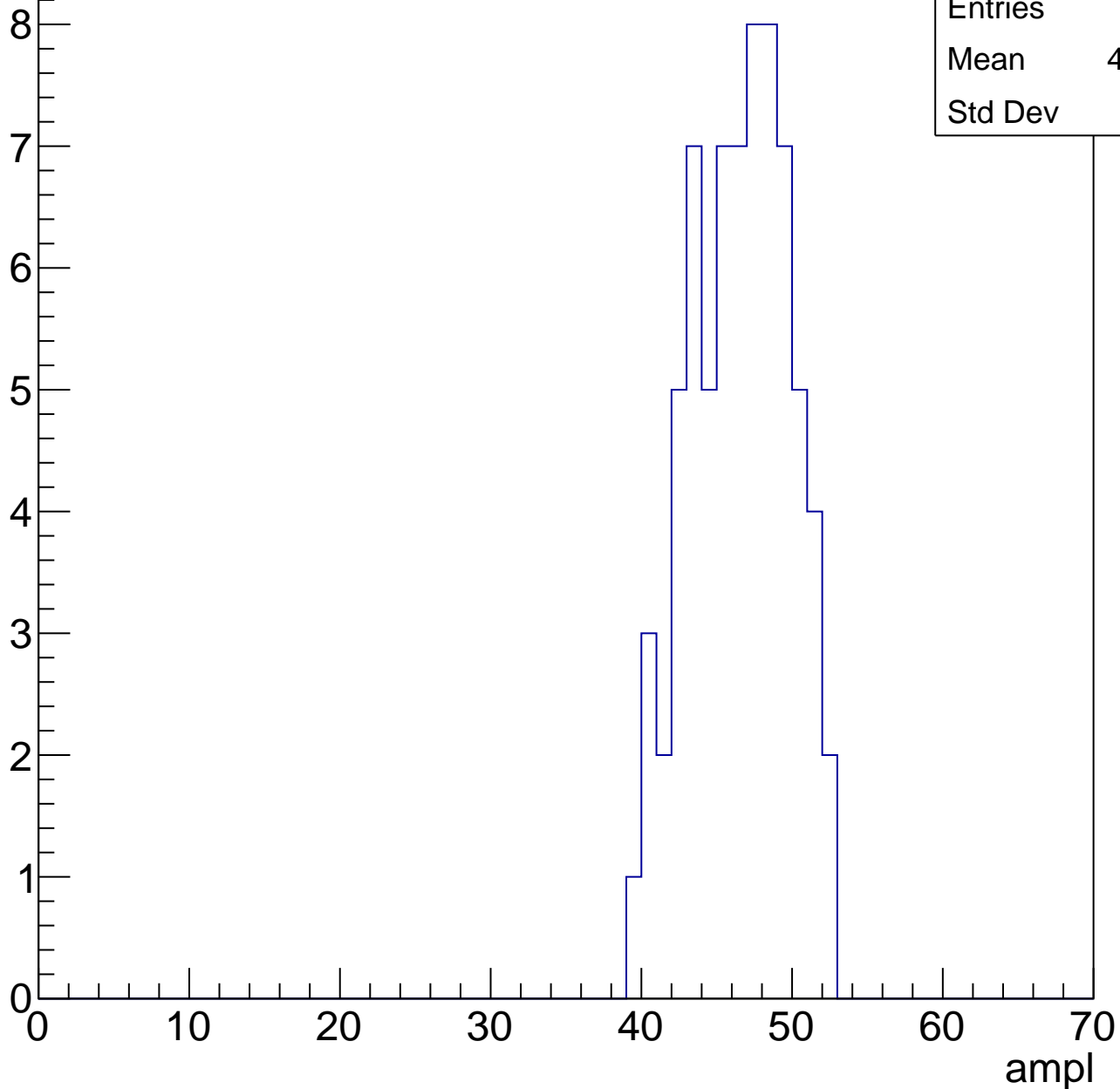
**Gaus Width: 3.9561**



# B1L002S, U26-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



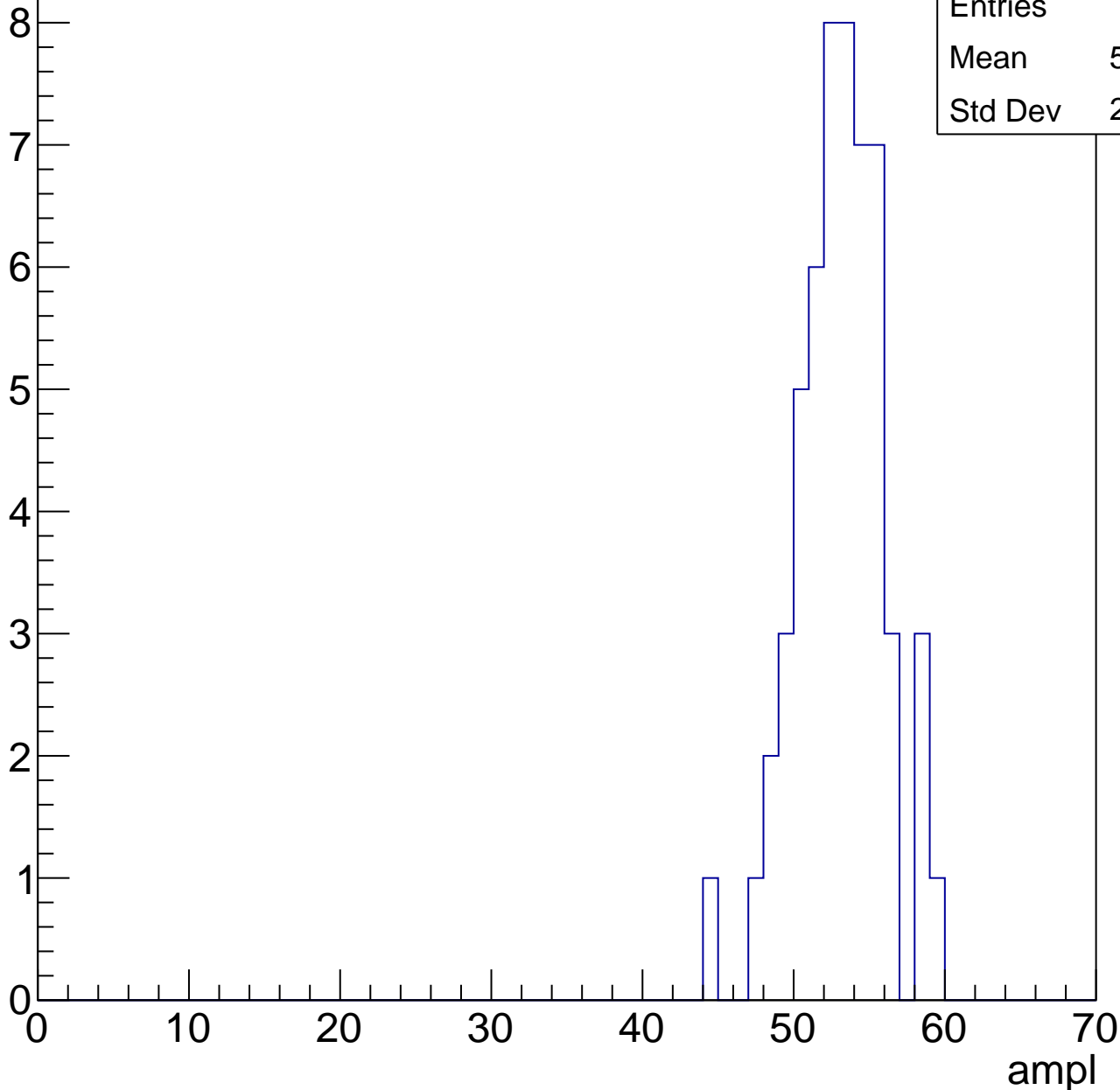
Entries	71
Mean	46.06
Std Dev	3.21

# B1L002S, U26-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.62
Std Dev	2.882

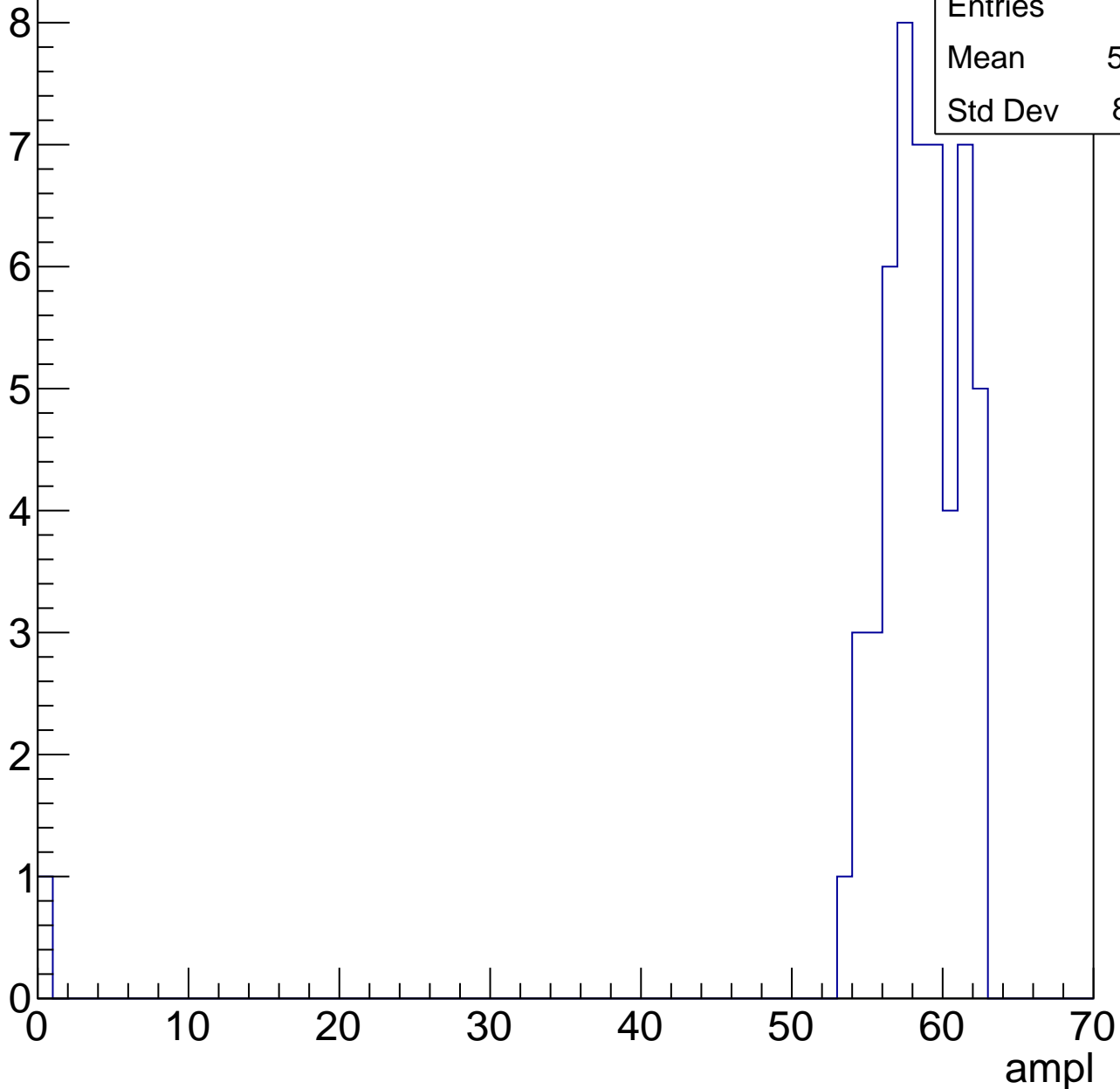


# B1L002S, U26-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	57.08
Std Dev	8.341

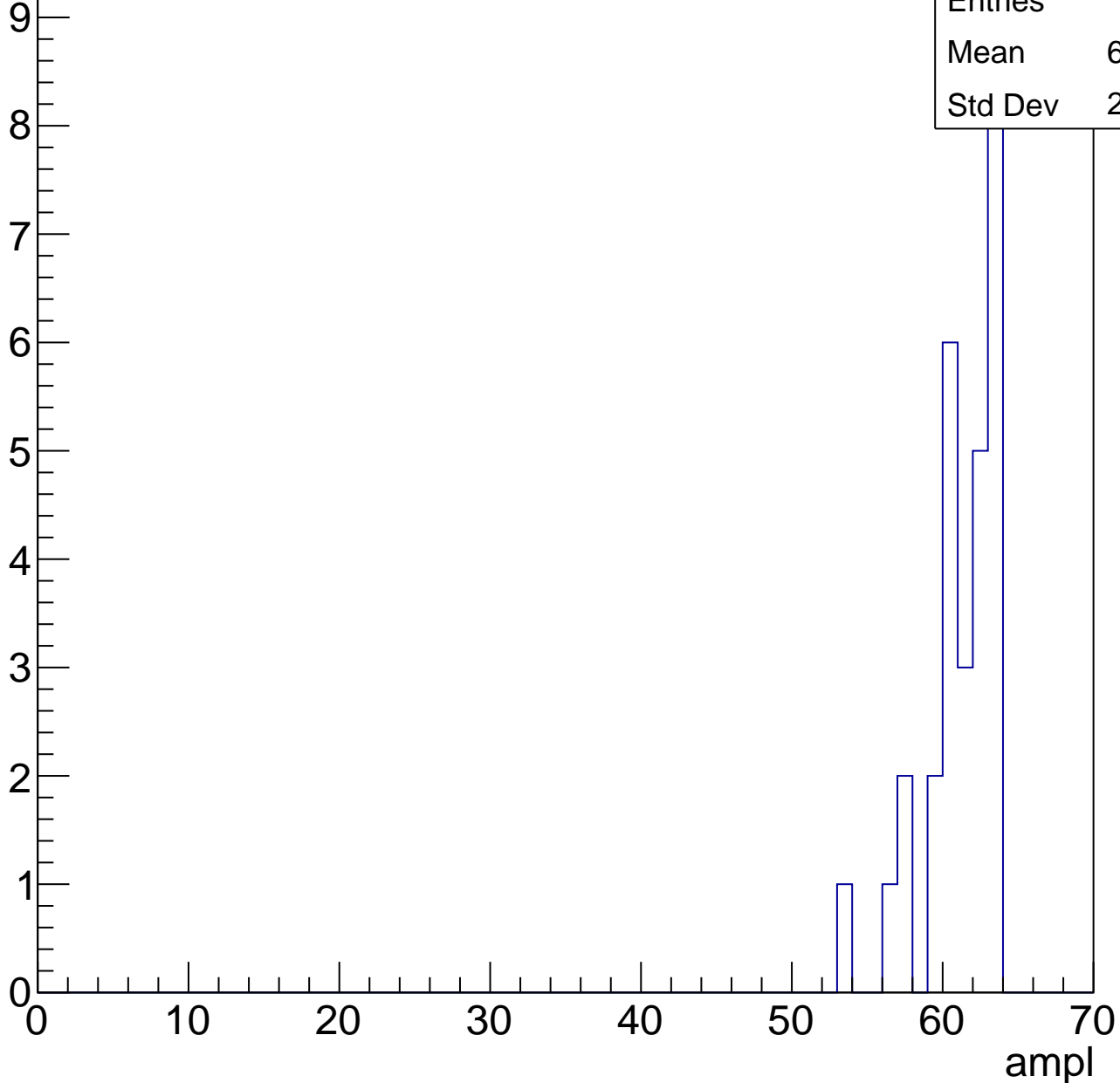


# B1L002S, U26-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	60.72
Std Dev	2.448





# B1L002S, U26-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch115, adc0

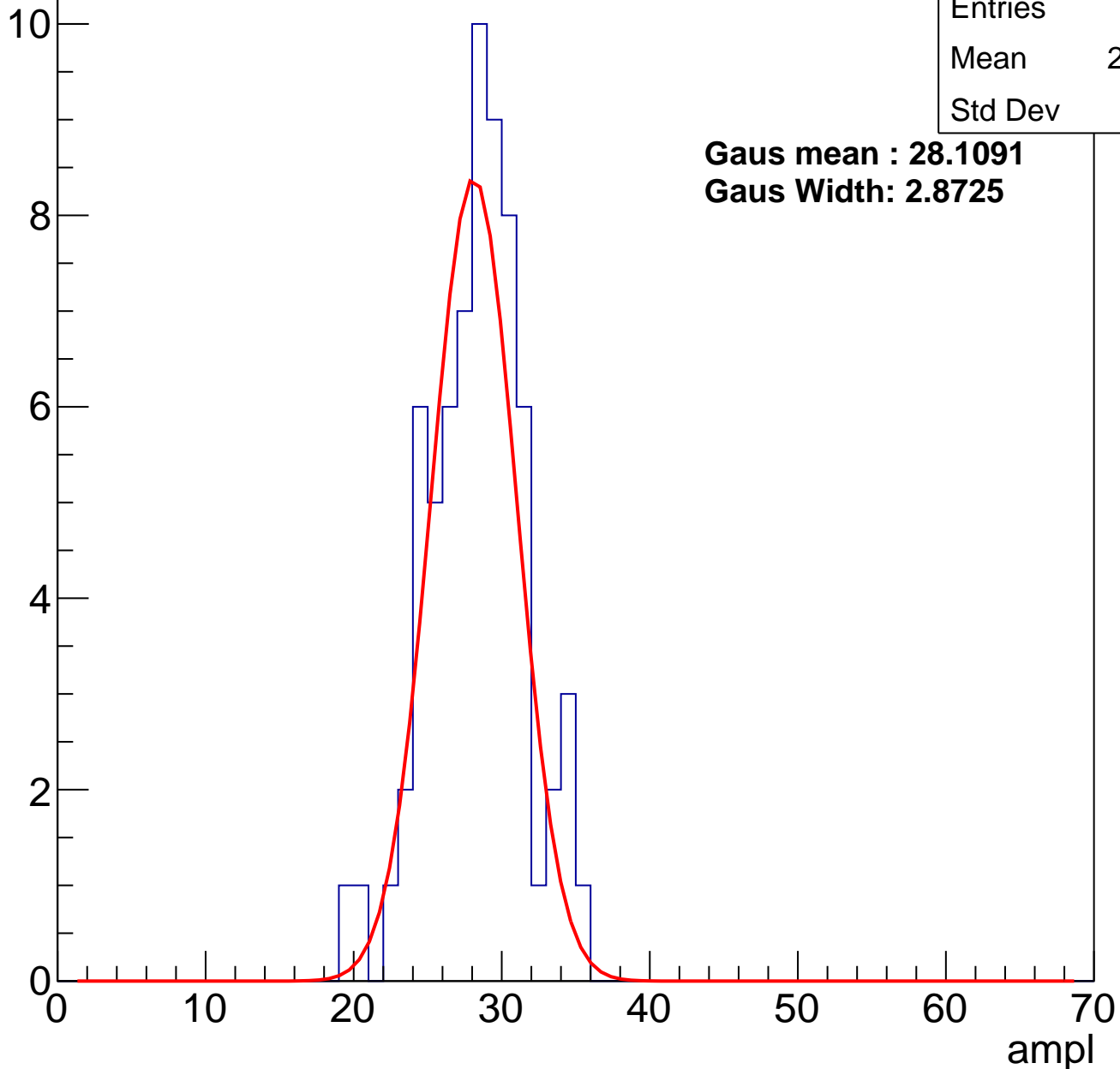
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27.87
Std Dev	3.23

**Gaus mean : 28.1091**

**Gaus Width: 2.8725**

Entry



# B1L002S, U26-ch115, adc1

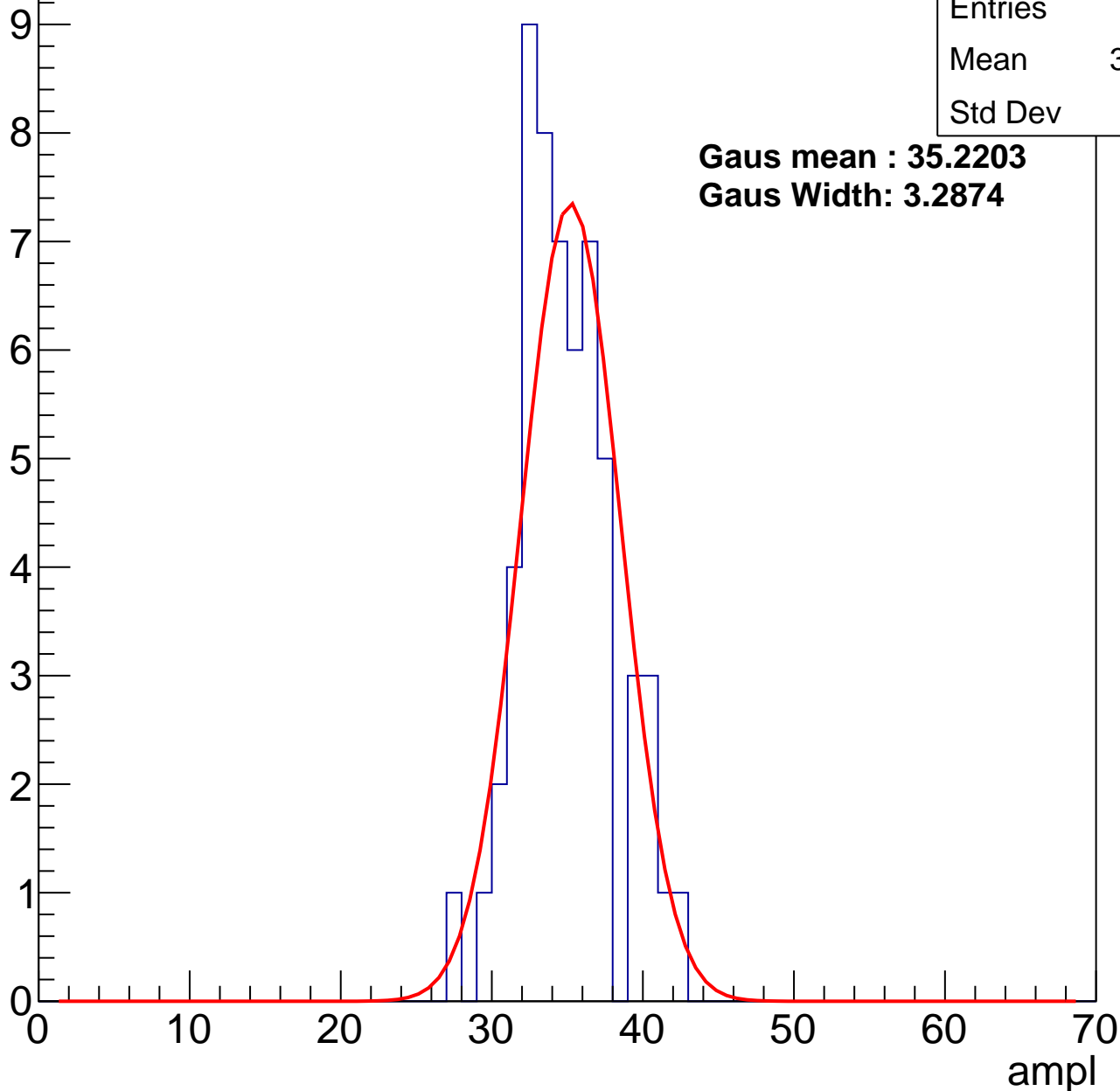
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	34.43
Std Dev	3.08

**Gaus mean : 35.2203**

**Gaus Width: 3.2874**



# B1L002S, U26-ch115, adc2

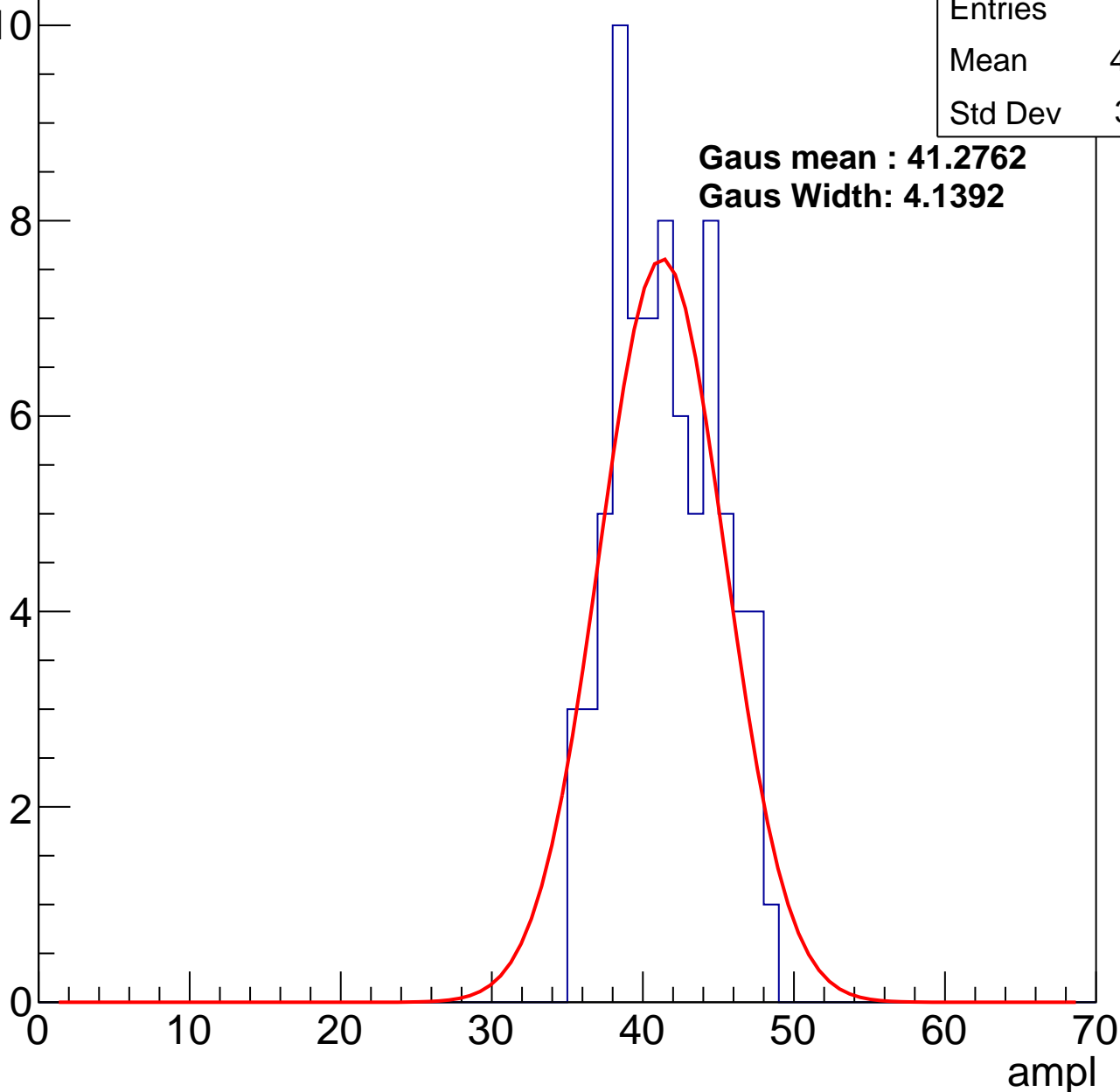
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	41.09
Std Dev	3.361

**Gaus mean : 41.2762**

**Gaus Width: 4.1392**

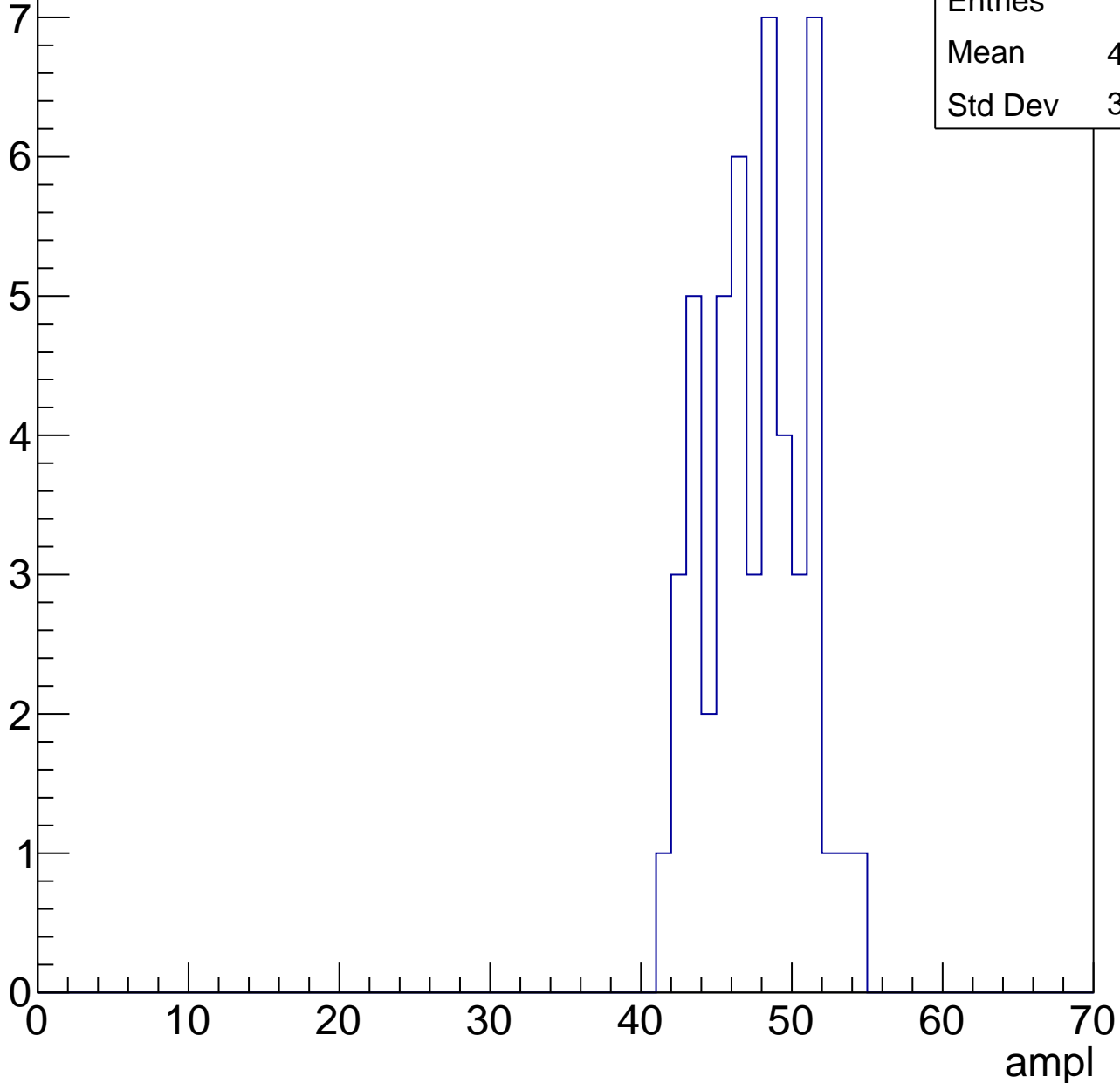


# B1L002S, U26-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	47.14
Std Dev	3.213

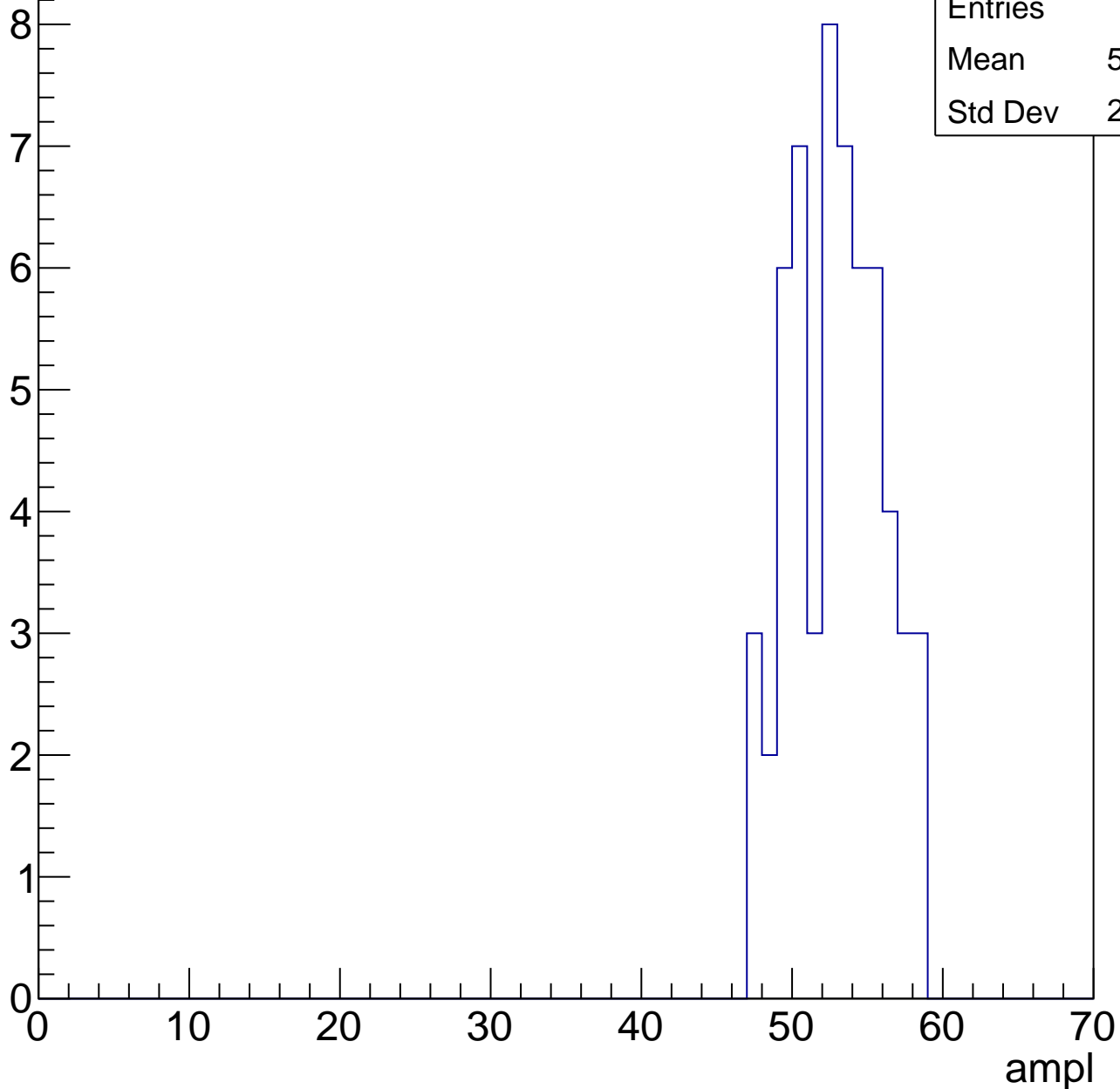


# B1L002S, U26-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	52.48
Std Dev	2.967

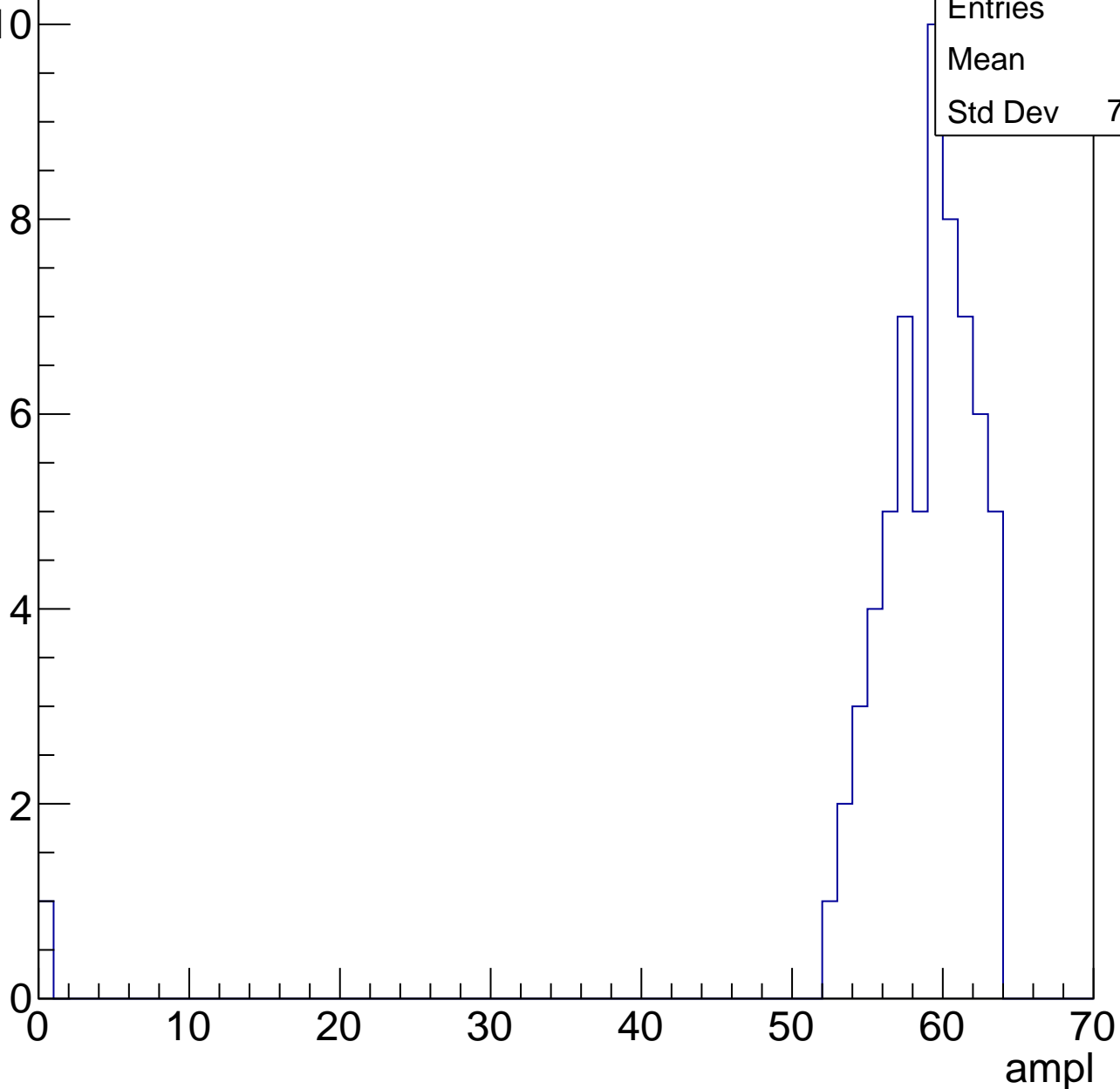


# B1L002S, U26-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.7
Std Dev	7.788

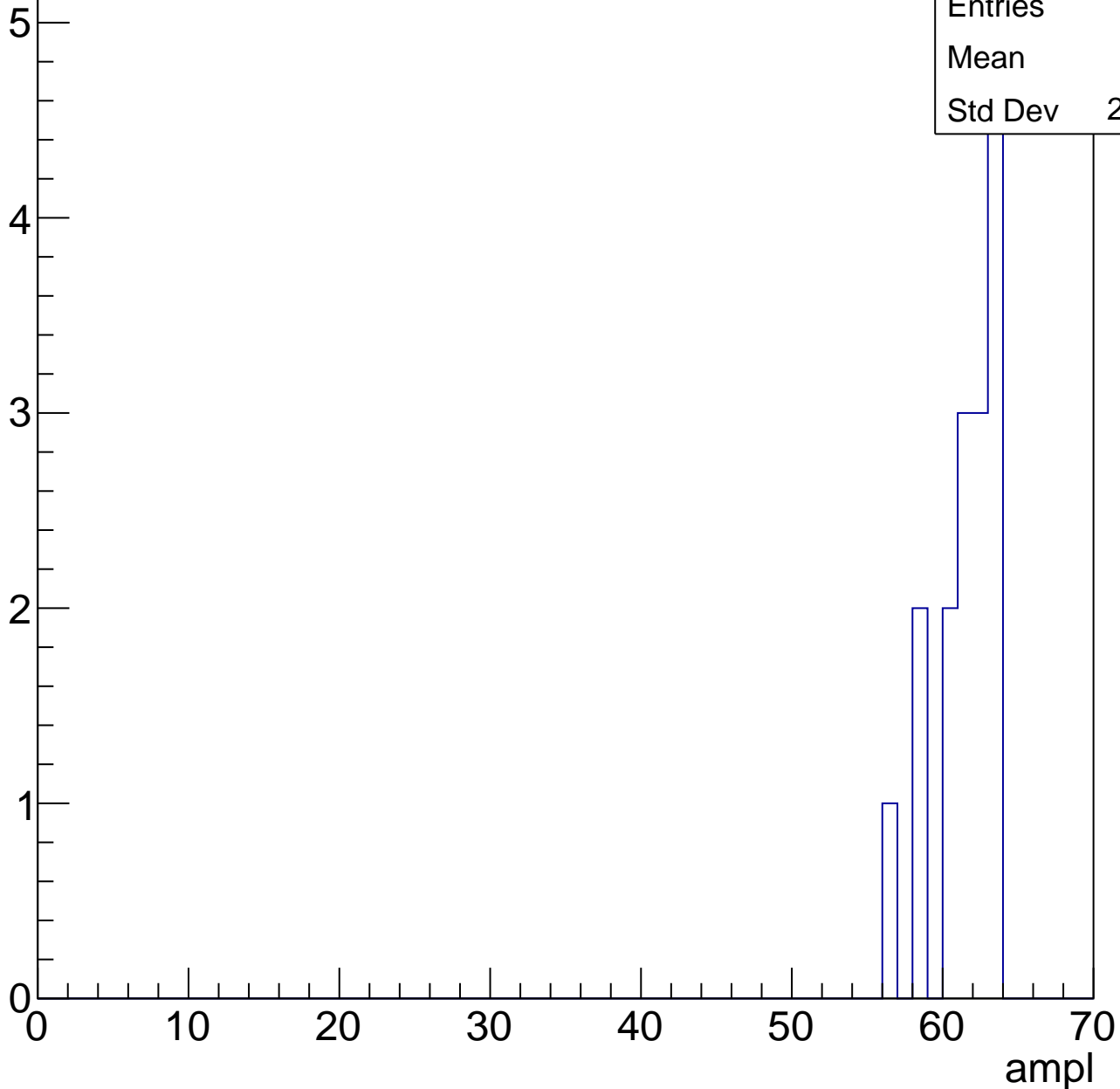


# B1L002S, U26-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	16
Mean	61
Std Dev	2.062

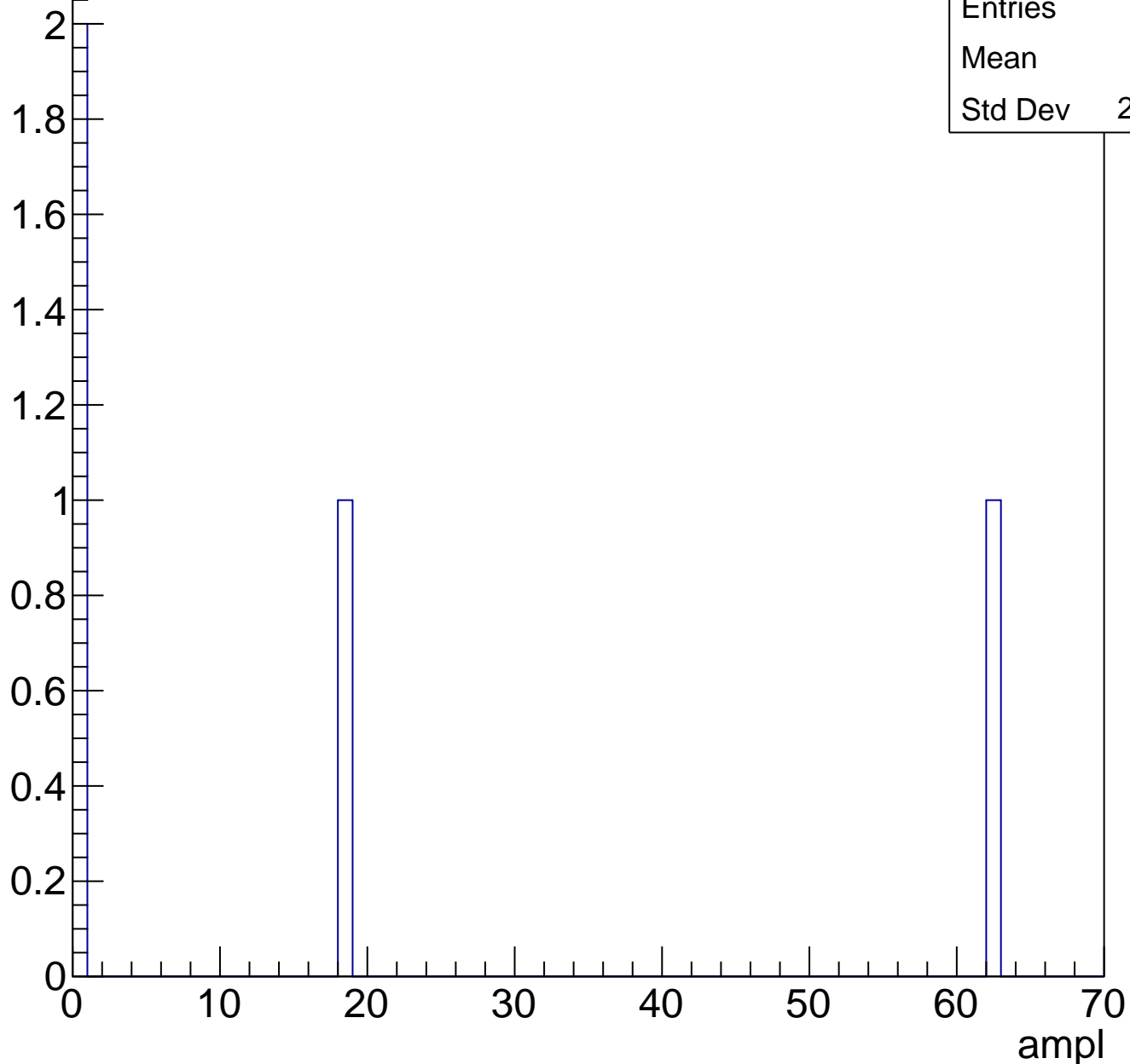




# B1L002S, U26-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20
Std Dev	25.34

# B1L002S, U26-ch116, adc0

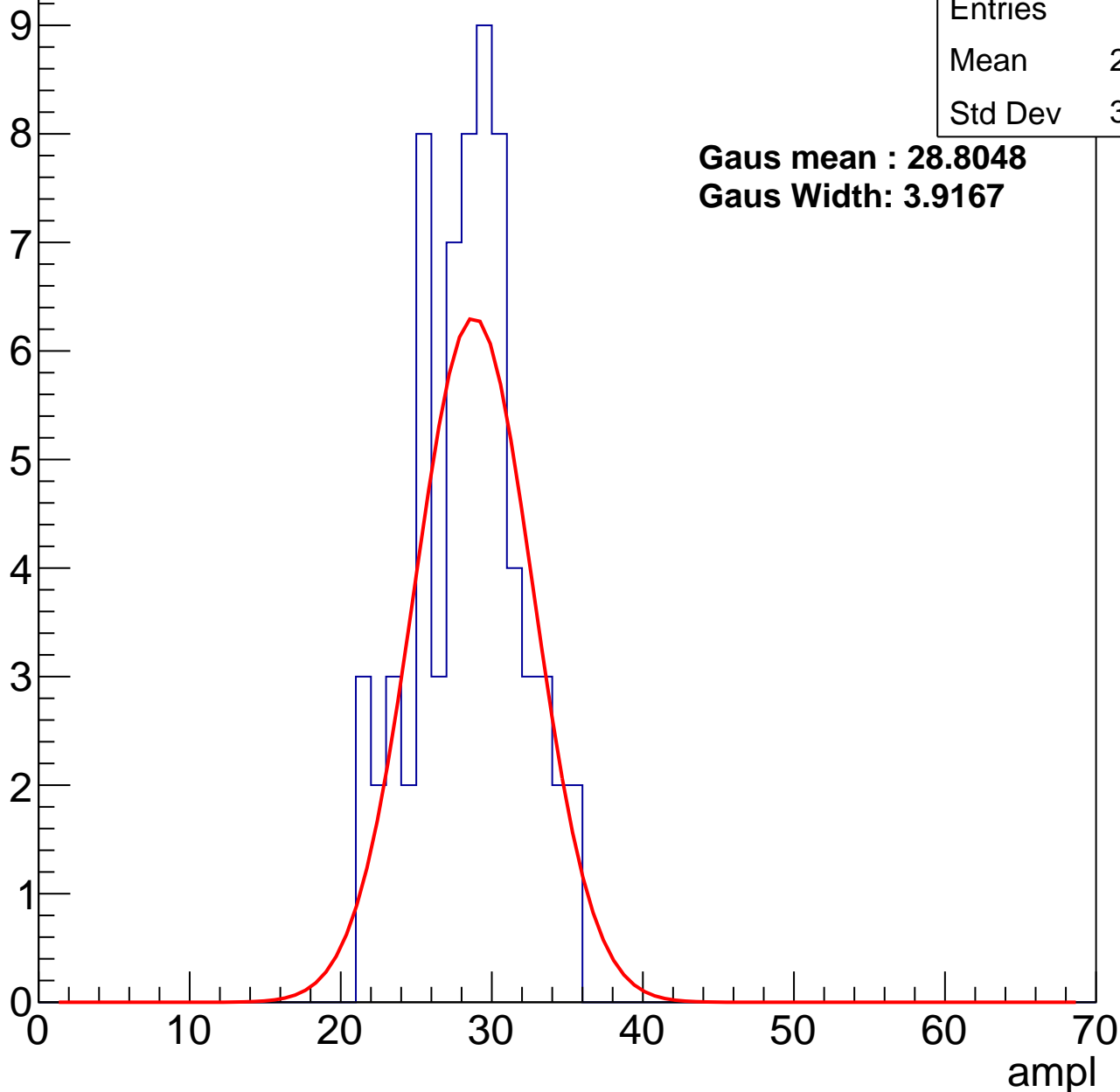
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	27.96
Std Dev	3.427

**Gaus mean : 28.8048**

**Gaus Width: 3.9167**



# B1L002S, U26-ch116, adc1

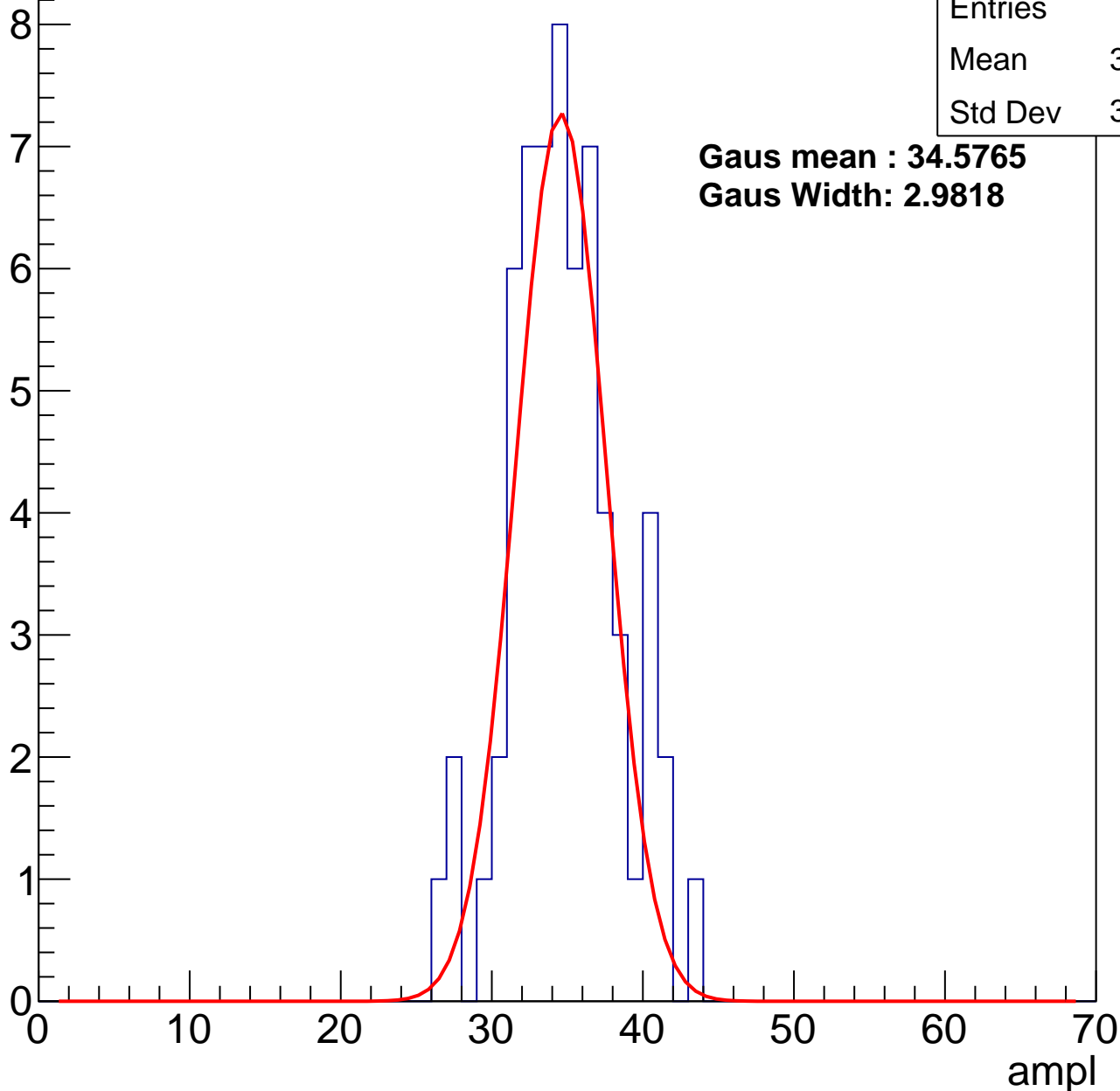
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.35
Std Dev	3.515

**Gaus mean : 34.5765**

**Gaus Width: 2.9818**



# B1L002S, U26-ch116, adc2

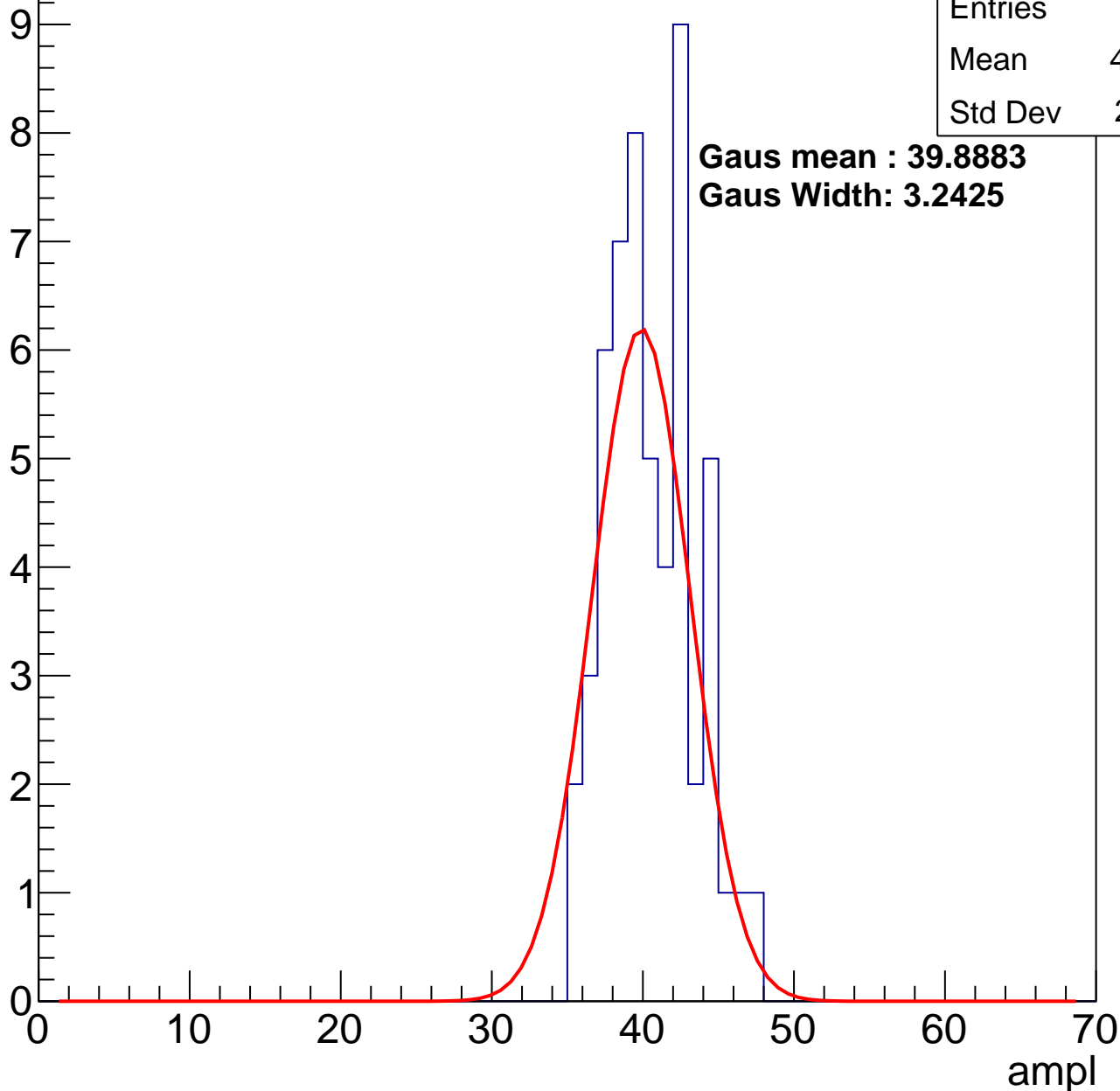
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.07
Std Dev	2.841

**Gaus mean : 39.8883**

**Gaus Width: 3.2425**

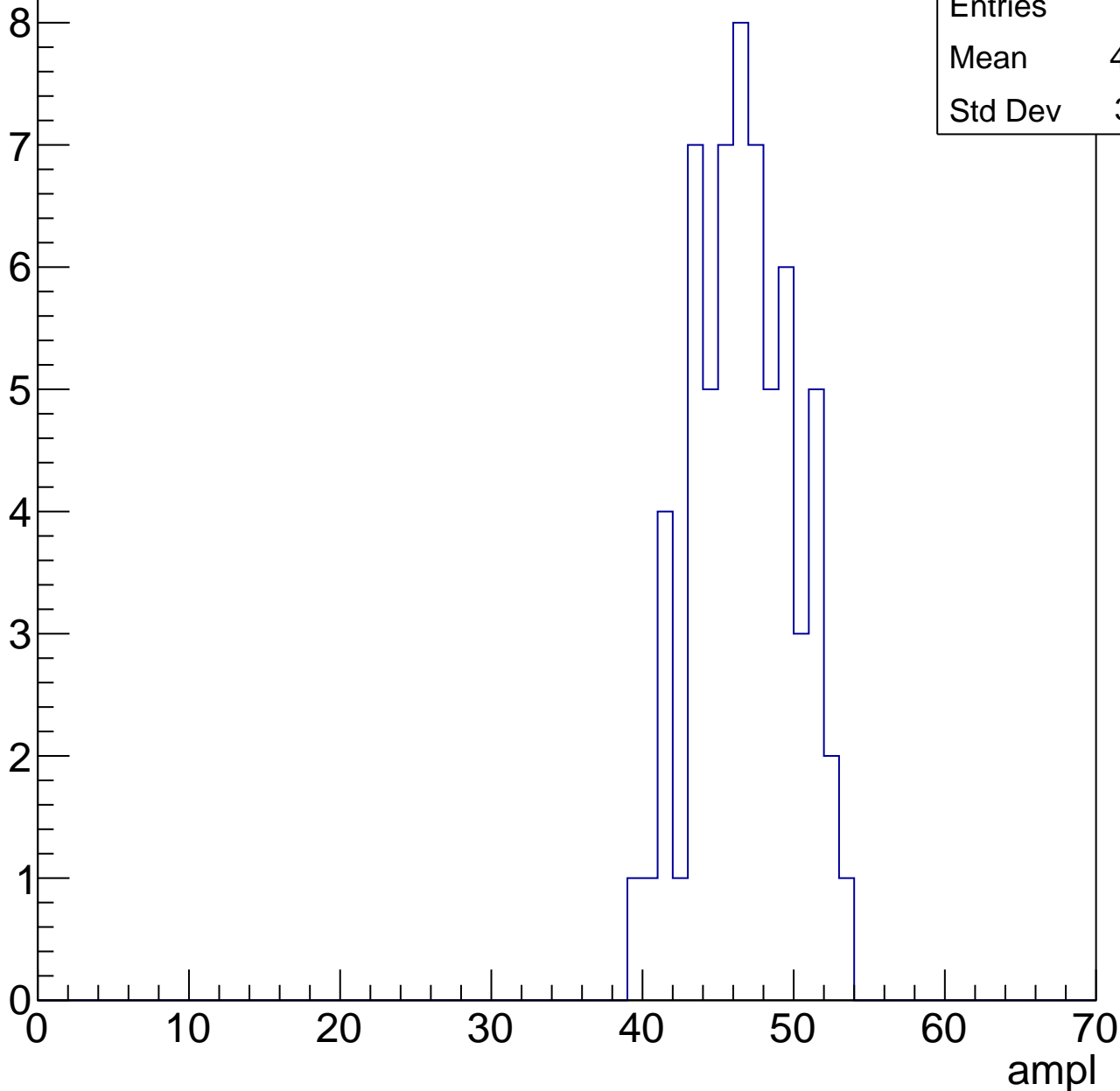


# B1L002S, U26-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	46.25
Std Dev	3.241

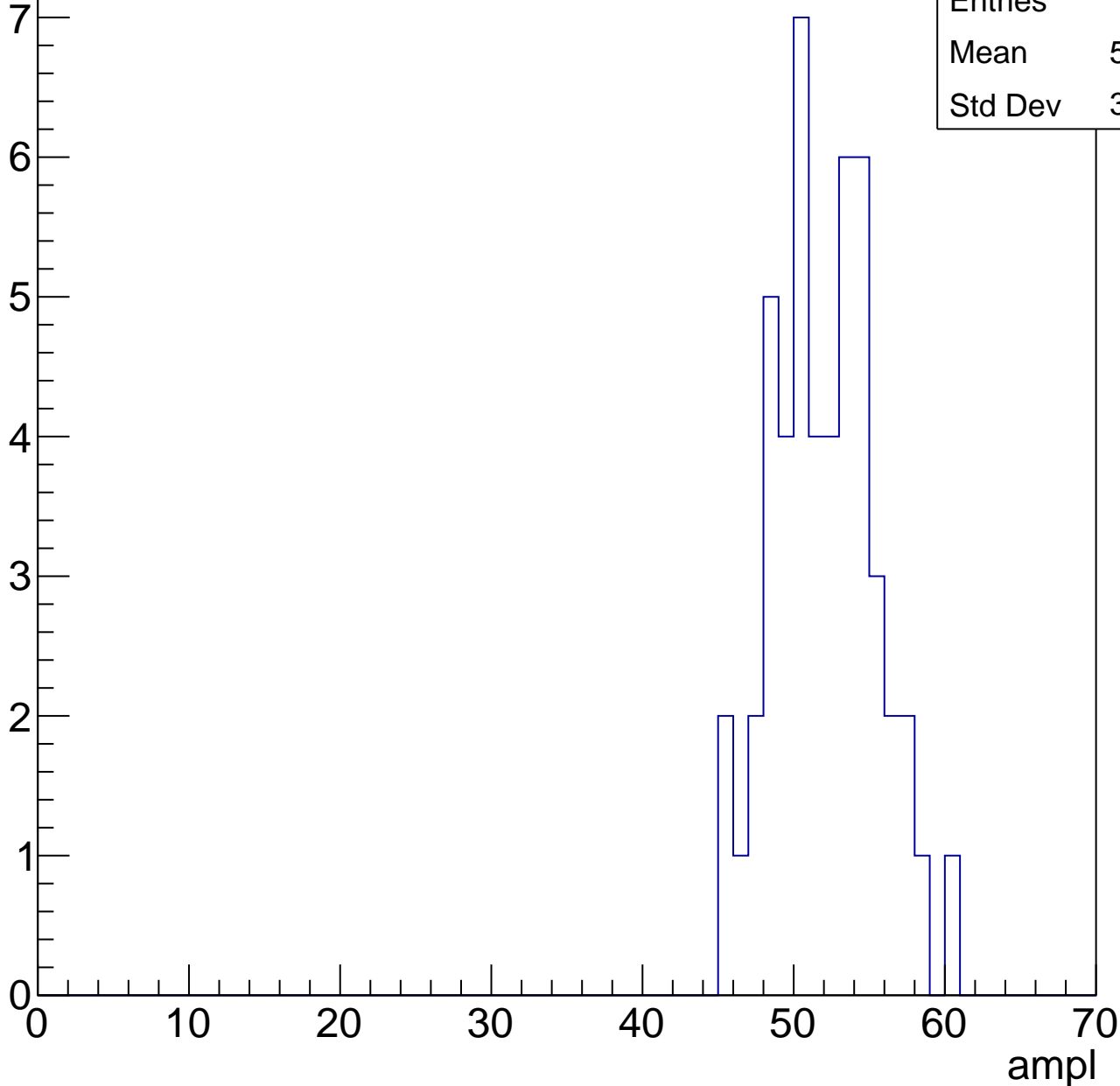


# B1L002S, U26-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	51.58
Std Dev	3.353

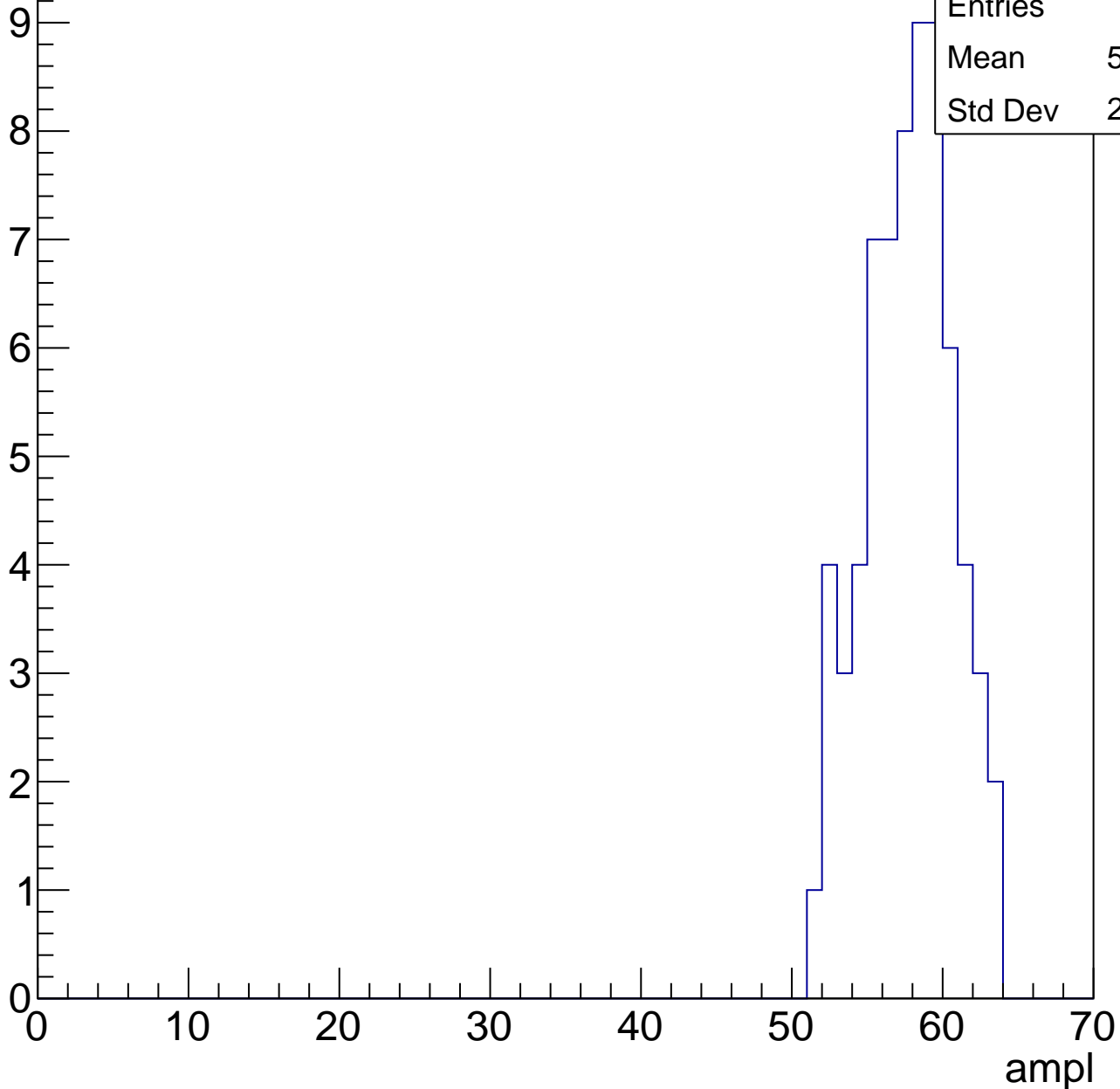


# B1L002S, U26-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	57.25
Std Dev	2.893

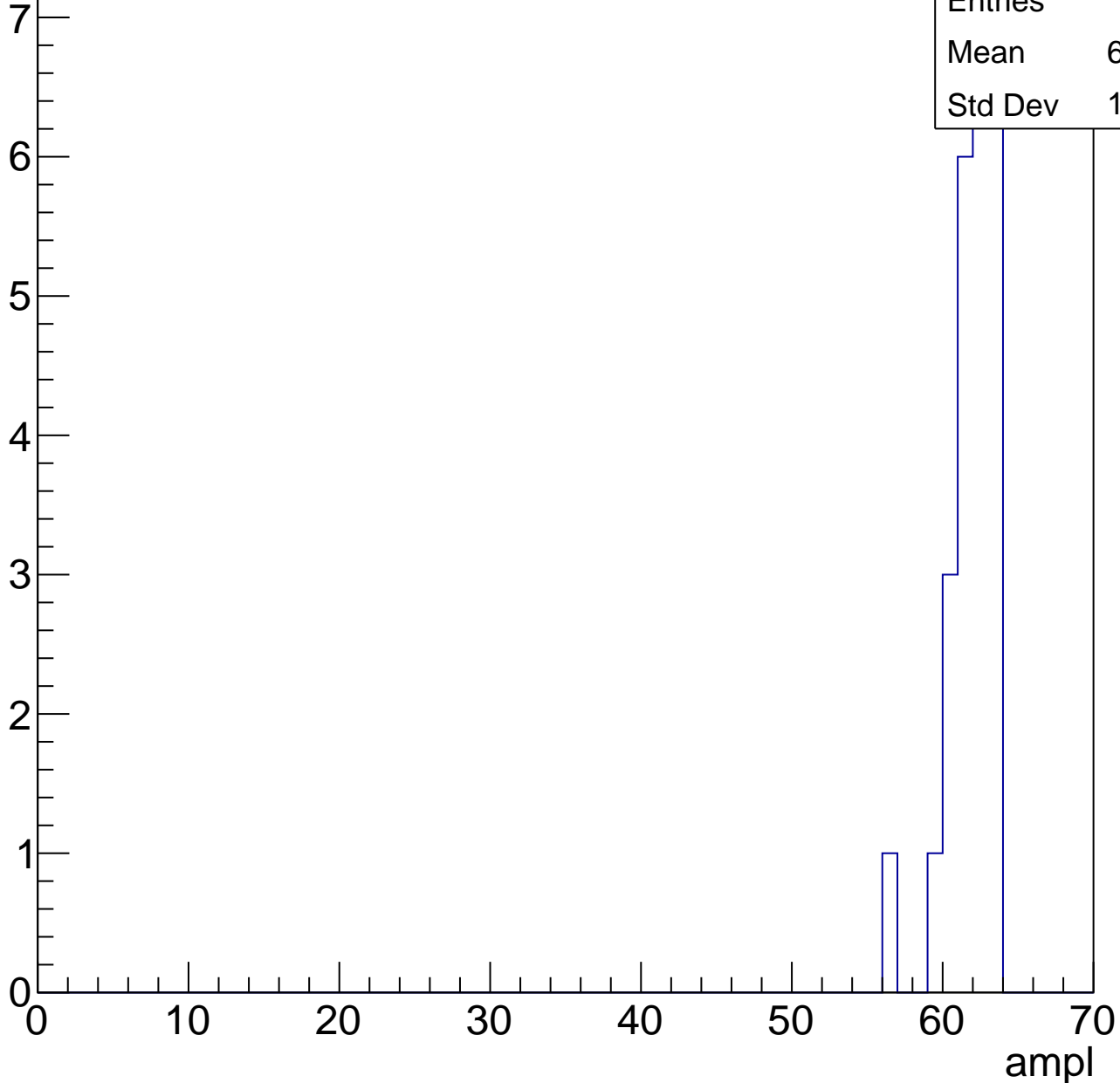


# B1L002S, U26-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.44
Std Dev	1.577

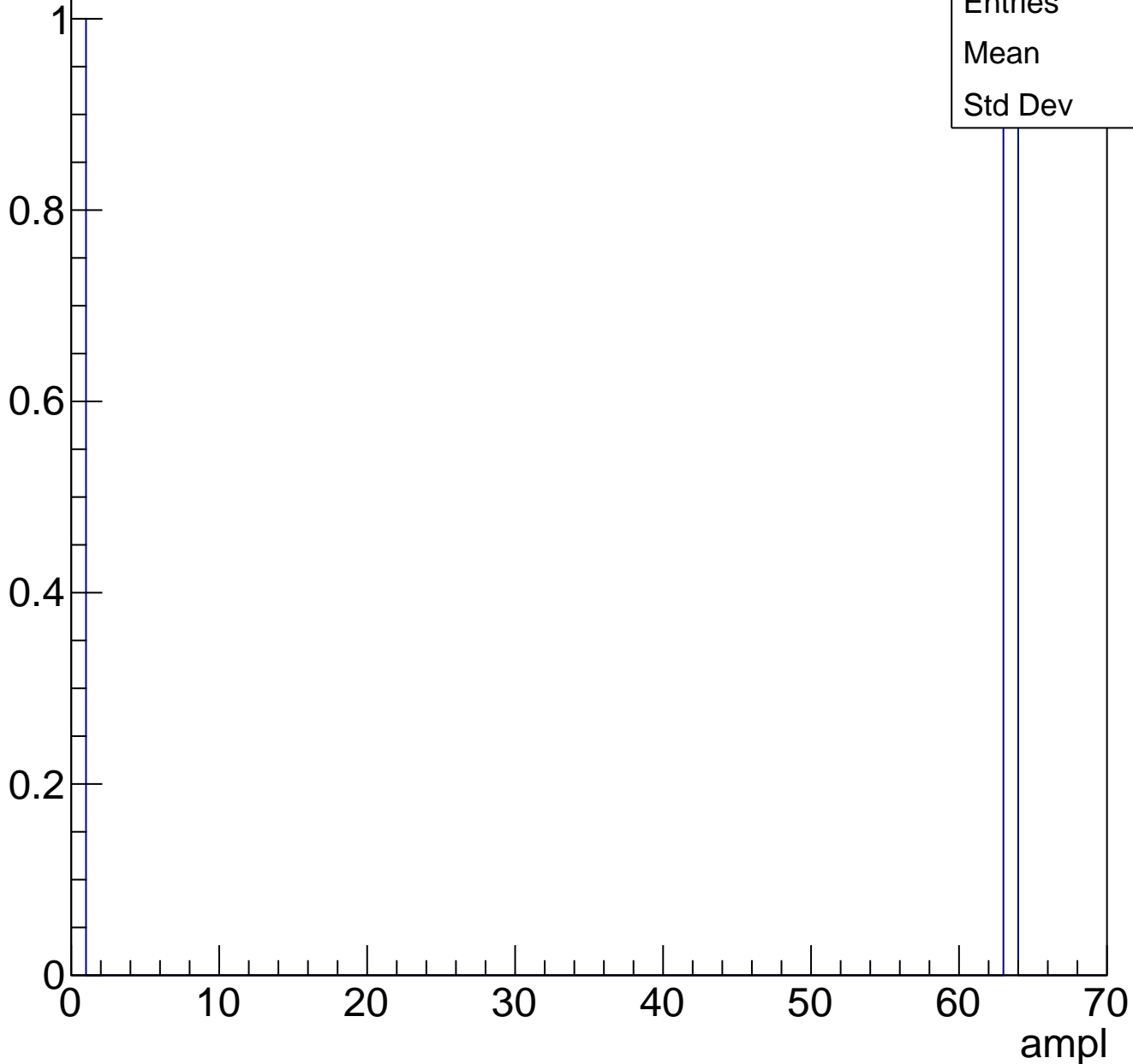




# B1L002S, U26-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch117, adc0

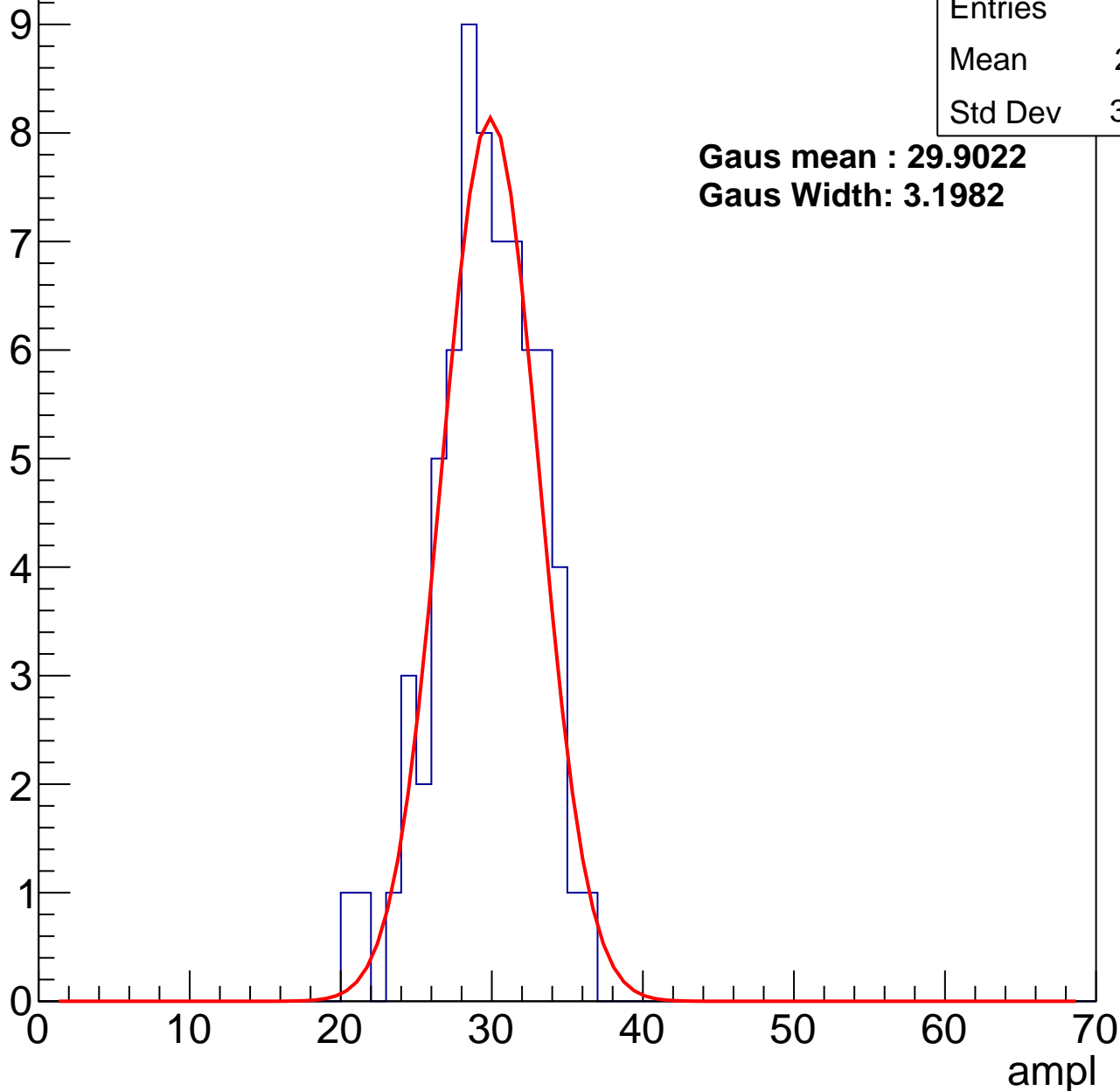
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	29.21
Std Dev	3.283

**Gaus mean : 29.9022**

**Gaus Width: 3.1982**



# B1L002S, U26-ch117, adc1

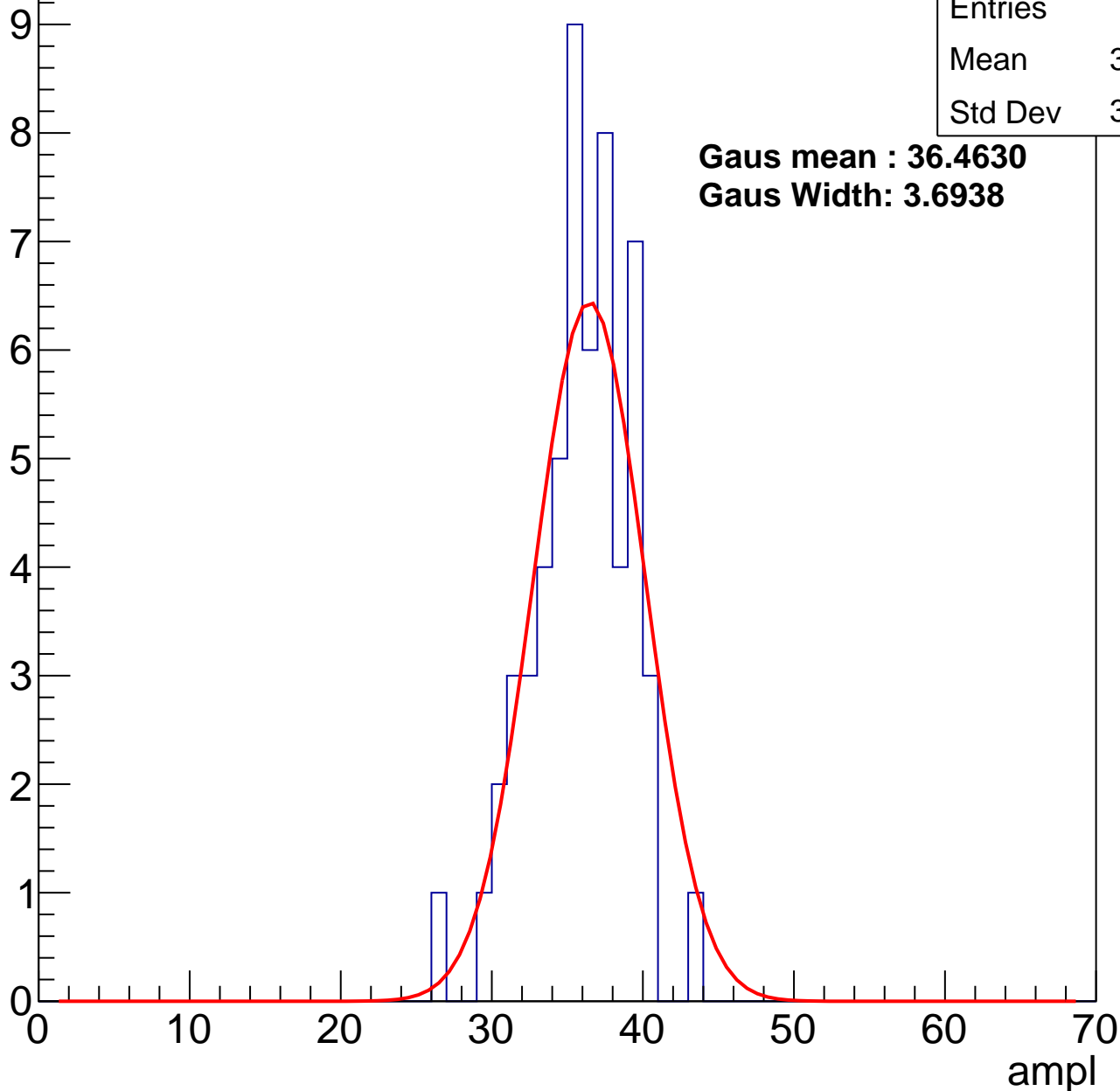
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	35.46
Std Dev	3.179

**Gaus mean : 36.4630**

**Gaus Width: 3.6938**



# B1L002S, U26-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	42.57
Std Dev	3.418

**Gaus mean : 43.1556**

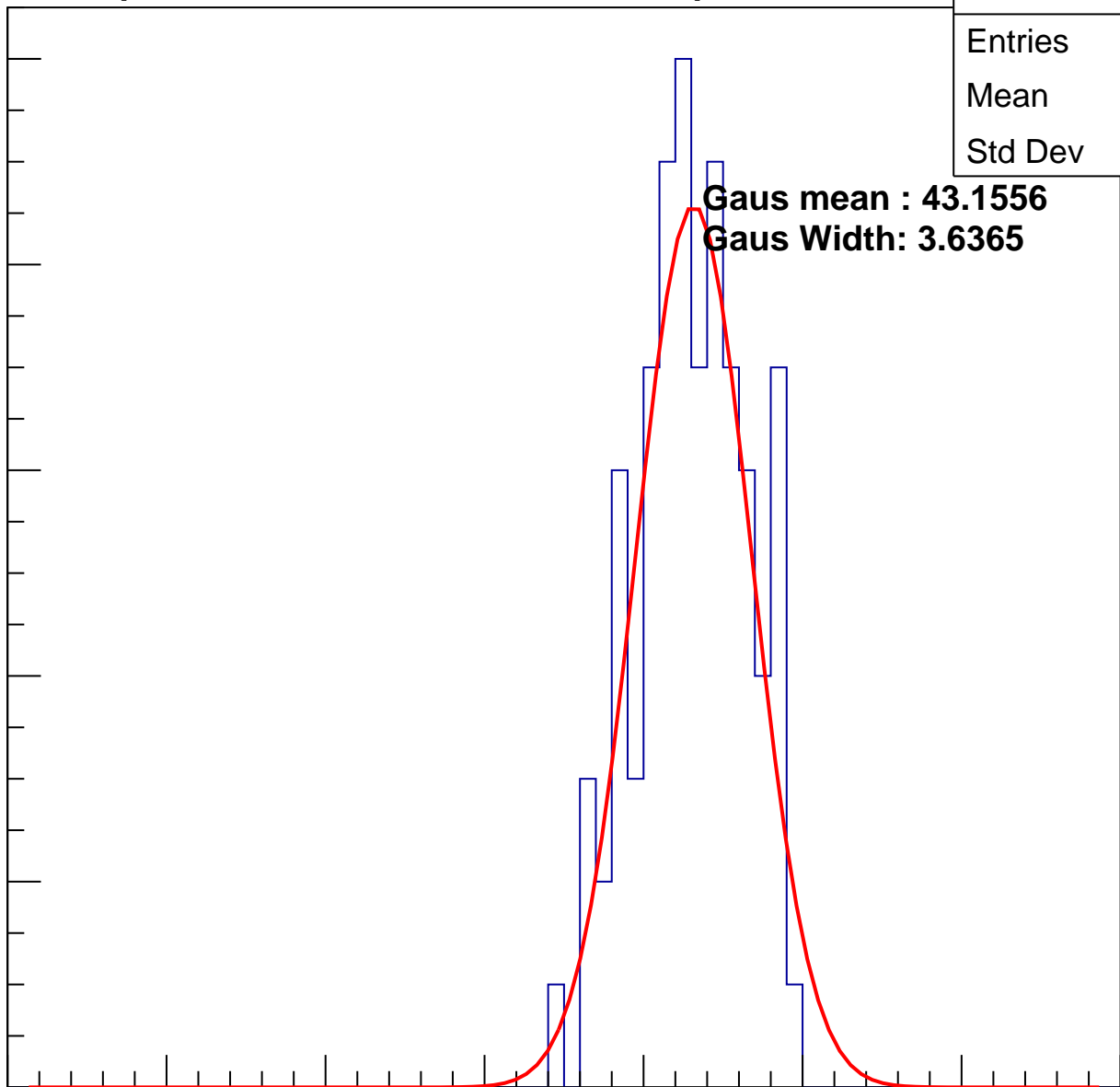
**Gaus Width: 3.6365**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

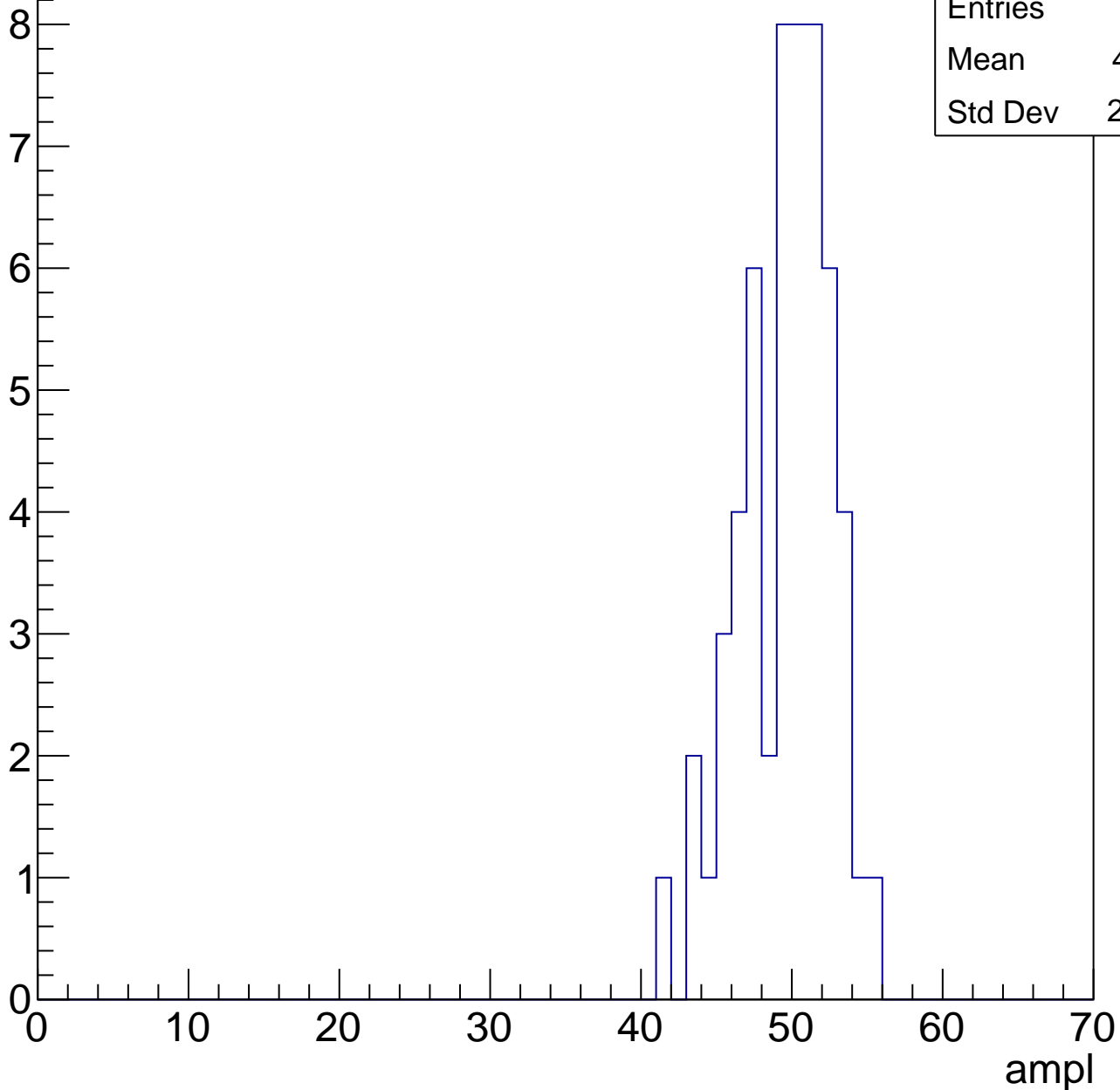


# B1L002S, U26-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

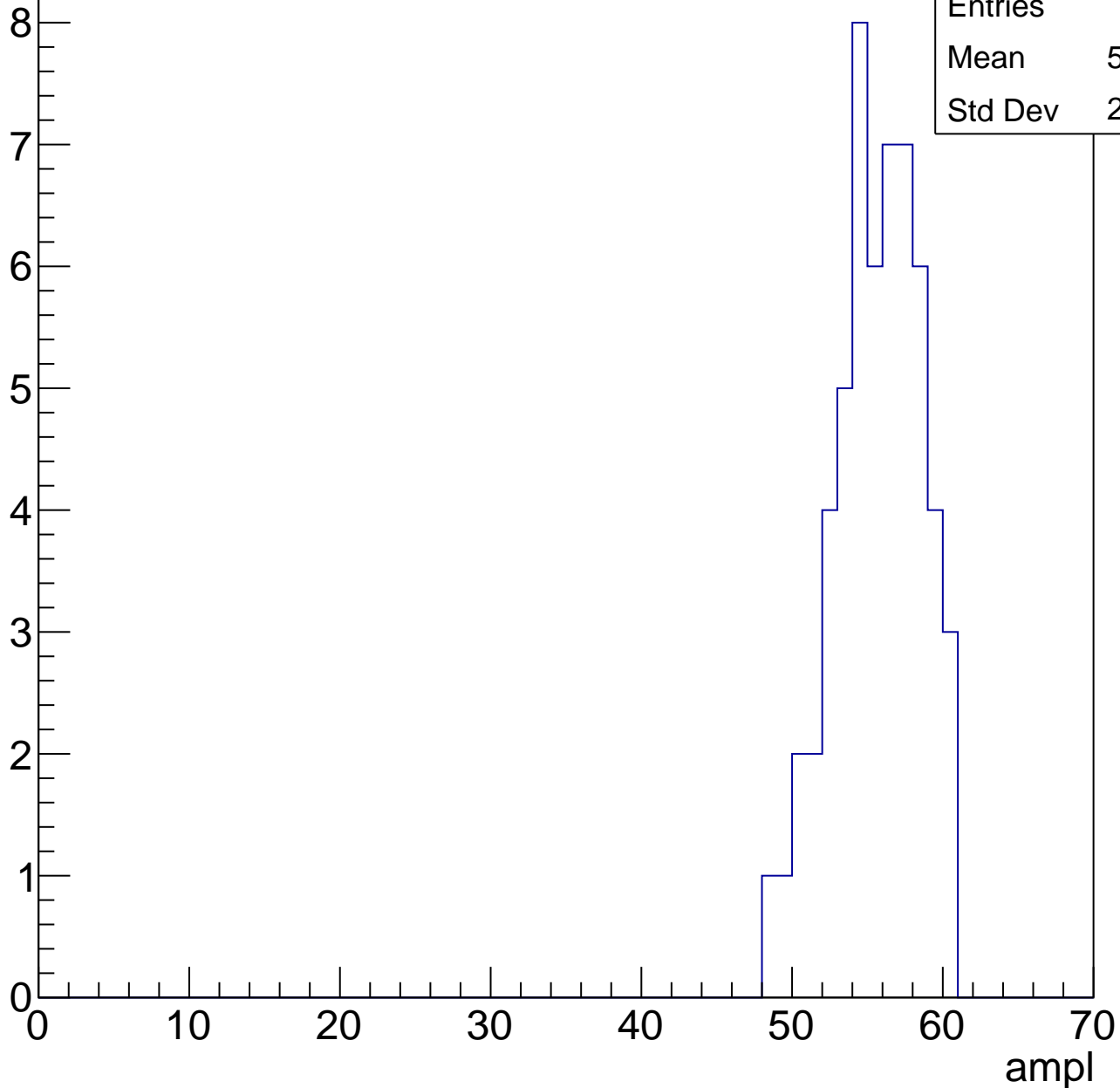
Entries	55
Mean	49.11
Std Dev	2.983



# B1L002S, U26-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



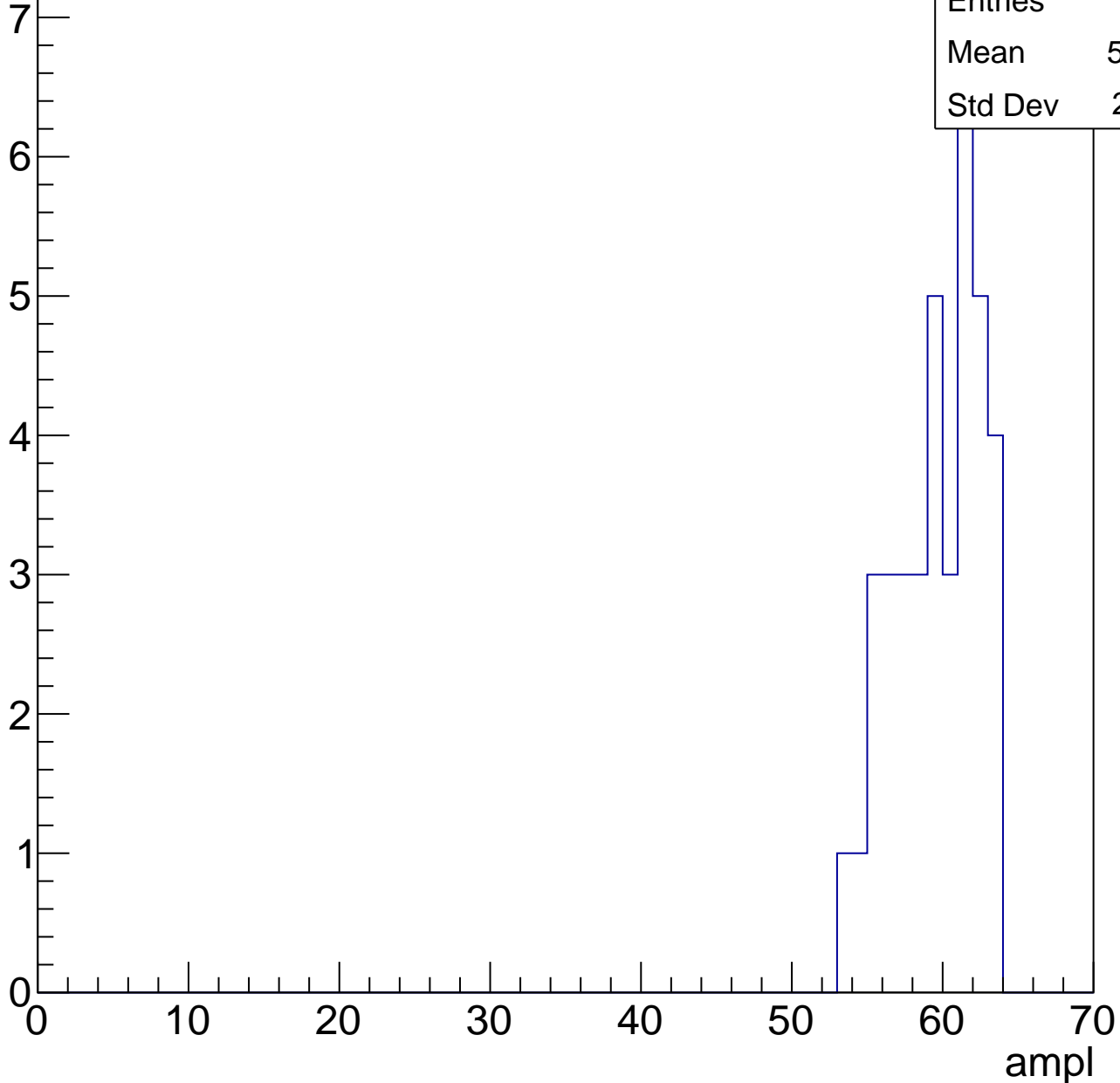
Entries	56
Mean	55.16
Std Dev	2.858

# B1L002S, U26-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	59.18
Std Dev	2.761

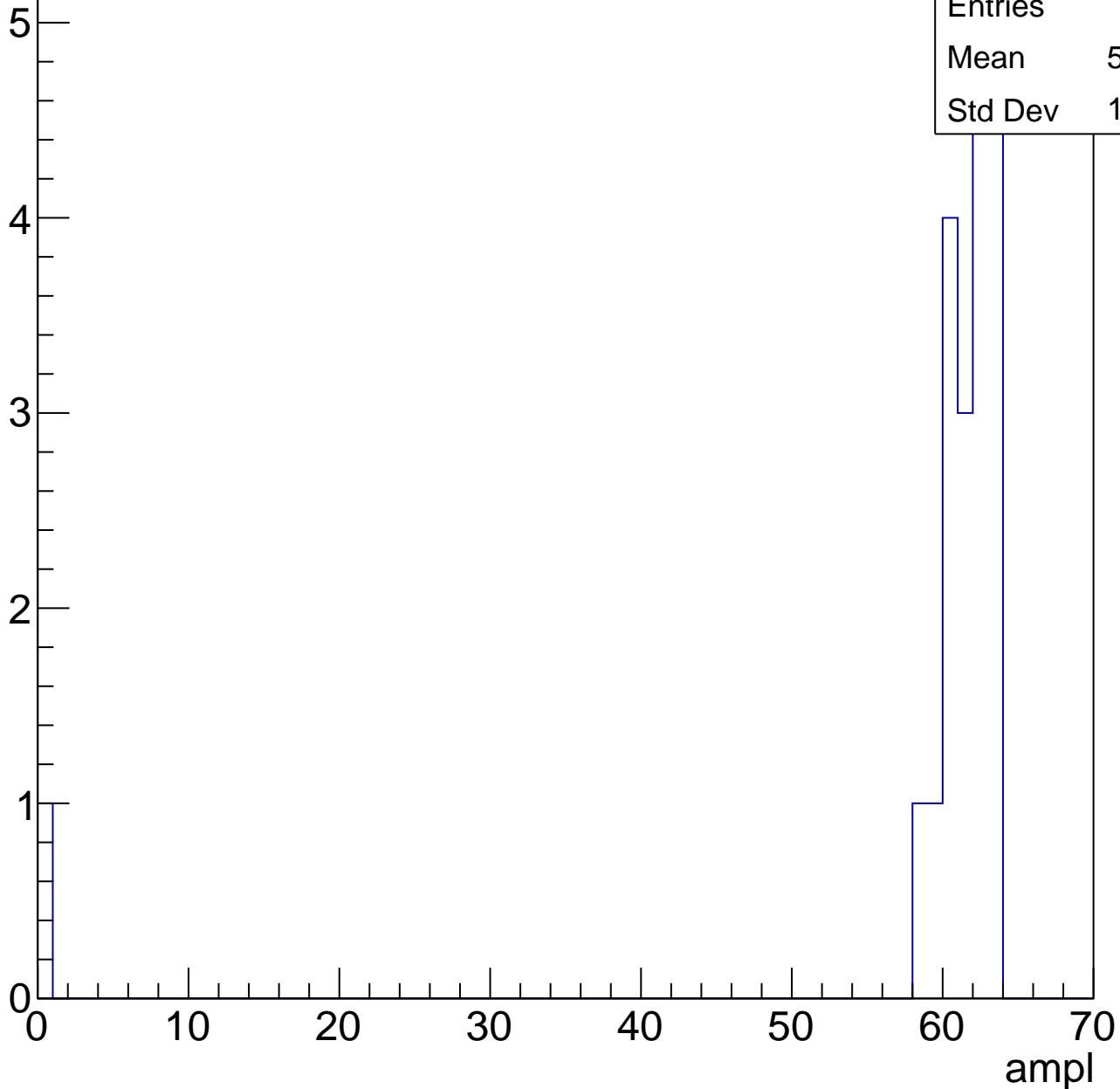


# B1L002S, U26-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.25
Std Dev	13.44





# B1L002S, U26-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

# B1L002S, U26-ch118, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	28.24
Std Dev	4.518

**Gaus mean : 29.2662**

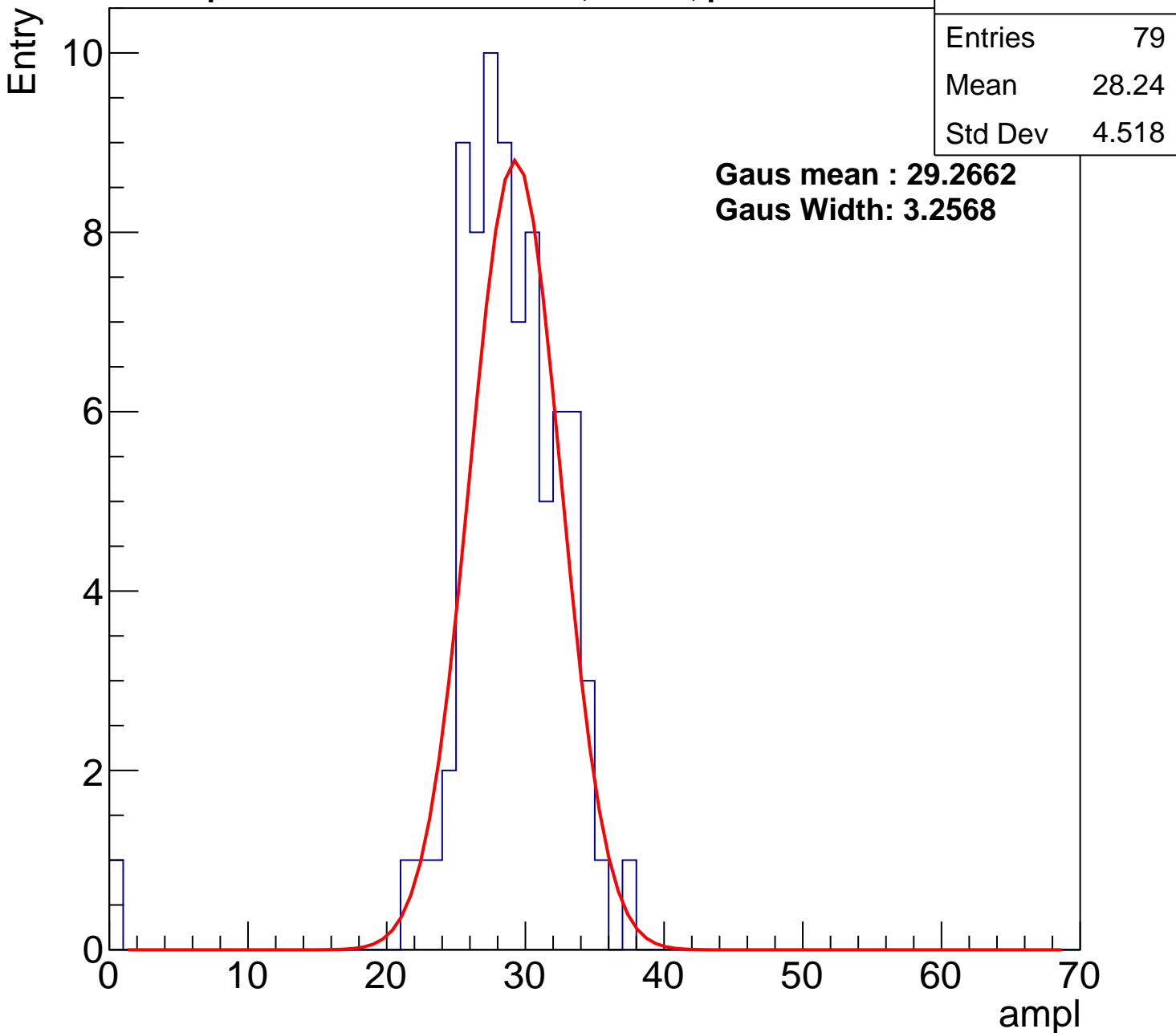
**Gaus Width: 3.2568**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch118, adc1

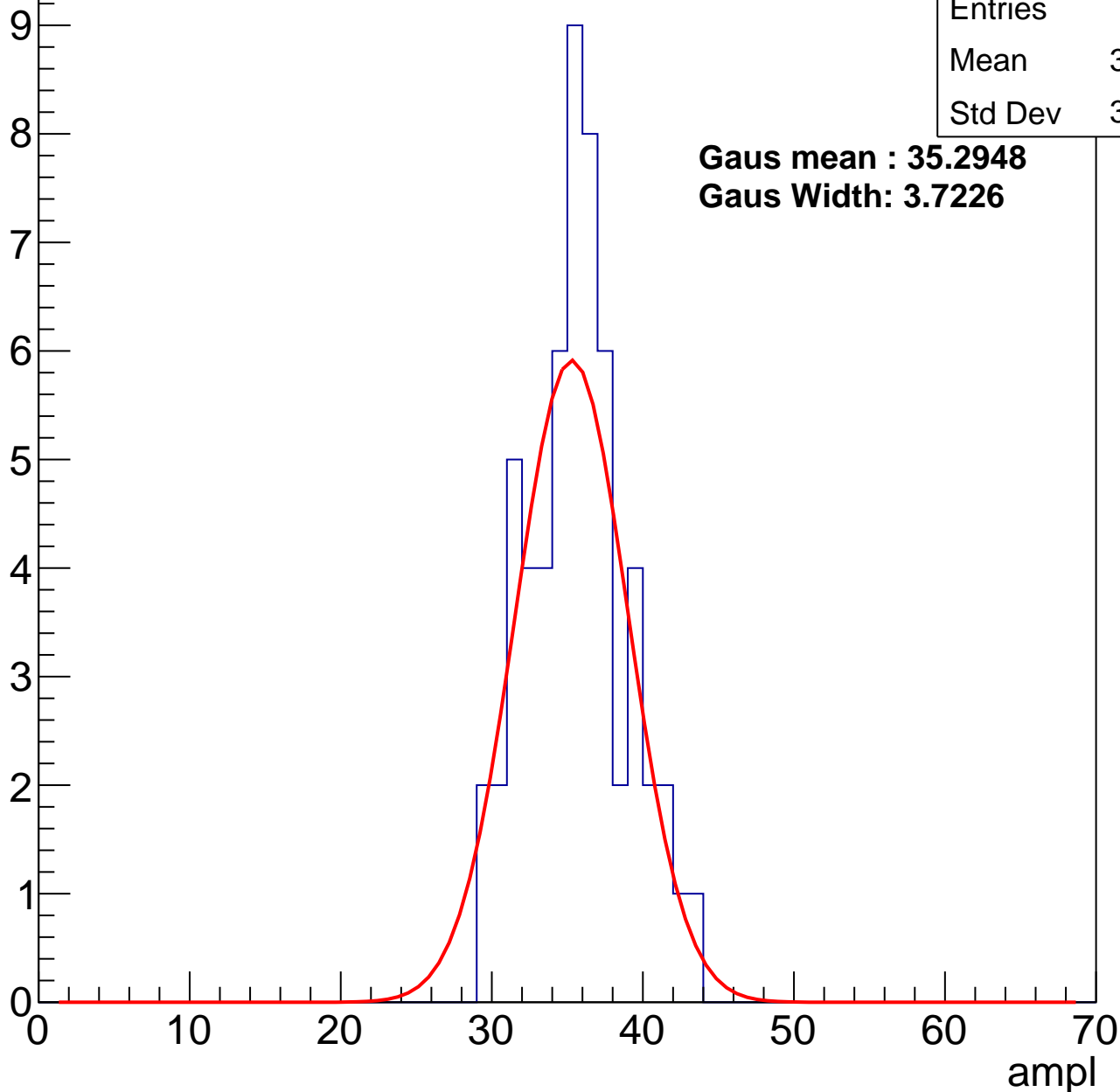
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.19
Std Dev	3.235

**Gaus mean : 35.2948**

**Gaus Width: 3.7226**



# B1L002S, U26-ch118, adc2

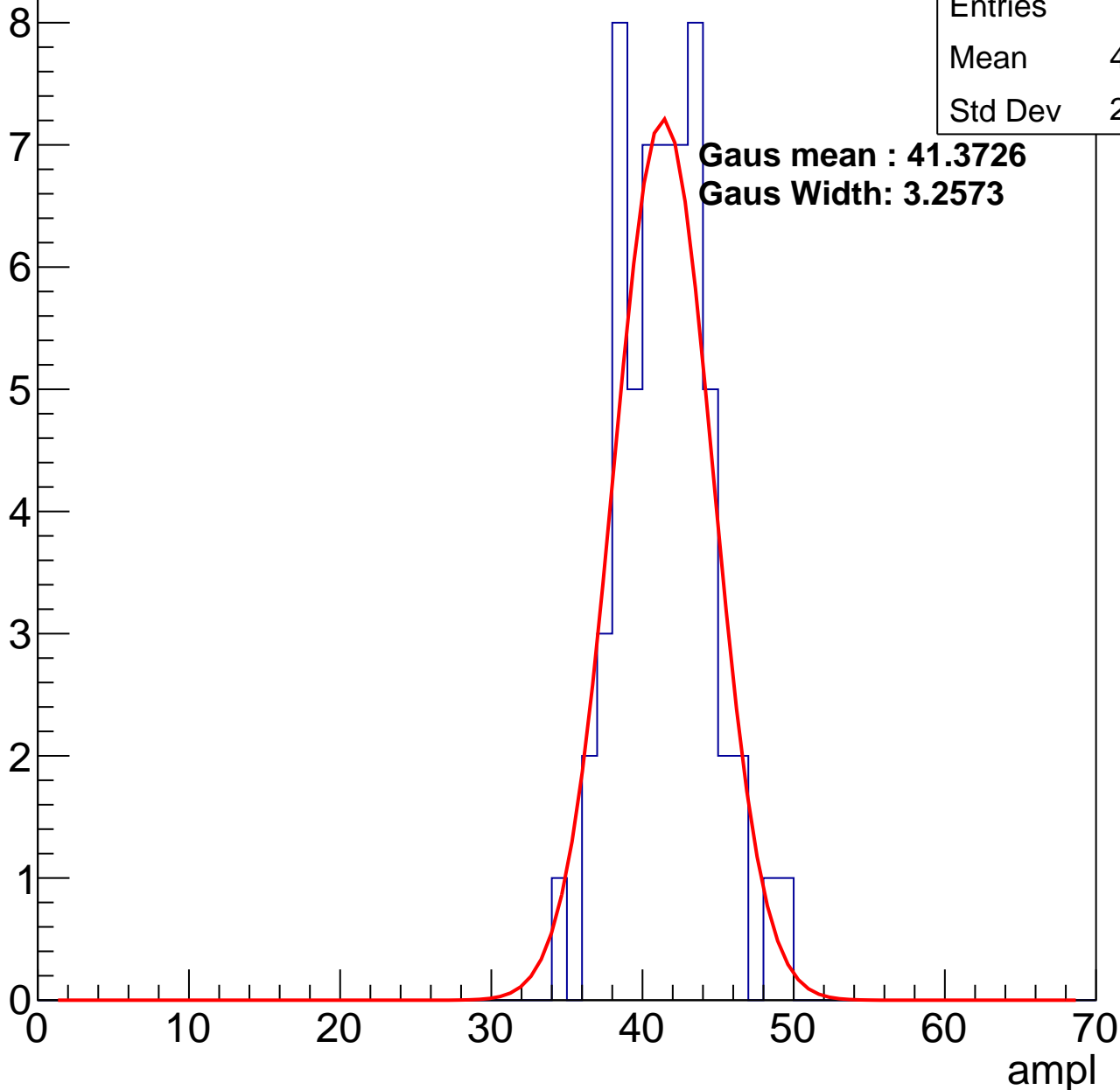
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.02
Std Dev	2.983

**Gaus mean : 41.3726**

**Gaus Width: 3.2573**



# B1L002S, U26-ch118, adc3

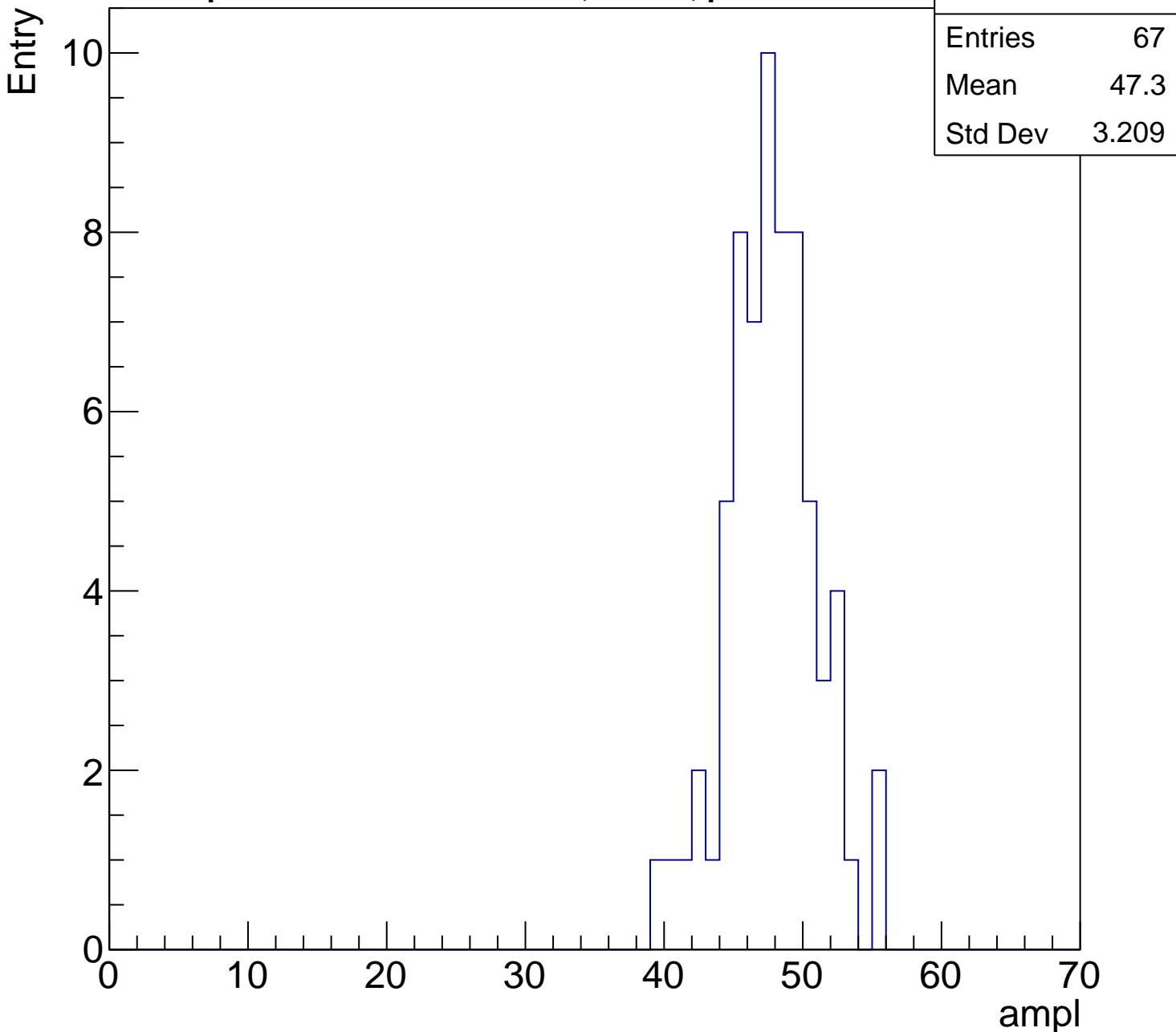
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	47.3
Std Dev	3.209

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl

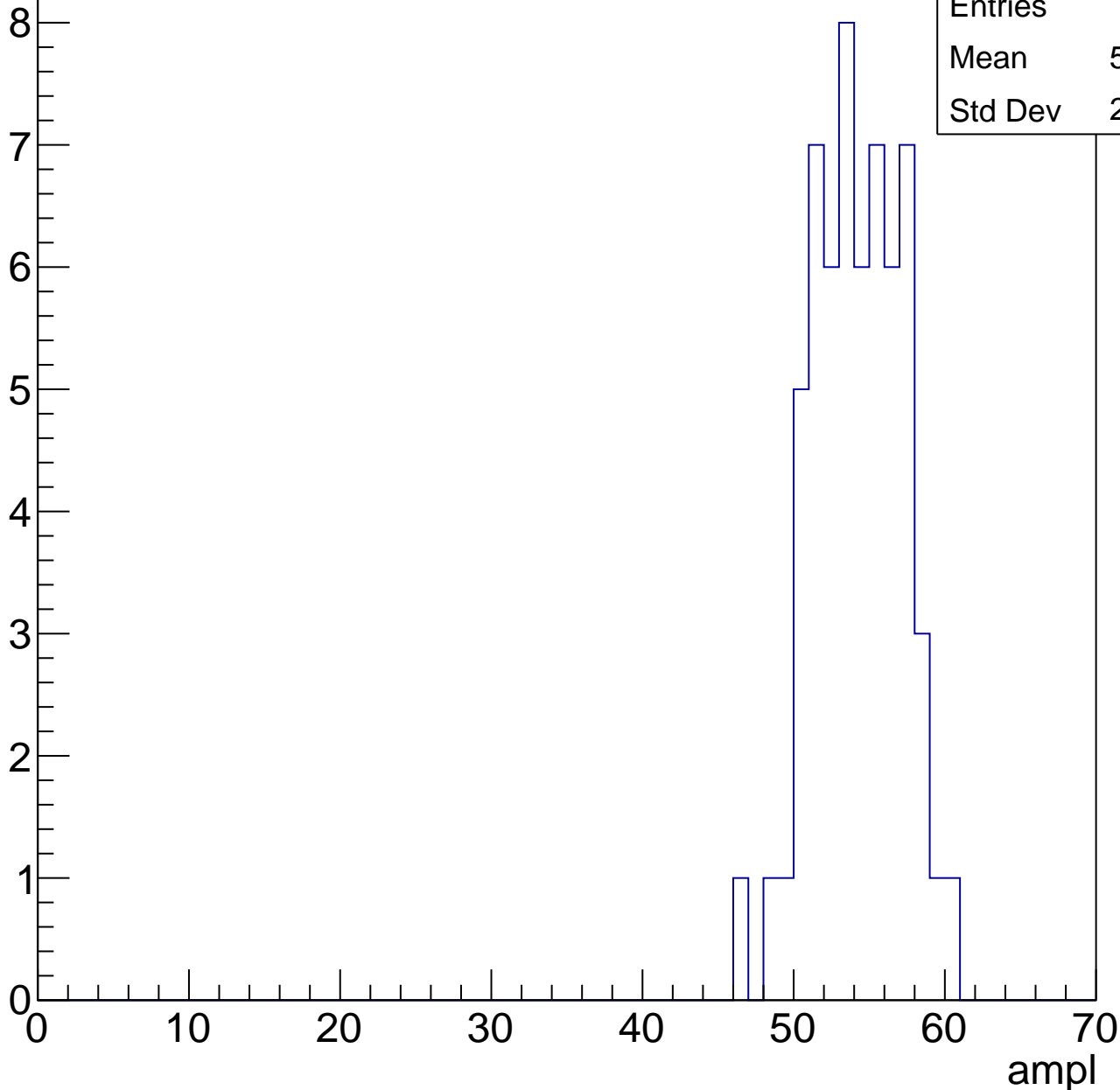


# B1L002S, U26-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

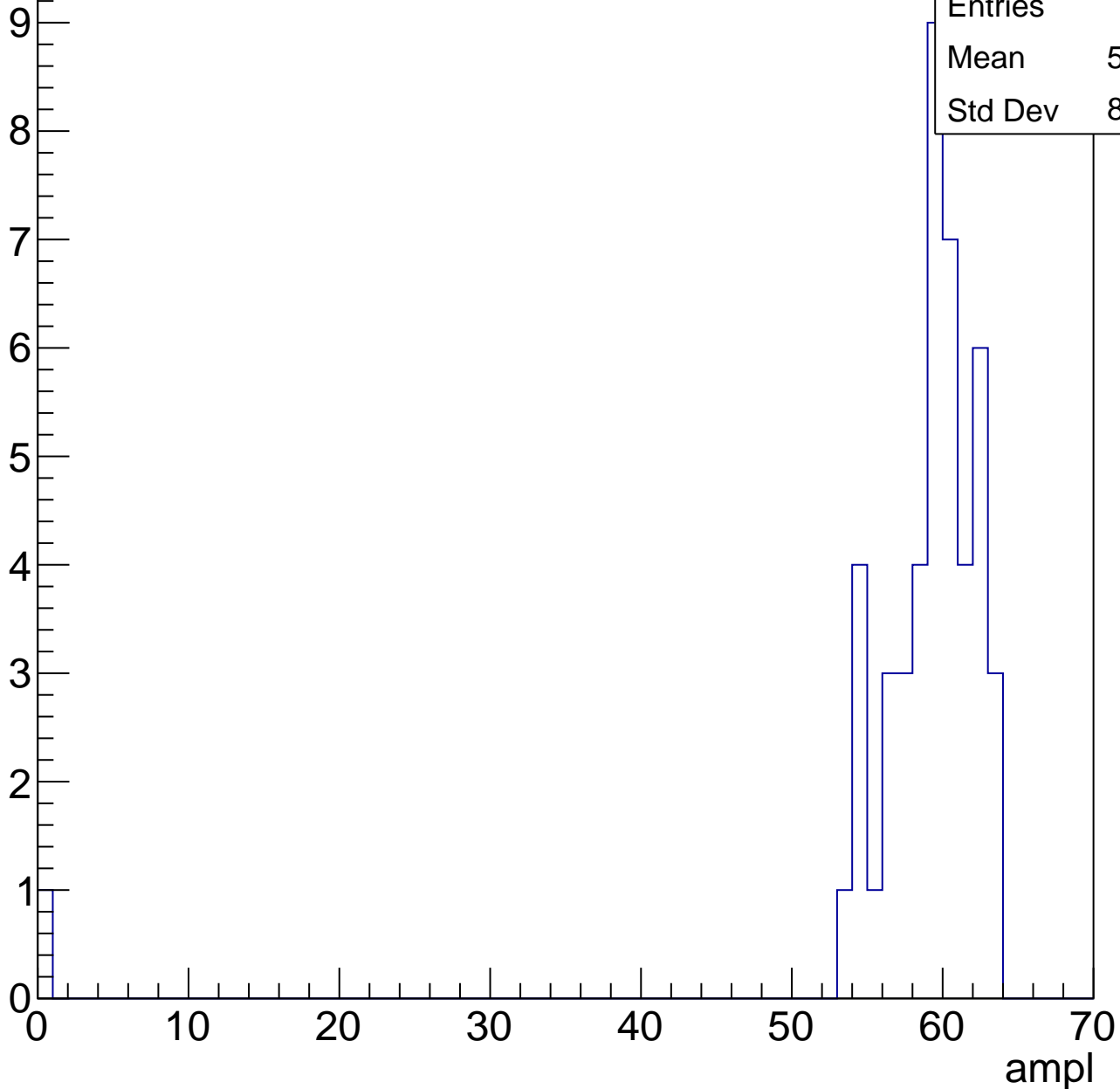
Entries	60
Mean	53.72
Std Dev	2.882



# B1L002S, U26-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

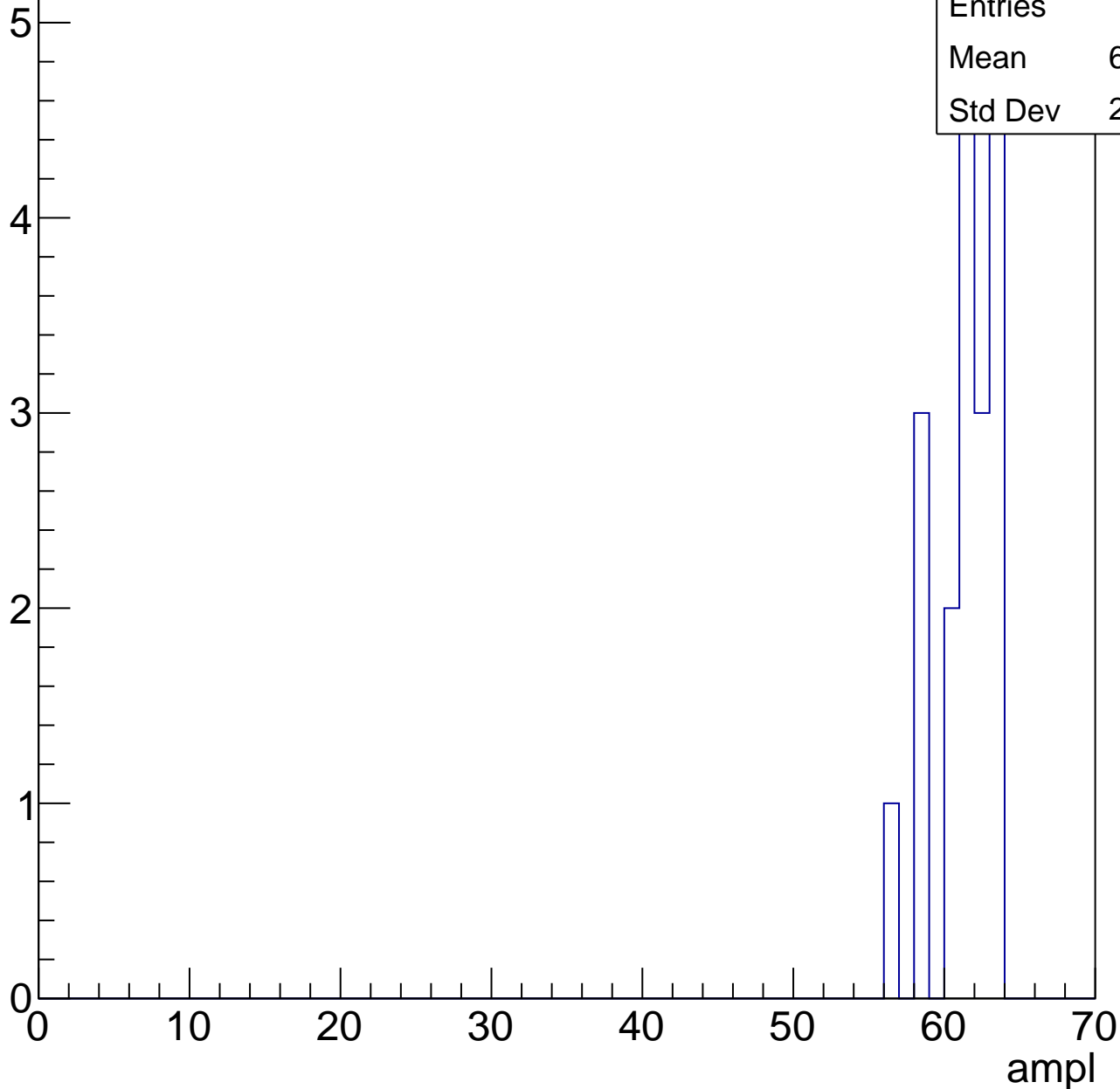


# B1L002S, U26-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	60.84
Std Dev	2.007

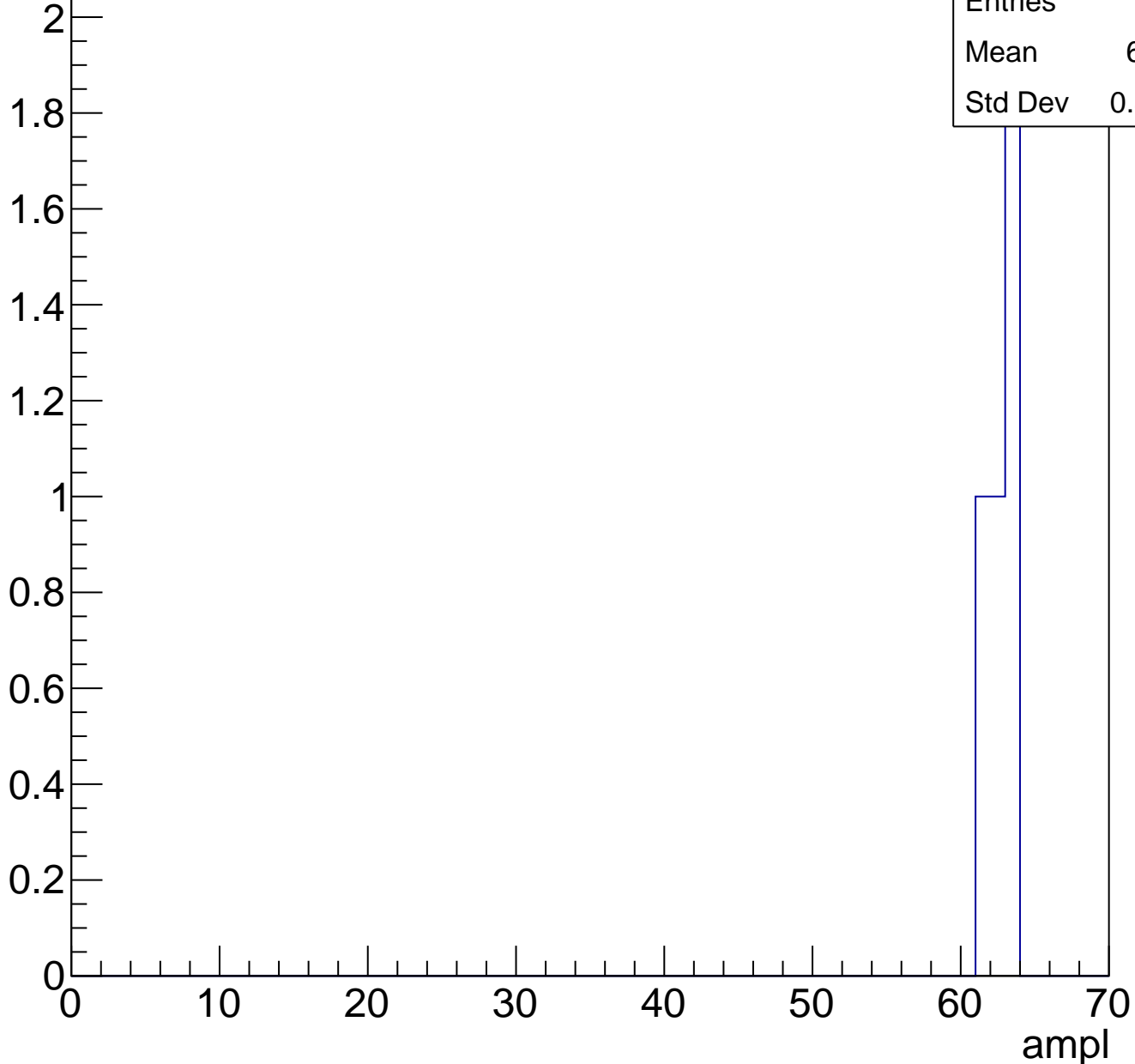




# B1L002S, U26-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

# B1L002S, U26-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	87
Mean	28.3
Std Dev	3.751

**Gaus mean : 28.7535**

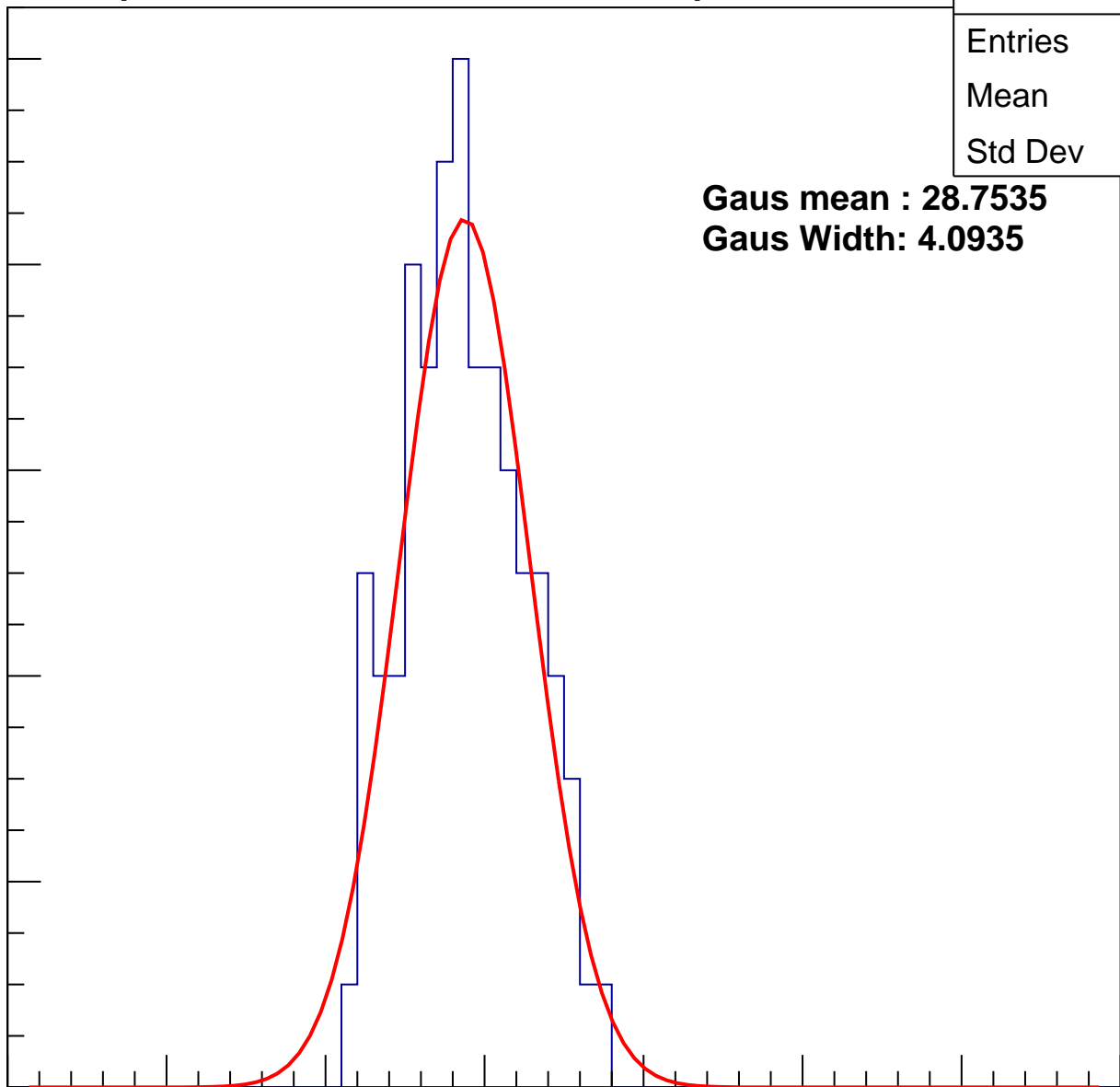
**Gaus Width: 4.0935**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch119, adc1

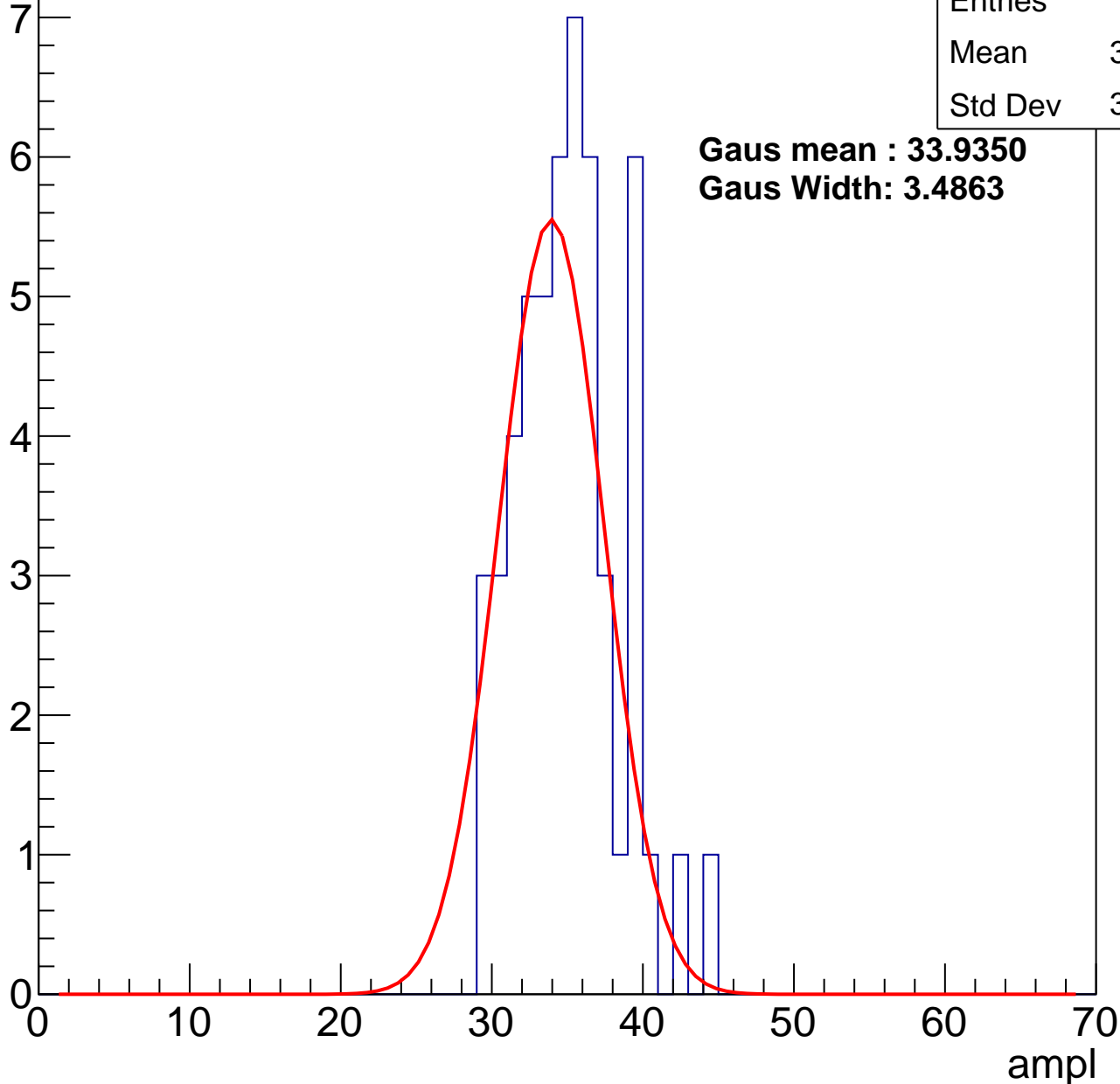
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	34.62
Std Dev	3.364

**Gaus mean : 33.9350**

**Gaus Width: 3.4863**



# B1L002S, U26-ch119, adc2

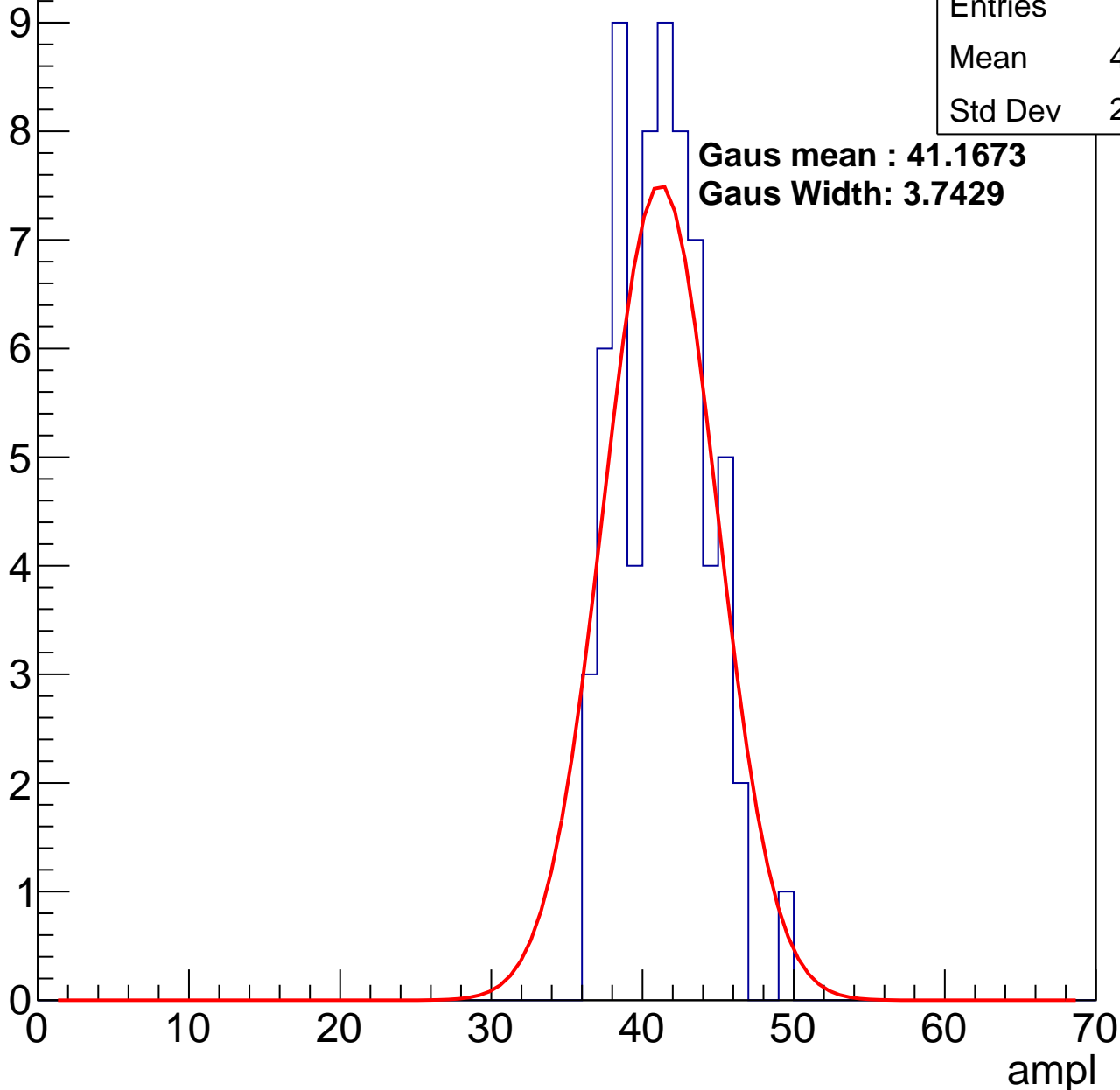
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.85
Std Dev	2.862

**Gaus mean : 41.1673**

**Gaus Width: 3.7429**

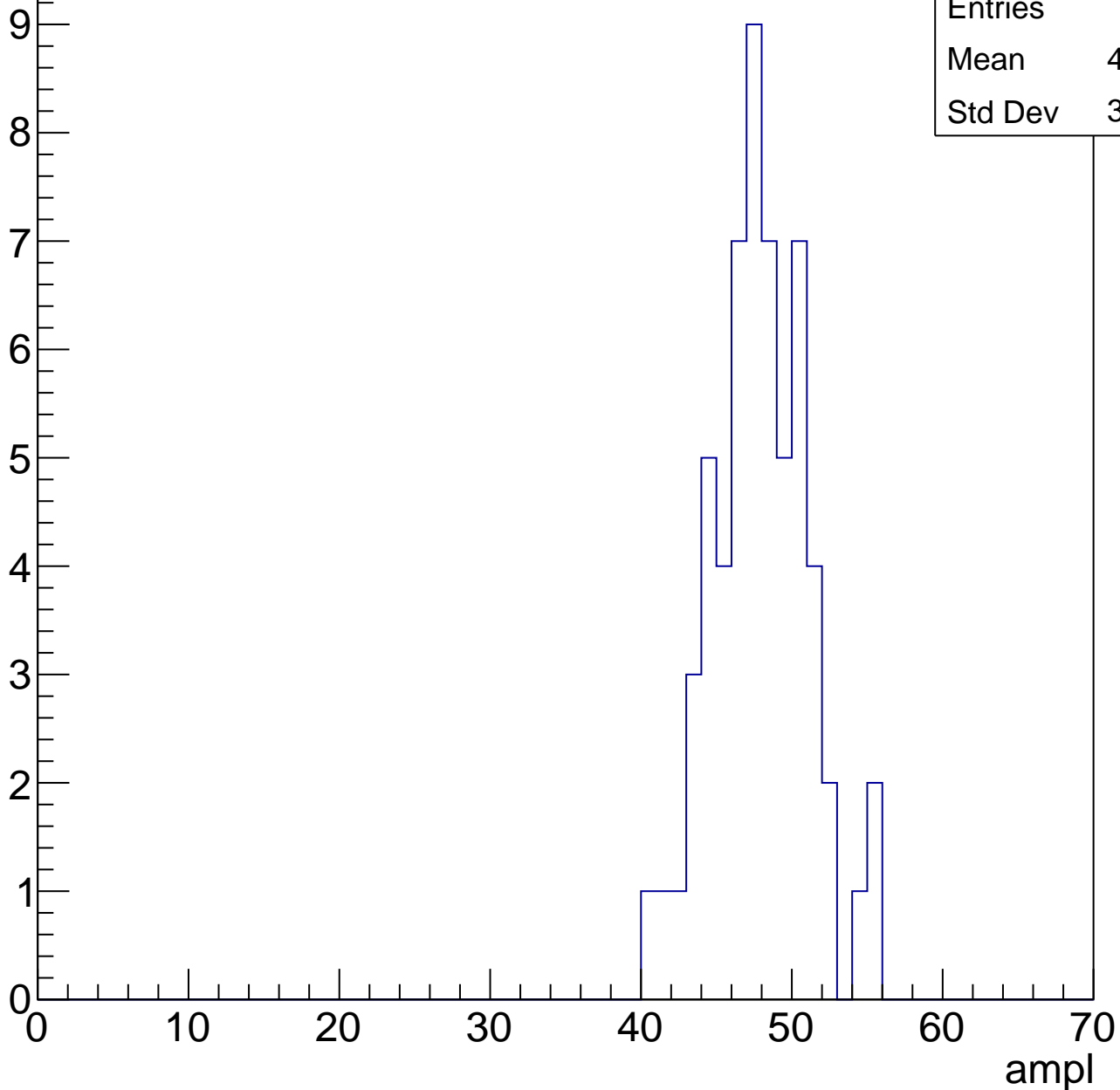


# B1L002S, U26-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.46
Std Dev	3.175

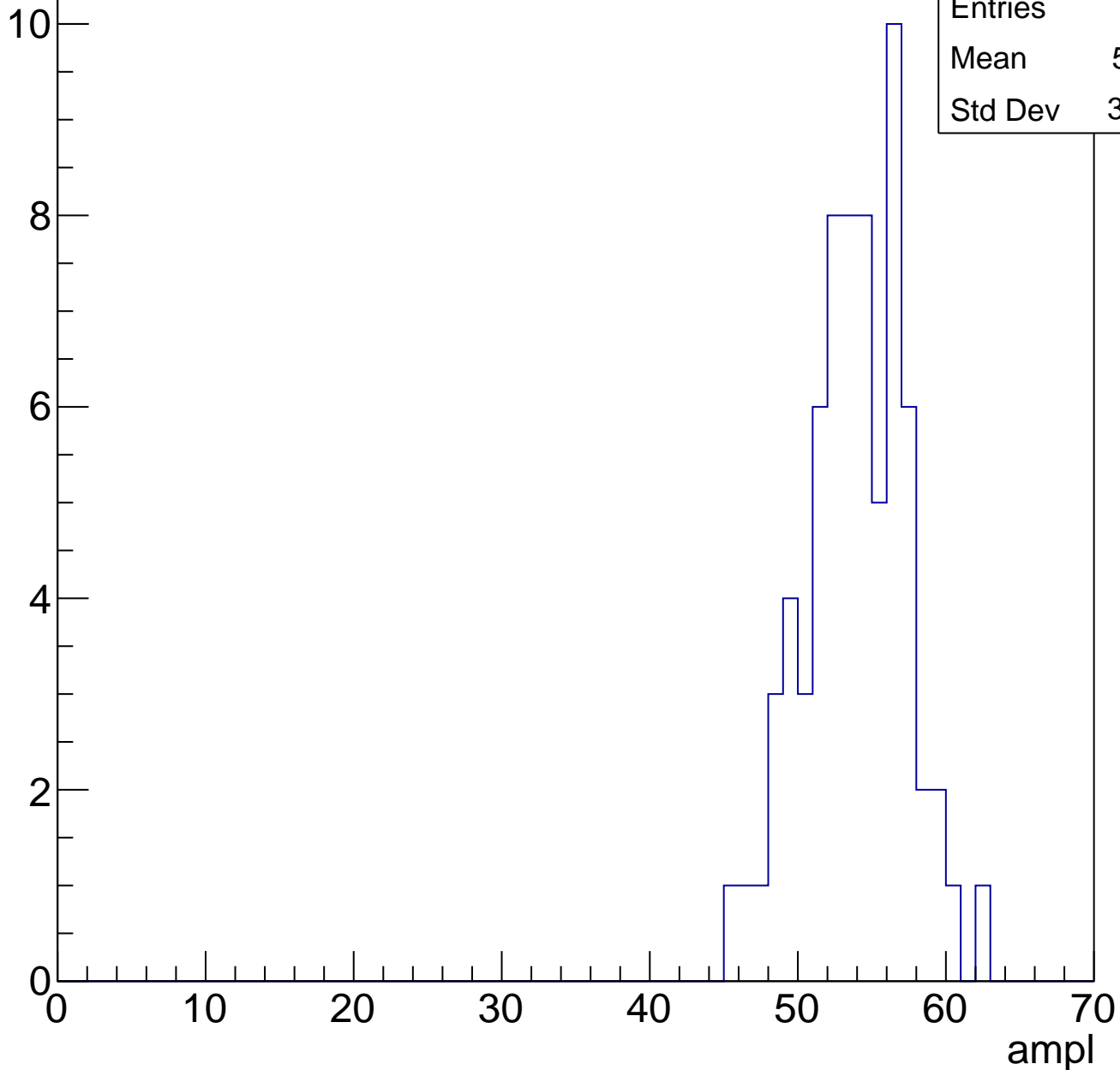


# B1L002S, U26-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	53.41
Std Dev	3.383

Entry

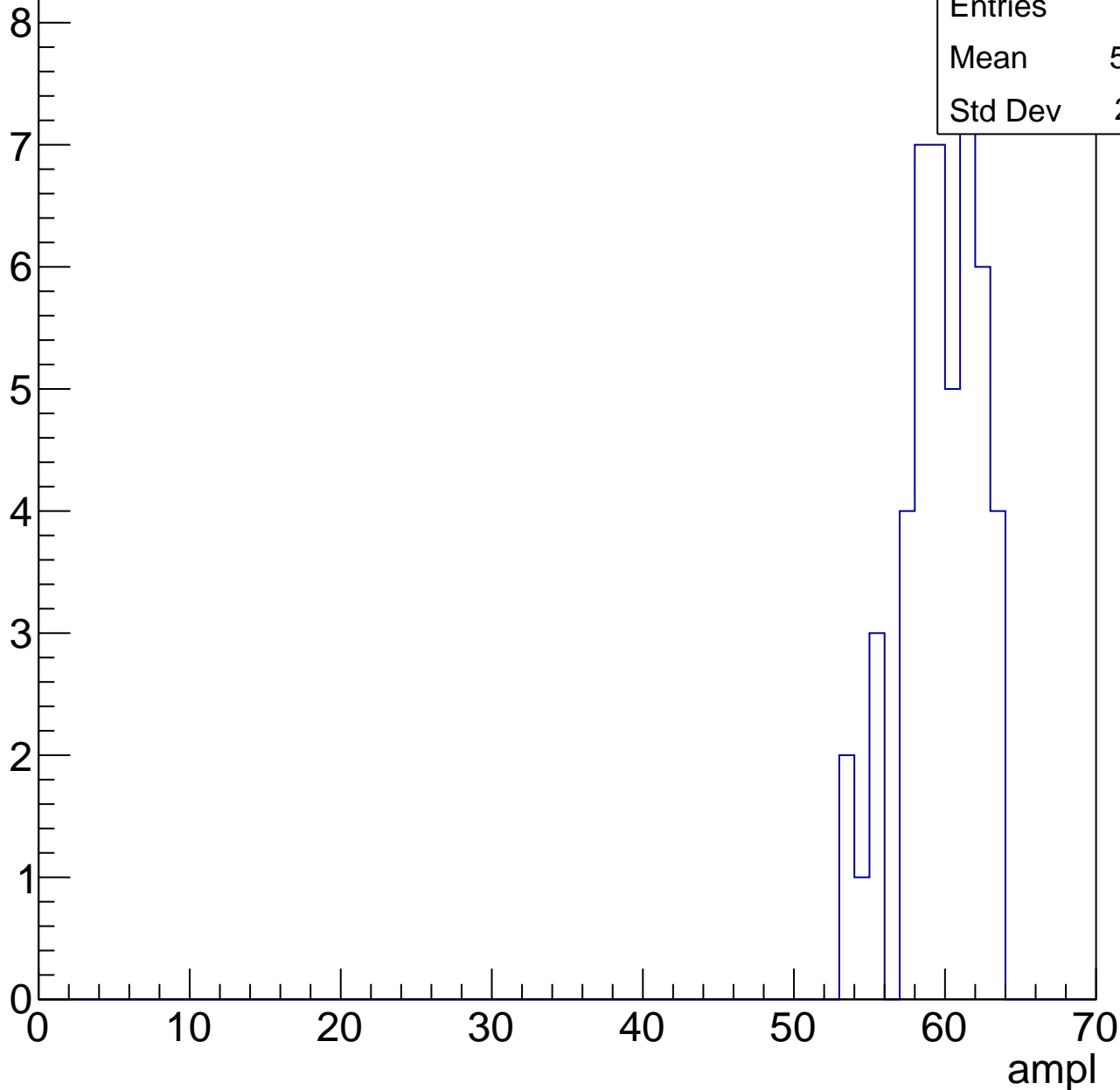


# B1L002S, U26-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	59.23
Std Dev	2.611

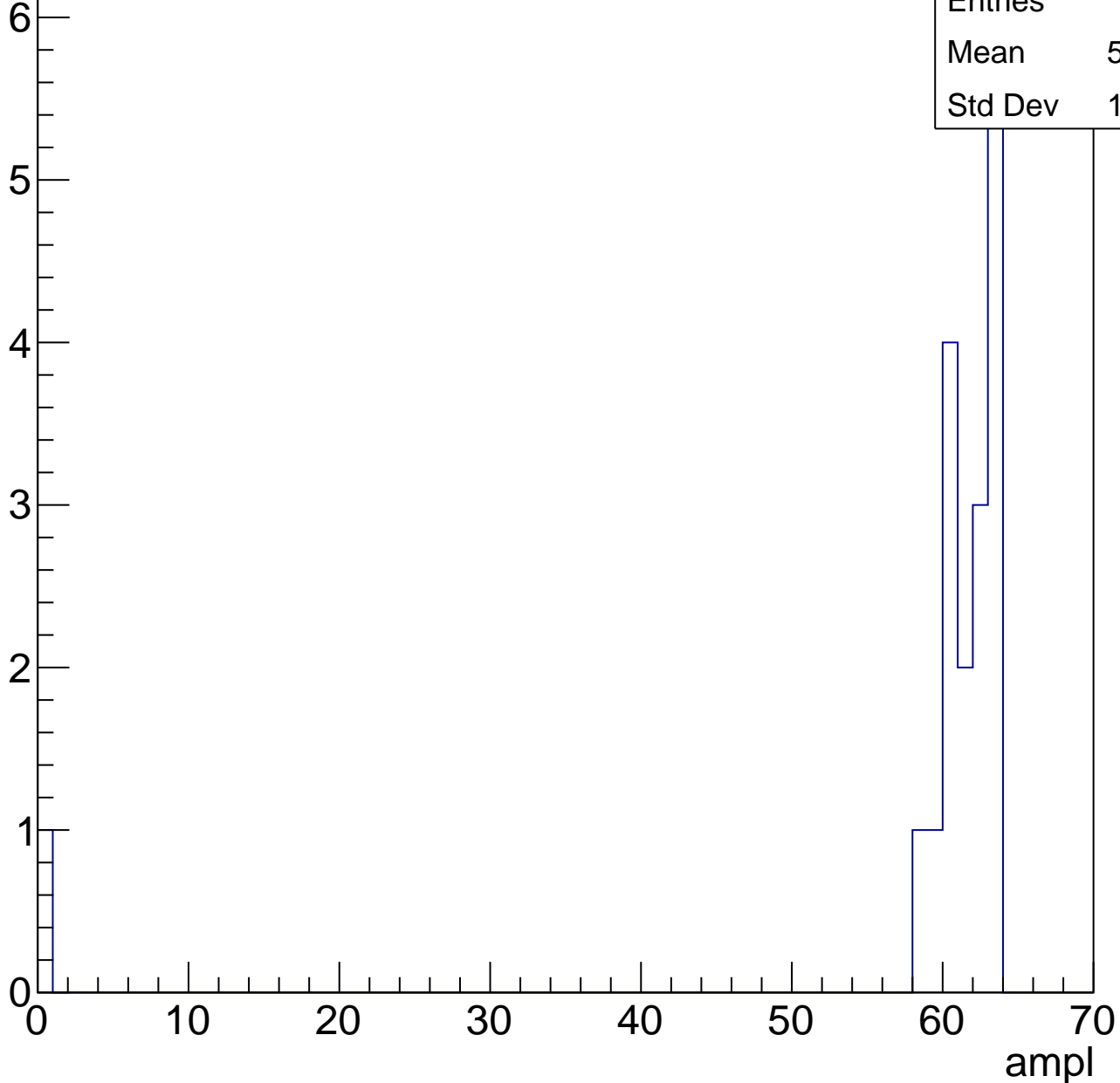


# B1L002S, U26-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	57.94
Std Dev	14.14





# B1L002S, U26-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch120, adc0

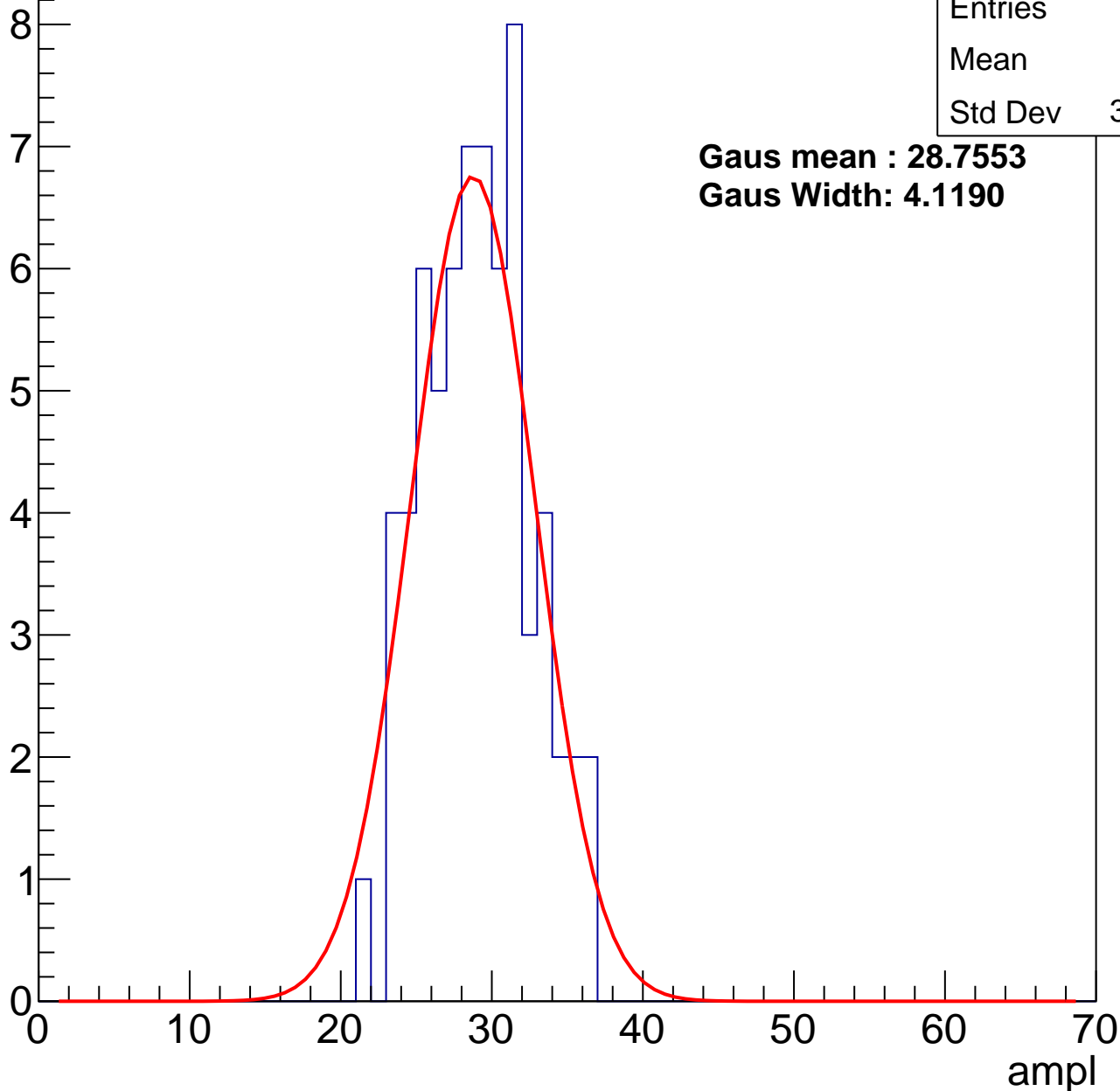
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.6
Std Dev	3.494

**Gaus mean : 28.7553**

**Gaus Width: 4.1190**



# B1L002S, U26-ch120, adc1

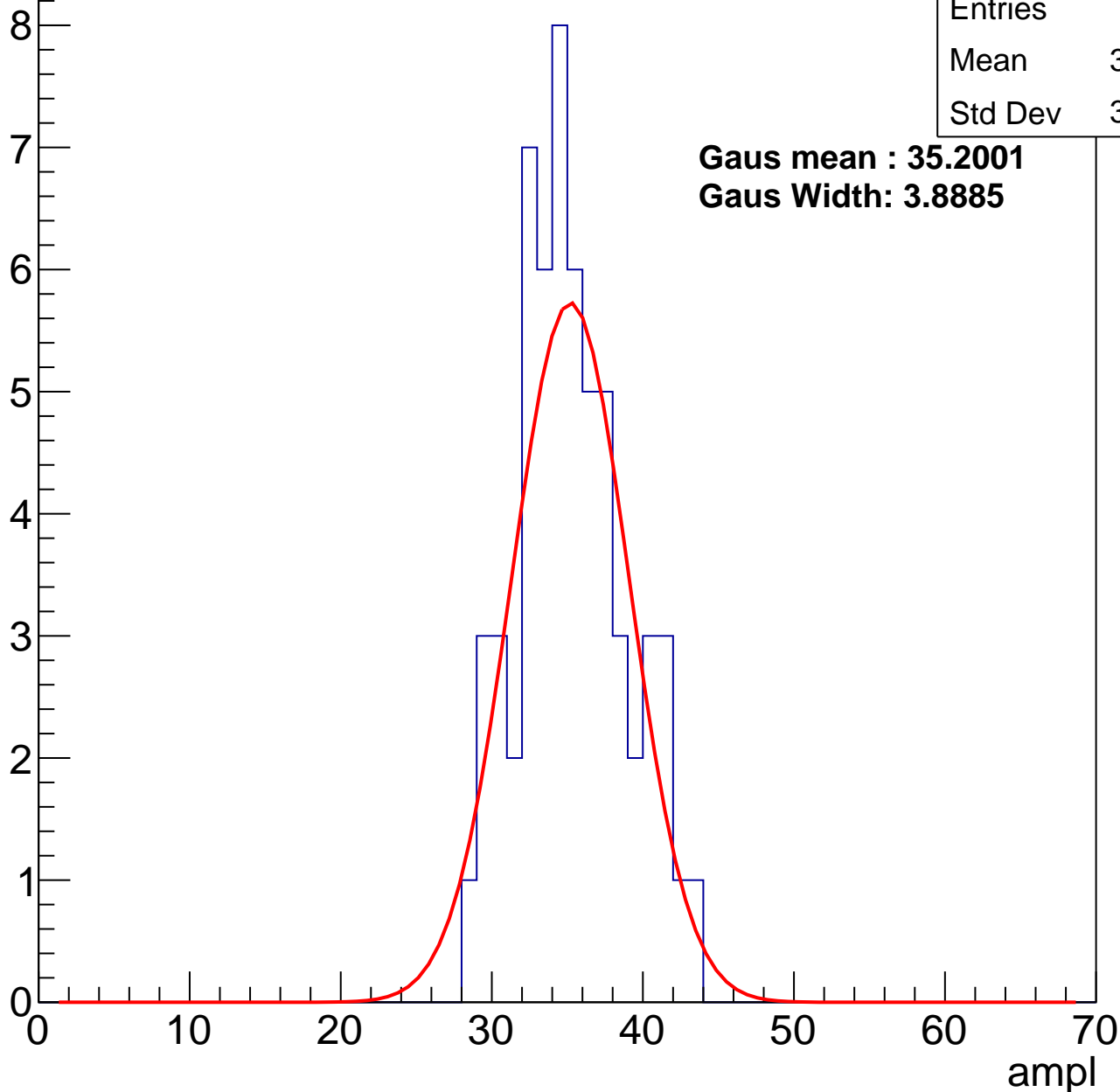
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.85
Std Dev	3.536

**Gaus mean : 35.2001**

**Gaus Width: 3.8885**



# B1L002S, U26-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	41.67
Std Dev	3.603

**Gaus mean : 41.3996**

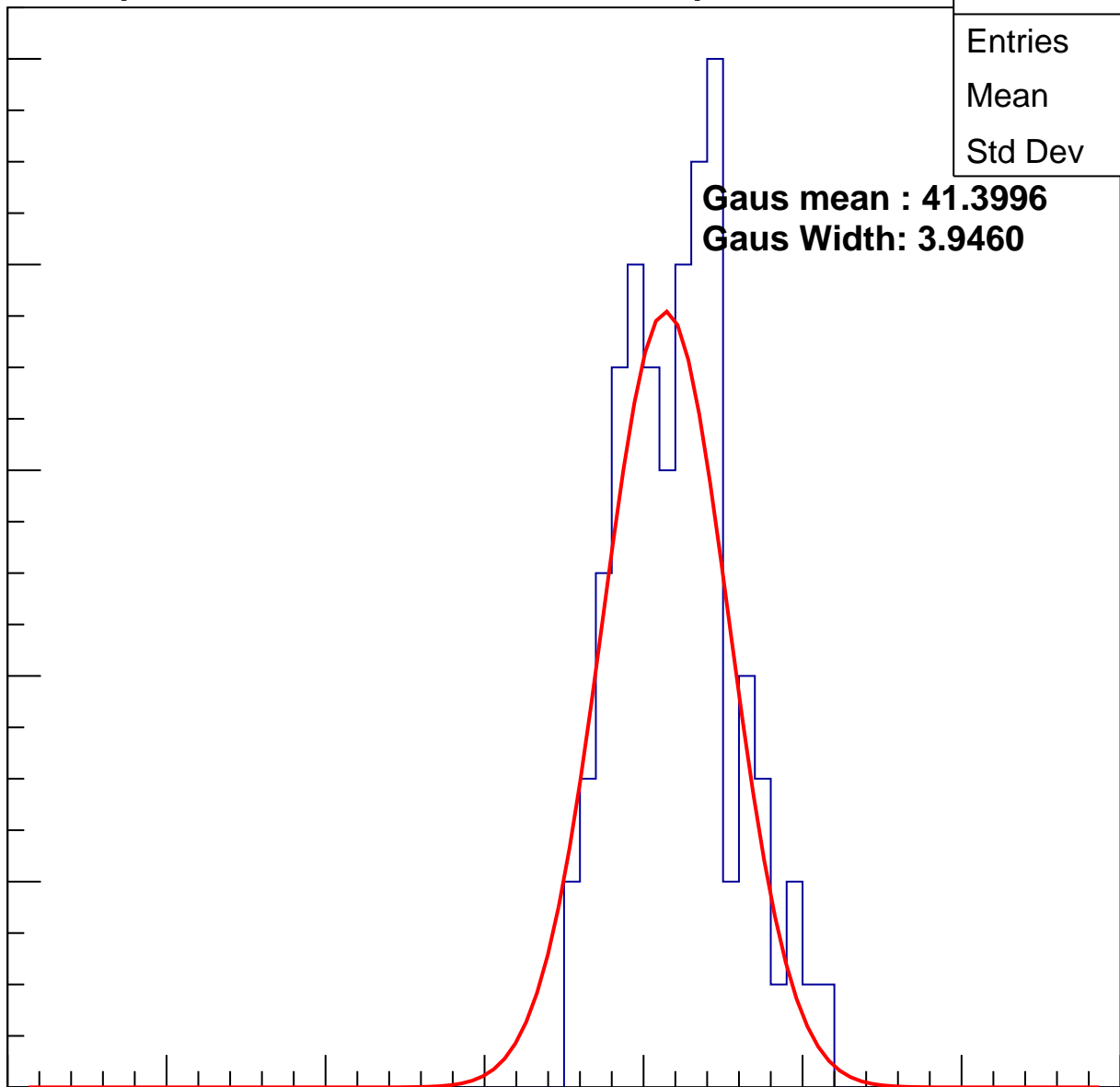
**Gaus Width: 3.9460**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

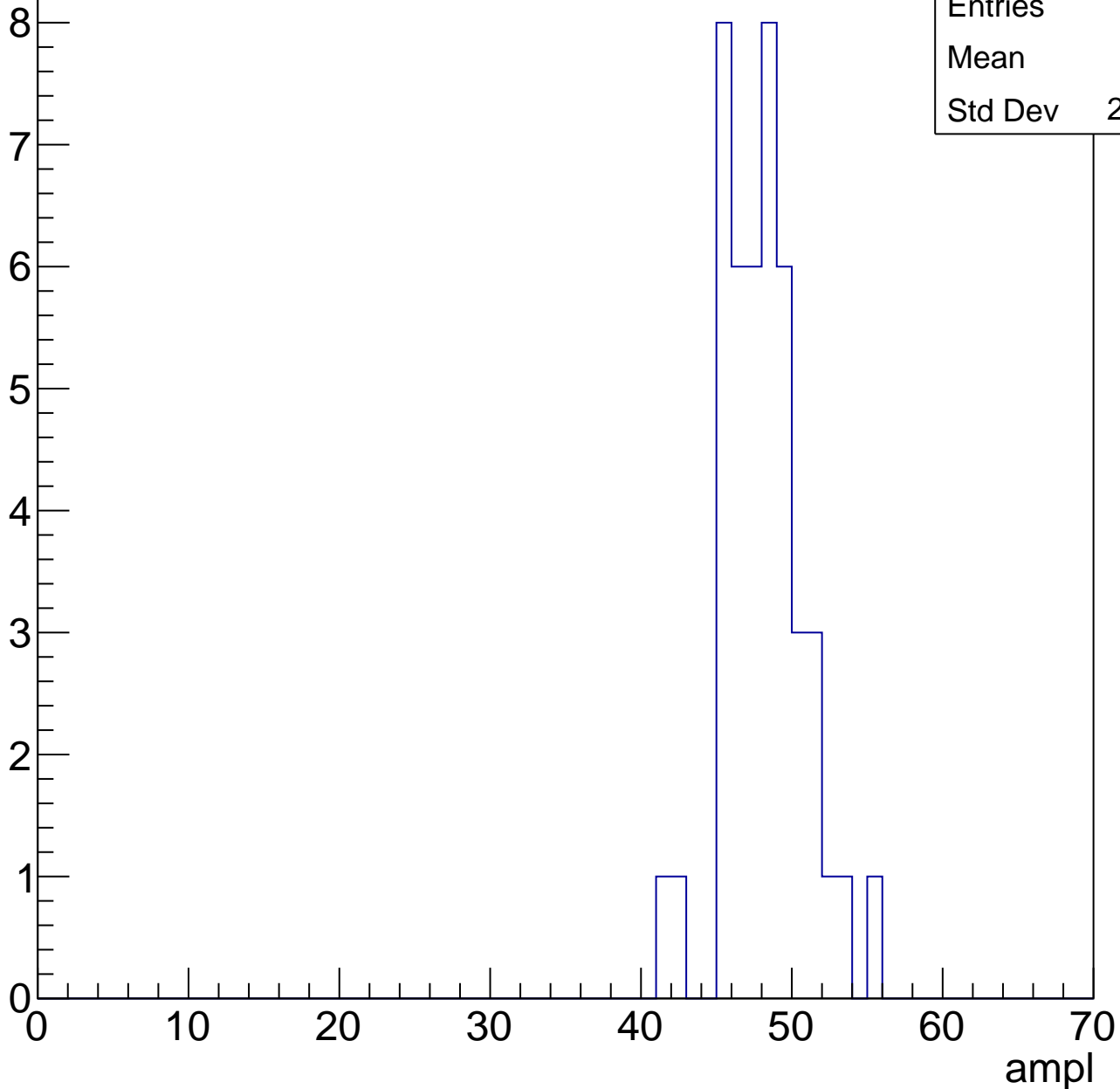


# B1L002S, U26-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	47.6
Std Dev	2.645

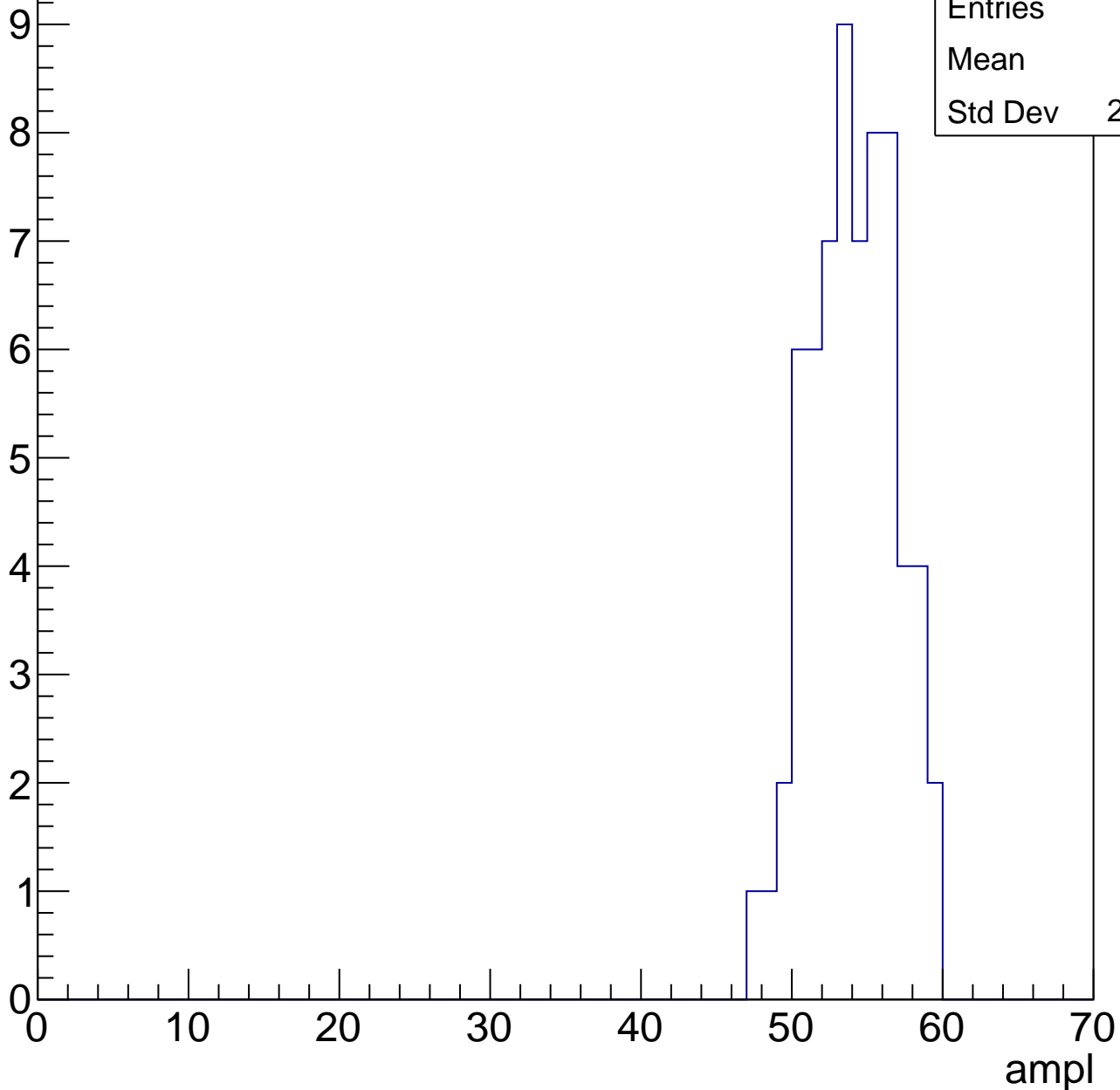


# B1L002S, U26-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	53.6
Std Dev	2.778

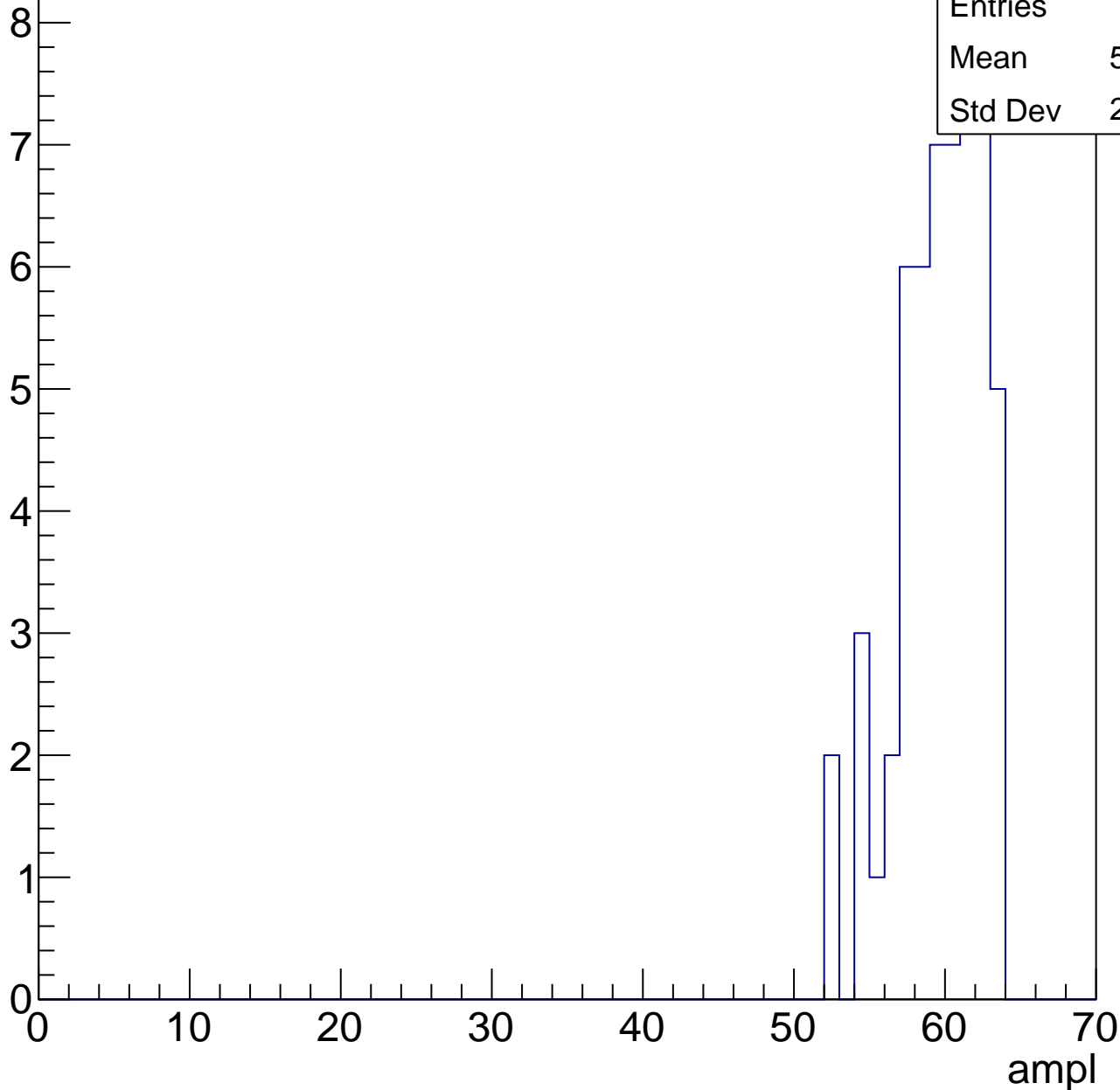


# B1L002S, U26-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

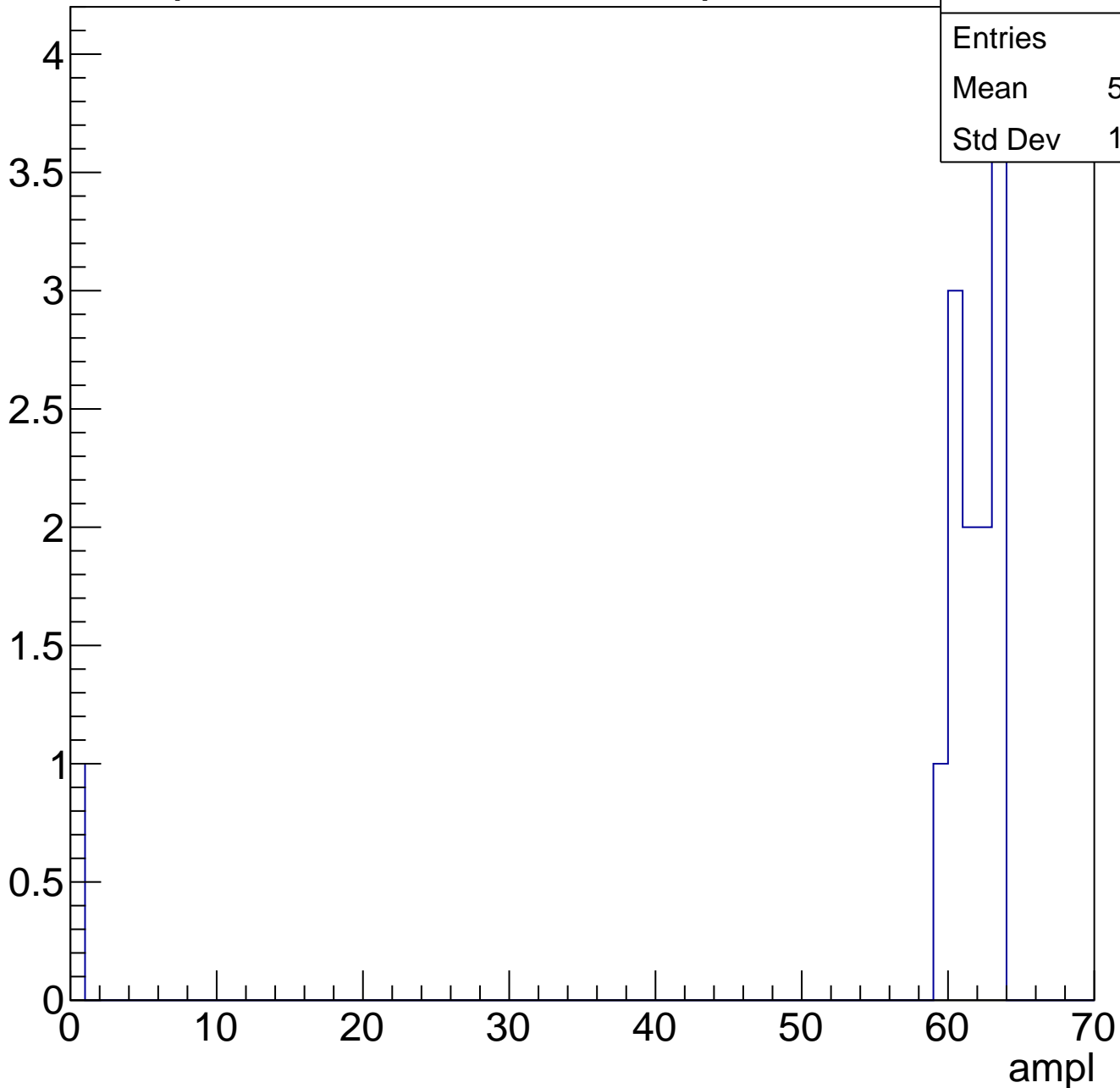
Entries	55
Mean	59.18
Std Dev	2.784



# B1L002S, U26-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

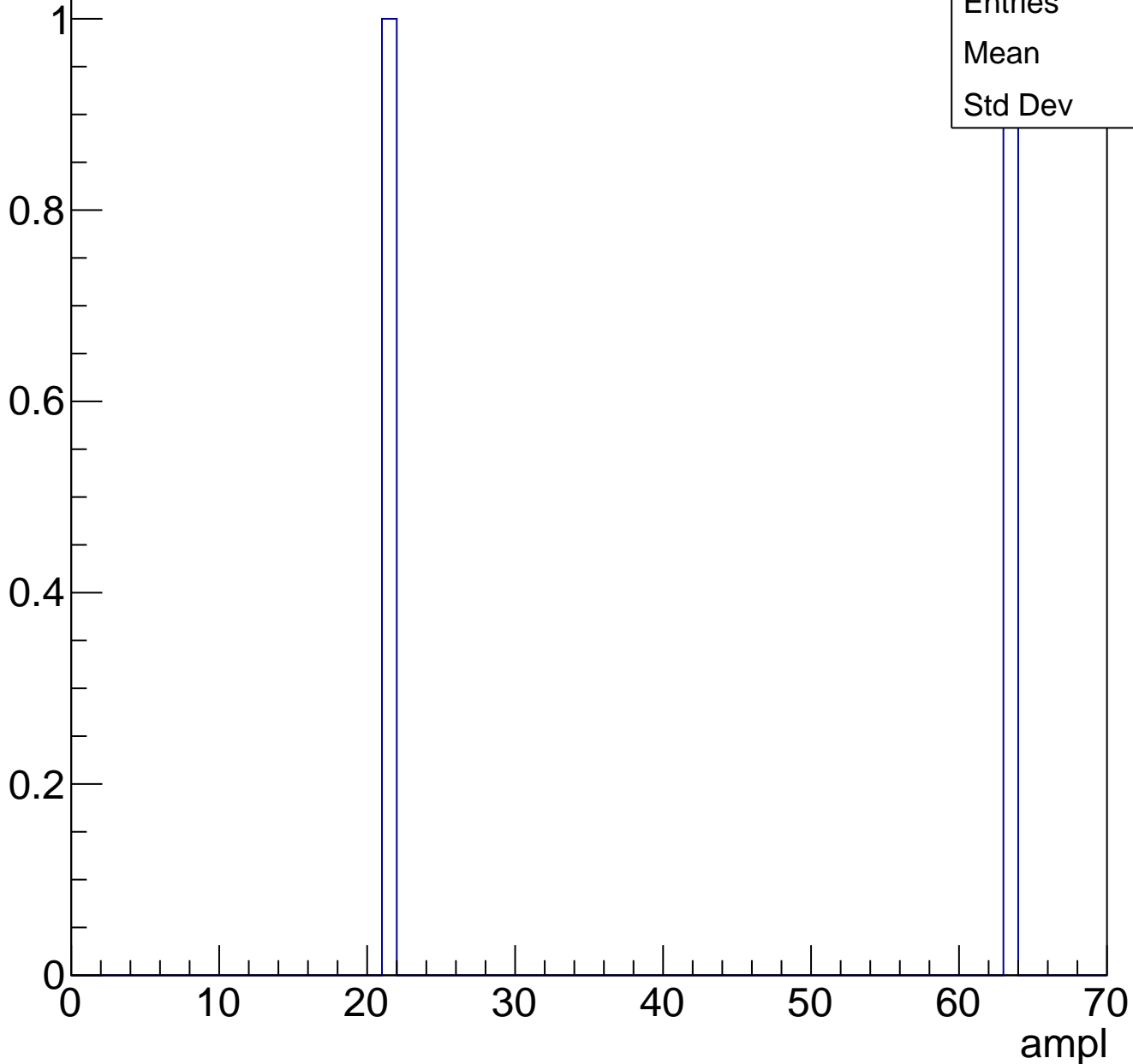




# B1L002S, U26-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch121, adc0

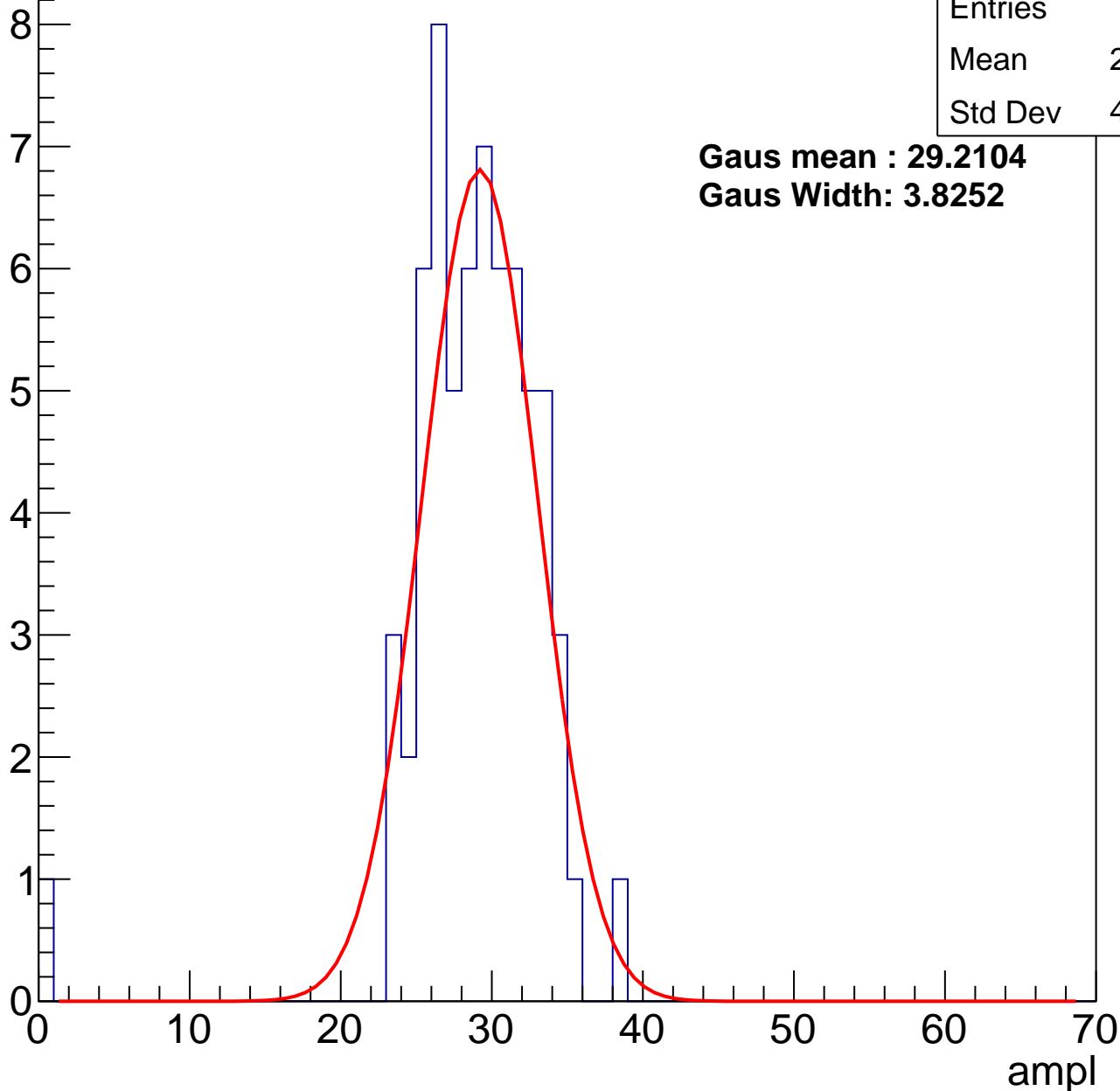
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.42
Std Dev	4.832

**Gaus mean : 29.2104**

**Gaus Width: 3.8252**



# B1L002S, U26-ch121, adc1

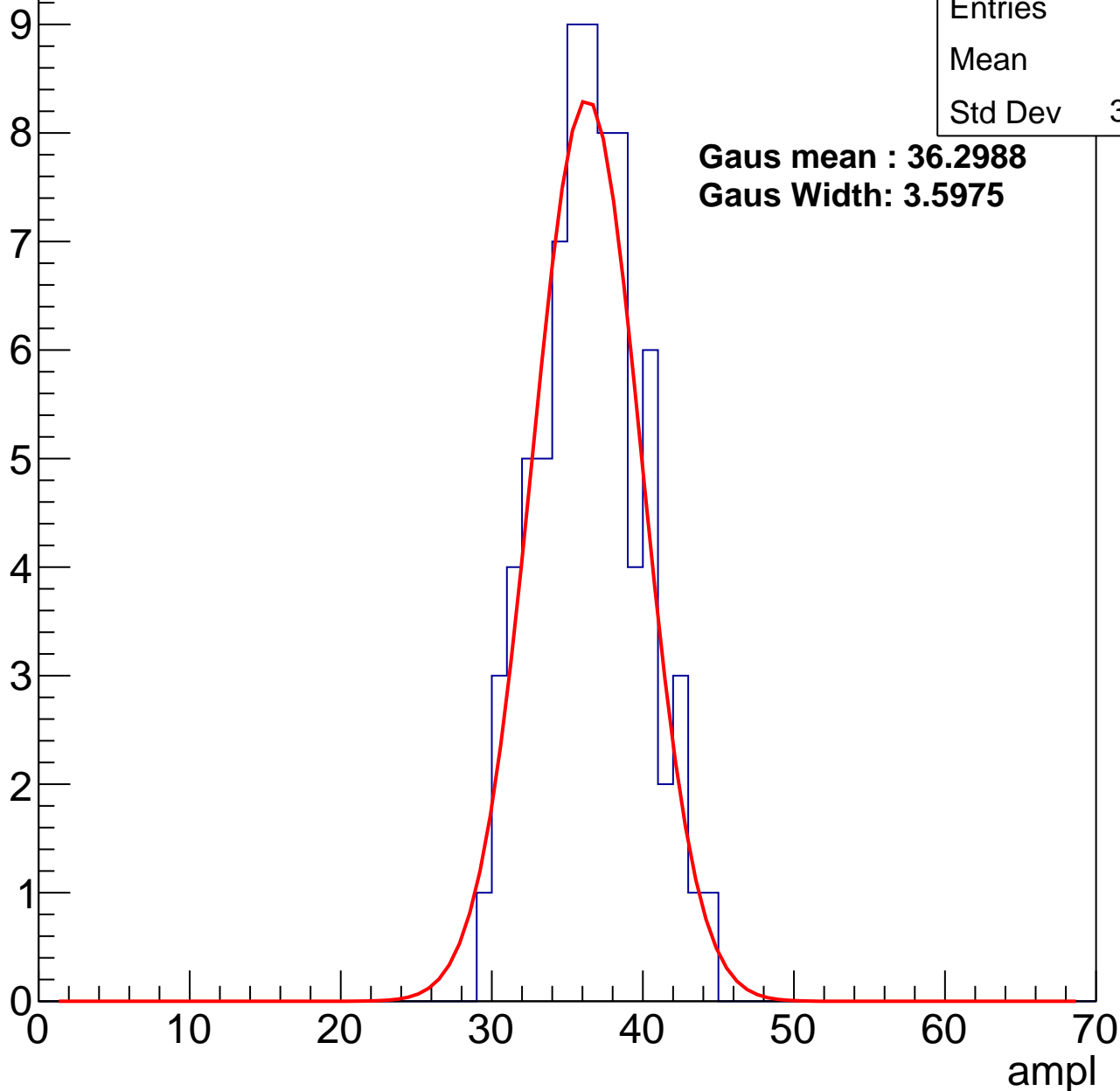
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	36
Std Dev	3.368

**Gaus mean : 36.2988**

**Gaus Width: 3.5975**



# B1L002S, U26-ch121, adc2

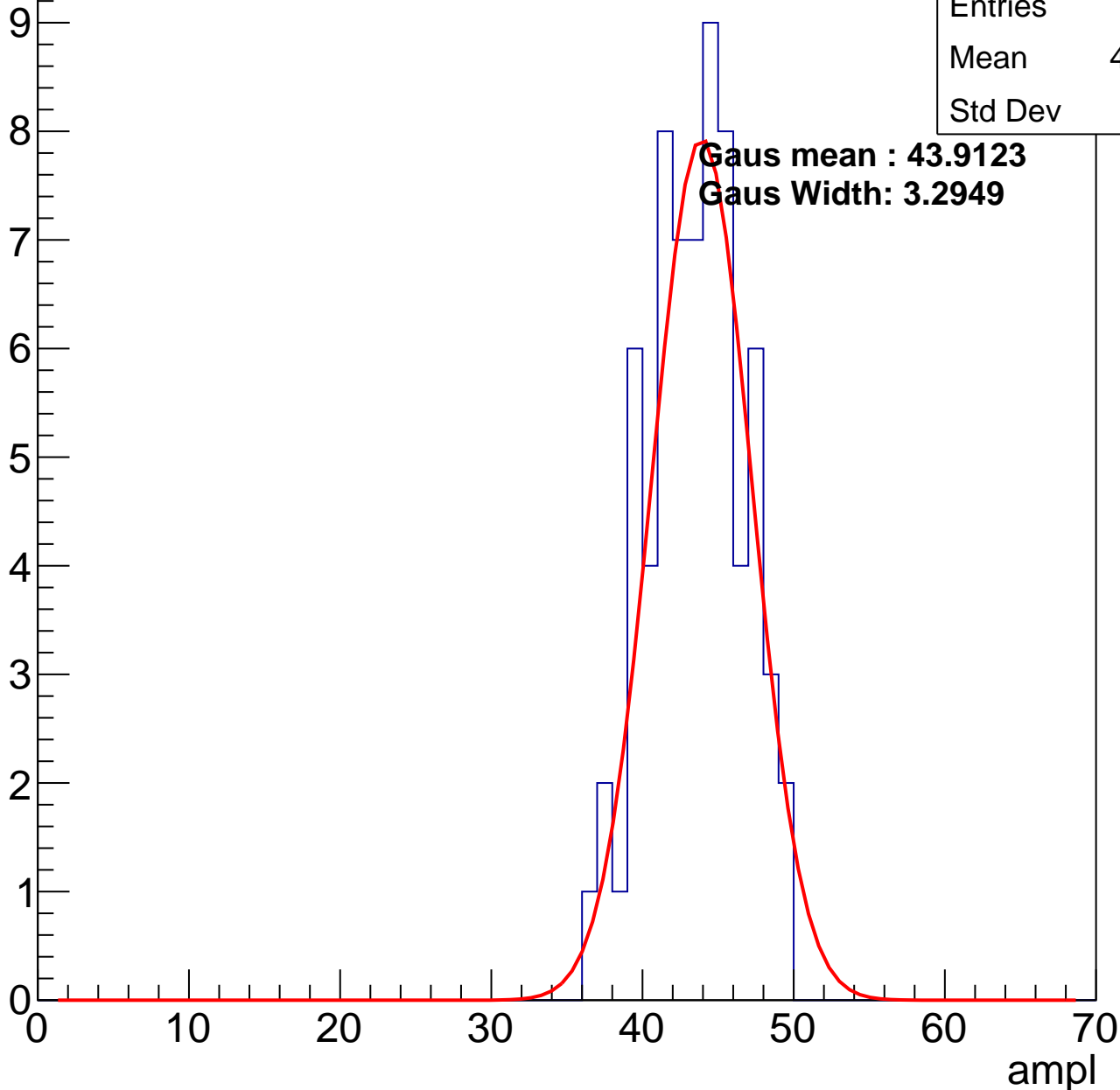
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	43.07
Std Dev	3.06

**Gaus mean : 43.9123**

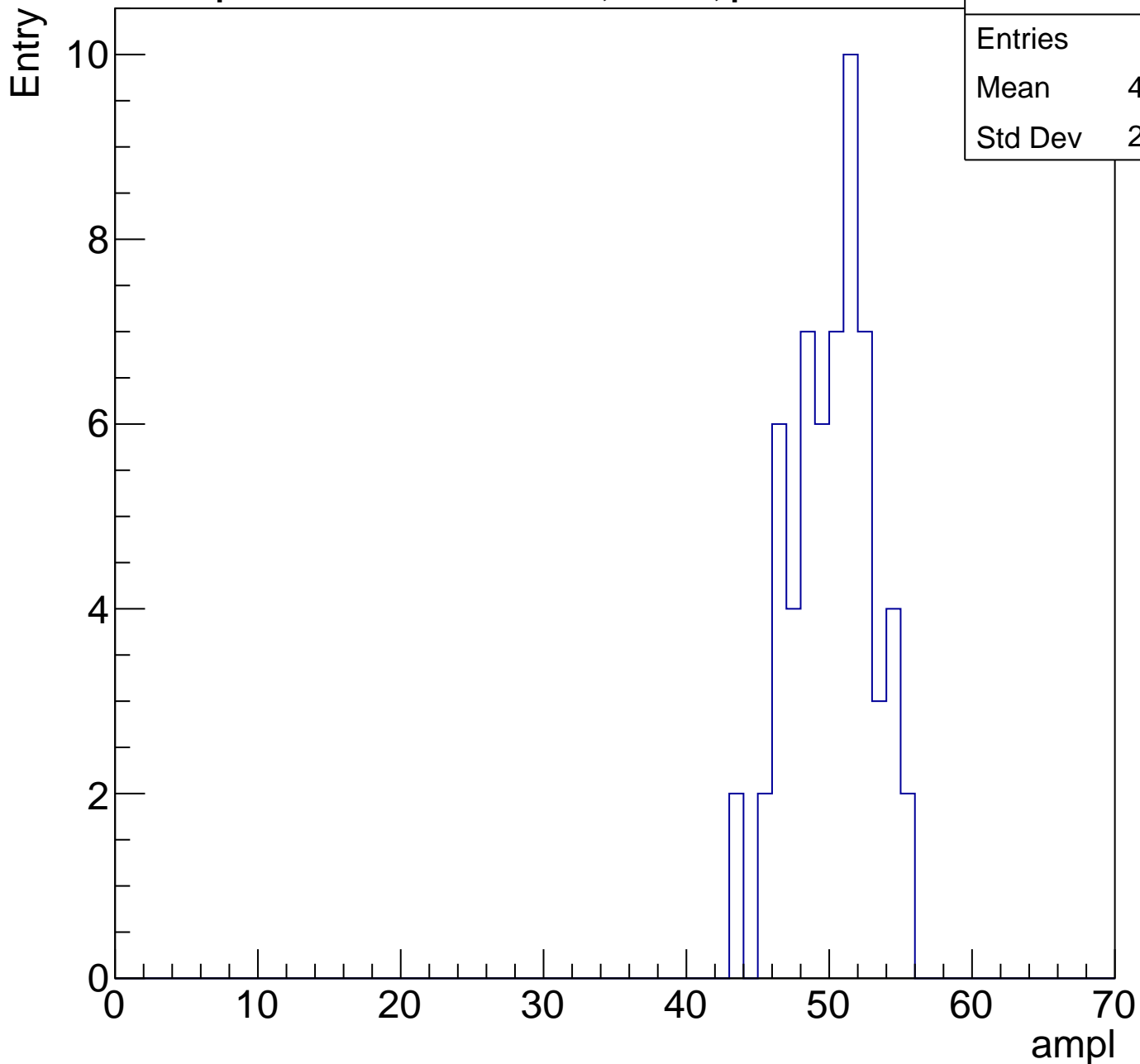
**Gaus Width: 3.2949**



# B1L002S, U26-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	49.65
Std Dev	2.845

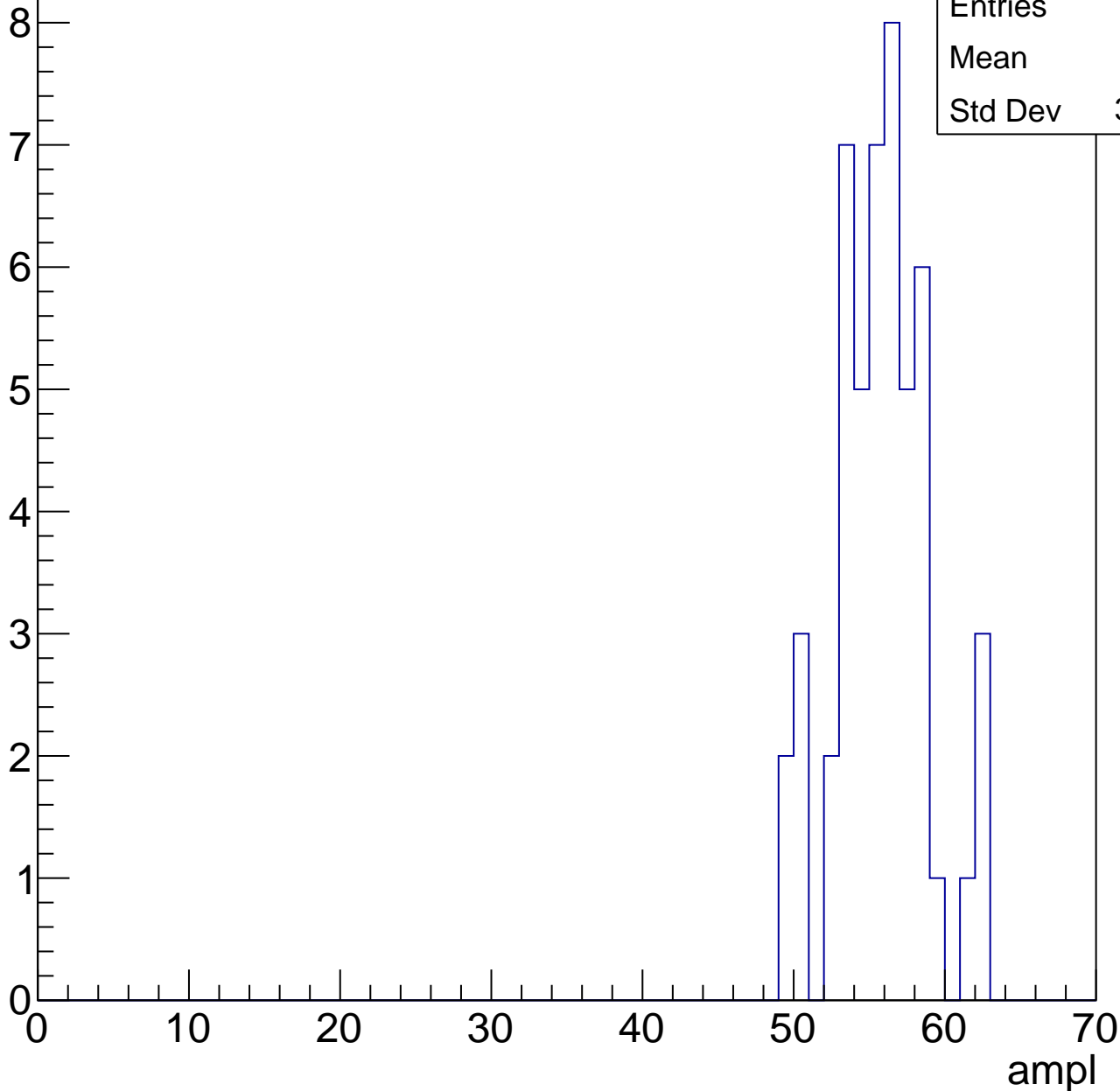


# B1L002S, U26-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

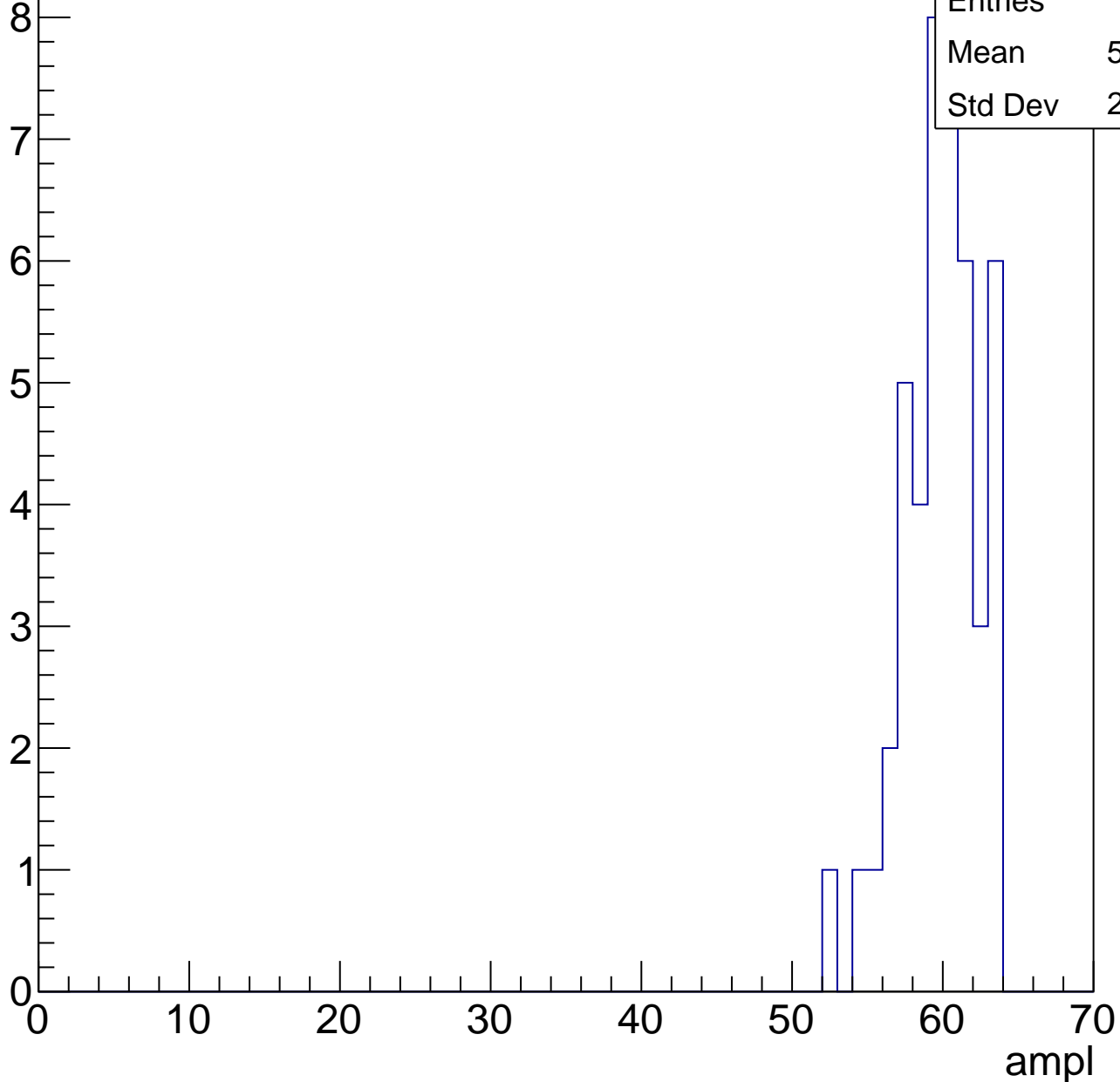
Entries	50
Mean	55.3
Std Dev	3.081



# B1L002S, U26-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

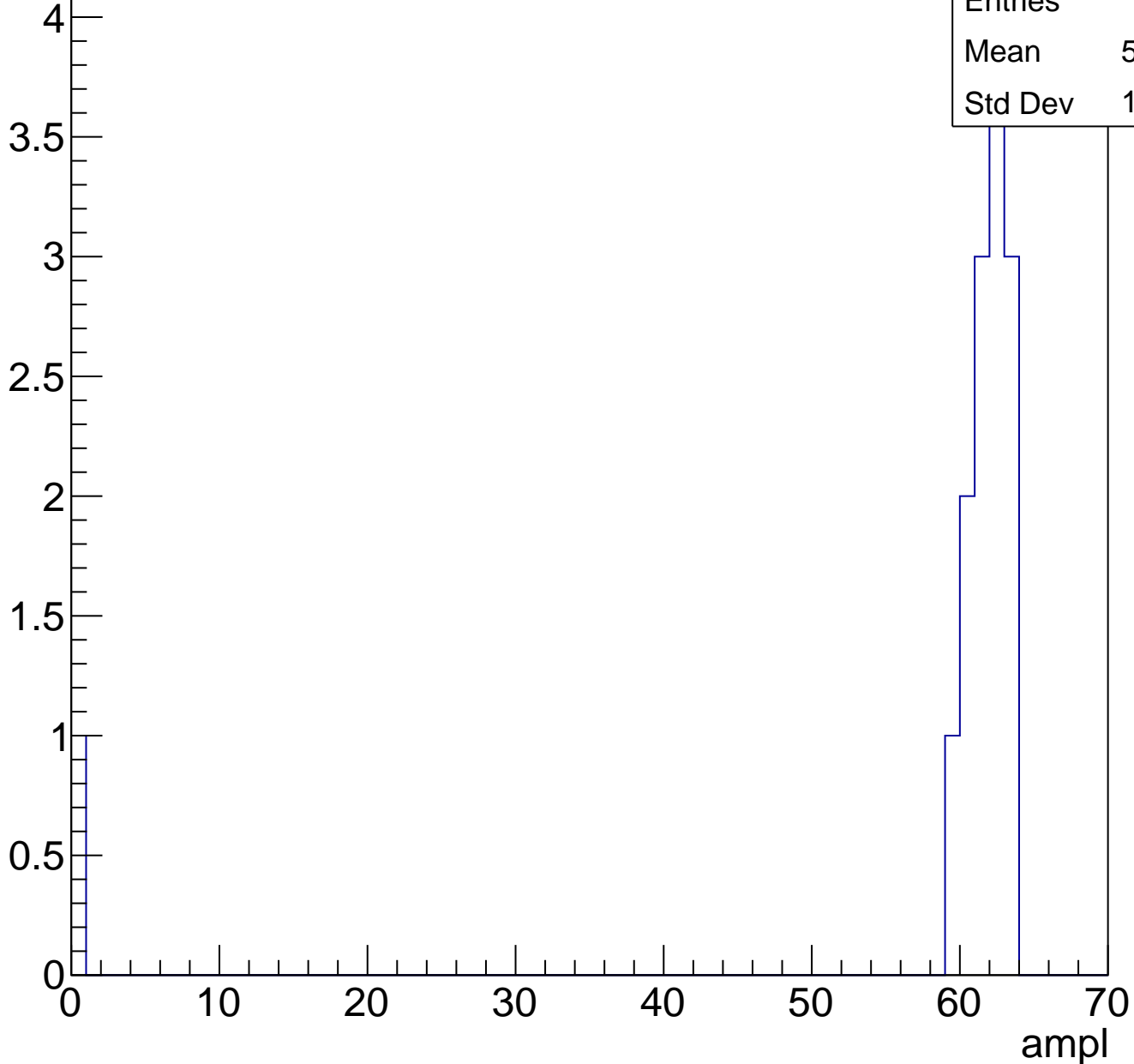


Entries	45
Mean	59.38
Std Dev	2.497

# B1L002S, U26-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	14
Mean	57.07
Std Dev	15.87



# B1L002S, U26-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch122, adc0

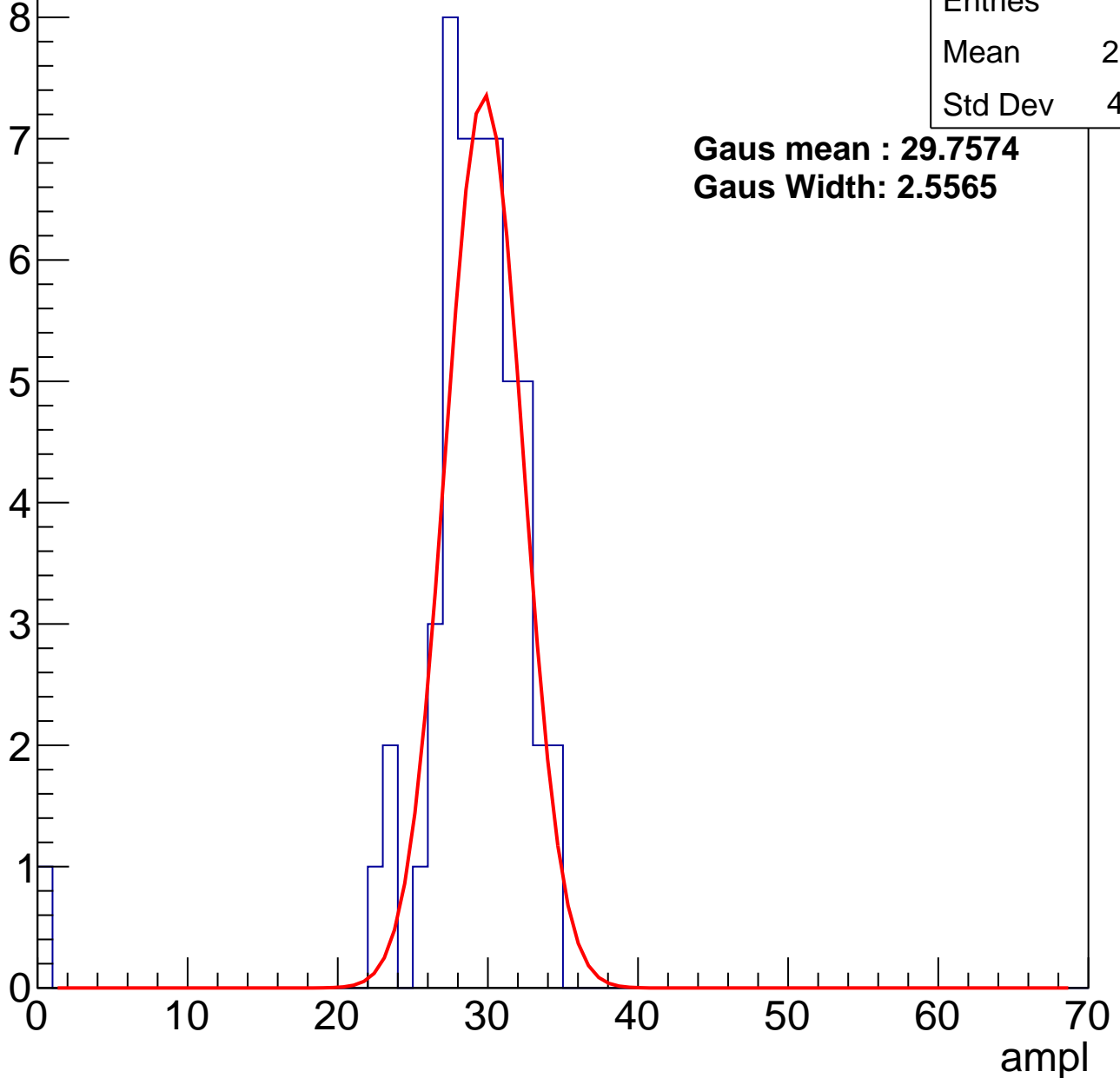
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	28.33
Std Dev	4.801

**Gaus mean : 29.7574**

**Gaus Width: 2.5565**



# B1L002S, U26-ch122, adc1

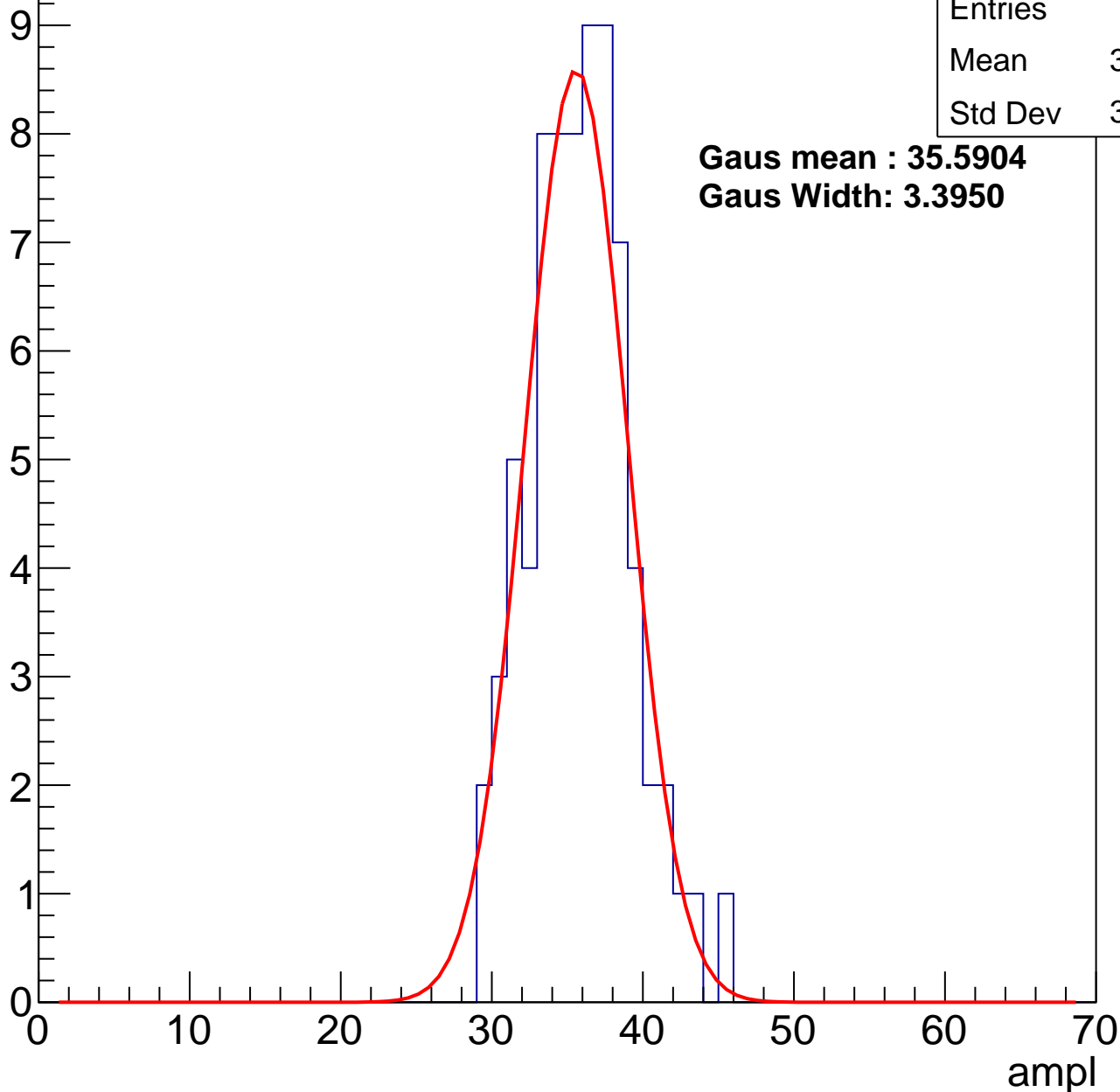
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	35.38
Std Dev	3.287

**Gaus mean : 35.5904**

**Gaus Width: 3.3950**



# B1L002S, U26-ch122, adc2

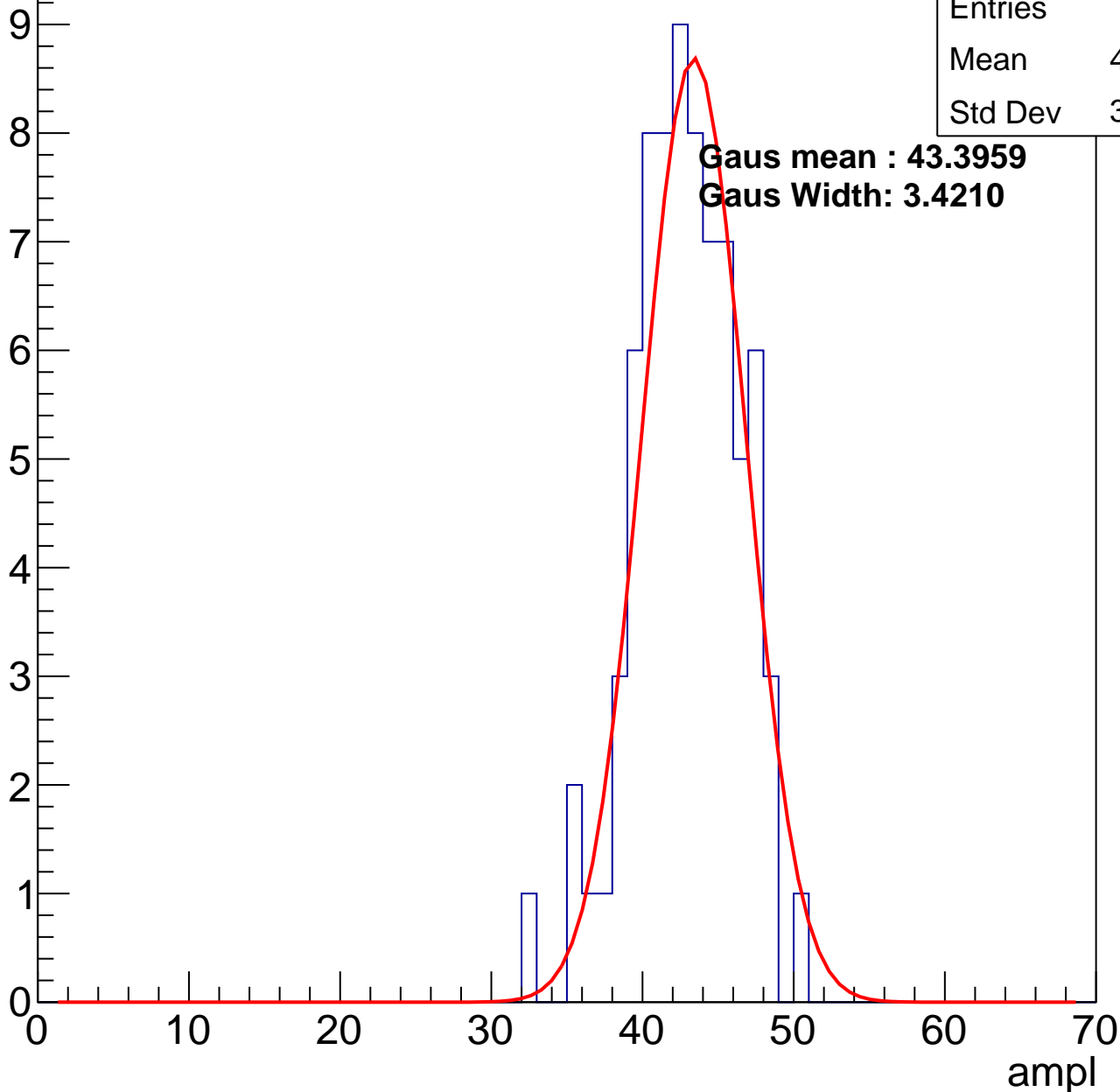
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	42.39
Std Dev	3.422

**Gaus mean : 43.3959**

**Gaus Width: 3.4210**

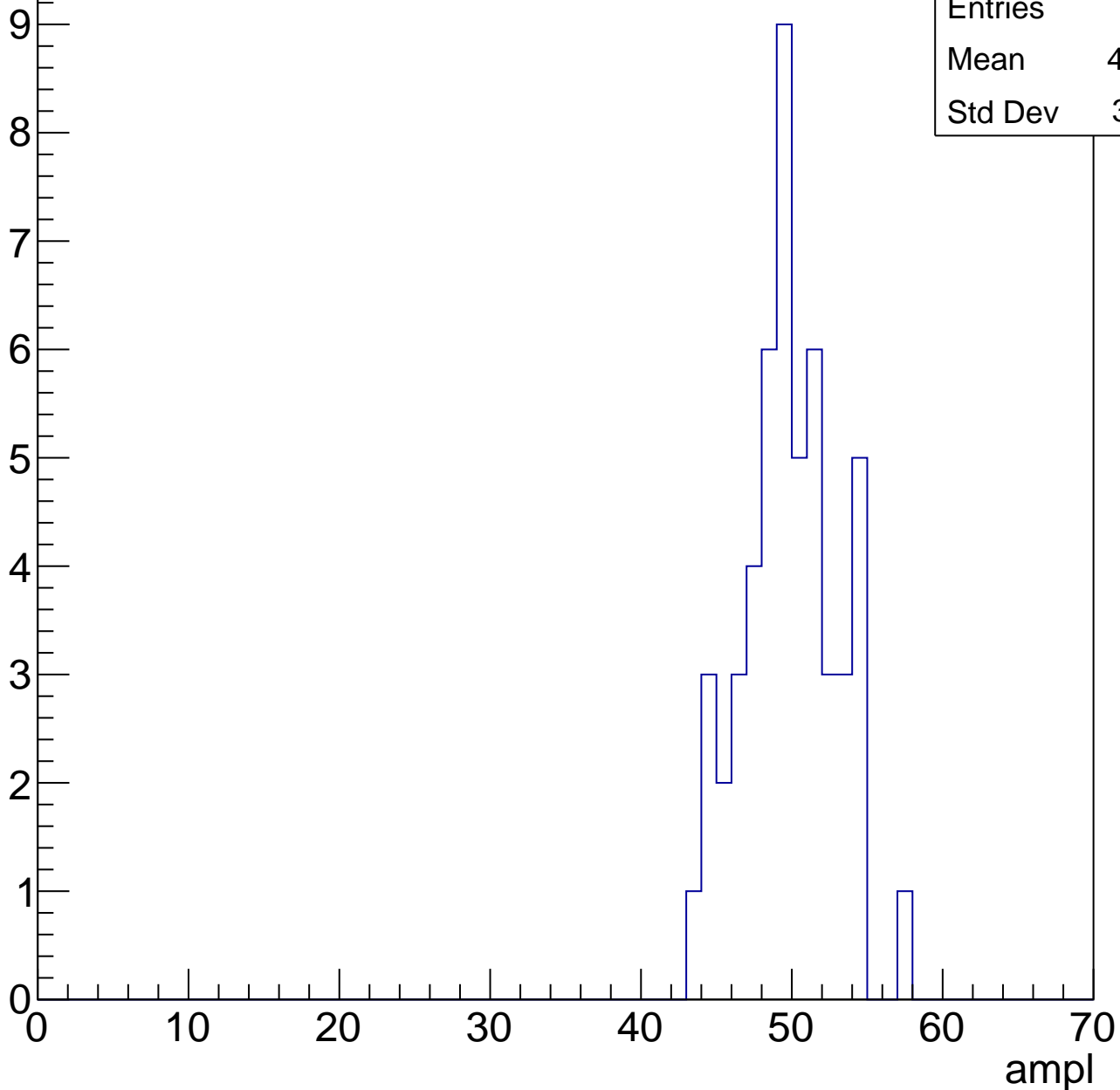


# B1L002S, U26-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	49.37
Std Dev	3.061

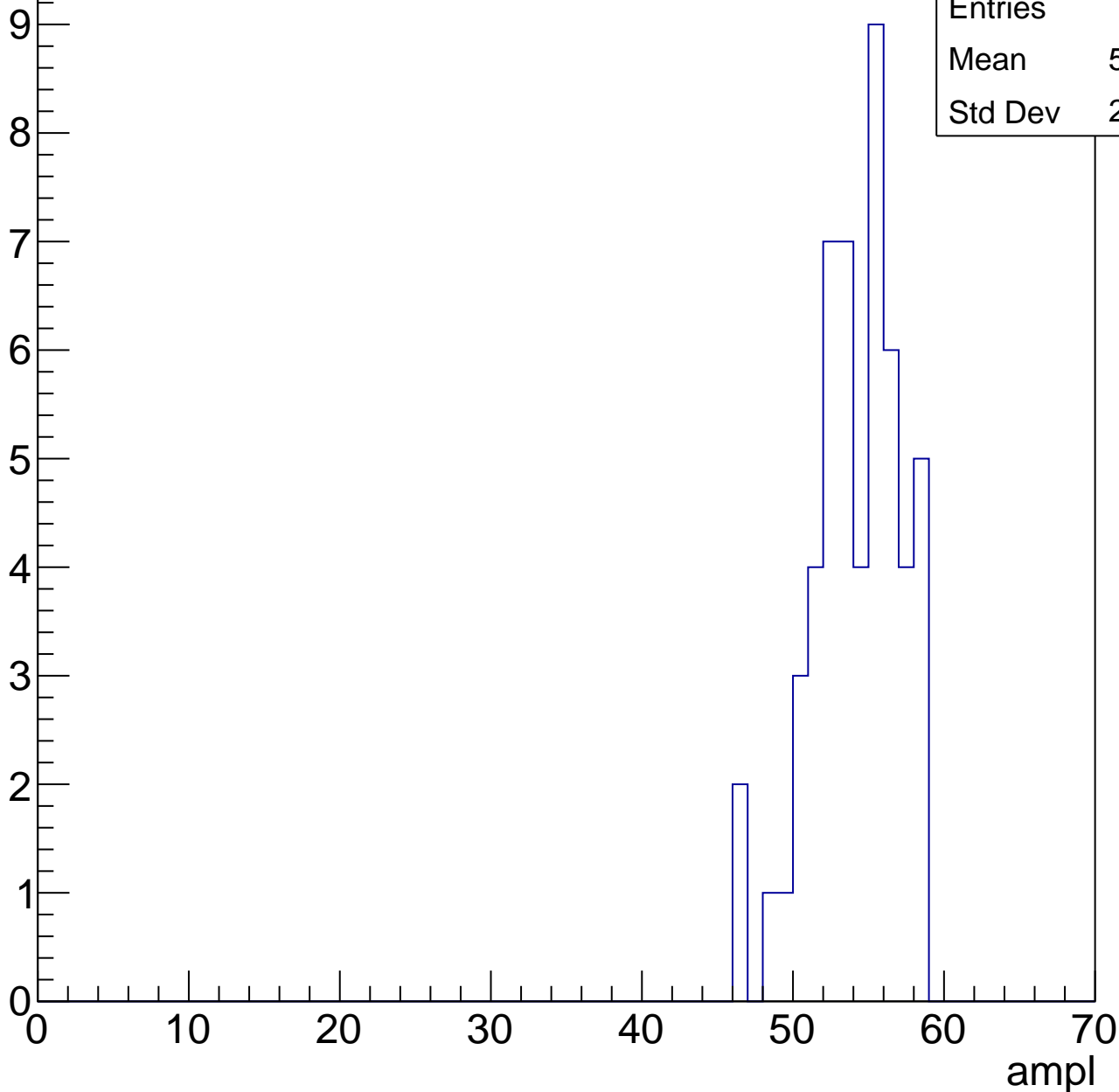


# B1L002S, U26-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	53.64
Std Dev	2.914

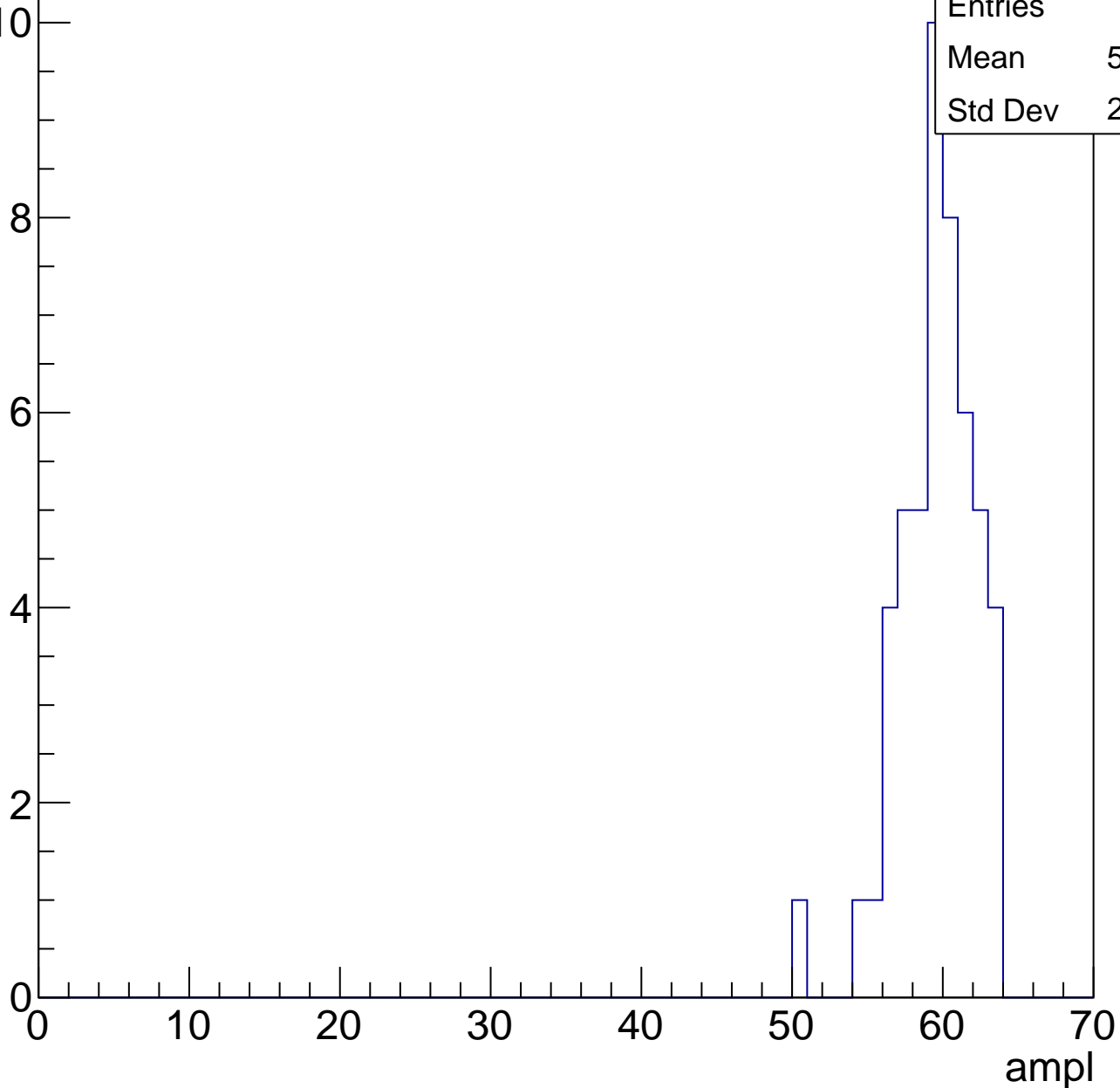


# B1L002S, U26-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	59.12
Std Dev	2.543

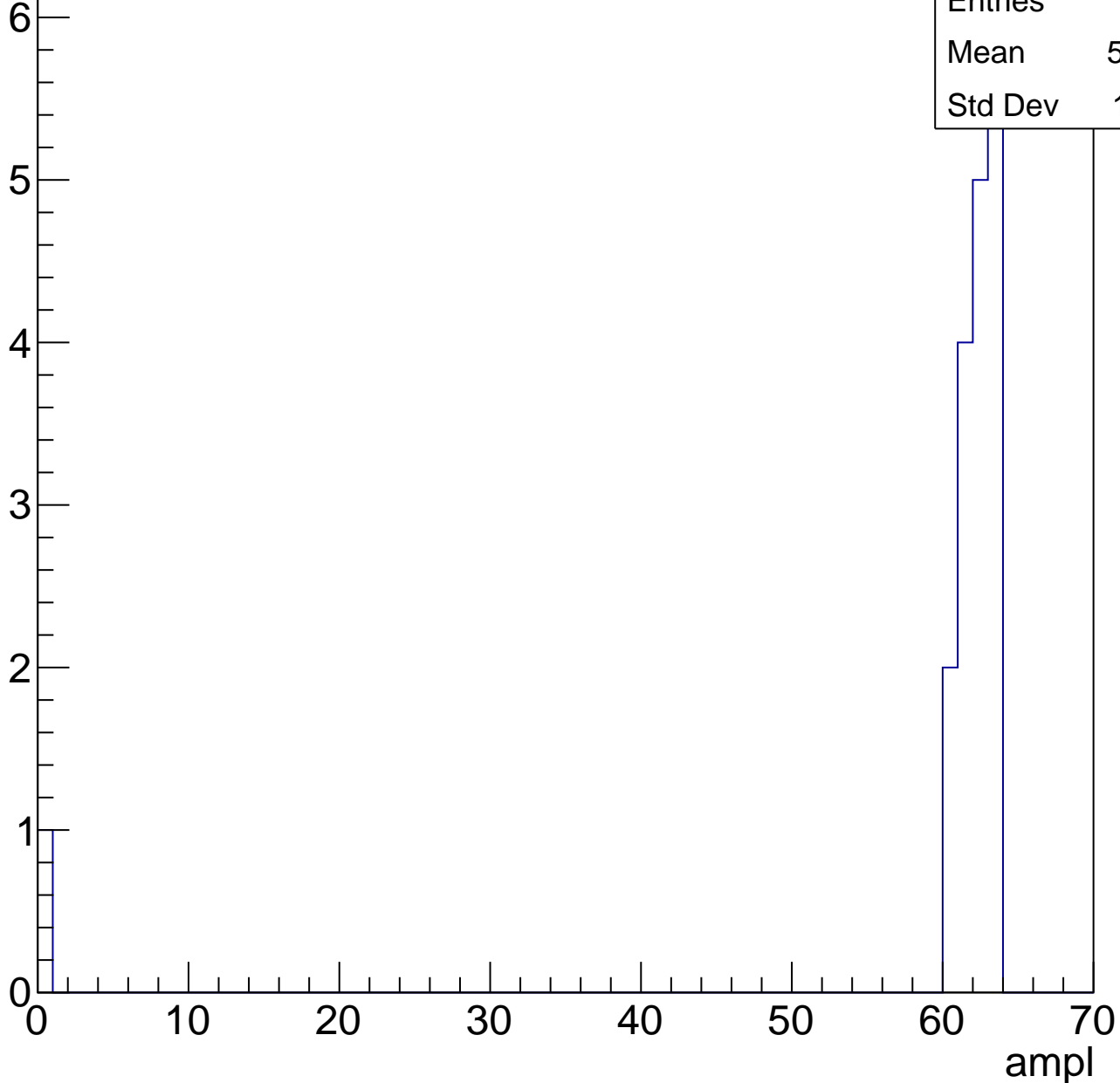


# B1L002S, U26-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.44
Std Dev	14.21





# B1L002S, U26-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch123, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	30.19
Std Dev	2.548

**Gaus mean : 30.3436**

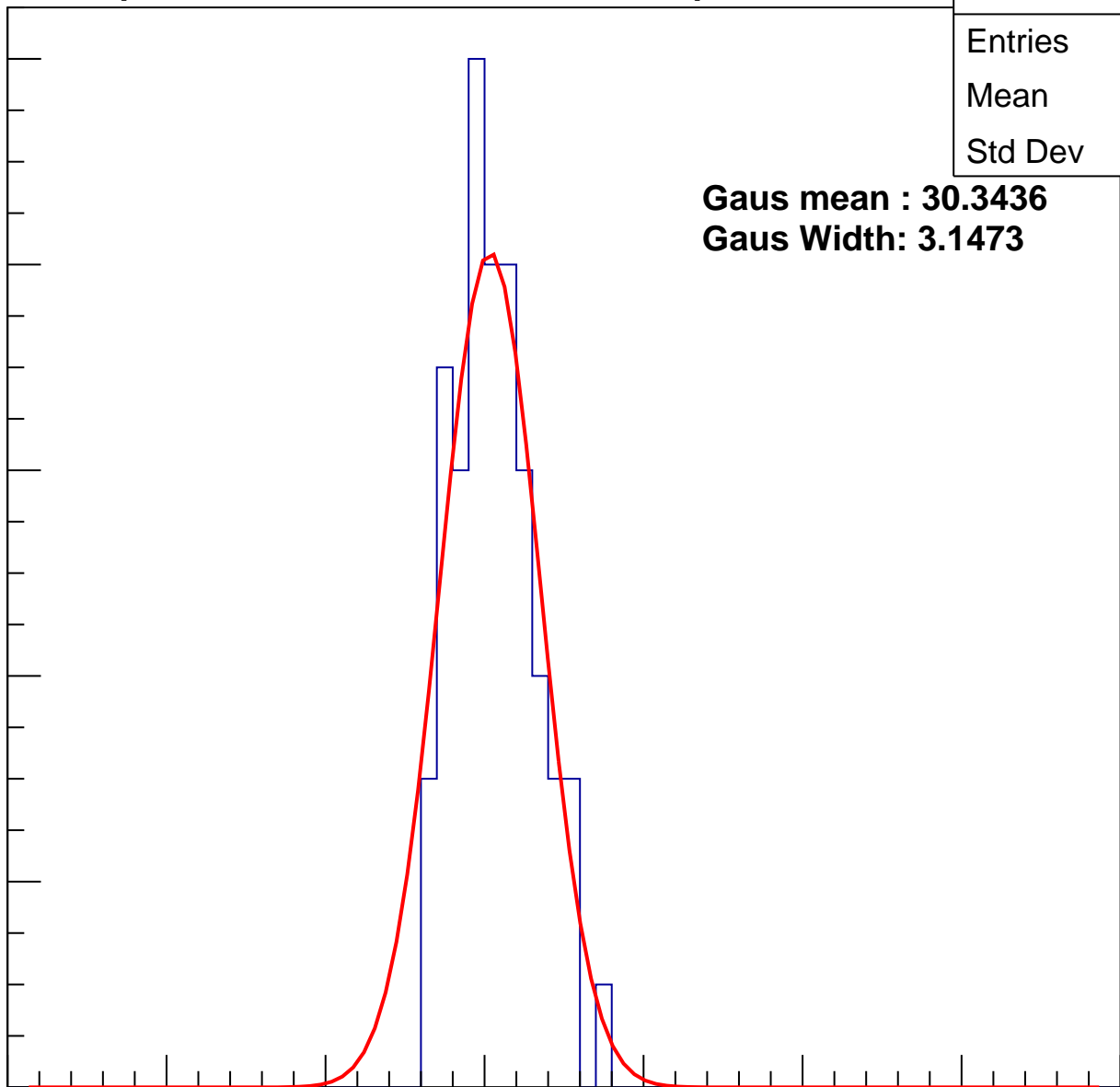
**Gaus Width: 3.1473**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	56
Mean	35.75
Std Dev	3.147

**Gaus mean : 35.9395**

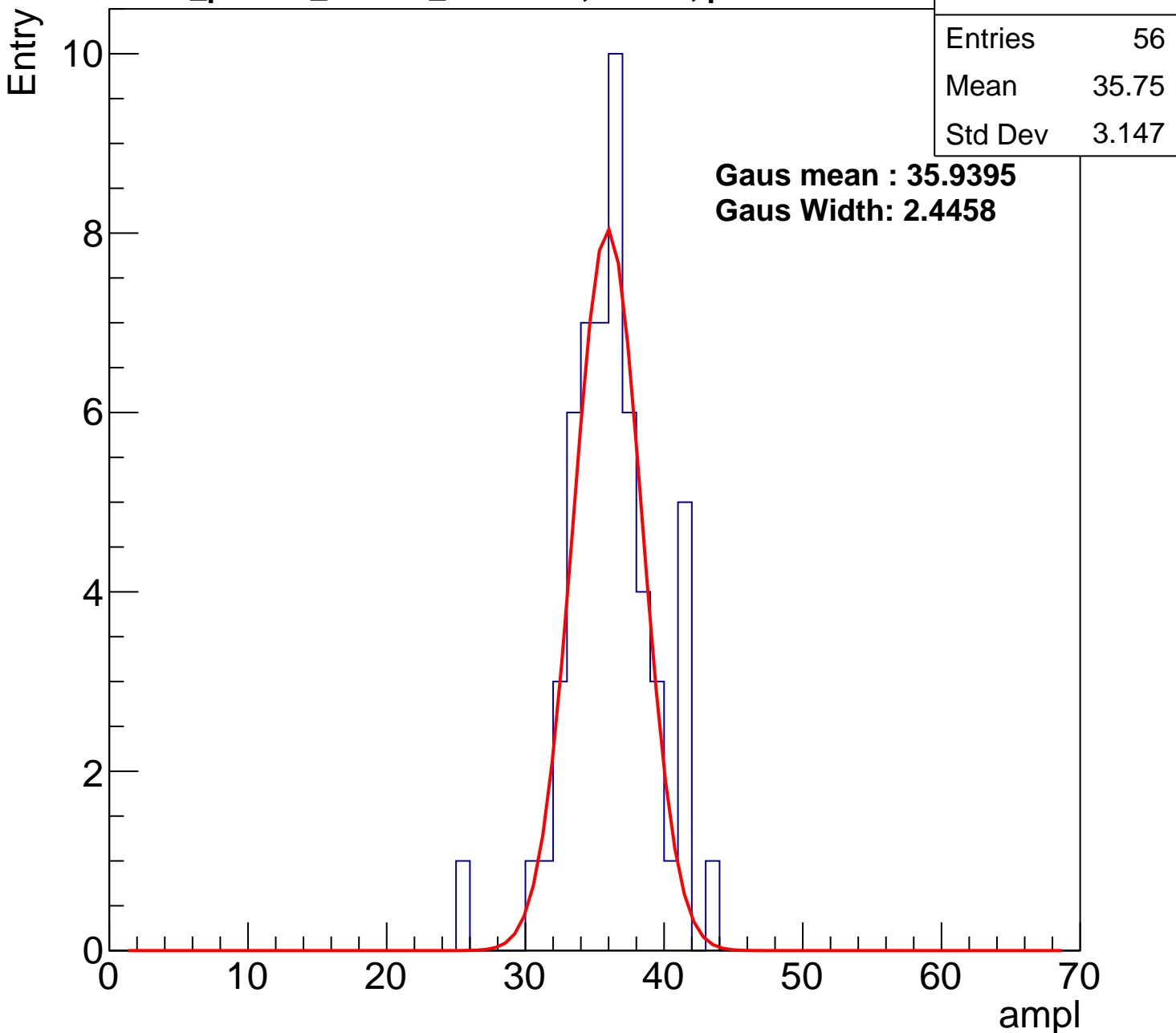
**Gaus Width: 2.4458**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch123, adc2

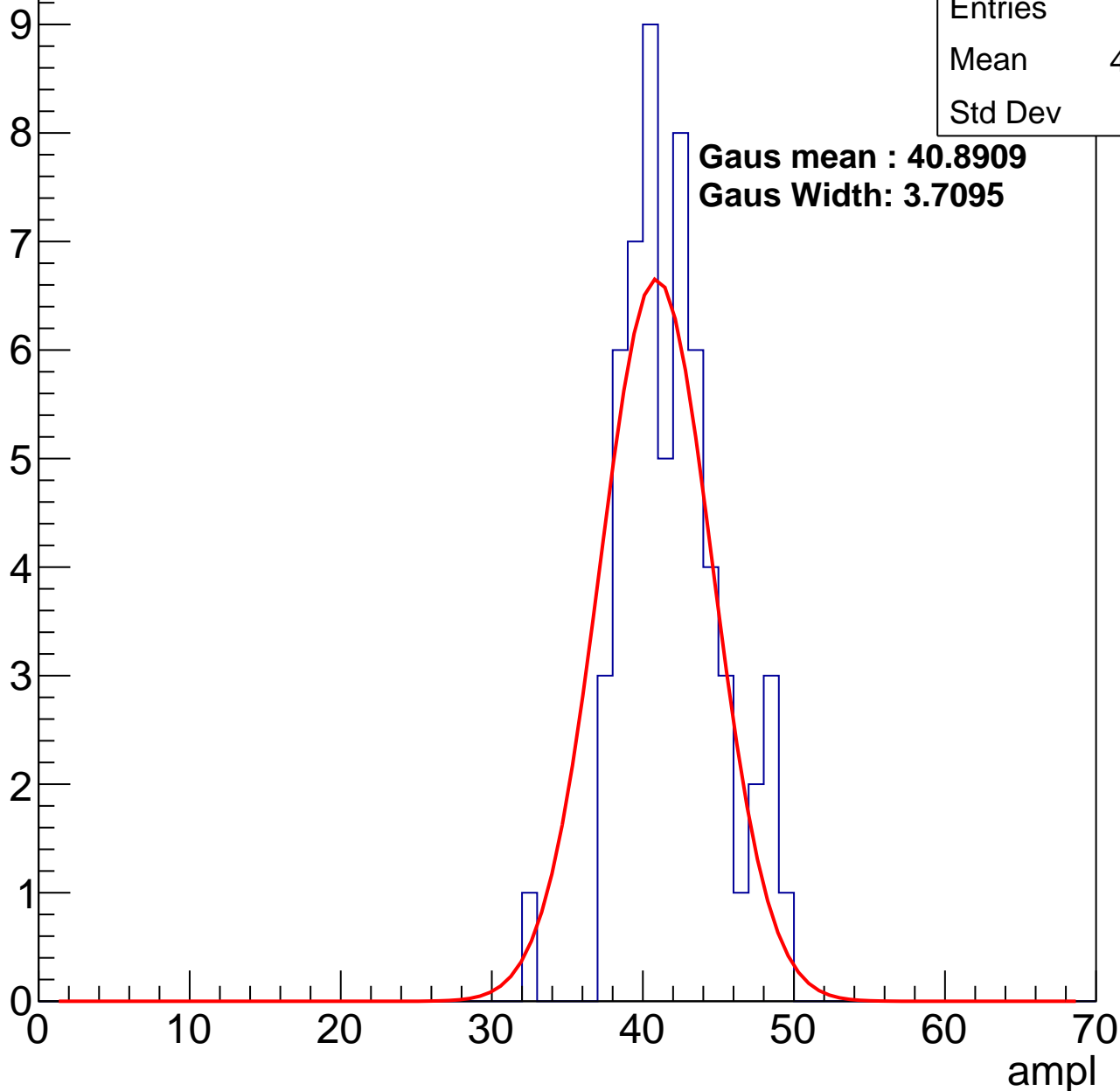
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.47
Std Dev	3.27

**Gaus mean : 40.8909**

**Gaus Width: 3.7095**

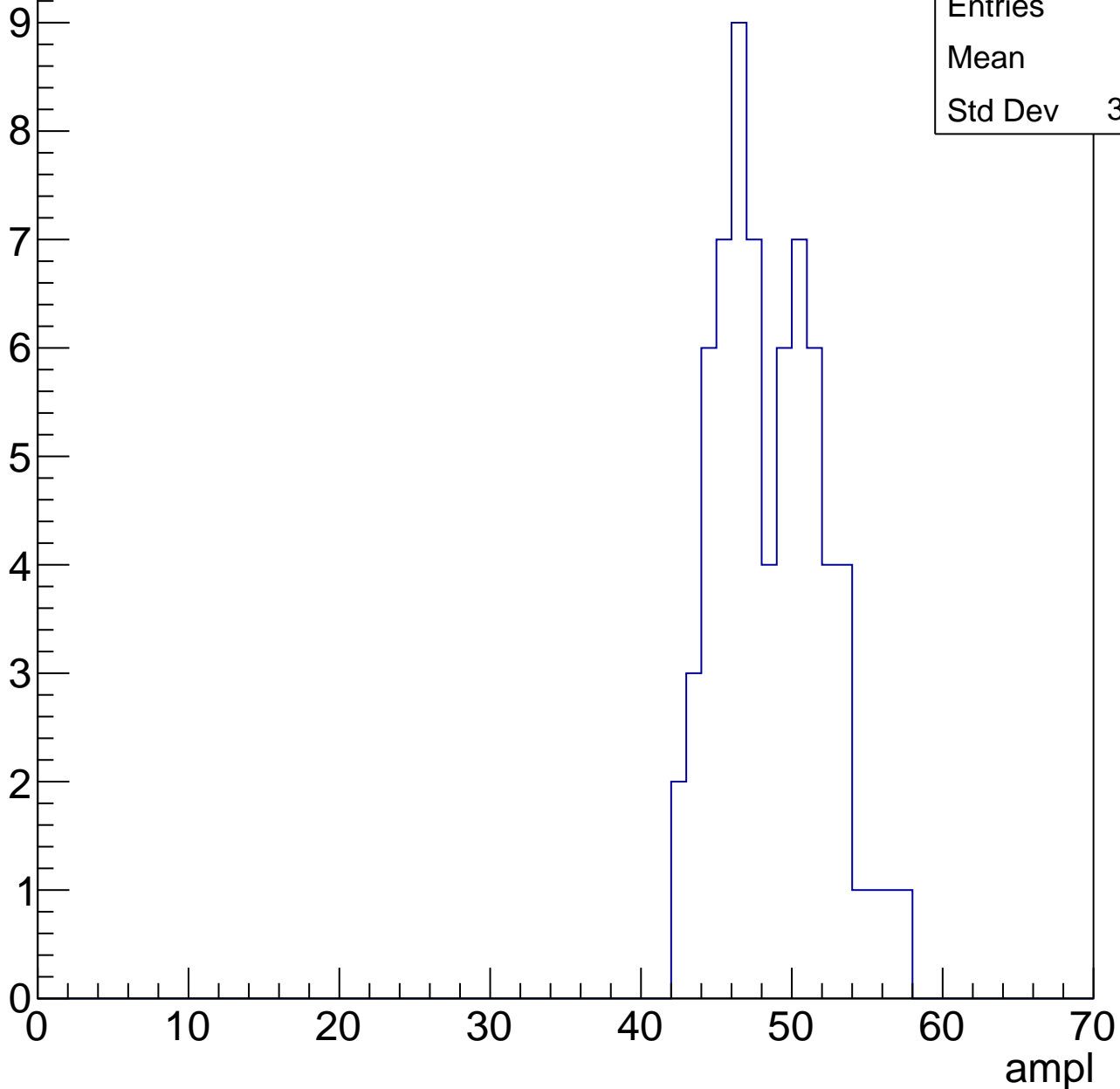


# B1L002S, U26-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	48.1
Std Dev	3.469

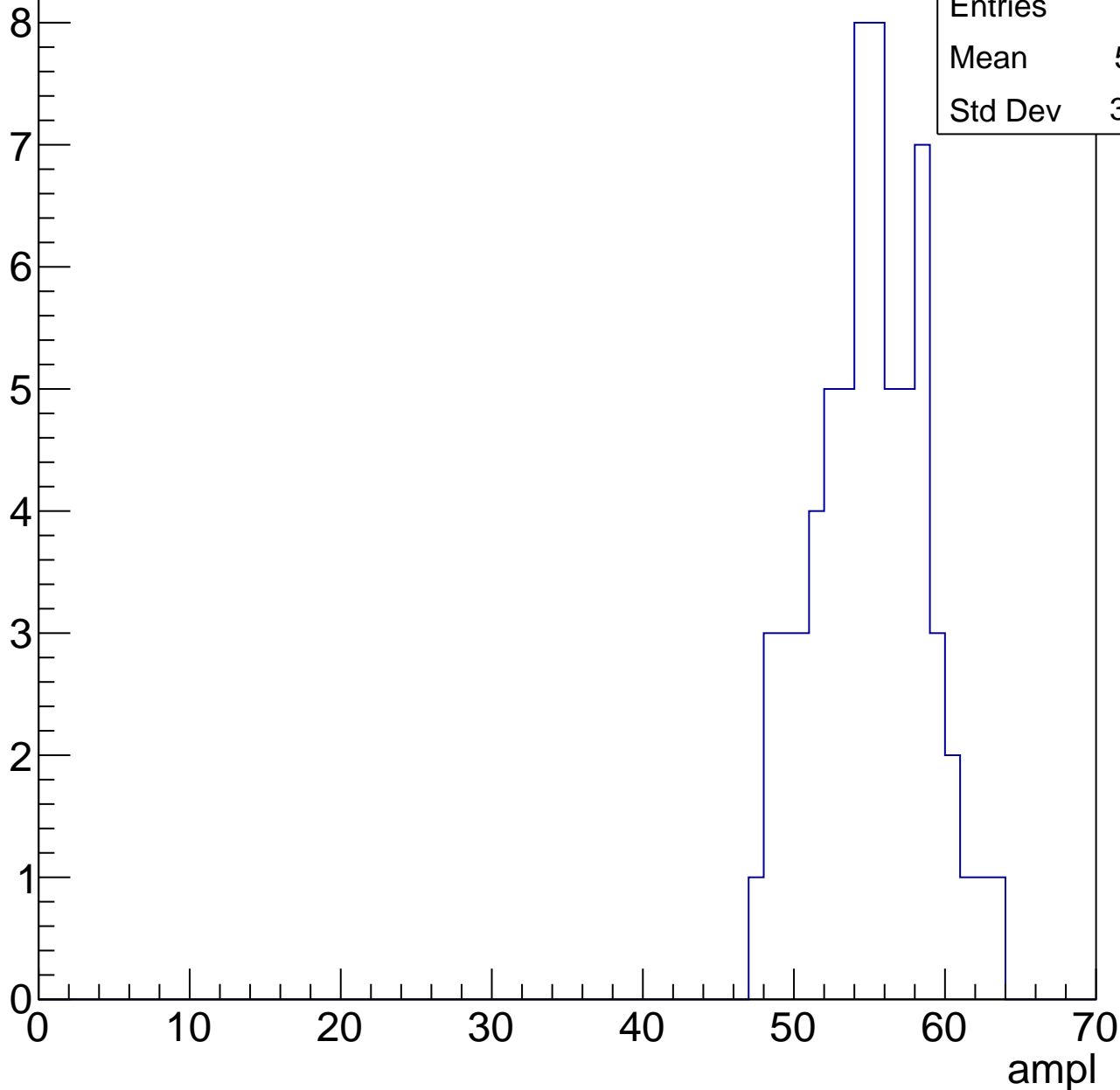


# B1L002S, U26-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	54.51
Std Dev	3.617

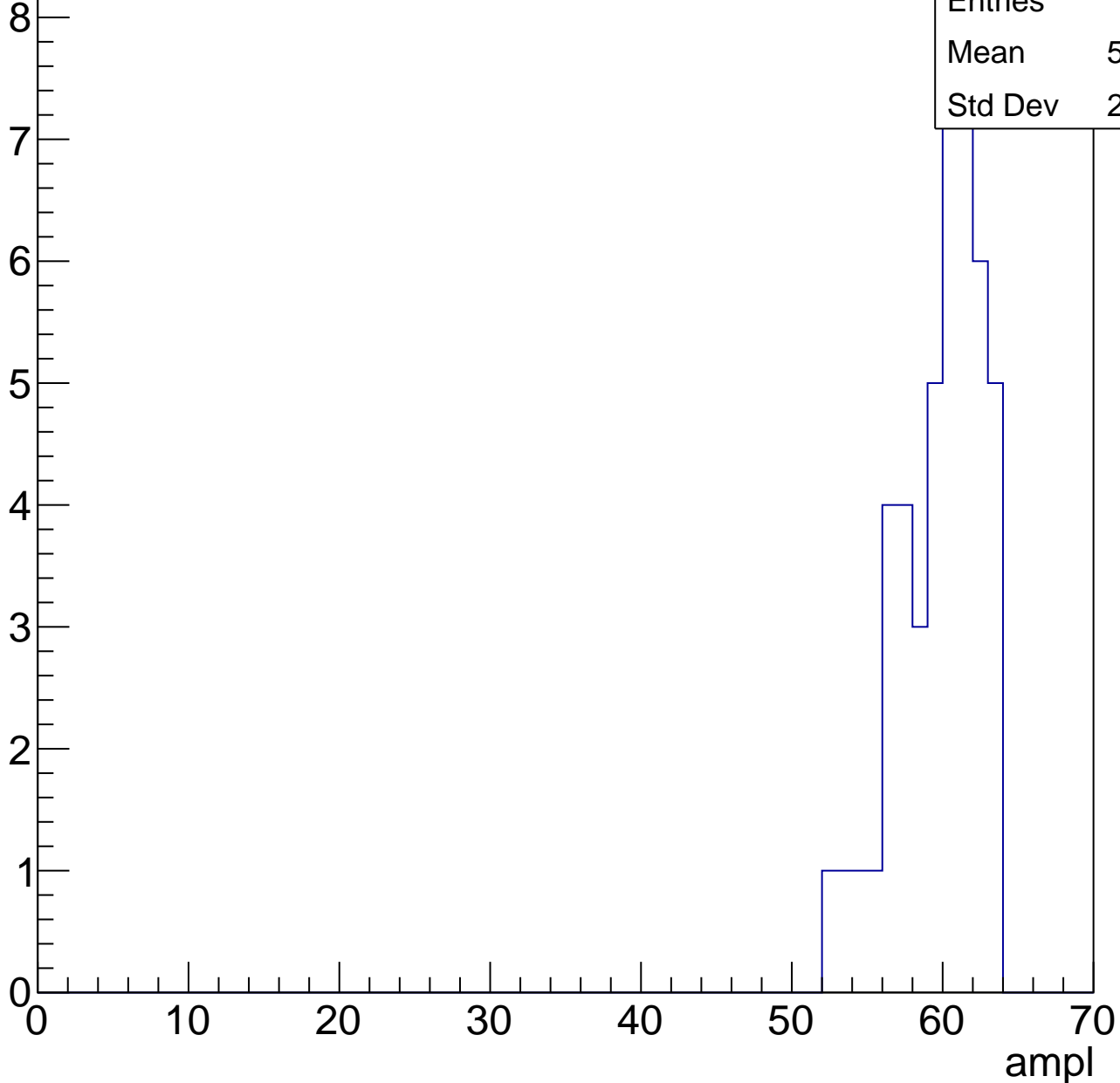


# B1L002S, U26-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

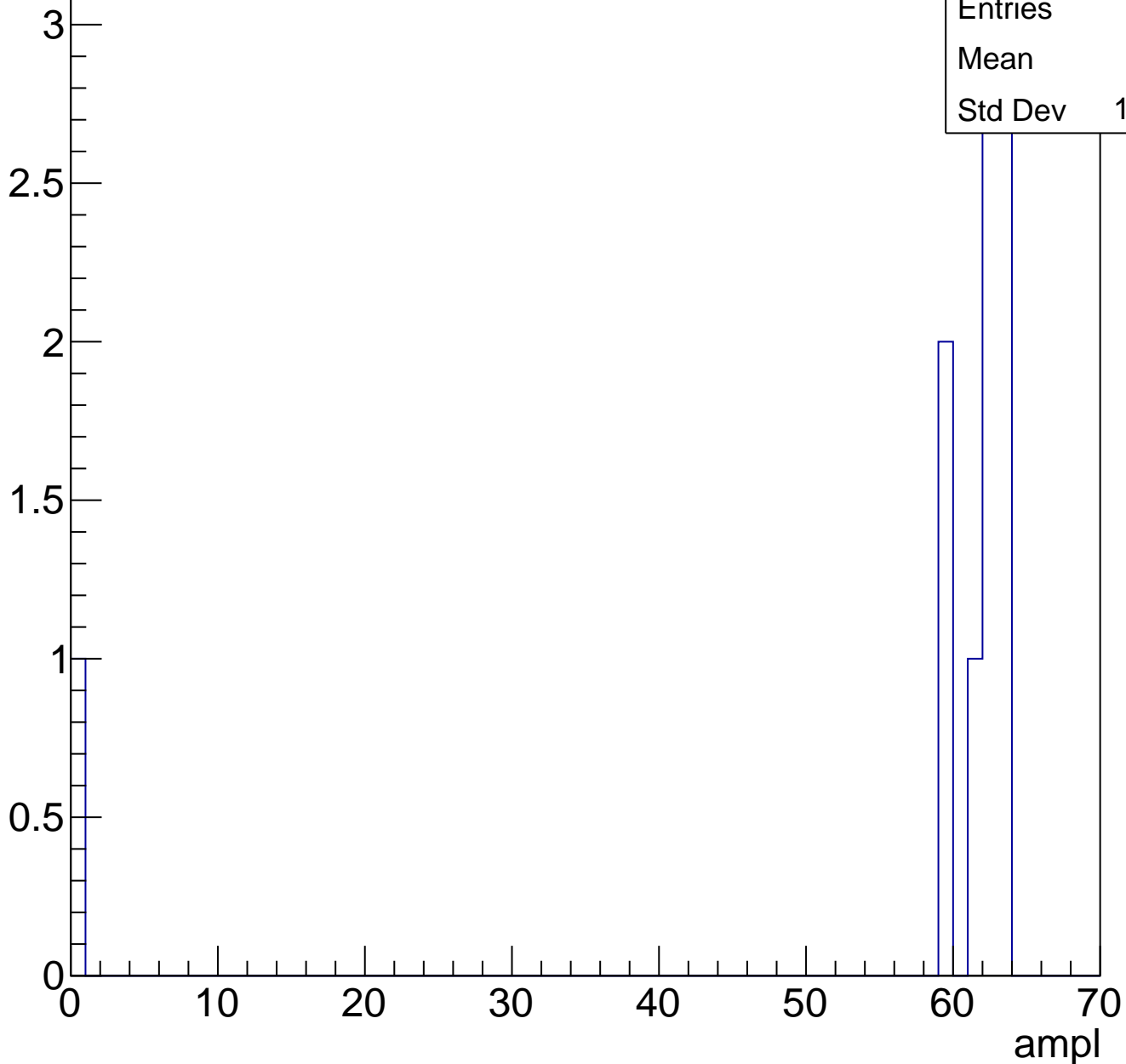
Entries	47
Mean	59.36
Std Dev	2.724



# B1L002S, U26-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U26-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

# B1L002S, U26-ch124, adc0

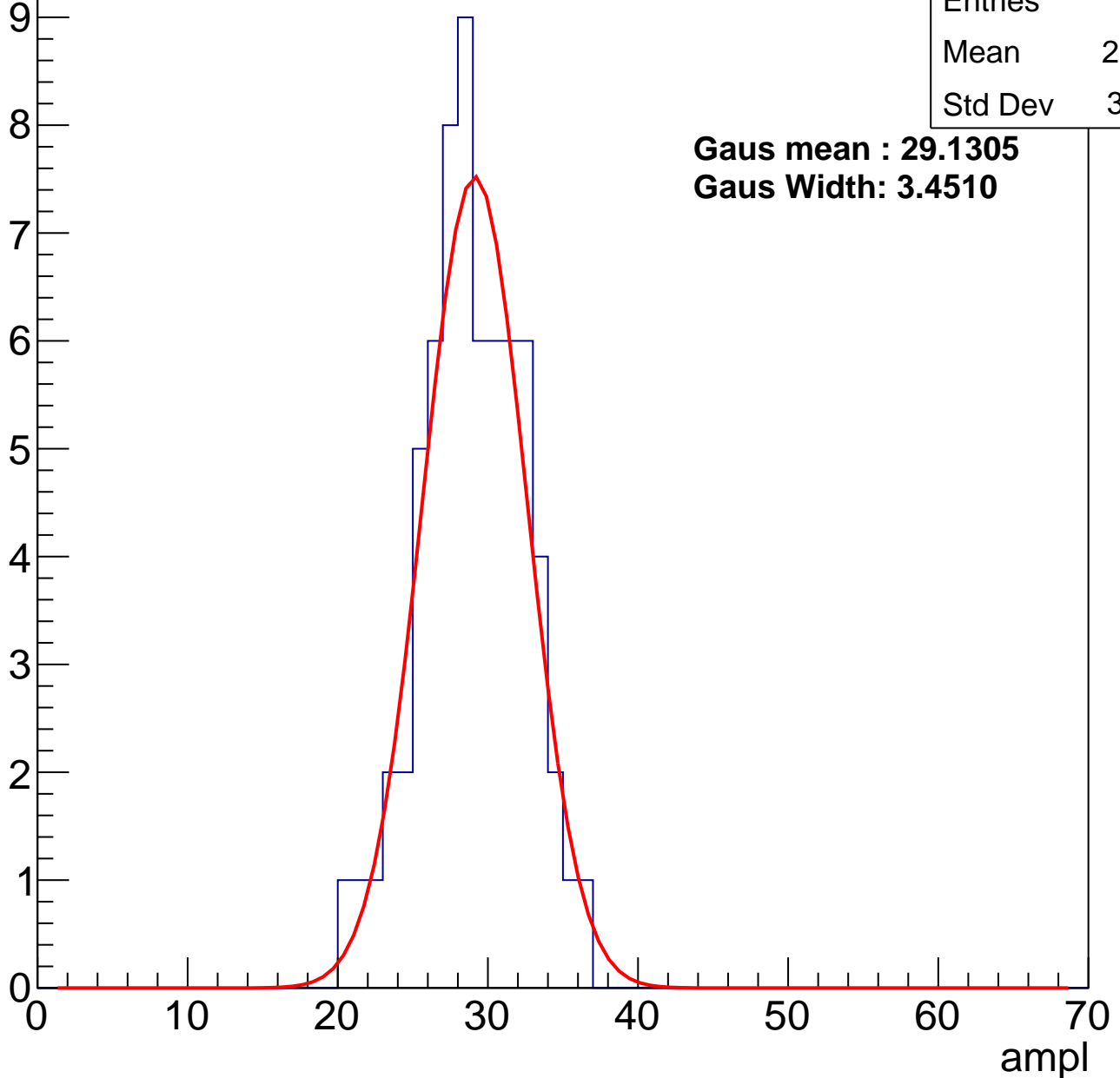
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.49
Std Dev	3.361

**Gaus mean : 29.1305**

**Gaus Width: 3.4510**



# B1L002S, U26-ch124, adc1

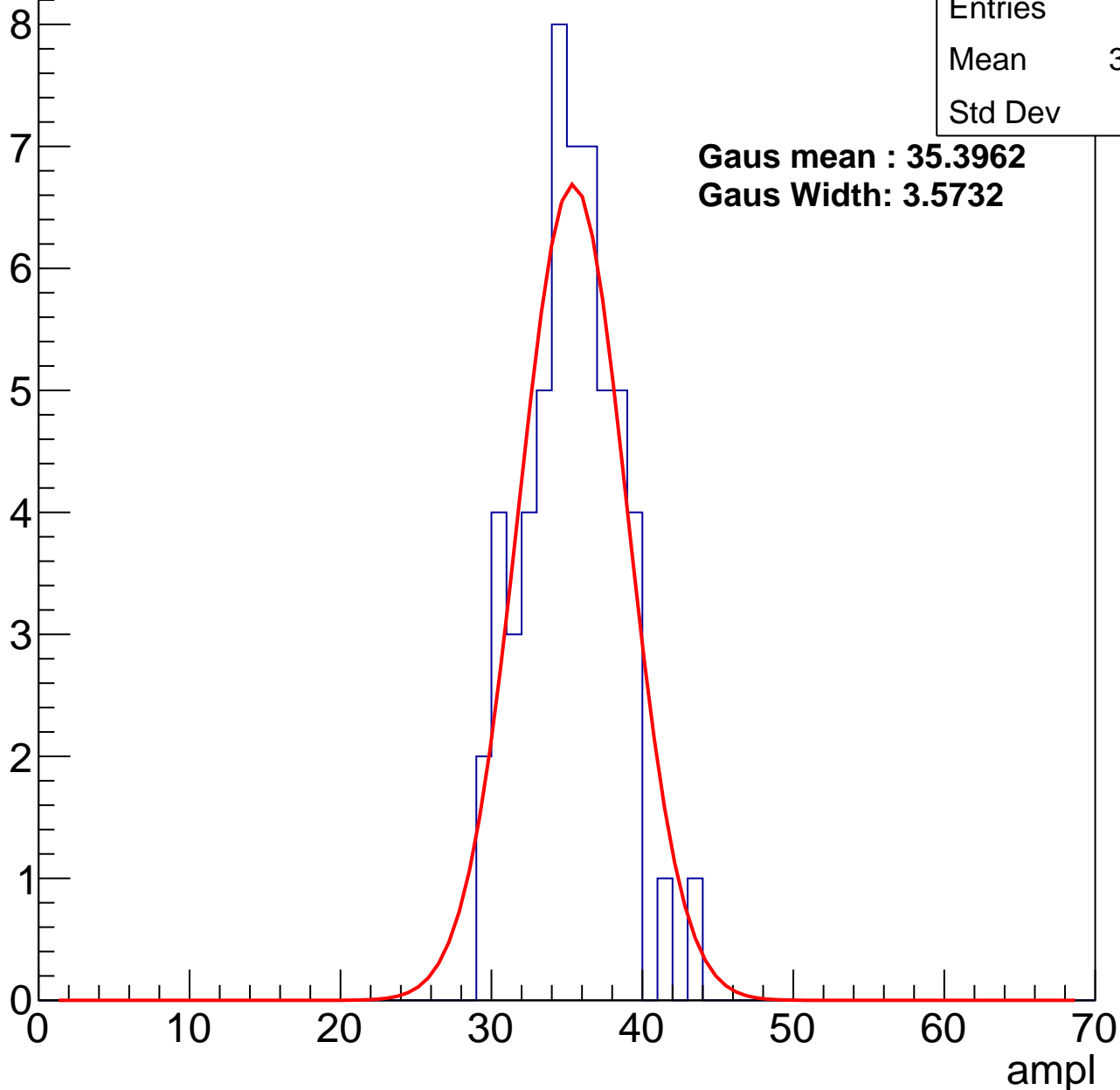
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.79
Std Dev	3.04

**Gaus mean : 35.3962**

**Gaus Width: 3.5732**



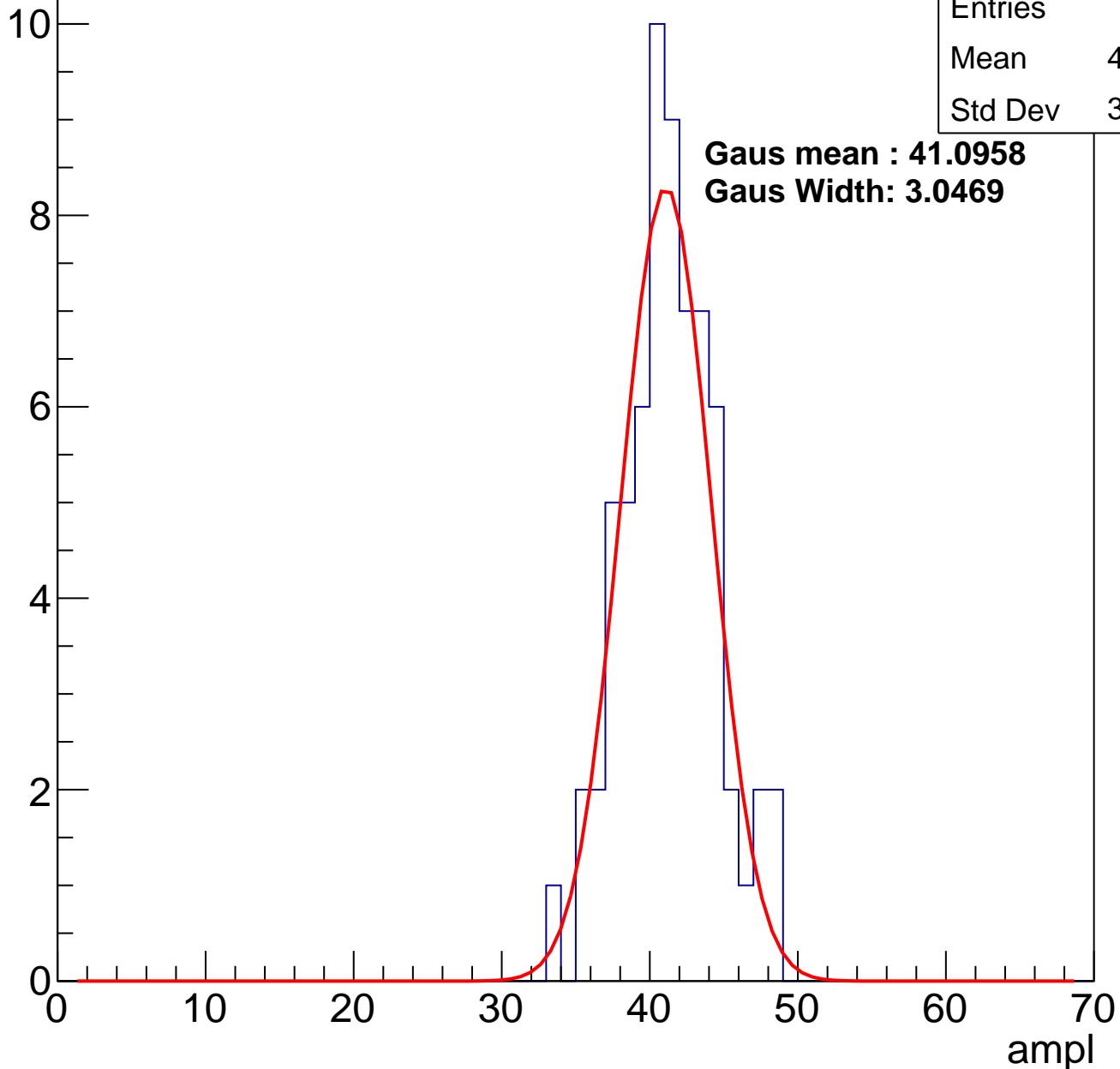
# B1L002S, U26-ch124, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	40.87
Std Dev	3.138

**Gaus mean : 41.0958**  
**Gaus Width: 3.0469**

Entry

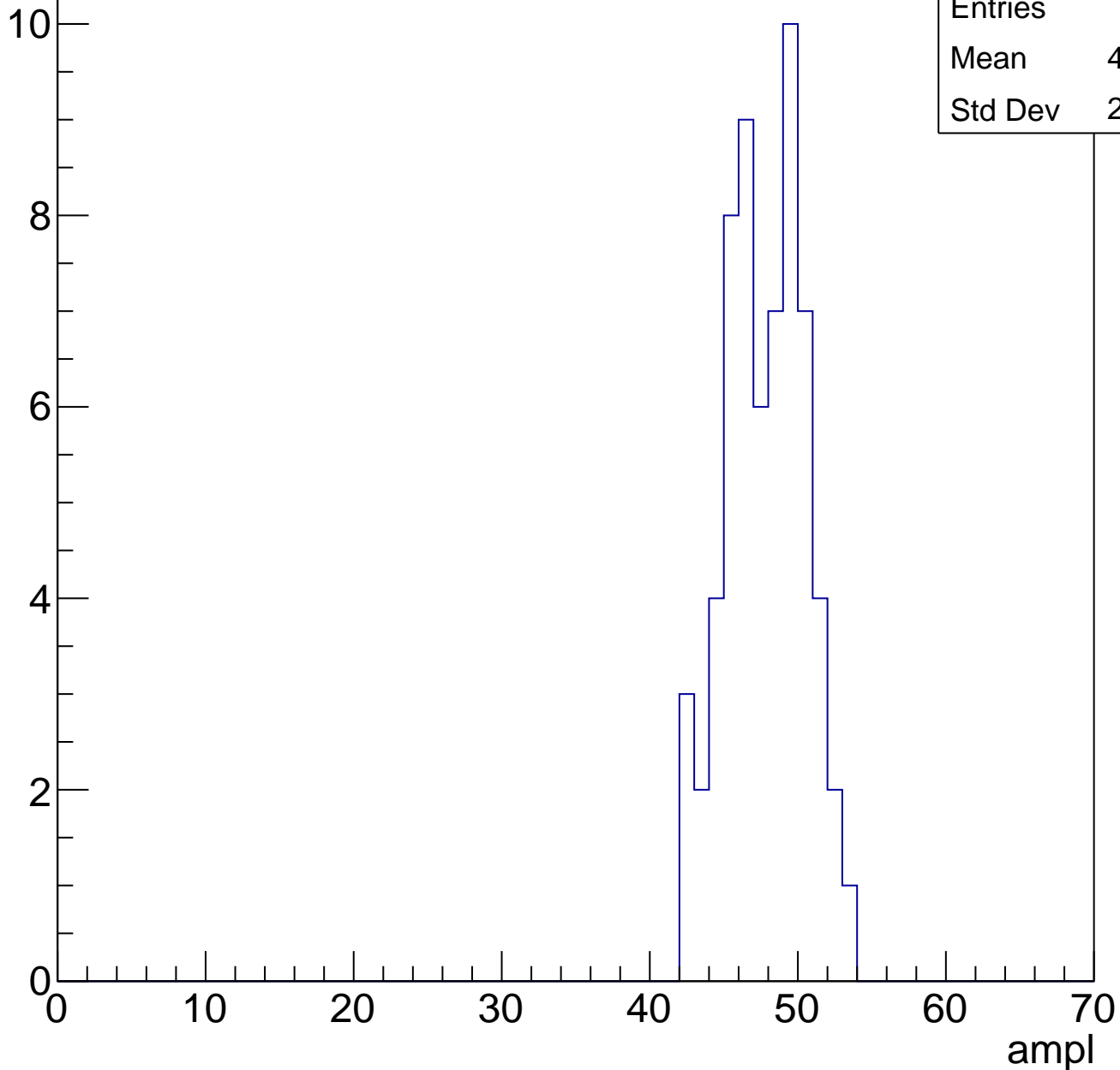


# B1L002S, U26-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	47.32
Std Dev	2.636

Entry

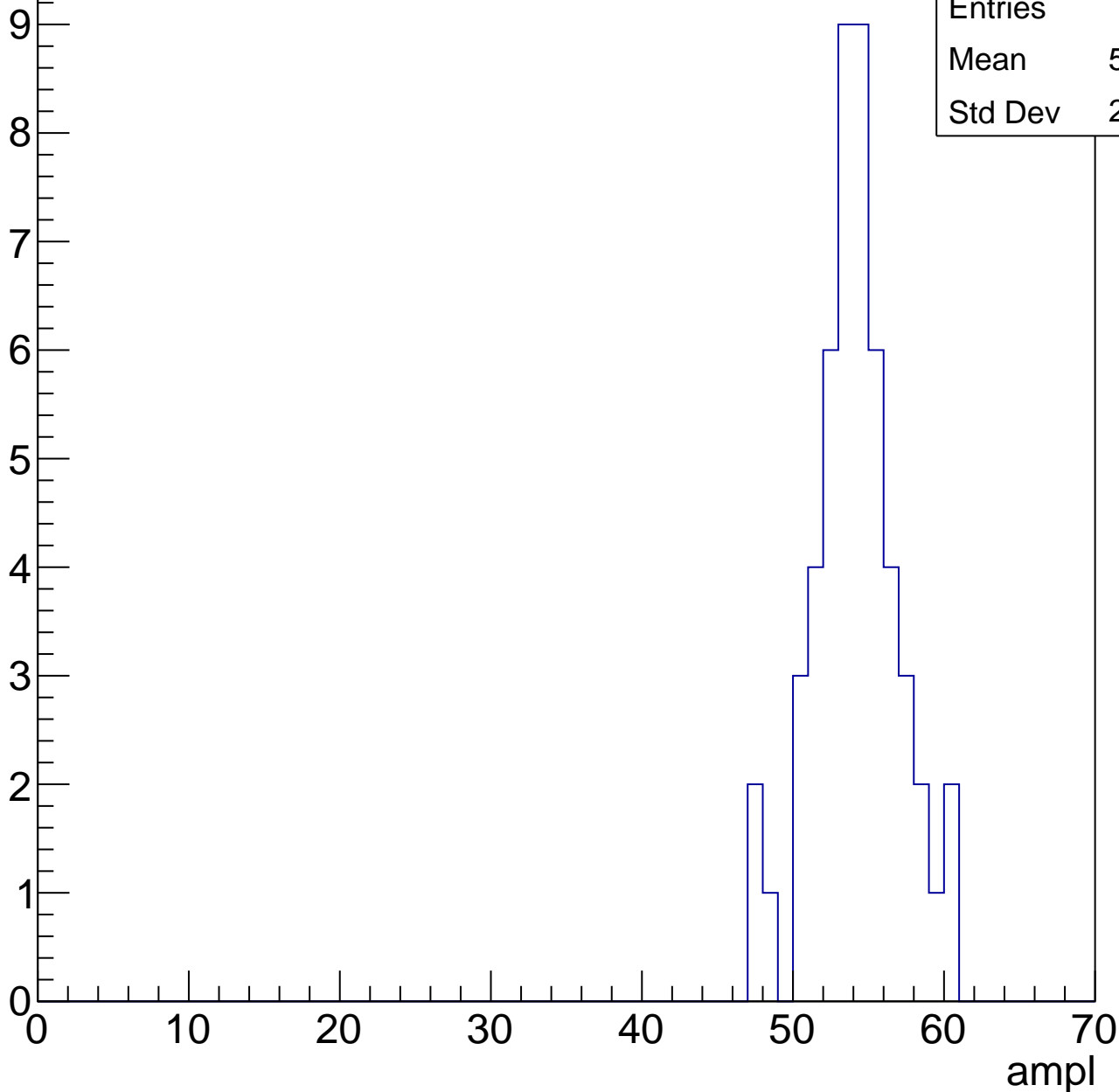


# B1L002S, U26-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.67
Std Dev	2.854

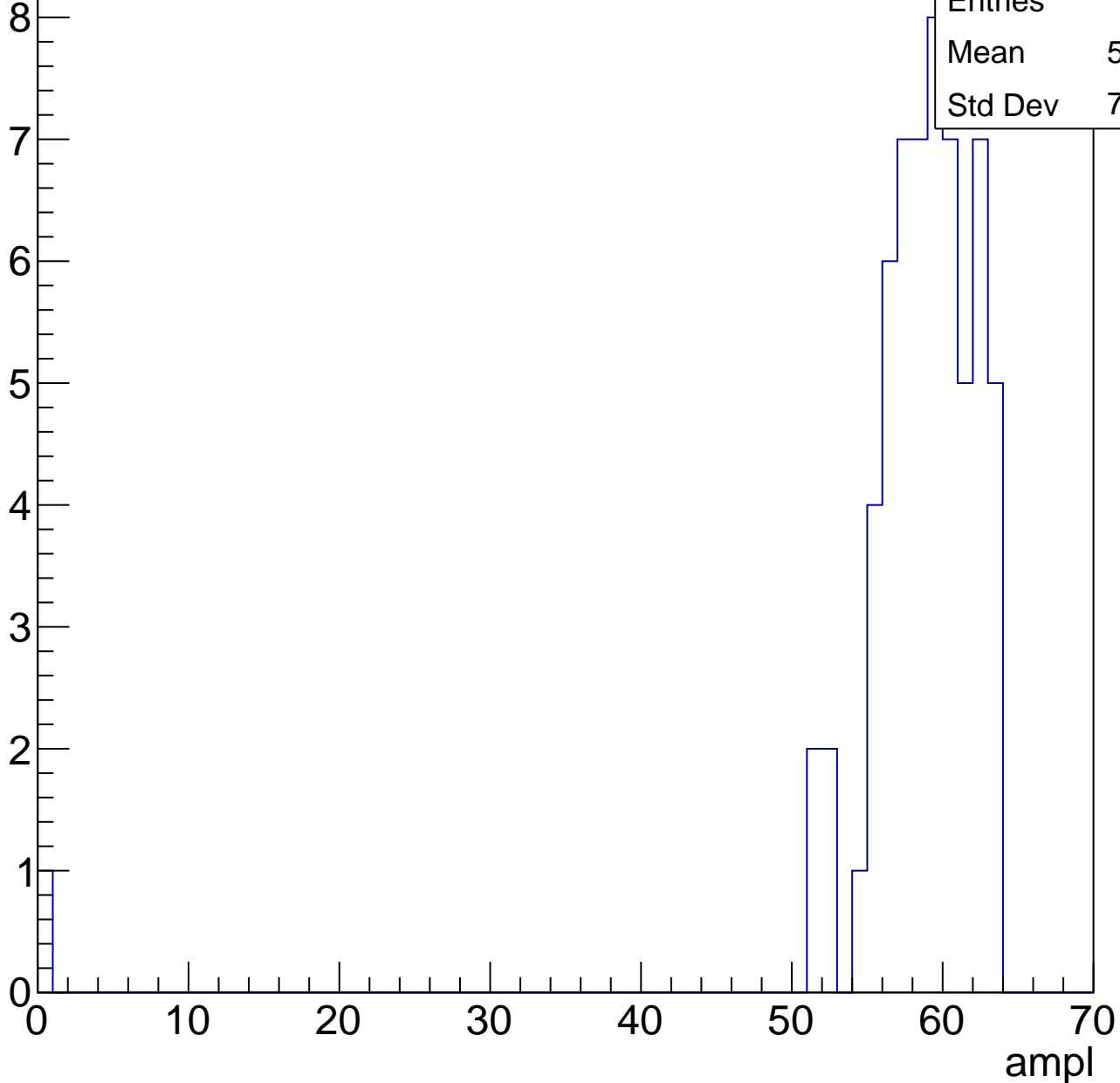


# B1L002S, U26-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	57.53
Std Dev	7.953

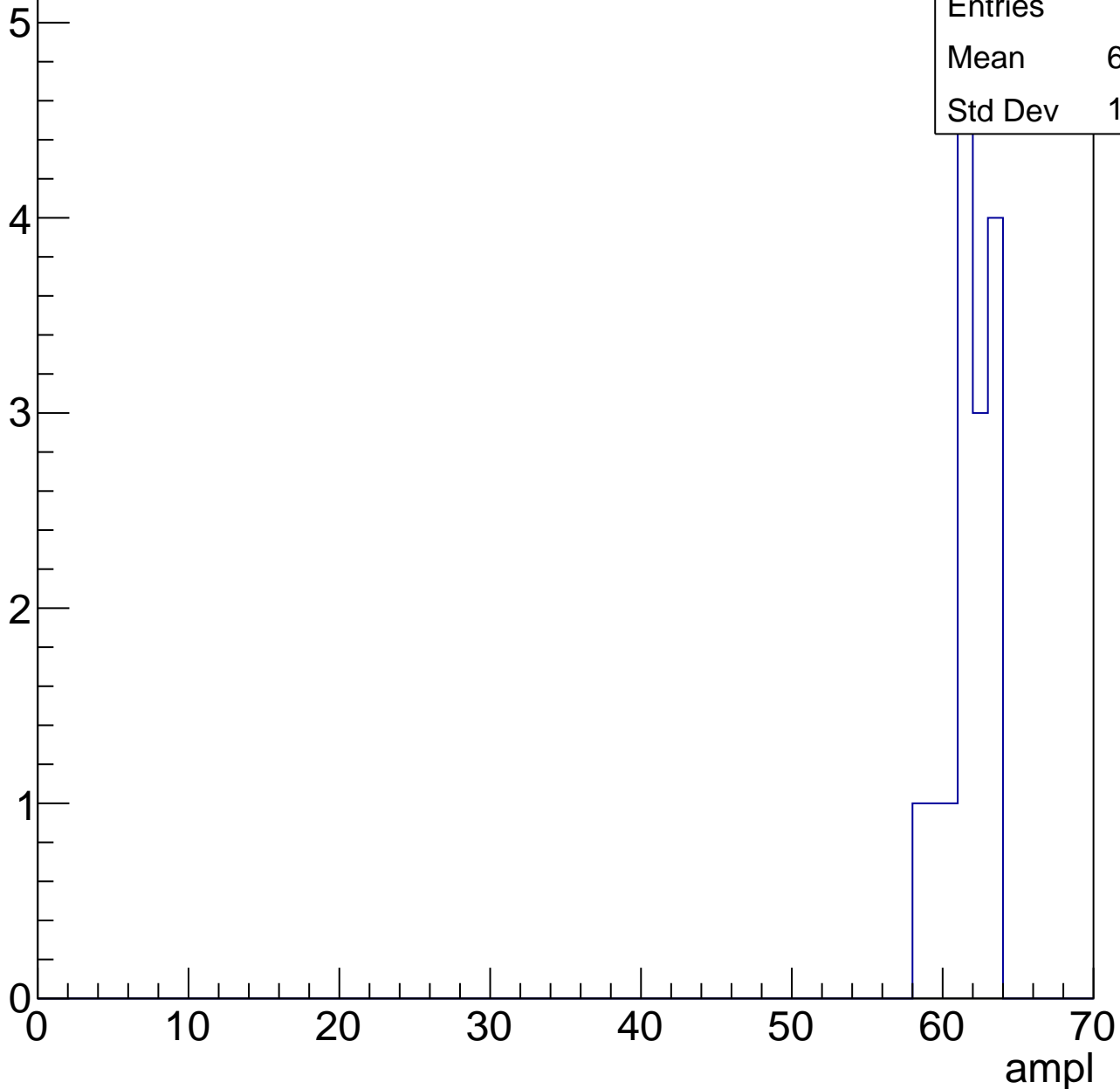


# B1L002S, U26-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	61.33
Std Dev	1.445





# B1L002S, U26-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U26-ch125, adc0

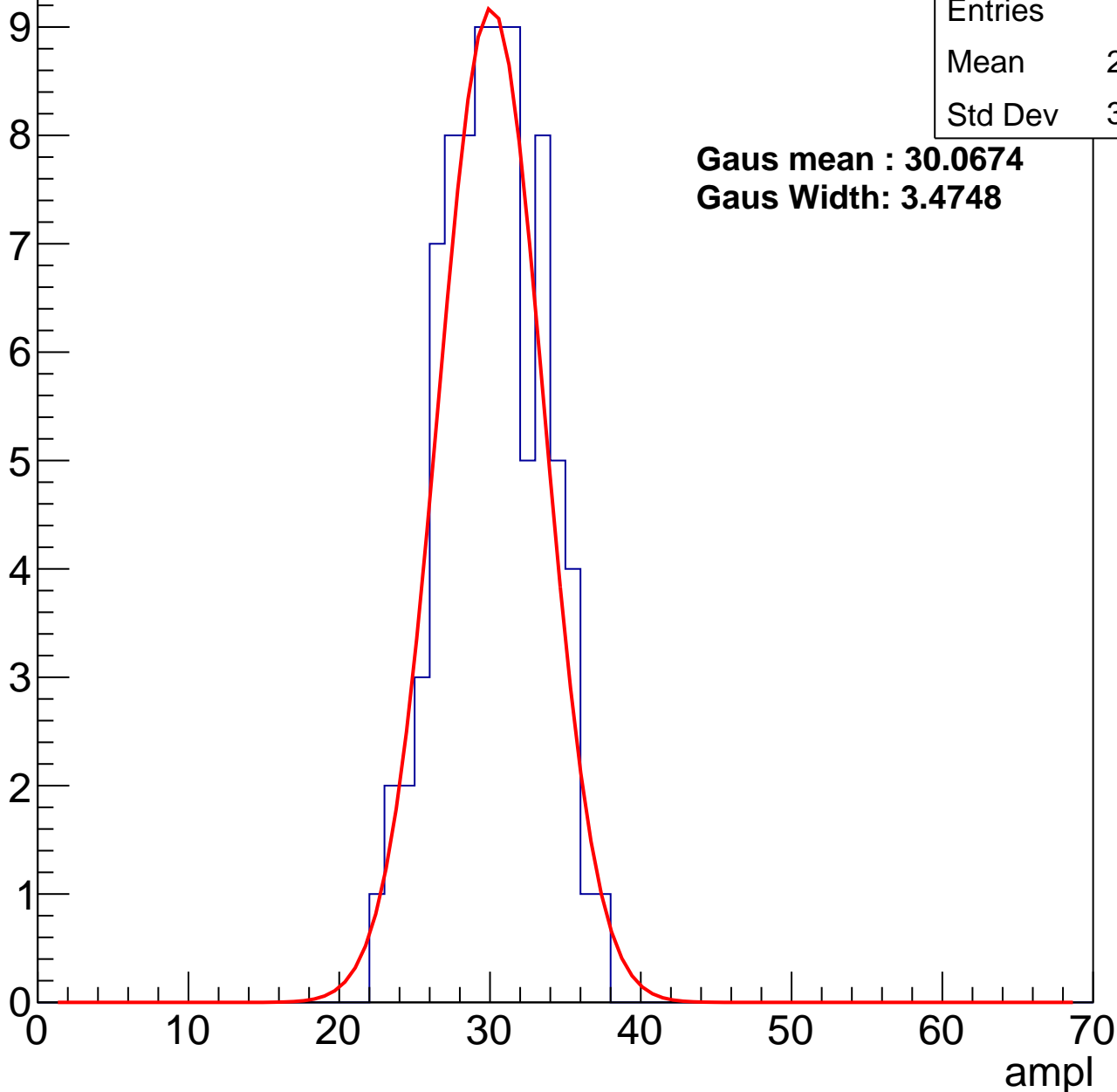
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	29.63
Std Dev	3.293

**Gaus mean : 30.0674**

**Gaus Width: 3.4748**



# B1L002S, U26-ch125, adc1

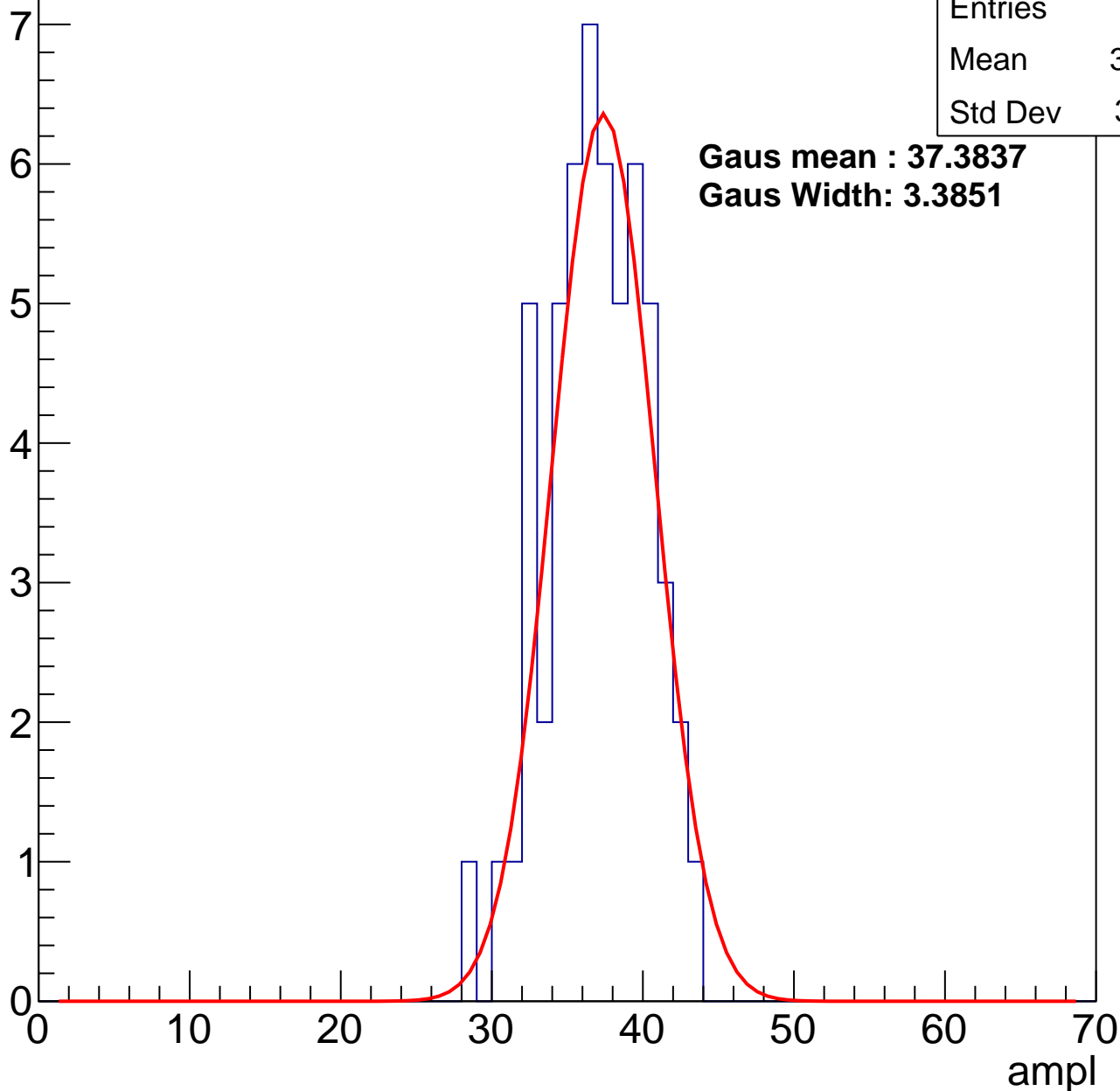
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	36.48
Std Dev	3.251

**Gaus mean : 37.3837**

**Gaus Width: 3.3851**



# B1L002S, U26-ch125, adc2

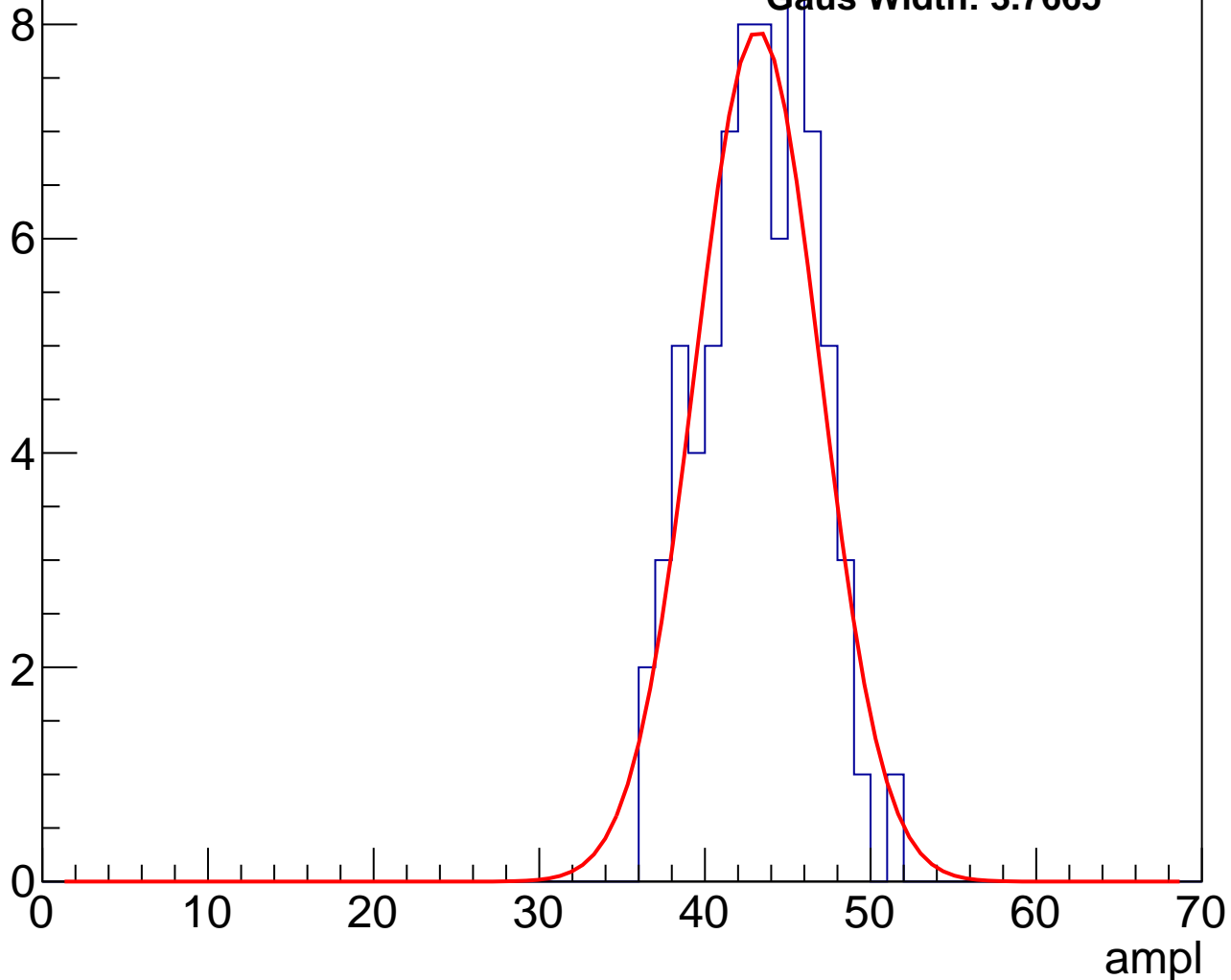
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	42.81
Std Dev	3.341

**Gaus mean : 43.1847**

**Gaus Width: 3.7665**

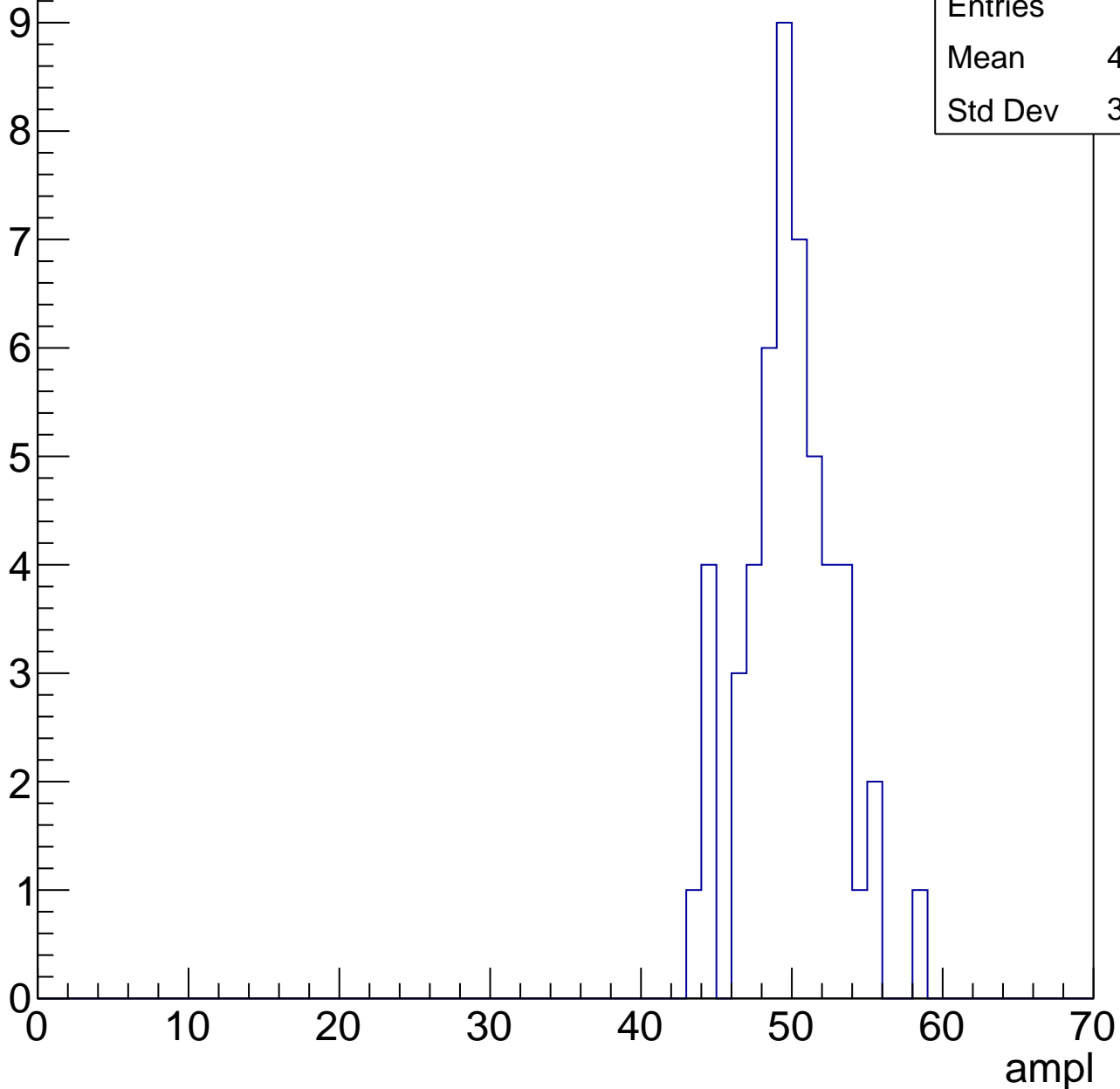


# B1L002S, U26-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	49.43
Std Dev	3.069

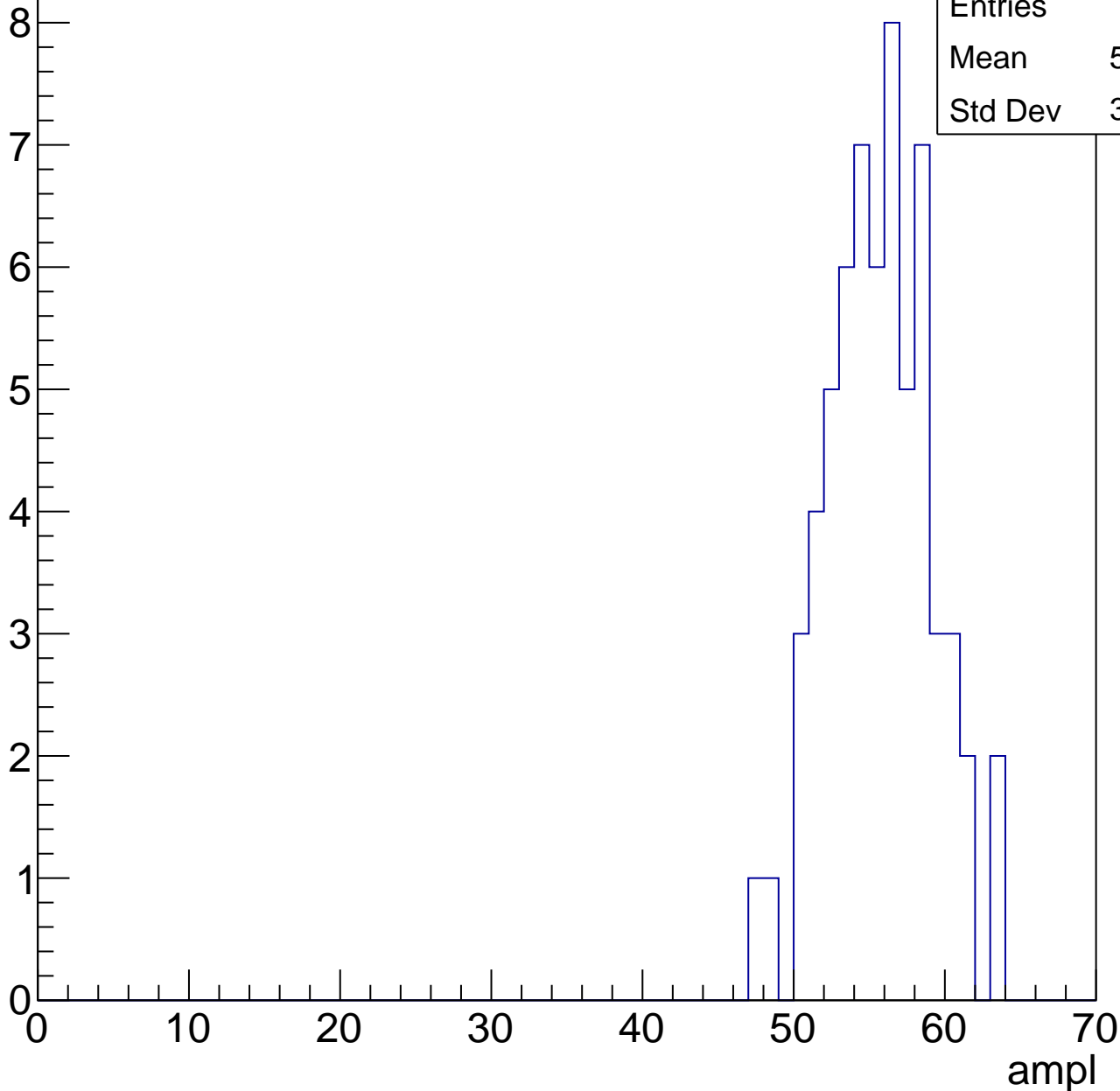


# B1L002S, U26-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	55.22
Std Dev	3.425

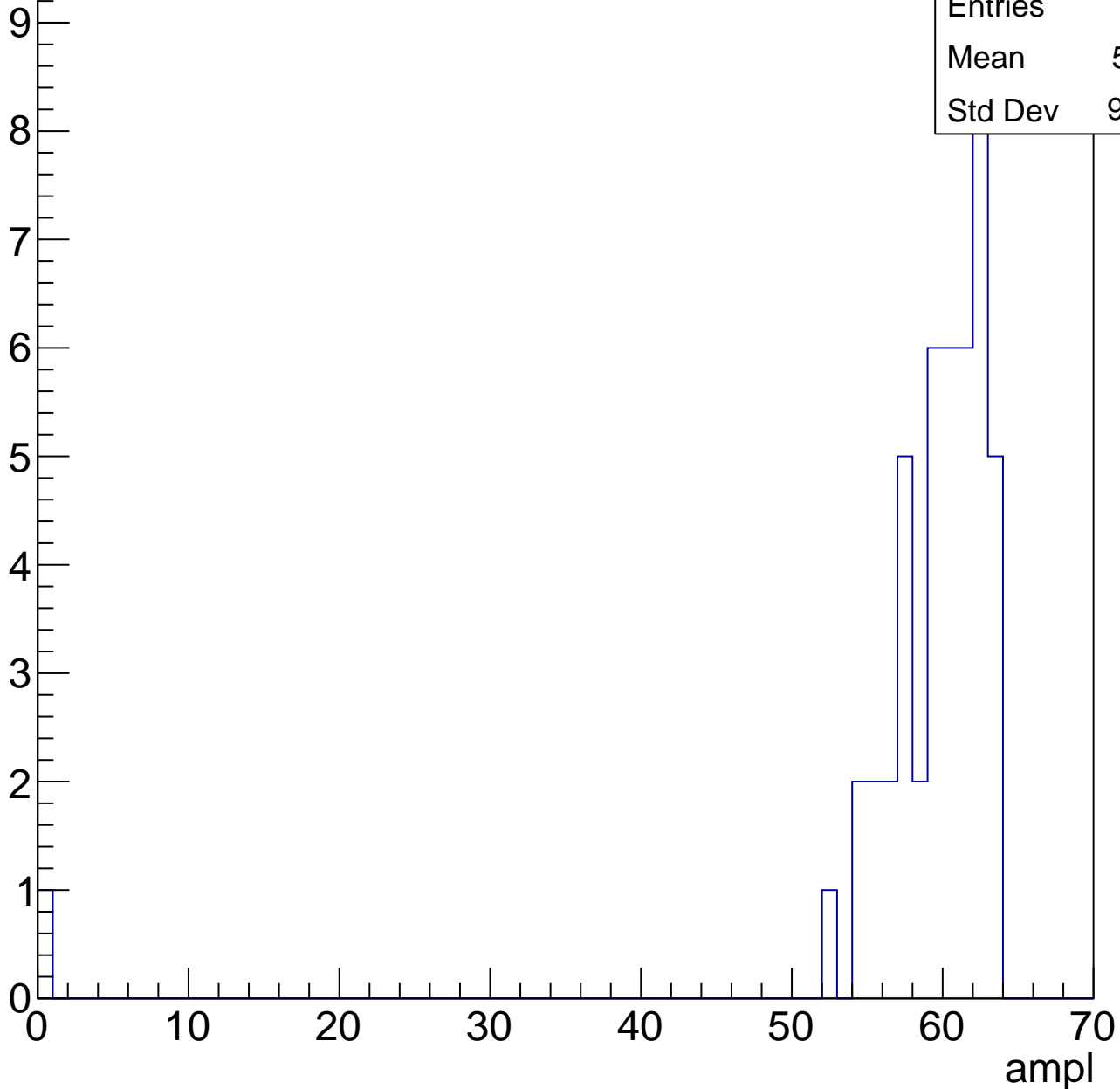


# B1L002S, U26-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

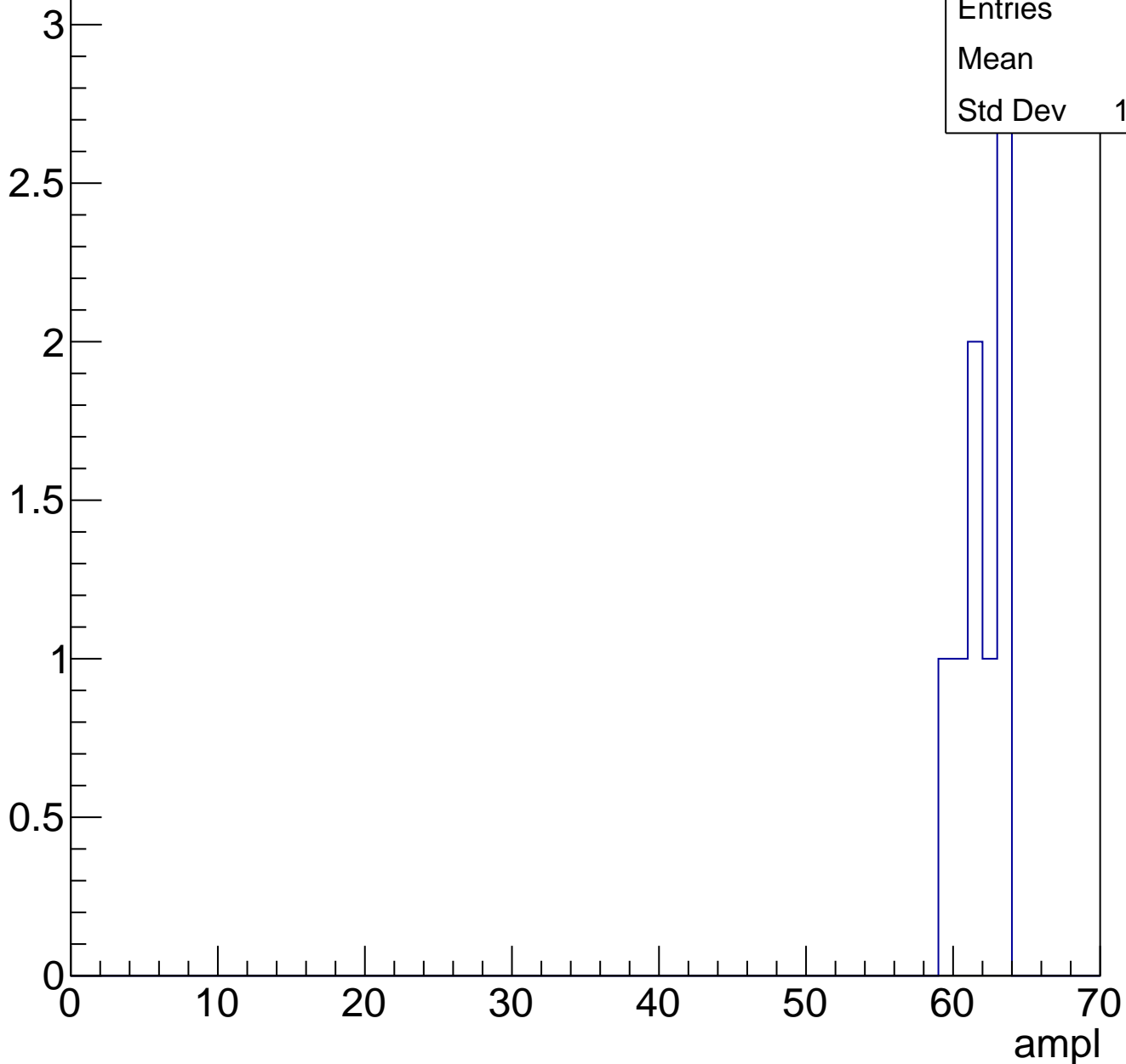
Entries	47
Mean	58.21
Std Dev	9.008



# B1L002S, U26-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



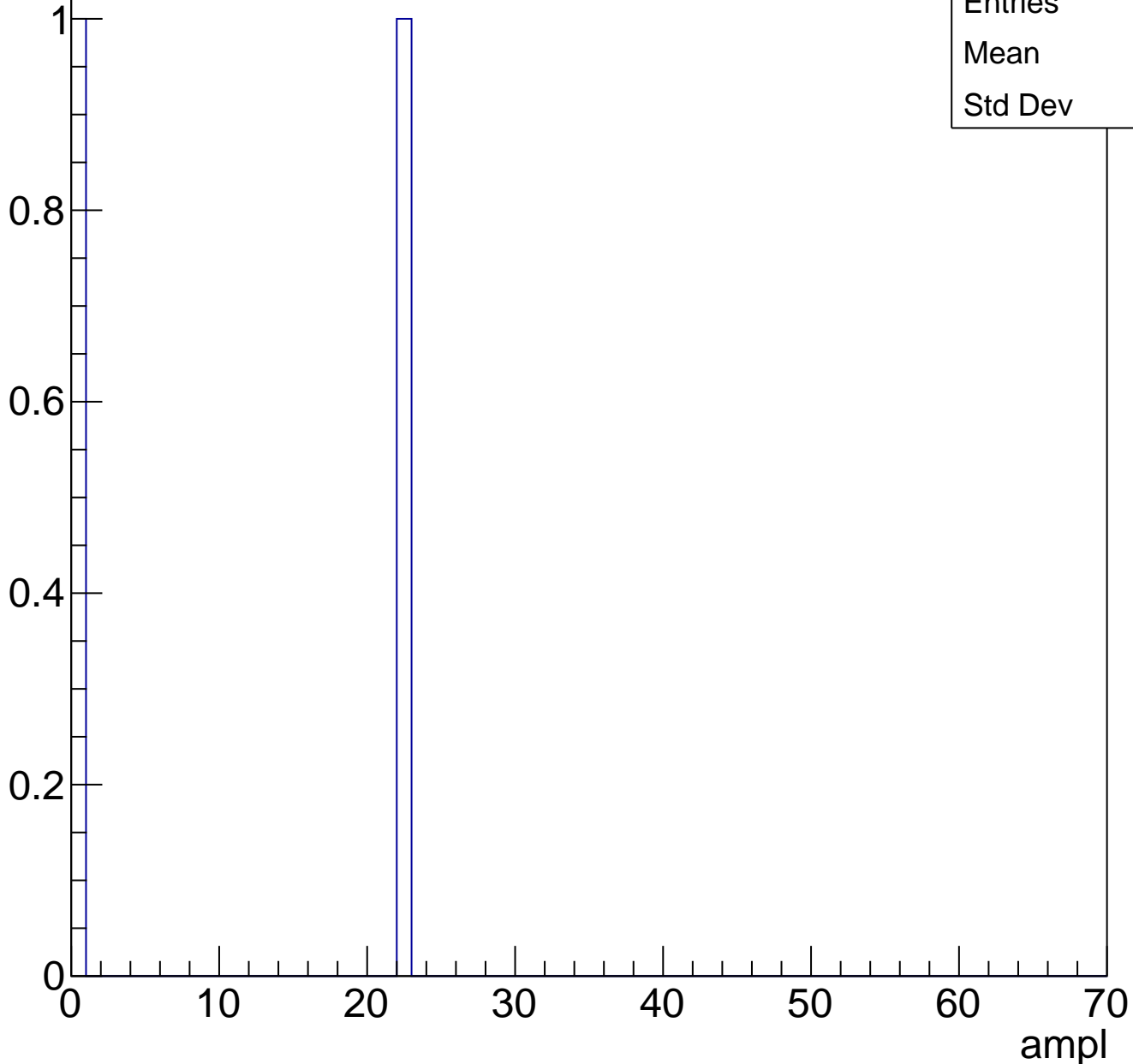
Entries	8
Mean	61.5
Std Dev	1.414



# B1L002S, U26-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	11
Std Dev	11

# B1L002S, U26-ch126, adc0

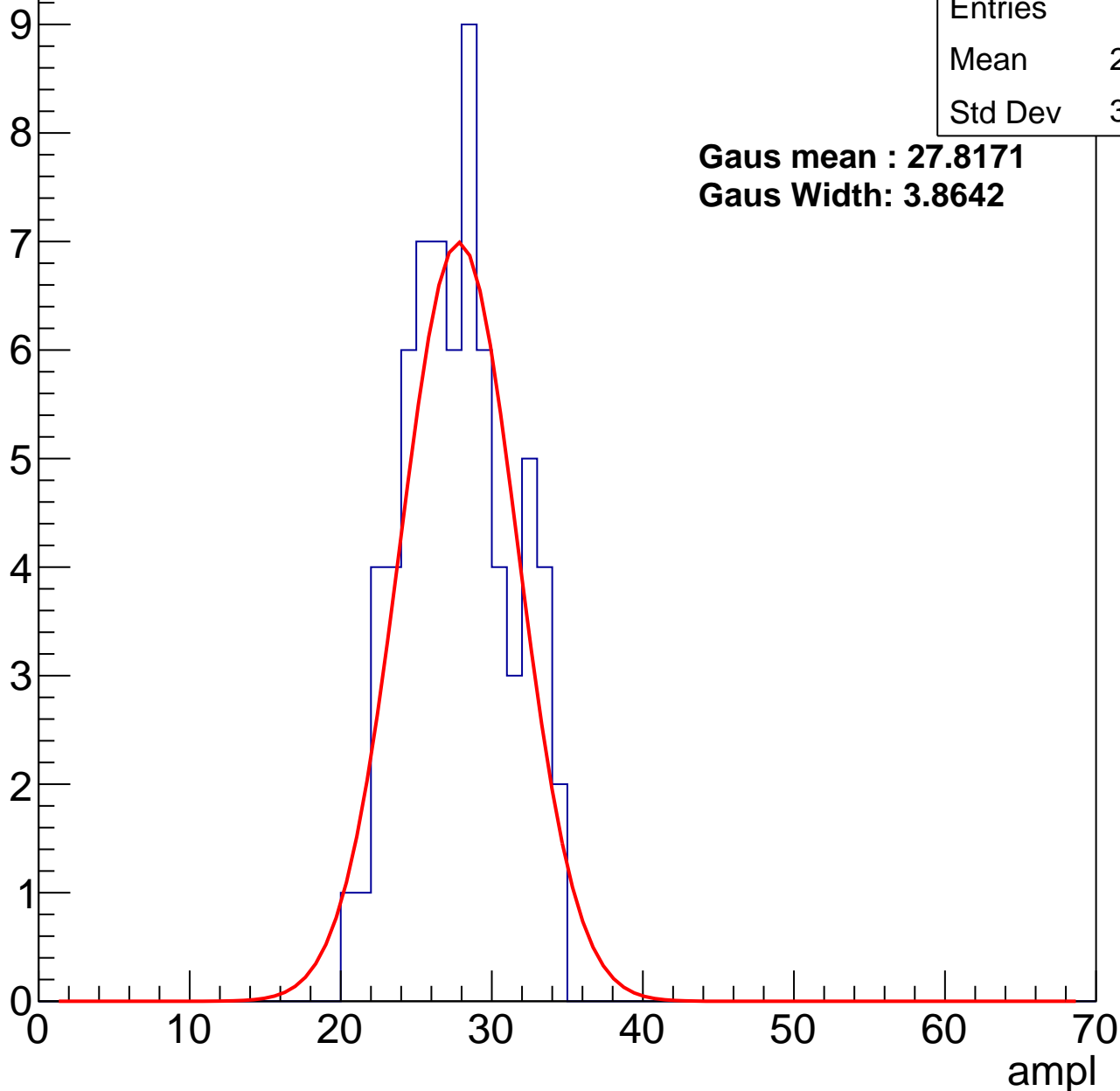
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.29
Std Dev	3.439

**Gaus mean : 27.8171**

**Gaus Width: 3.8642**



# B1L002S, U26-ch126, adc1

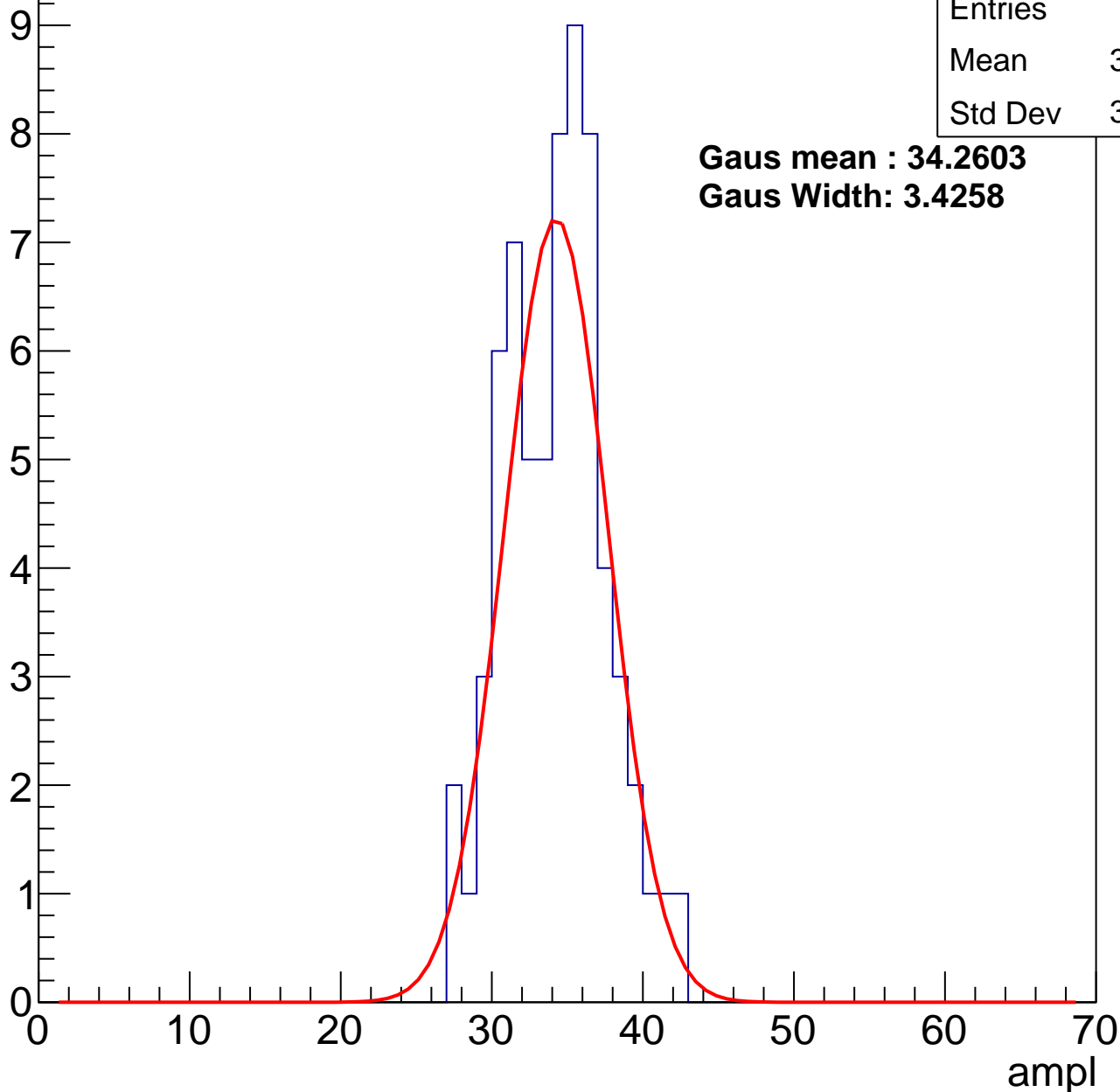
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.77
Std Dev	3.288

**Gaus mean : 34.2603**

**Gaus Width: 3.4258**



# B1L002S, U26-ch126, adc2

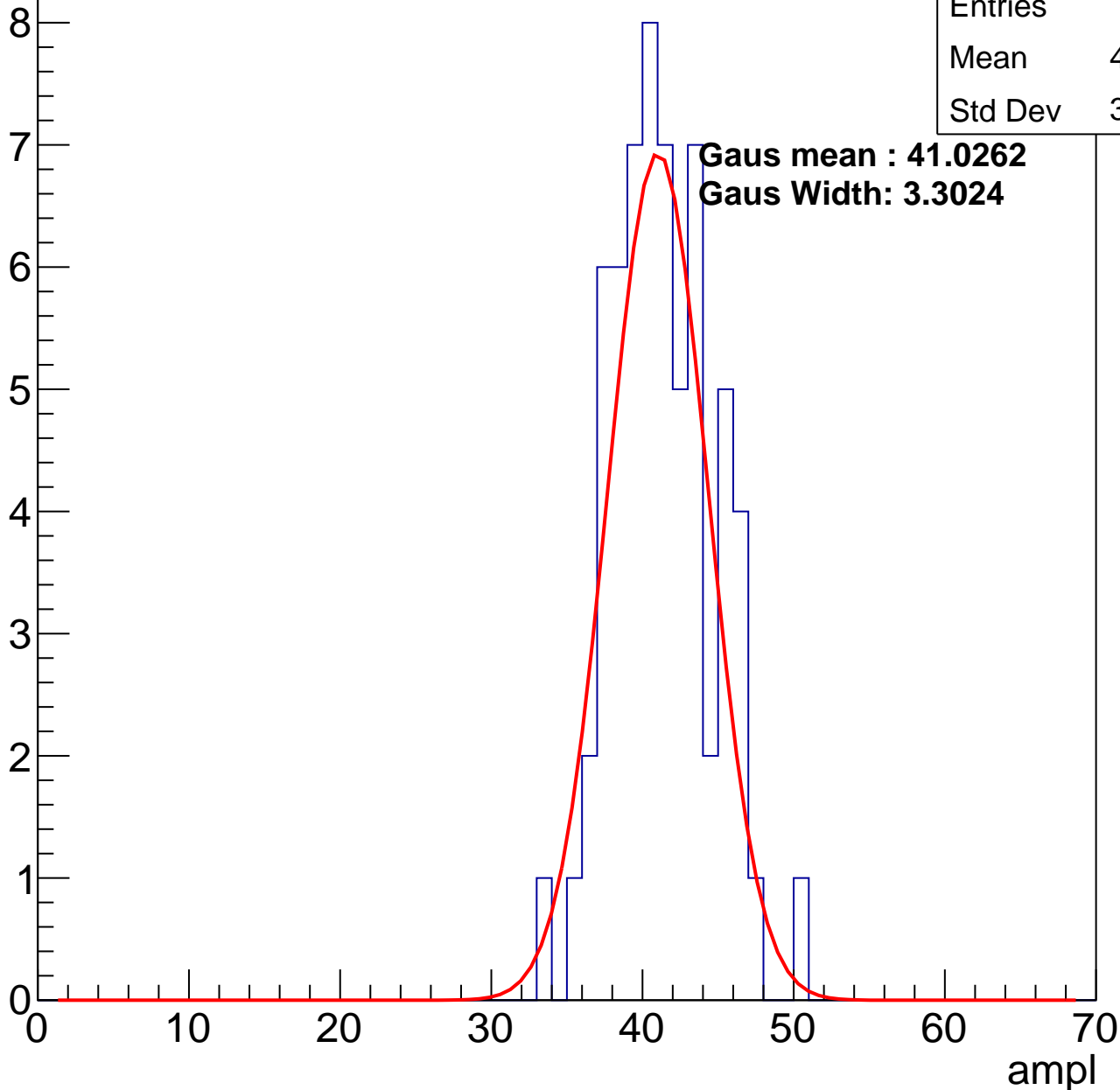
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.87
Std Dev	3.293

**Gaus mean : 41.0262**

**Gaus Width: 3.3024**

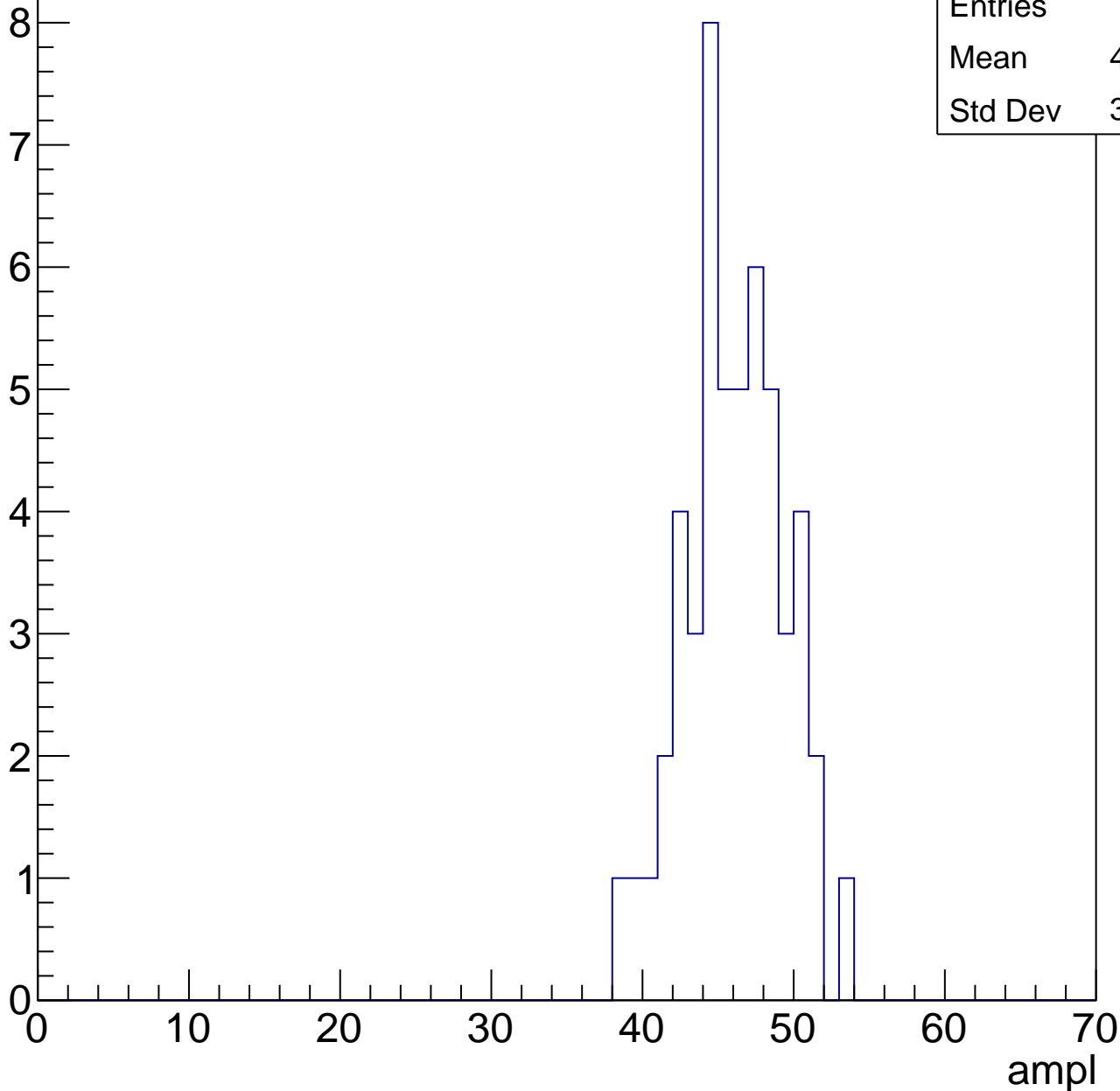


# B1L002S, U26-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	45.63
Std Dev	3.242

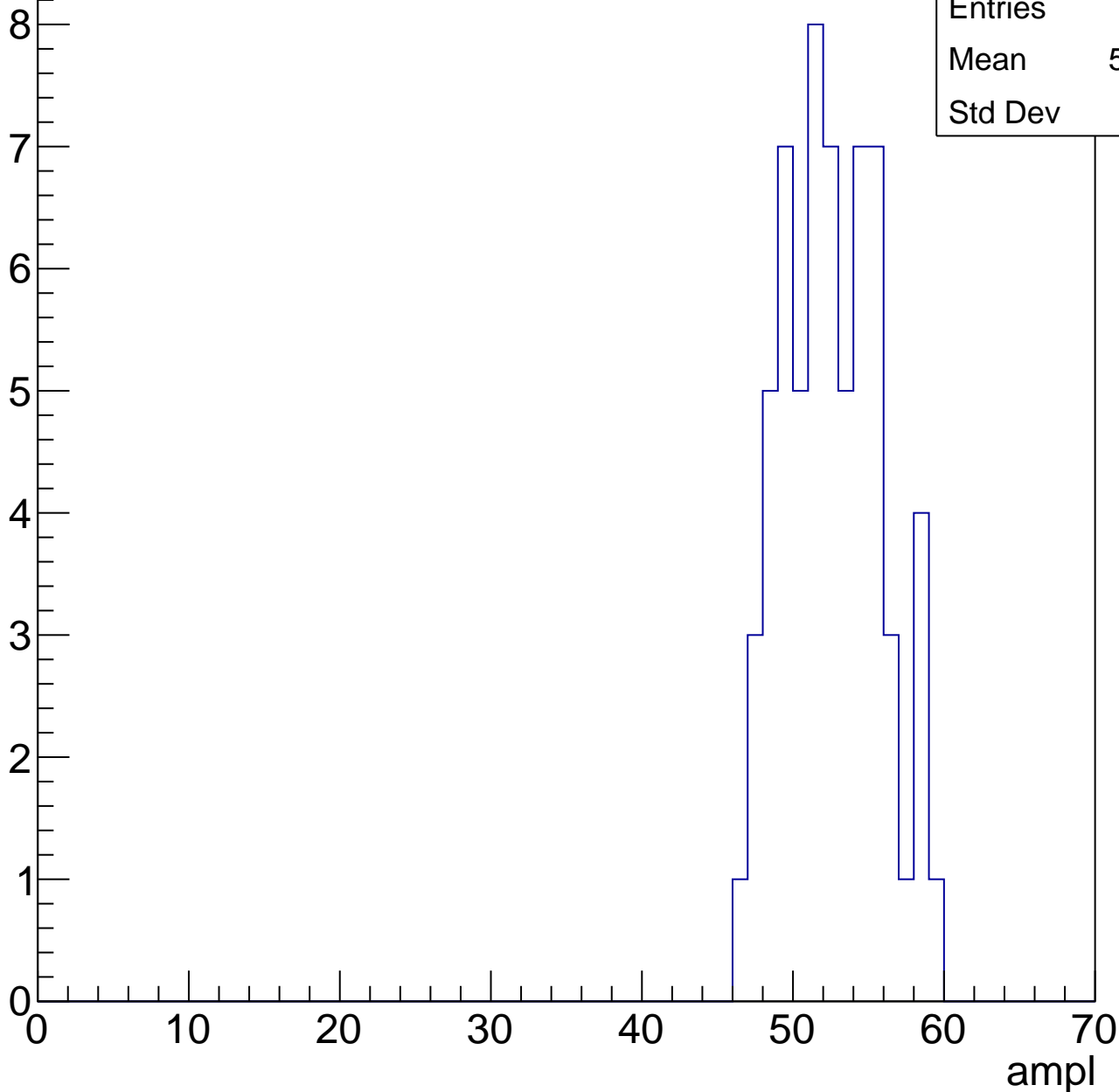


# B1L002S, U26-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

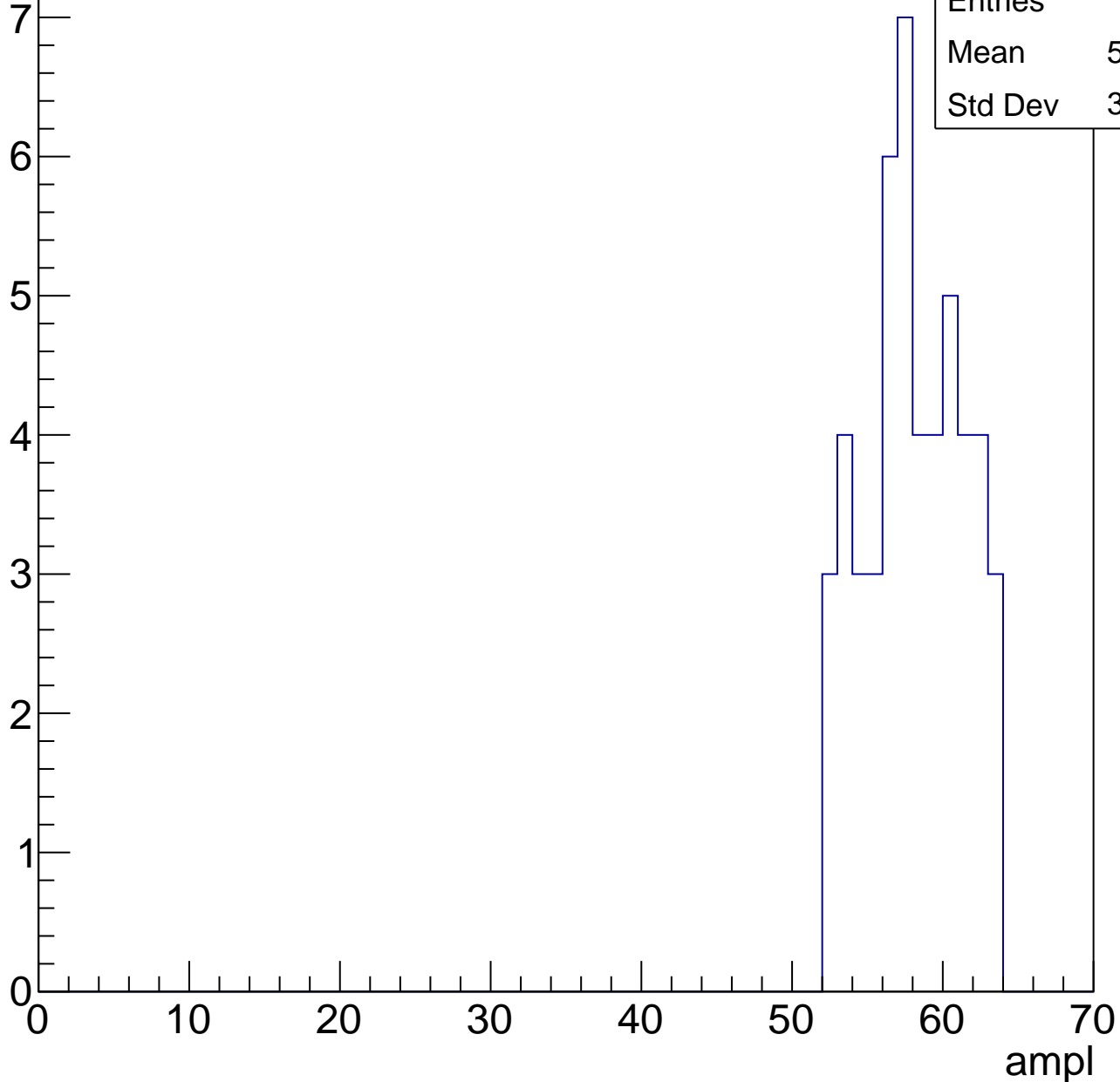
Entries	64
Mean	52.12
Std Dev	3.17



# B1L002S, U26-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



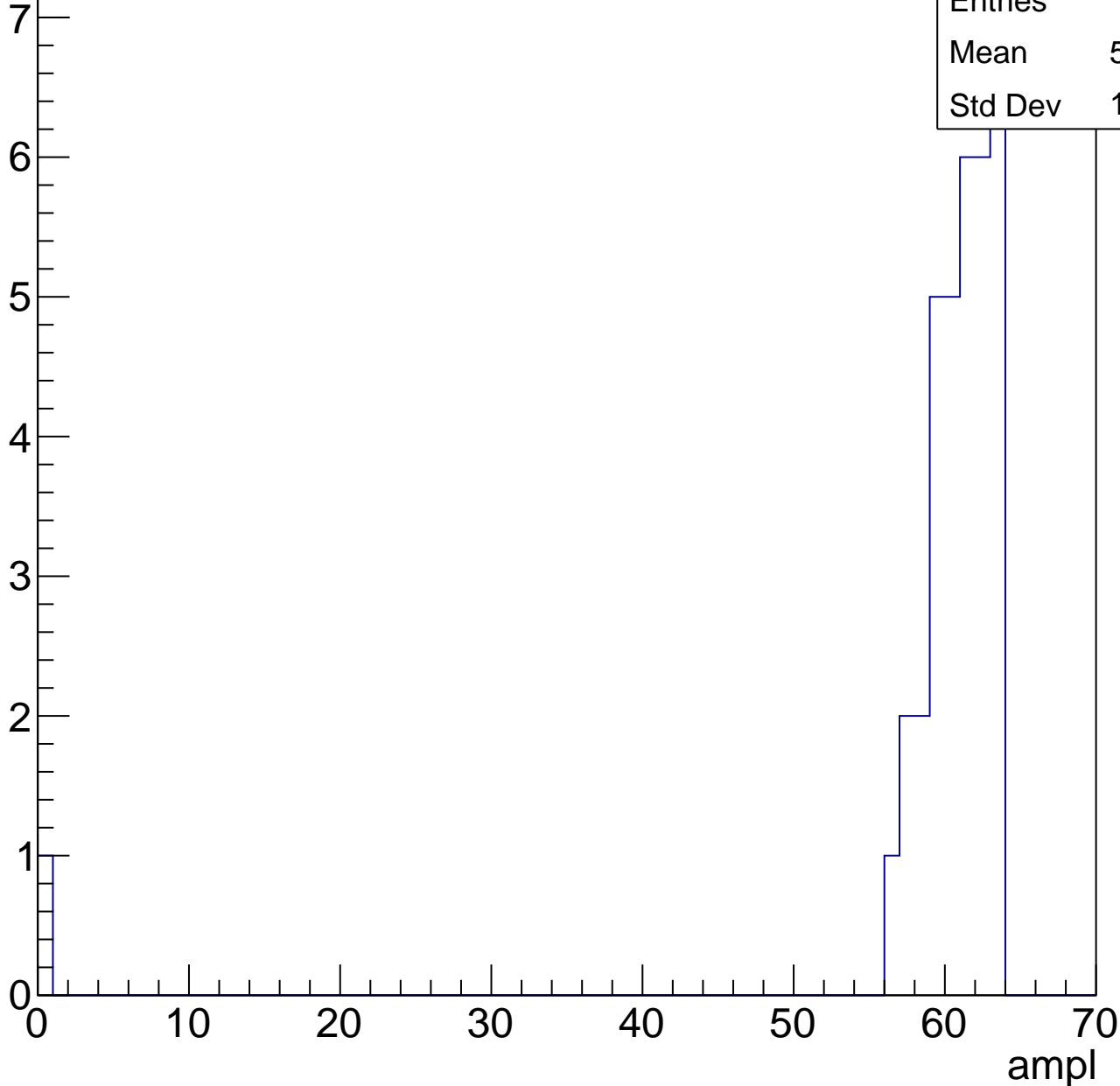
Entries	50
Mean	57.58
Std Dev	3.175

# B1L002S, U26-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.86
Std Dev	10.27





# B1L002S, U26-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U26-ch127, adc0

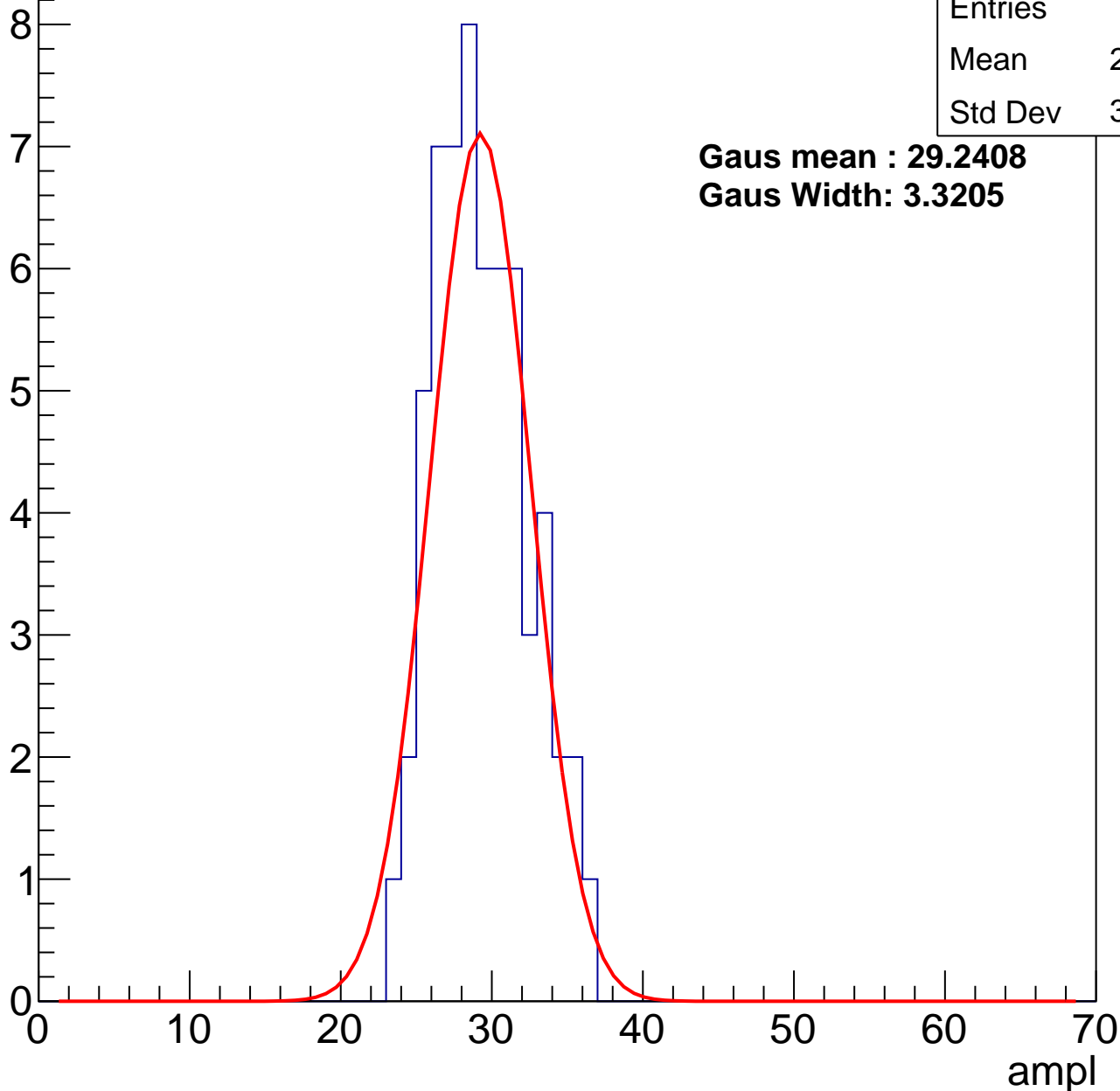
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.88
Std Dev	3.045

**Gaus mean : 29.2408**

**Gaus Width: 3.3205**



# B1L002S, U26-ch127, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	35.27
Std Dev	3.251

**Gaus mean : 35.8699**

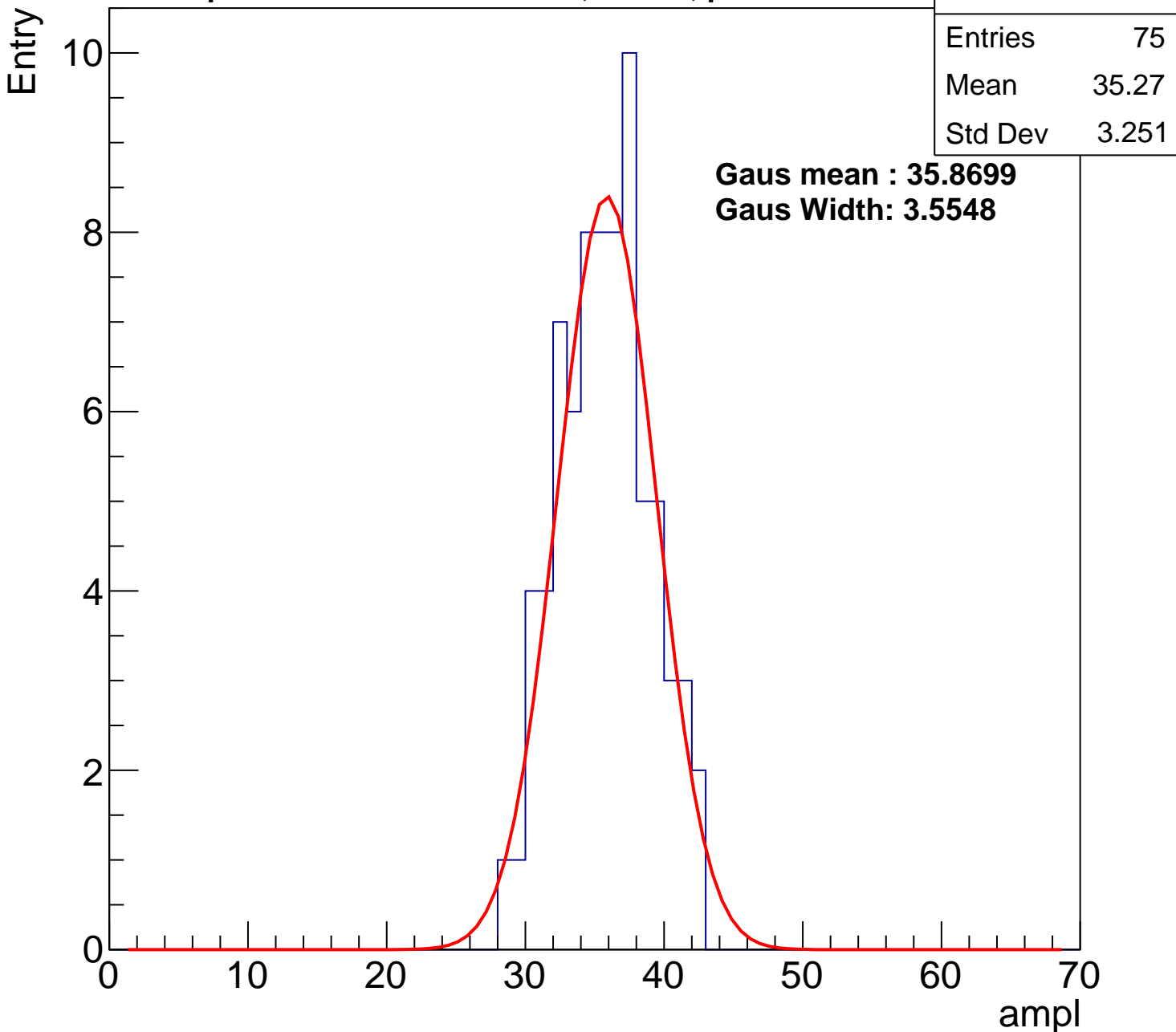
**Gaus Width: 3.5548**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U26-ch127, adc2

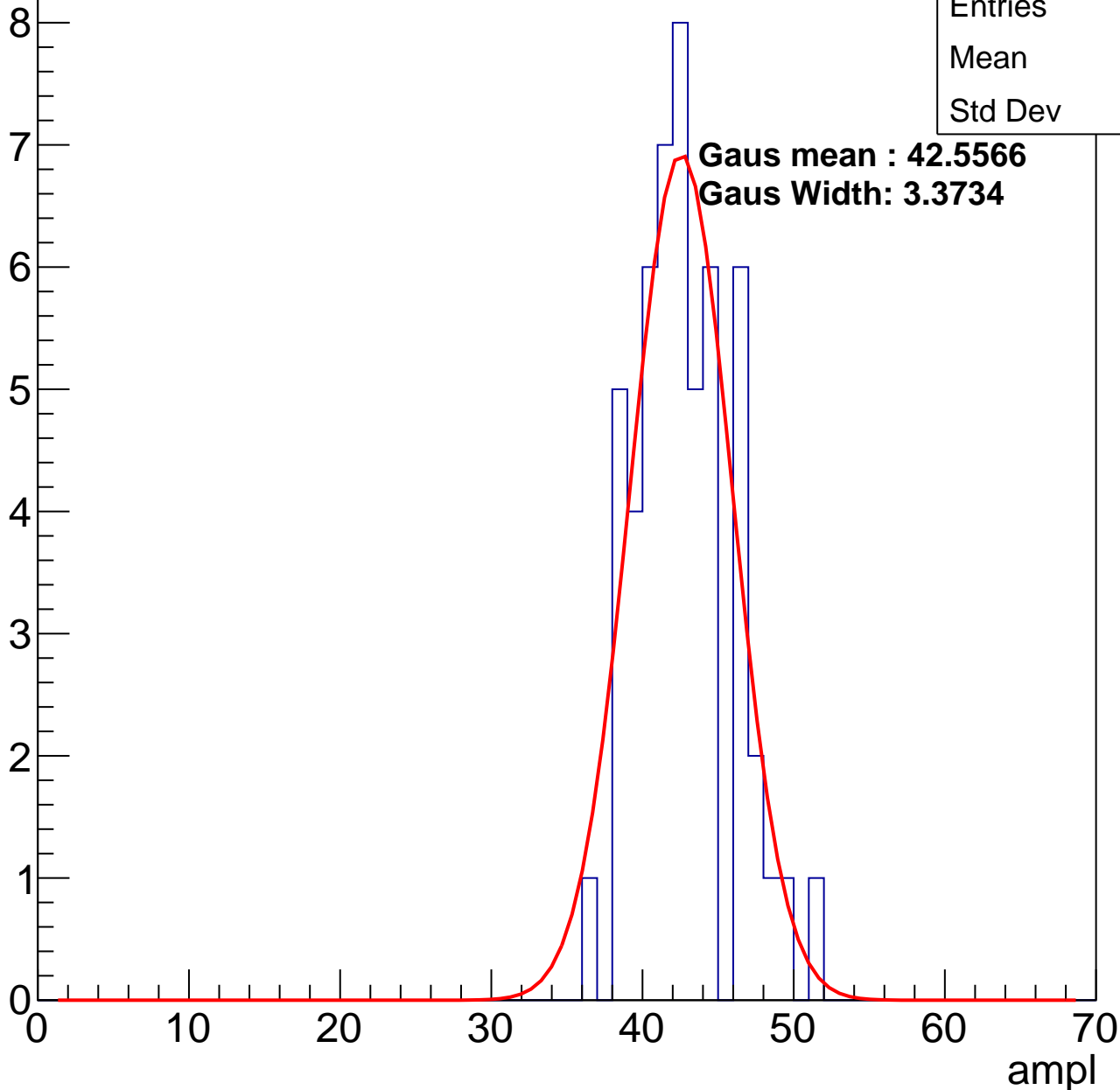
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	42.3
Std Dev	3.13

**Gaus mean : 42.5566**

**Gaus Width: 3.3734**

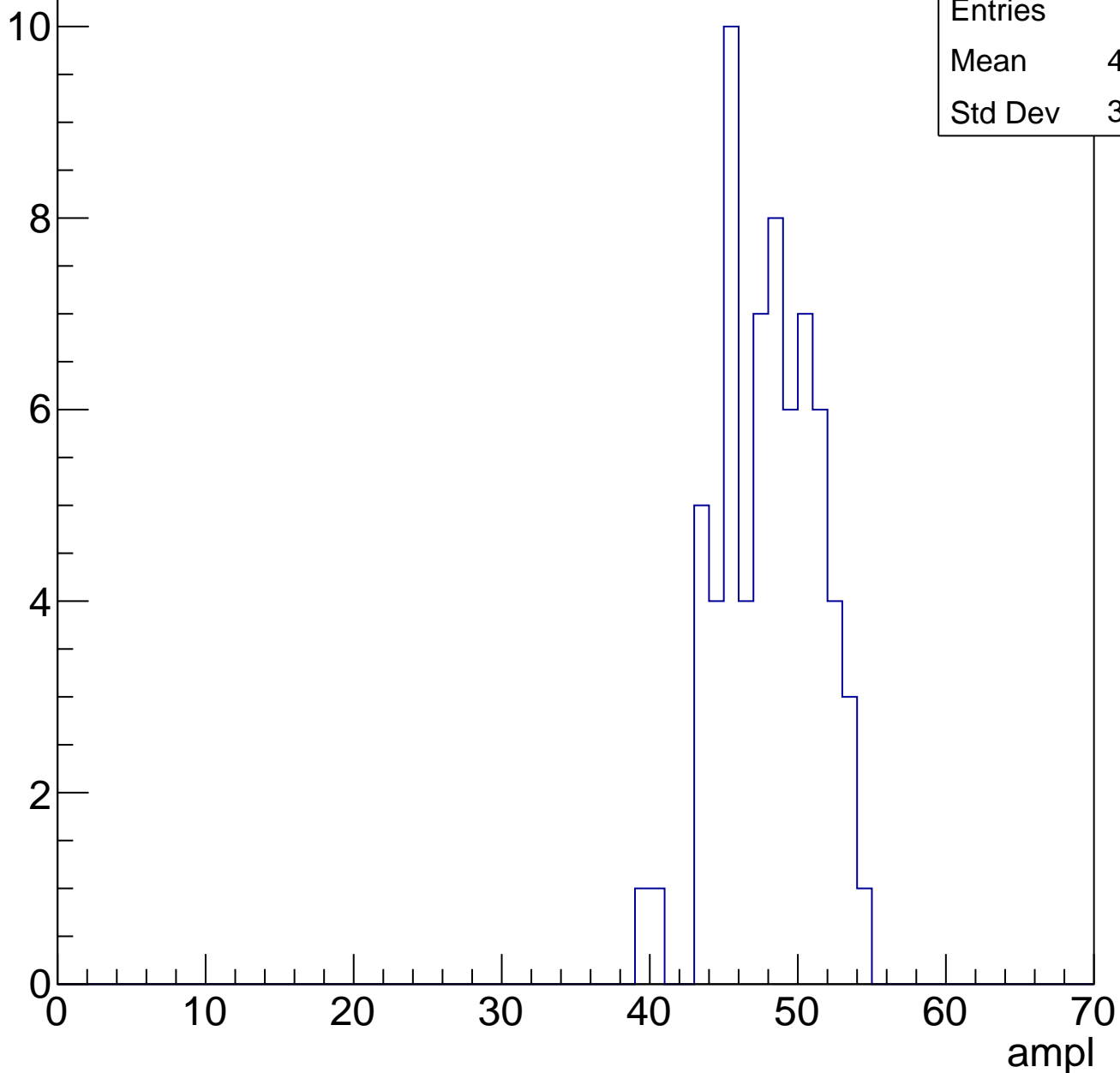


# B1L002S, U26-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	47.58
Std Dev	3.228

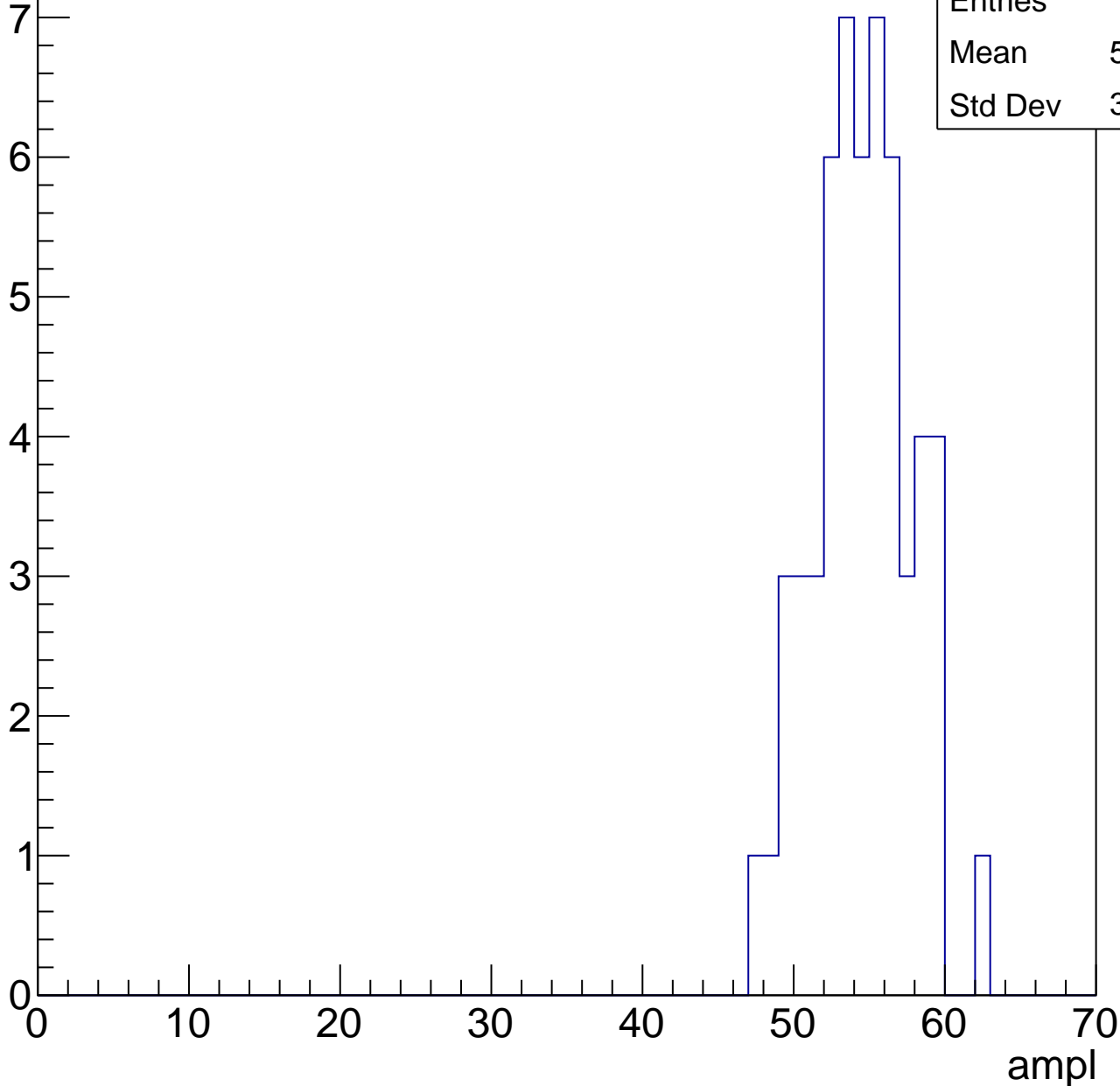


# B1L002S, U26-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	54.07
Std Dev	3.167

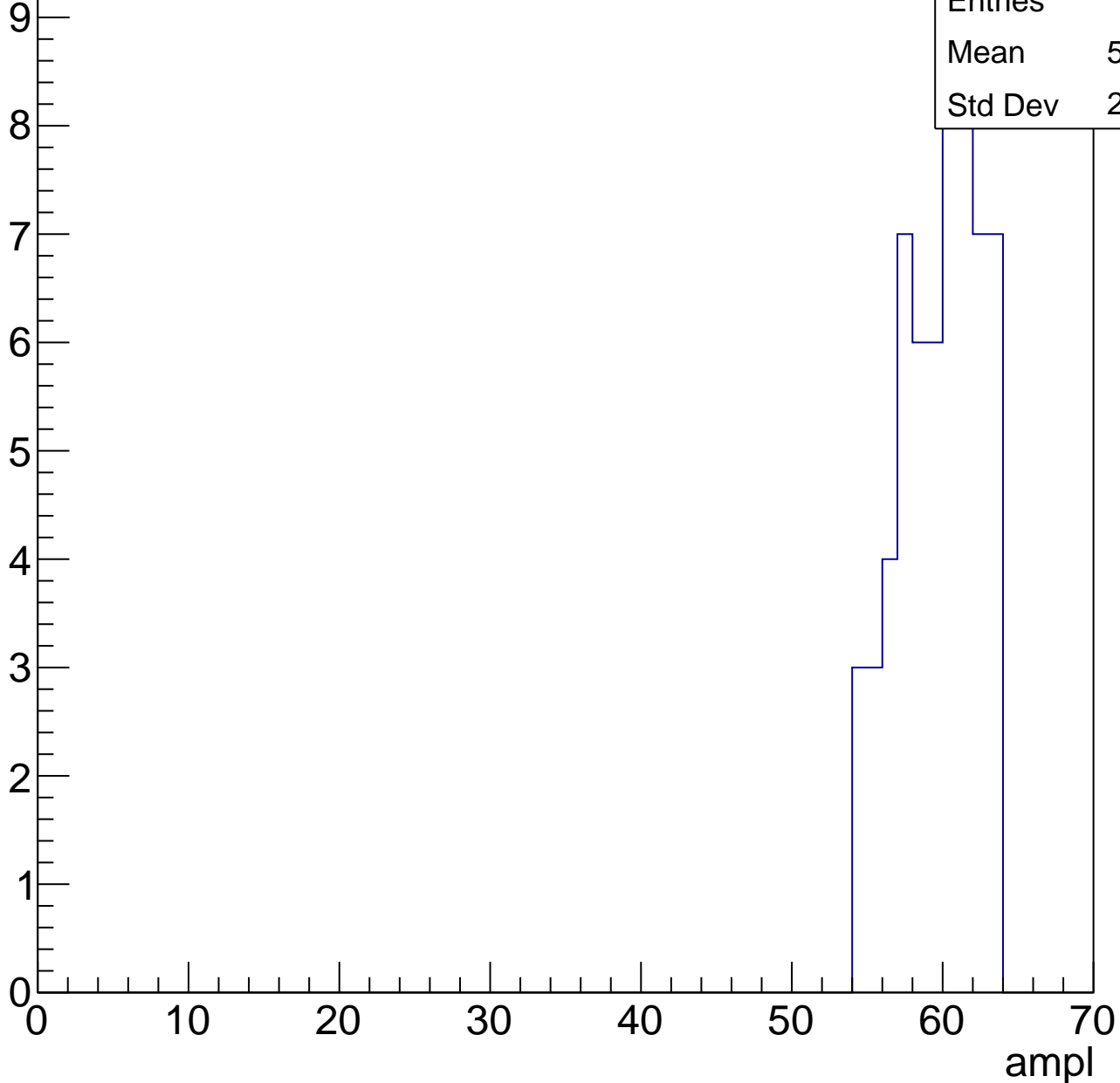


# B1L002S, U26-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	59.25
Std Dev	2.599



# B1L002S, U26-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

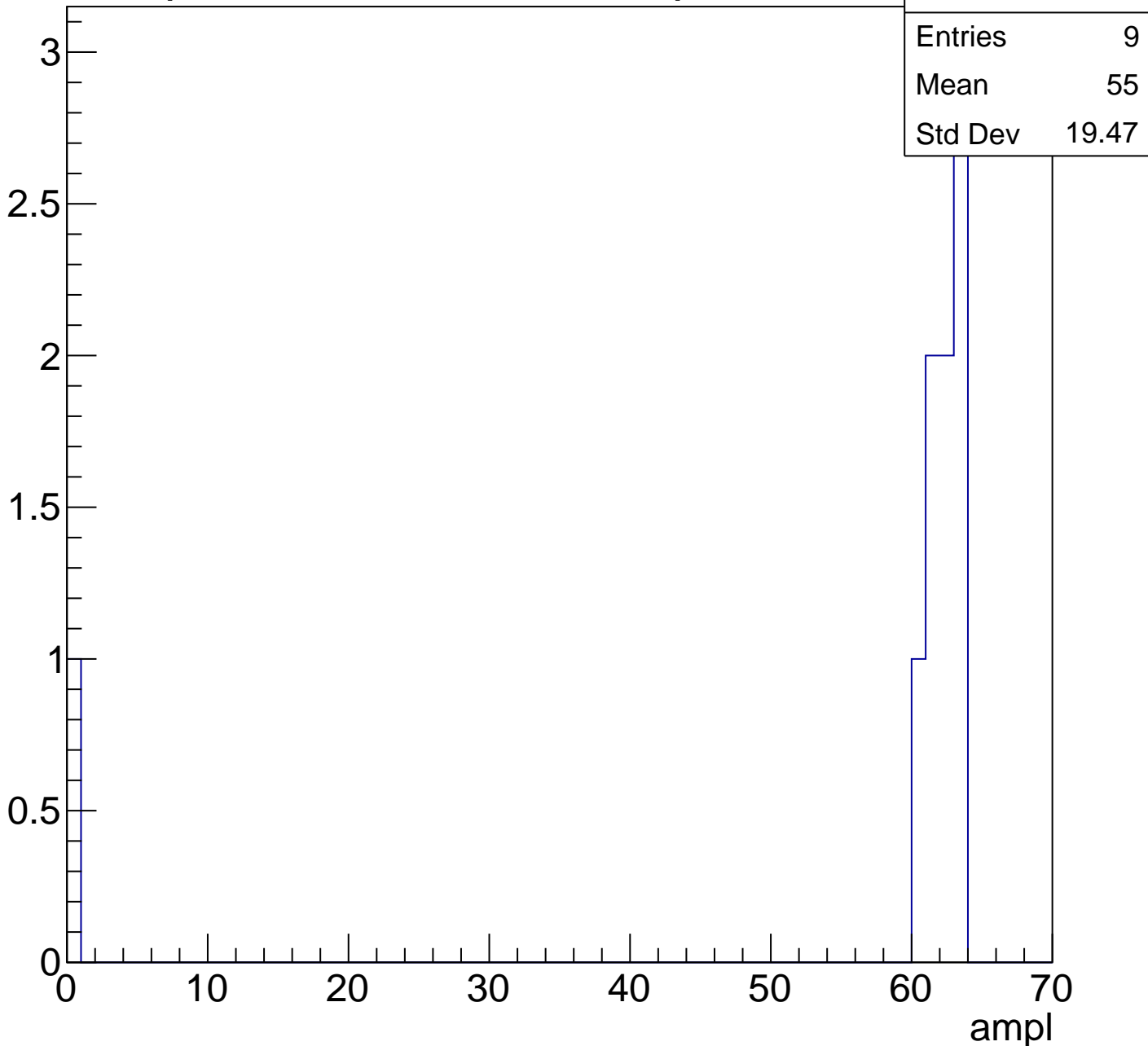
9

Mean

55

Std Dev

19.47





# B1L002S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B1L002S, U26-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

