

B0L102S, U25-ch0

calib_packv5_042523_0143.root, FC#12, port B1

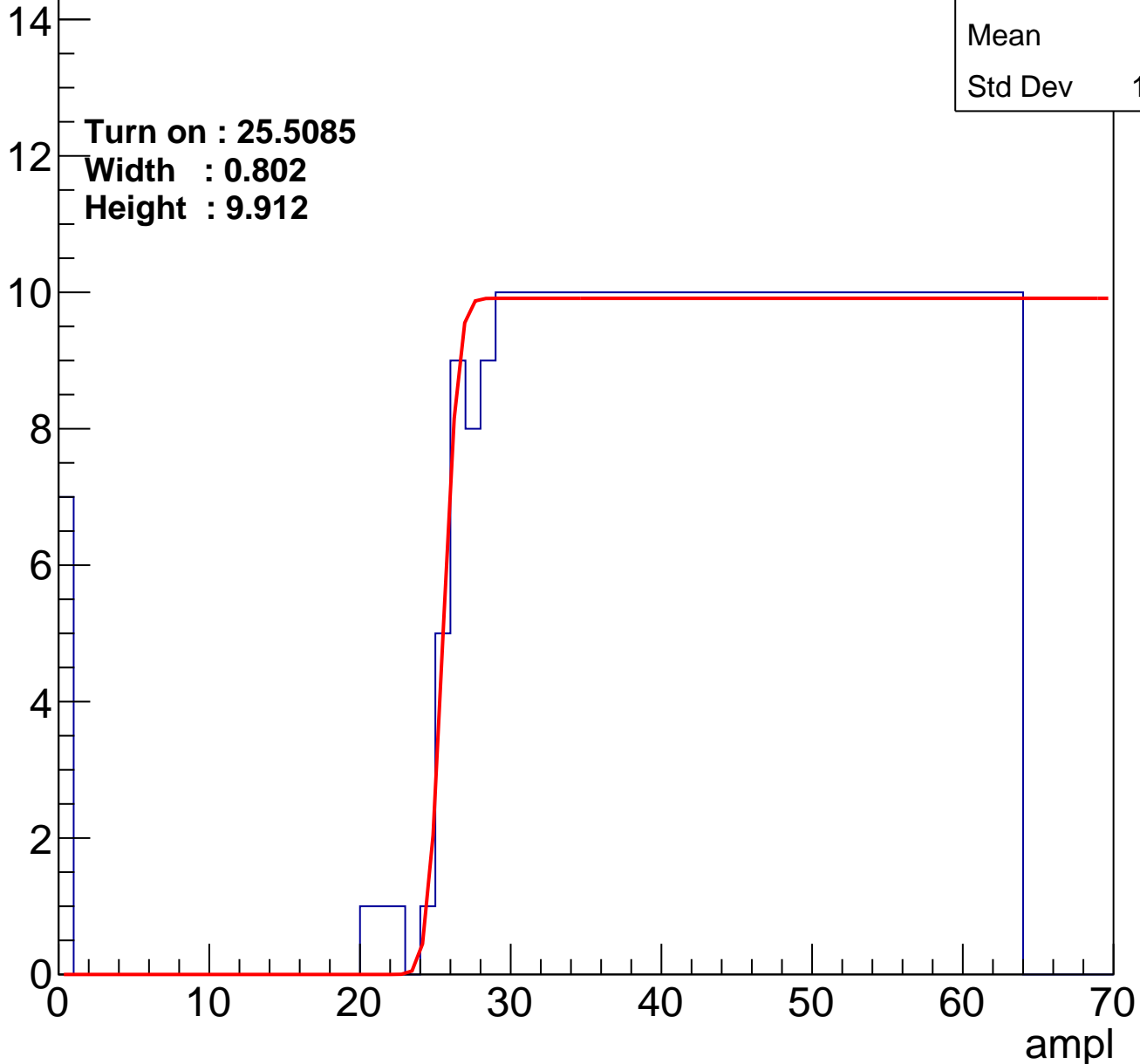
Entries	392
Mean	43.4
Std Dev	12.56

Turn on : 25.5085

Width : 0.802

Height : 9.912

Entry



B0L102S, U25-ch1

calib_packv5_042523_0143.root, FC#12, port B1

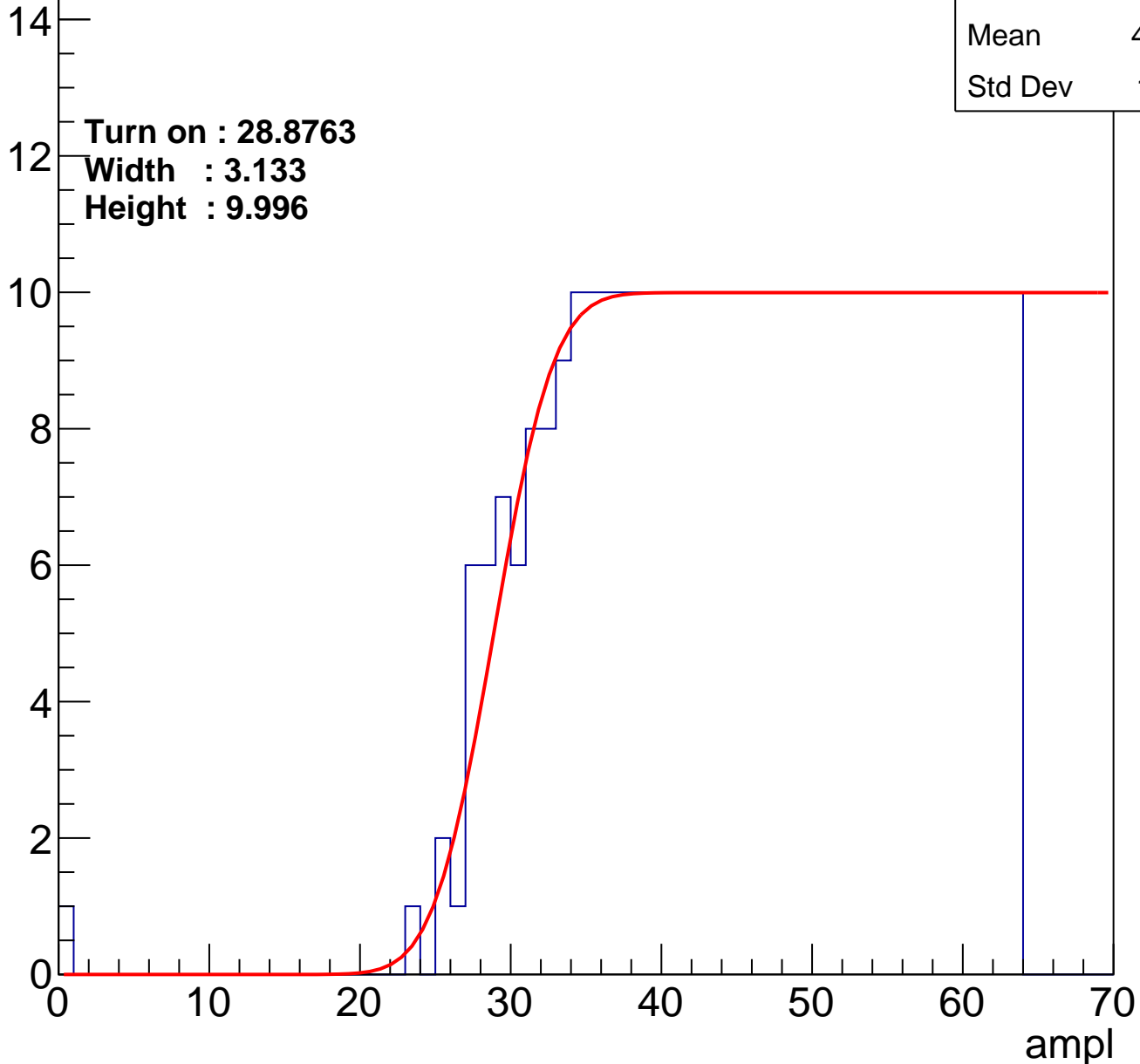
Entries	355
Mean	45.53
Std Dev	10.71

Turn on : 28.8763

Width : 3.133

Height : 9.996

Entry



B0L102S, U25-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.26
Std Dev	10.79

Turn on : 28.0146

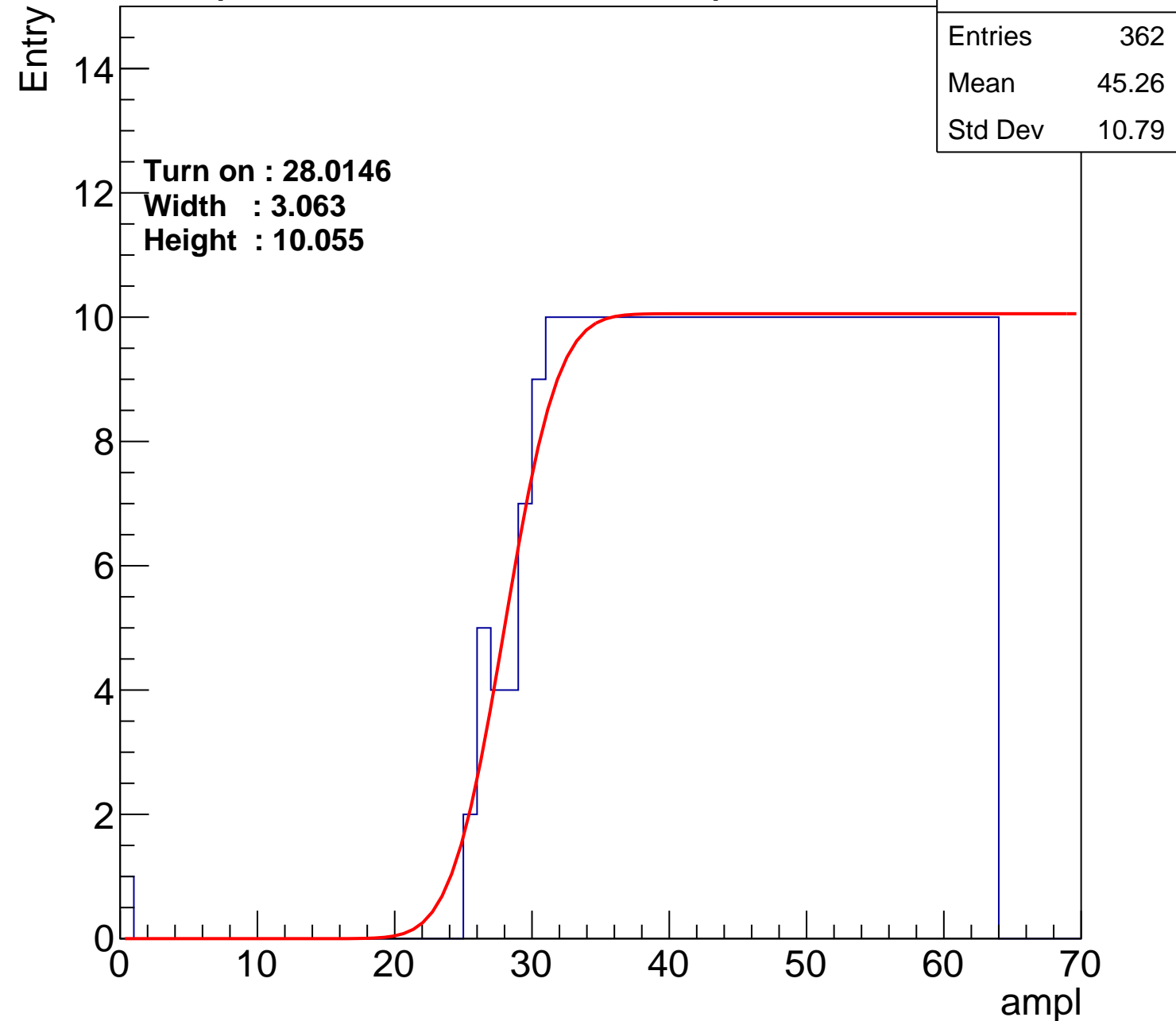
Width : 3.063

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.36
Std Dev	10.78

Turn on : 27.8547

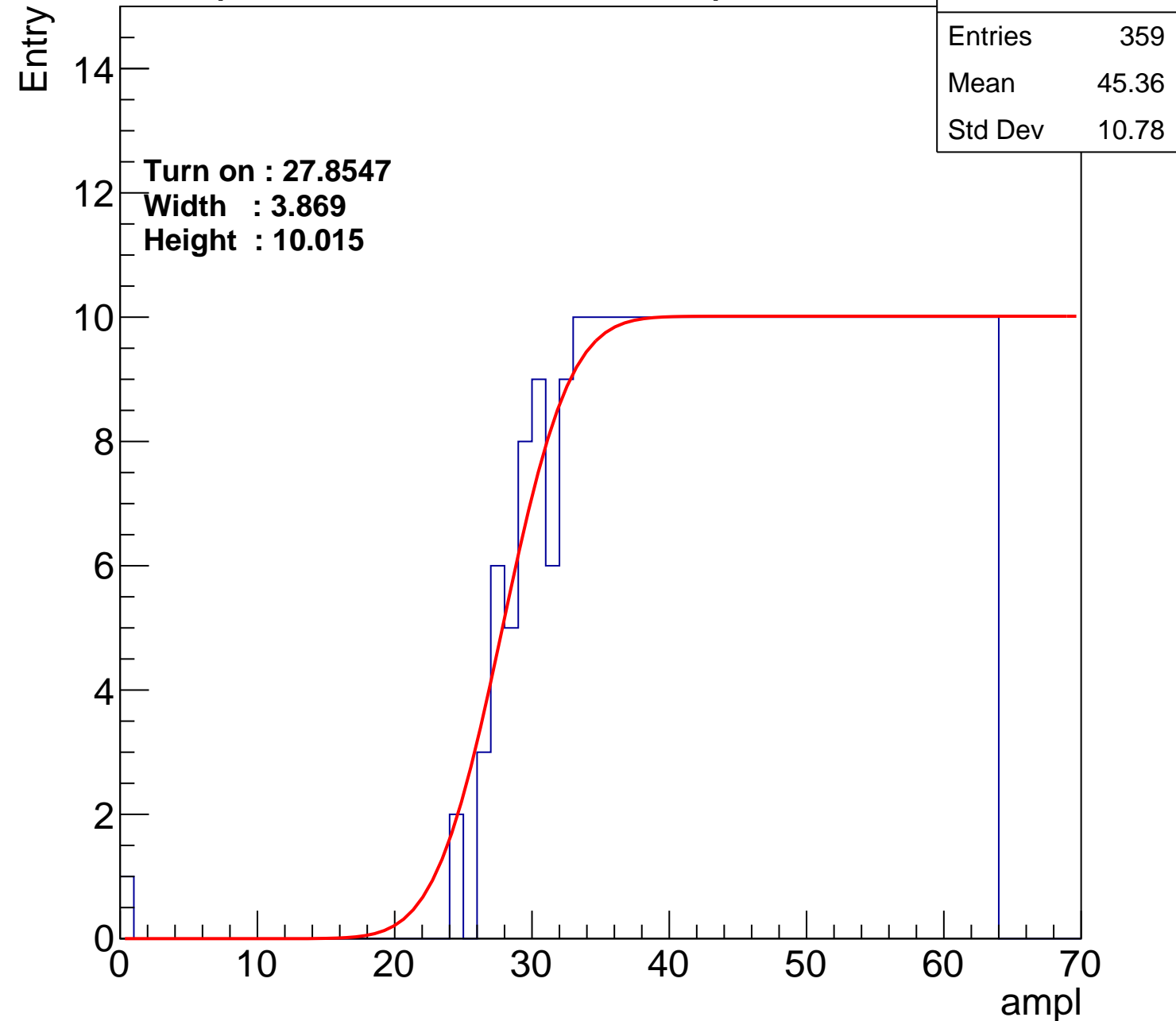
Width : 3.869

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch4

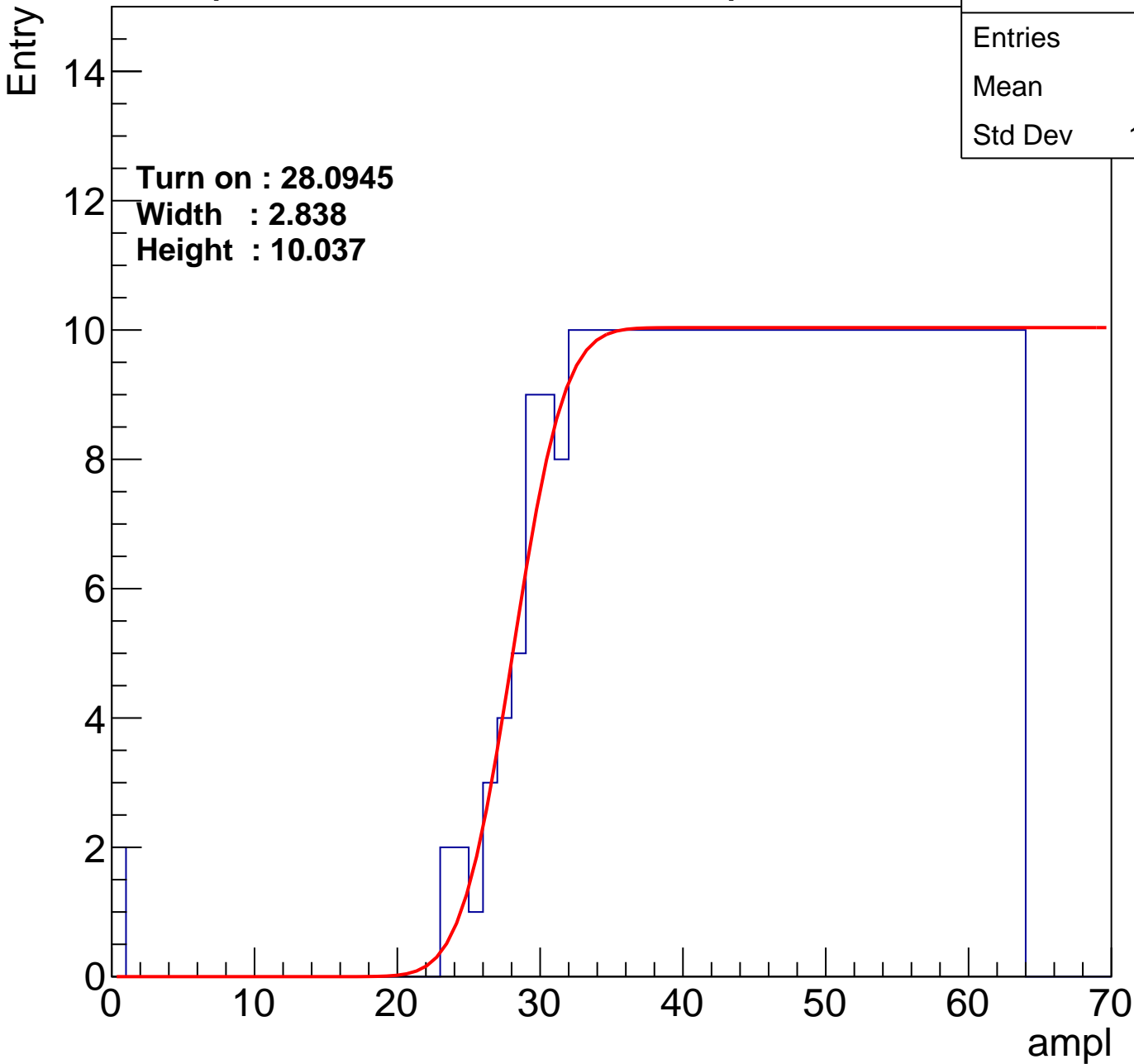
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.0945

Width : 2.838

Height : 10.037

Entries	365
Mean	45
Std Dev	11.14



B0L102S, U25-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.94
Std Dev	11.14

Turn on : 27.9077

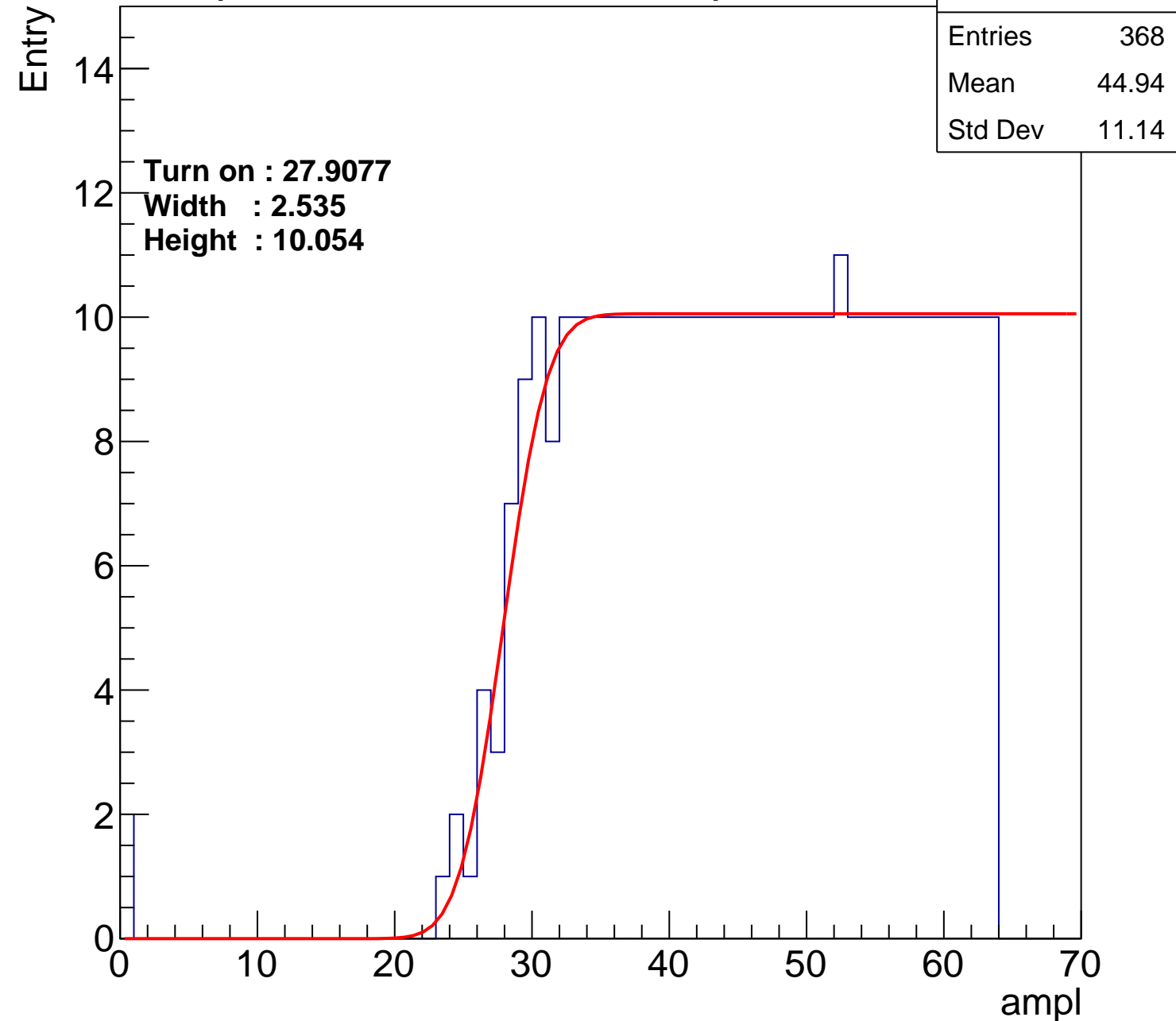
Width : 2.535

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.96
Std Dev	11.29

Turn on : 27.4890

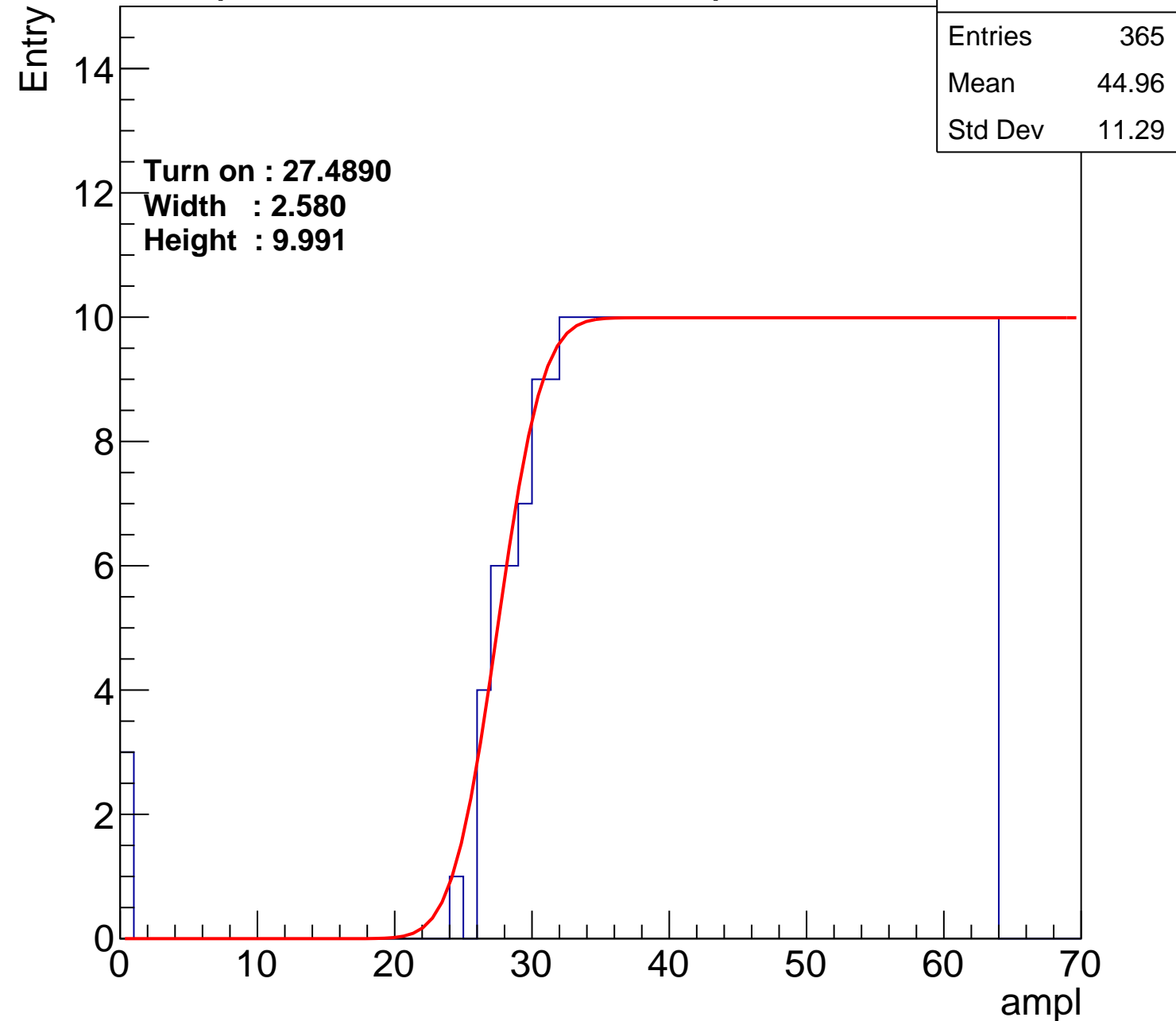
Width : 2.580

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 26.3596

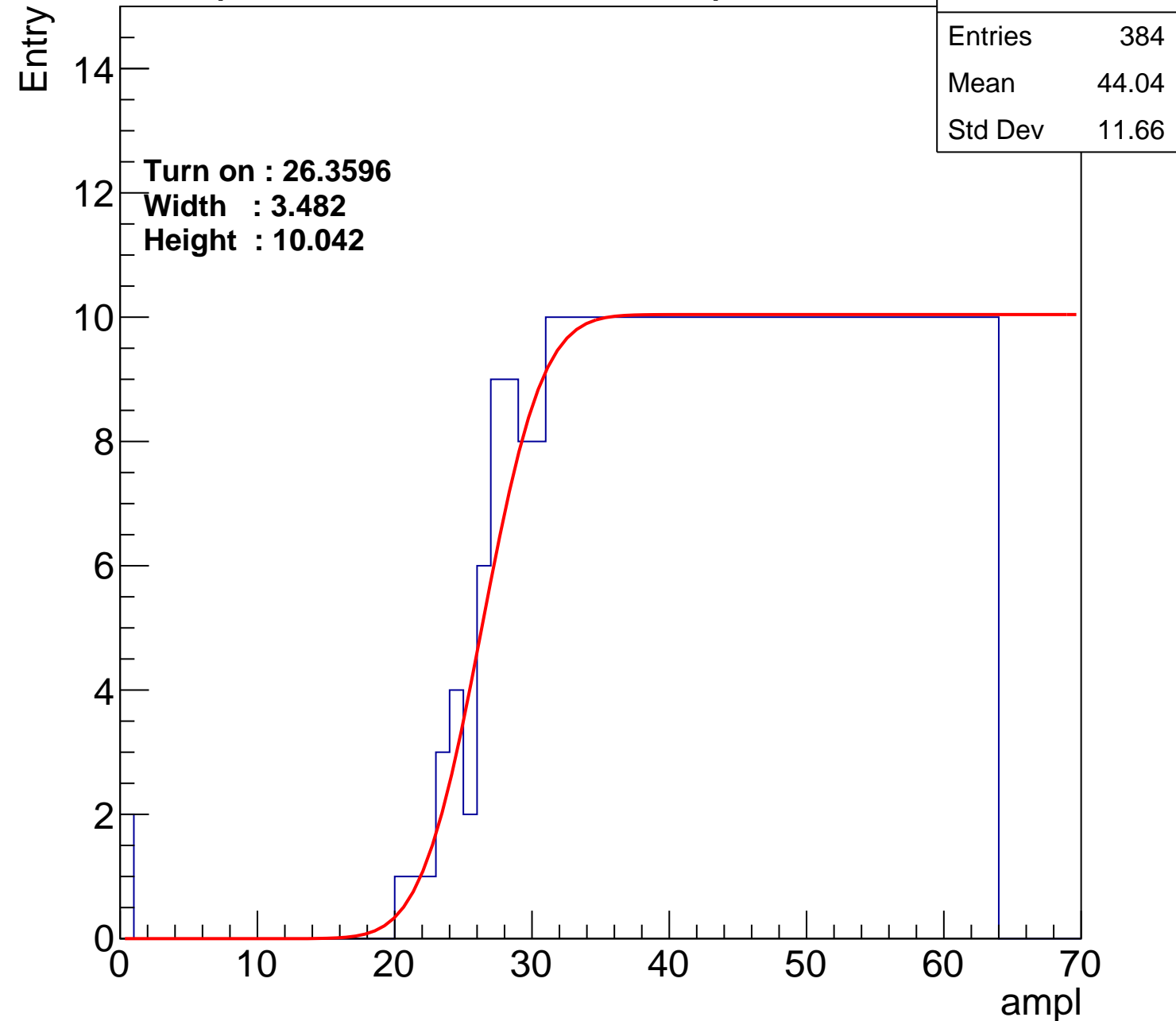
Width : 3.482

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.77
Std Dev	11.76

Turn on : 25.5650

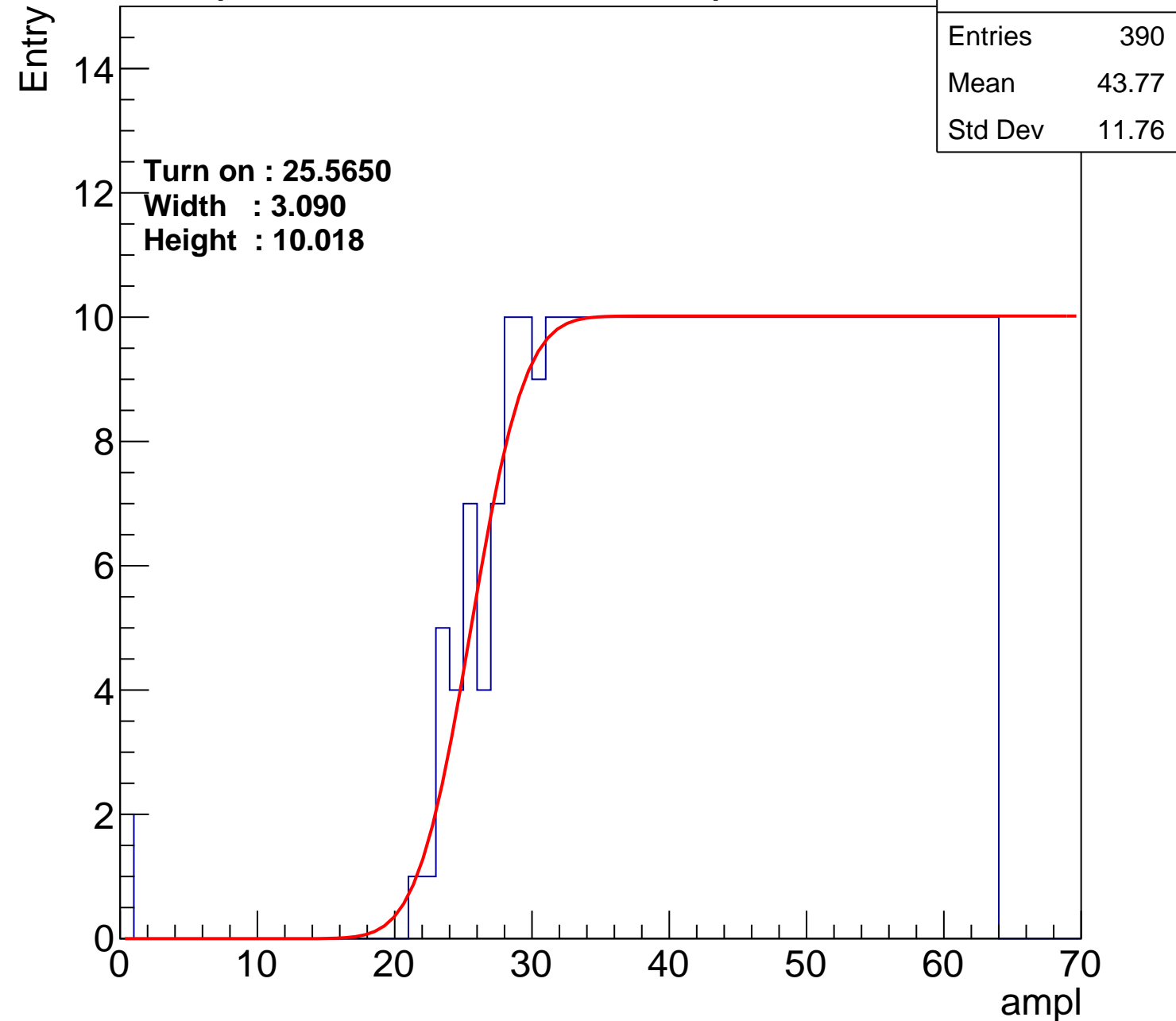
Width : 3.090

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.91
Std Dev	10.97

Turn on : 27.3723

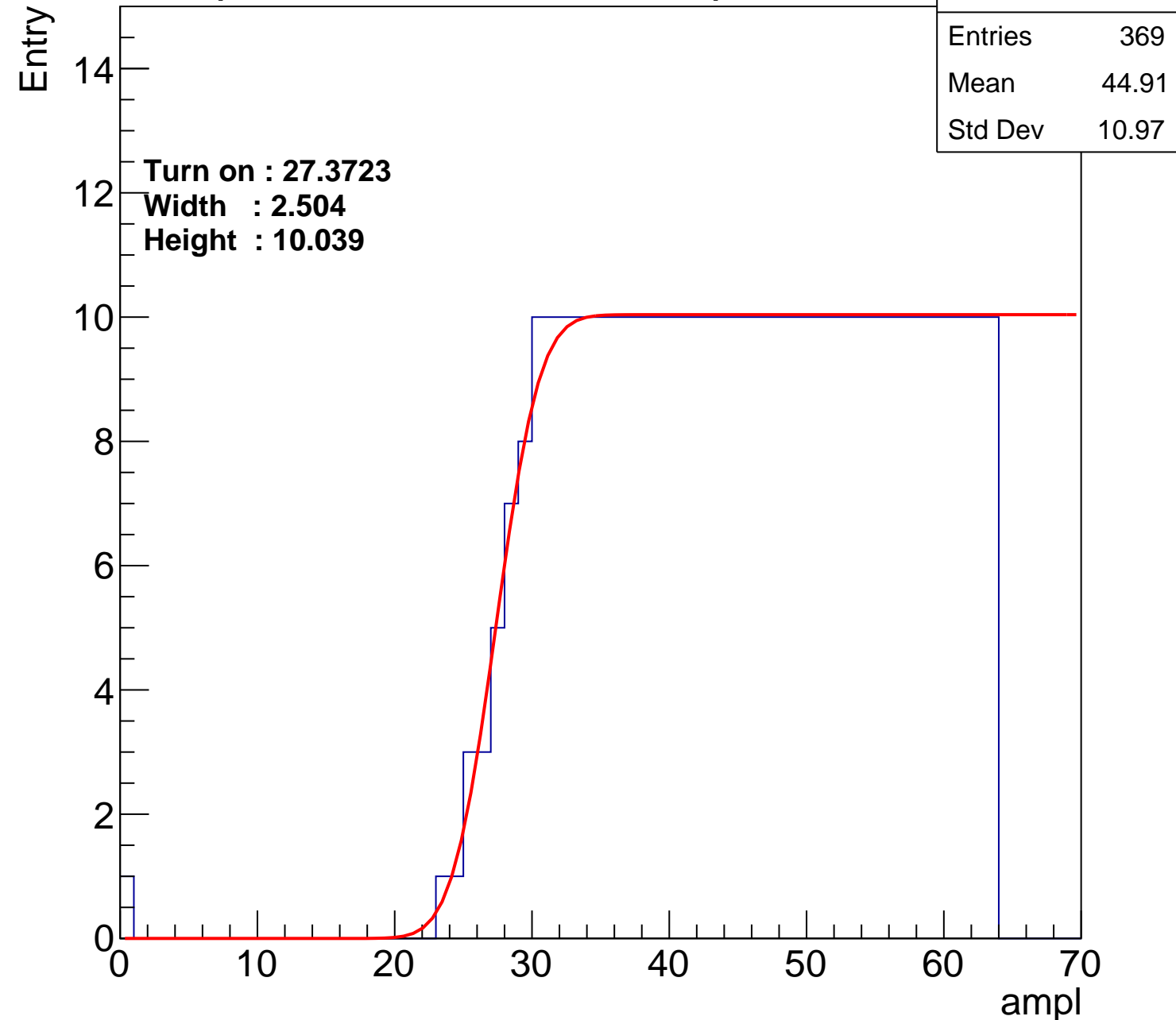
Width : 2.504

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch10

calib_packv5_042523_0143.root, FC#12, port B1

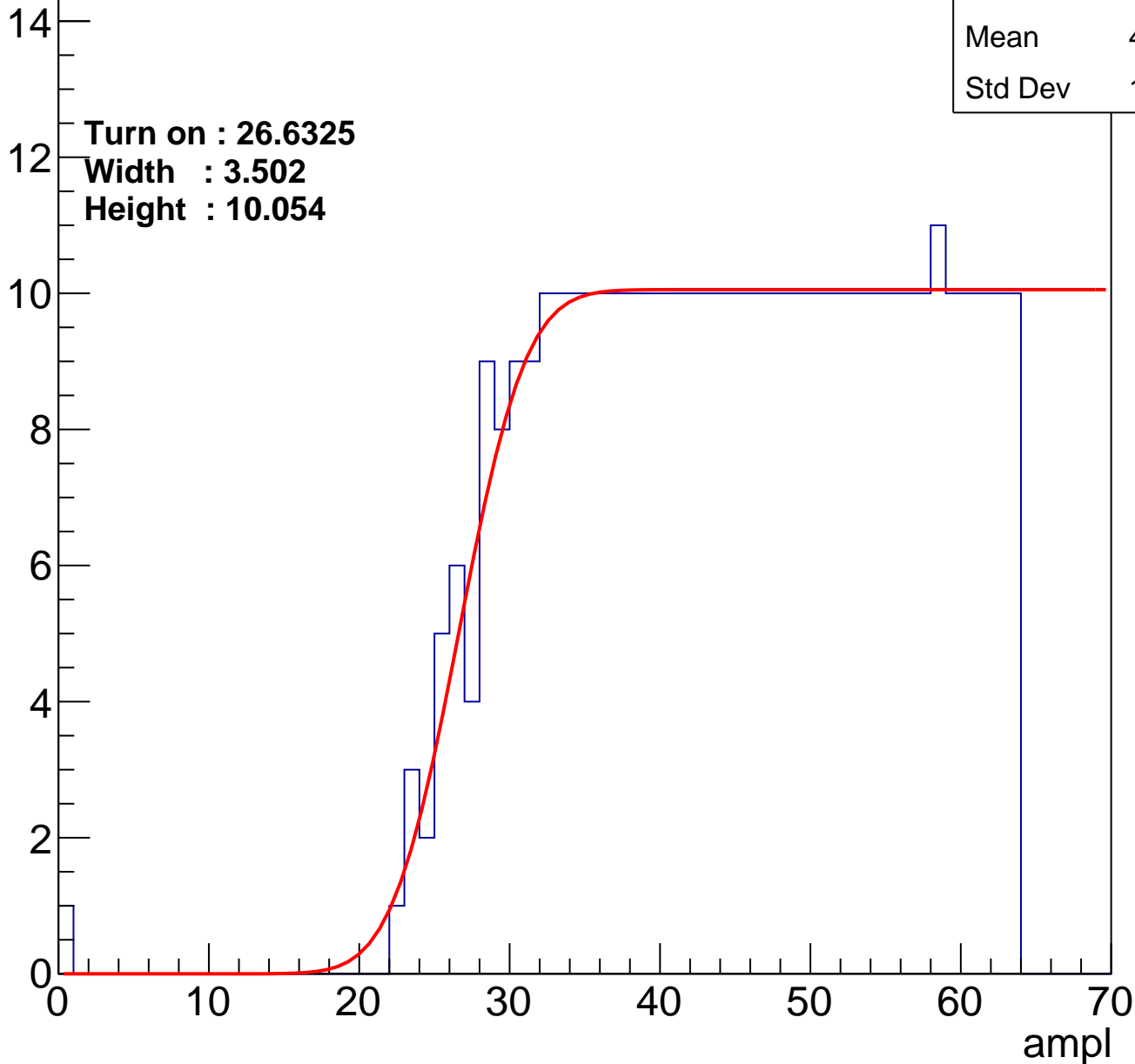
Entries	378
Mean	44.49
Std Dev	11.29

Turn on : 26.6325

Width : 3.502

Height : 10.054

Entry



B0L102S, U25-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.41
Std Dev	10.75

Turn on : 28.5862

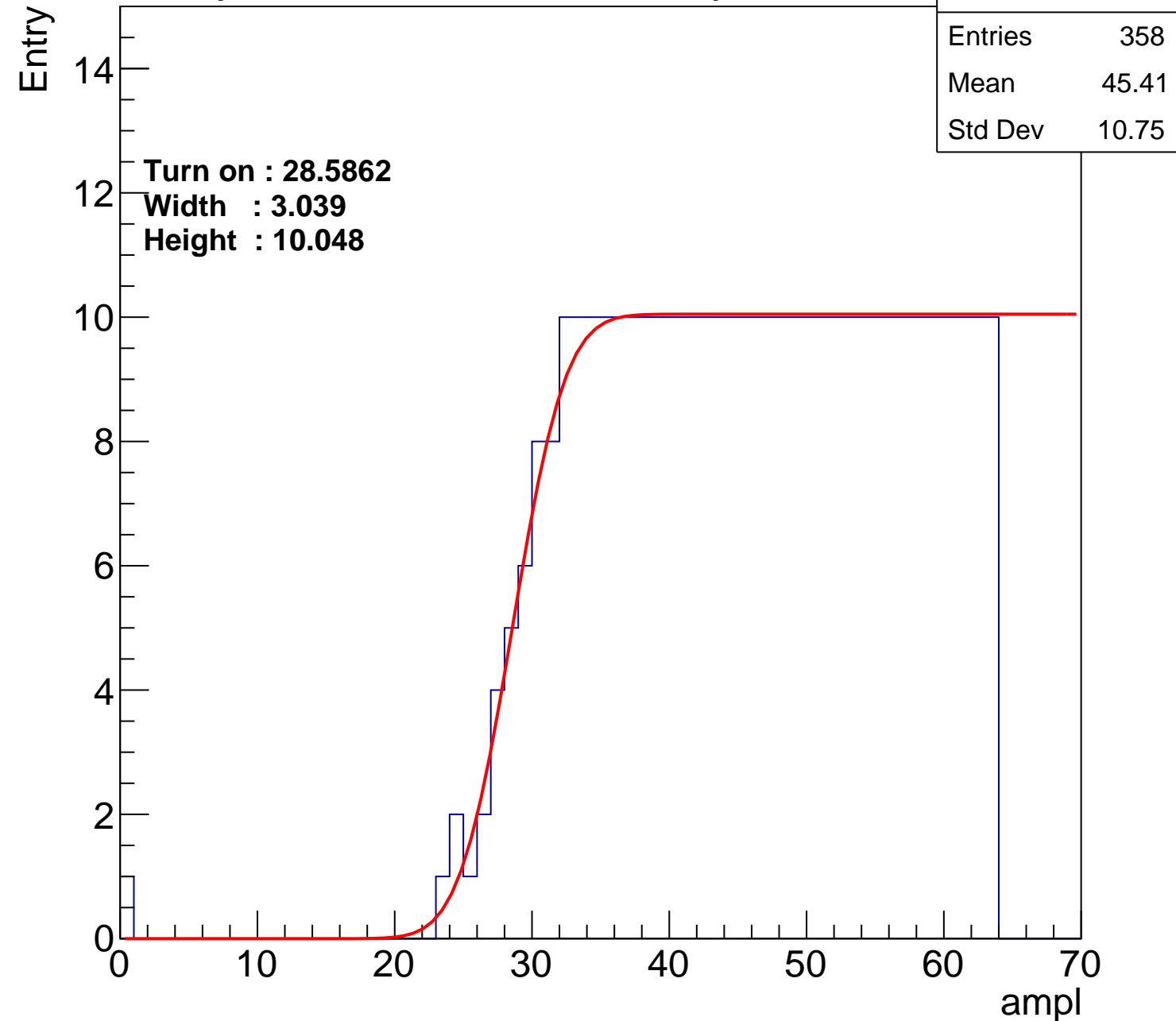
Width : 3.039

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.54
Std Dev	11.16

Turn on : 26.7679

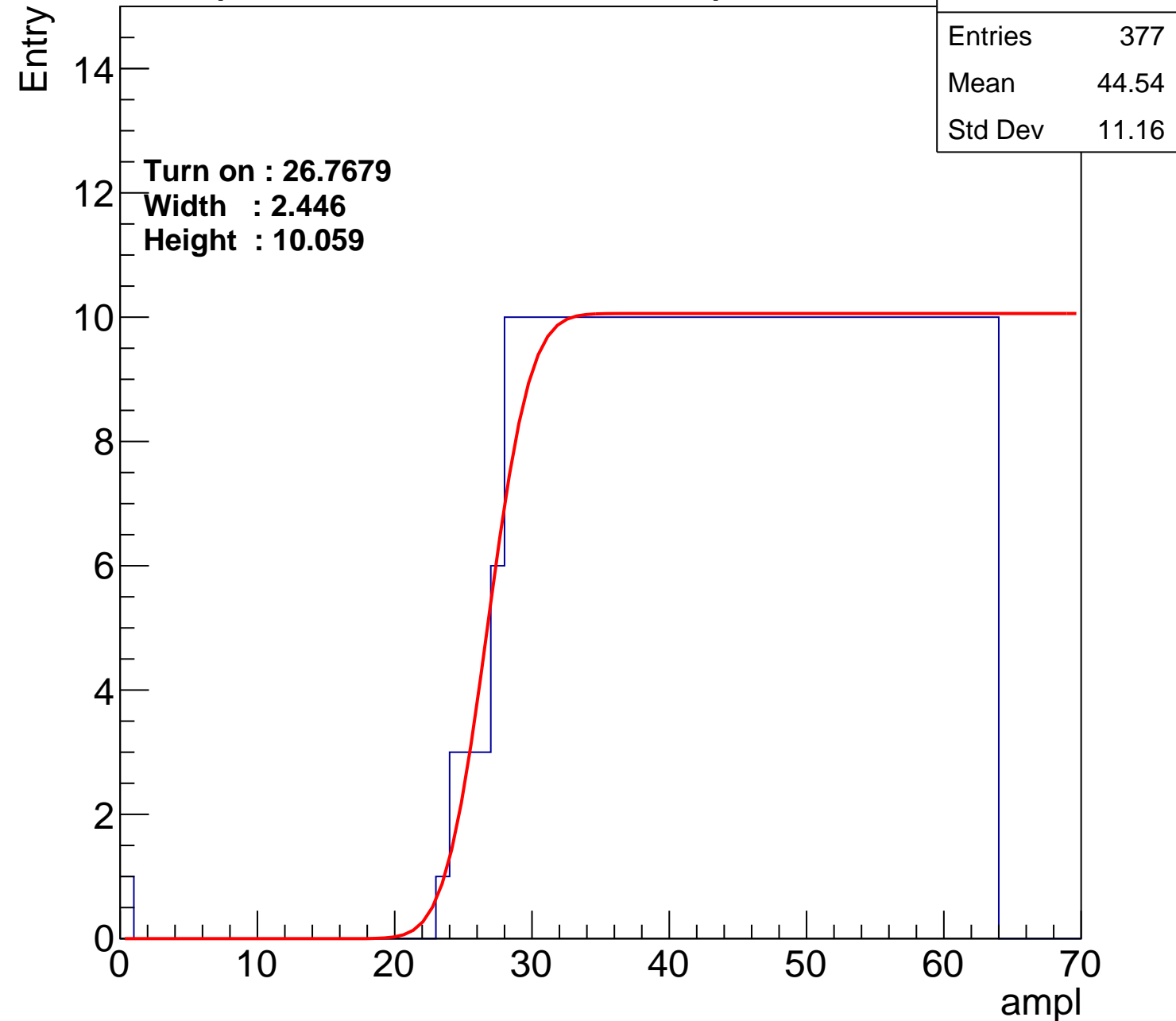
Width : 2.446

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.13
Std Dev	11.74

Turn on : 26.0866

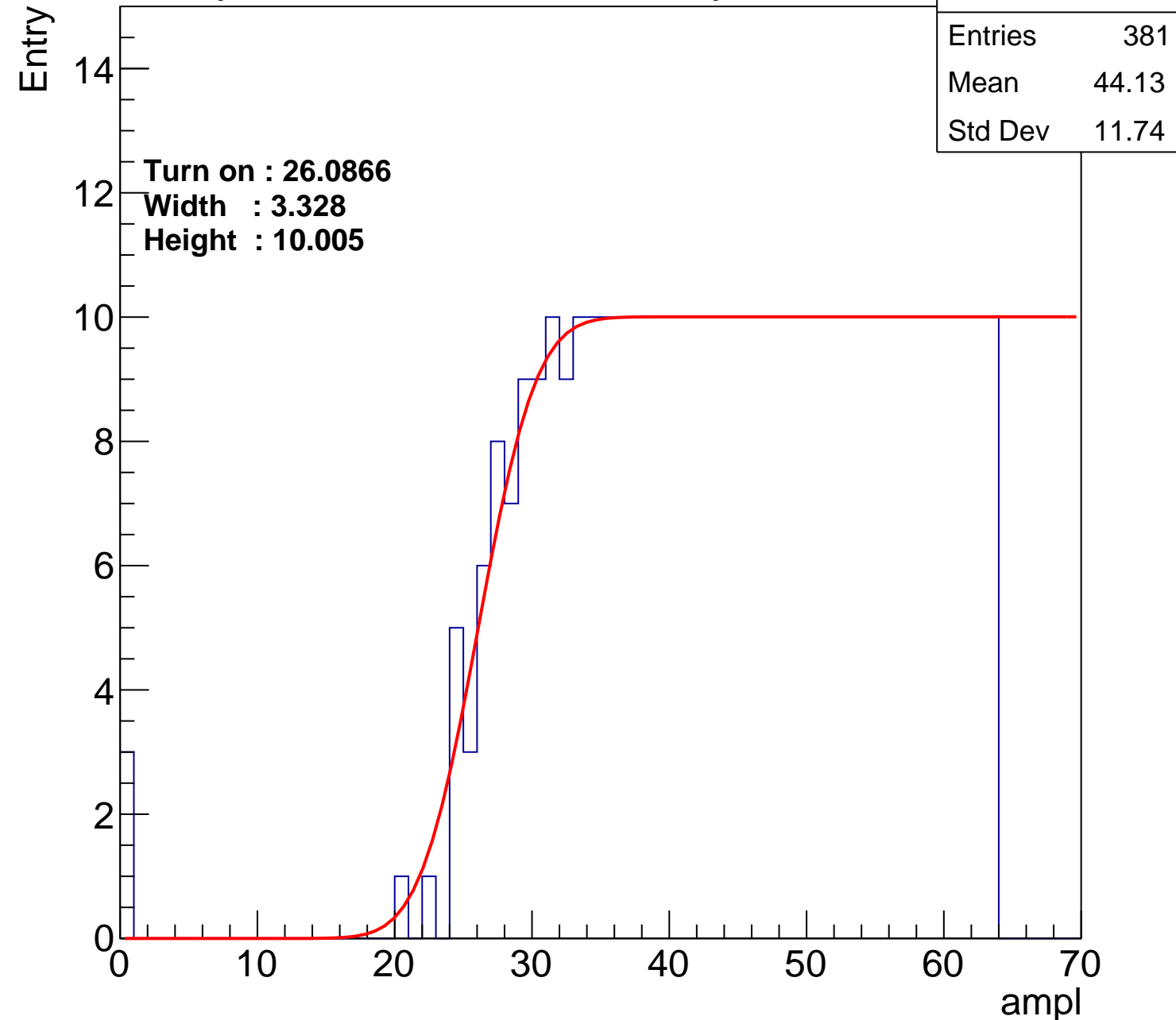
Width : 3.328

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.84
Std Dev	11.51

Turn on : 28.0412

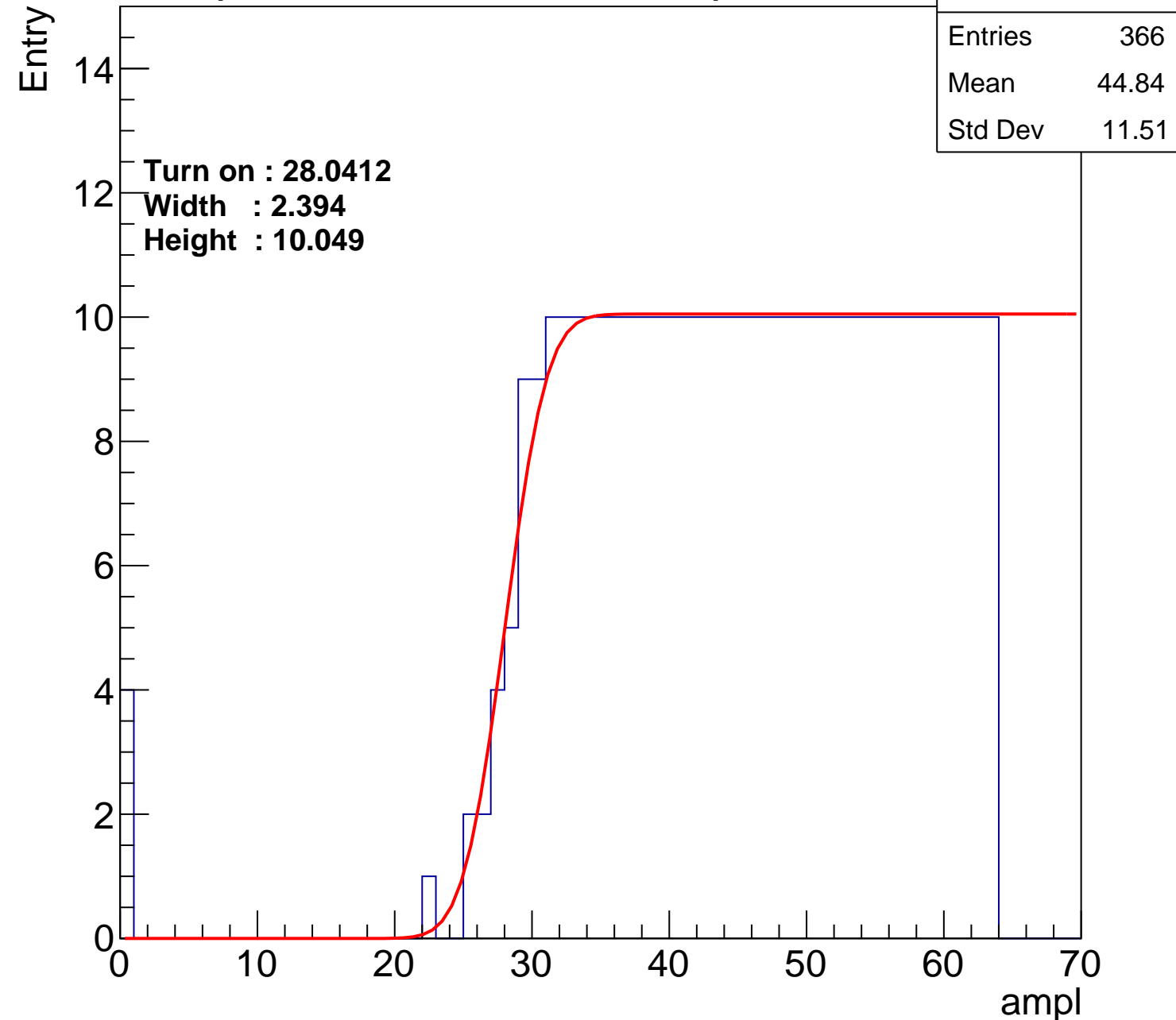
Width : 2.394

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

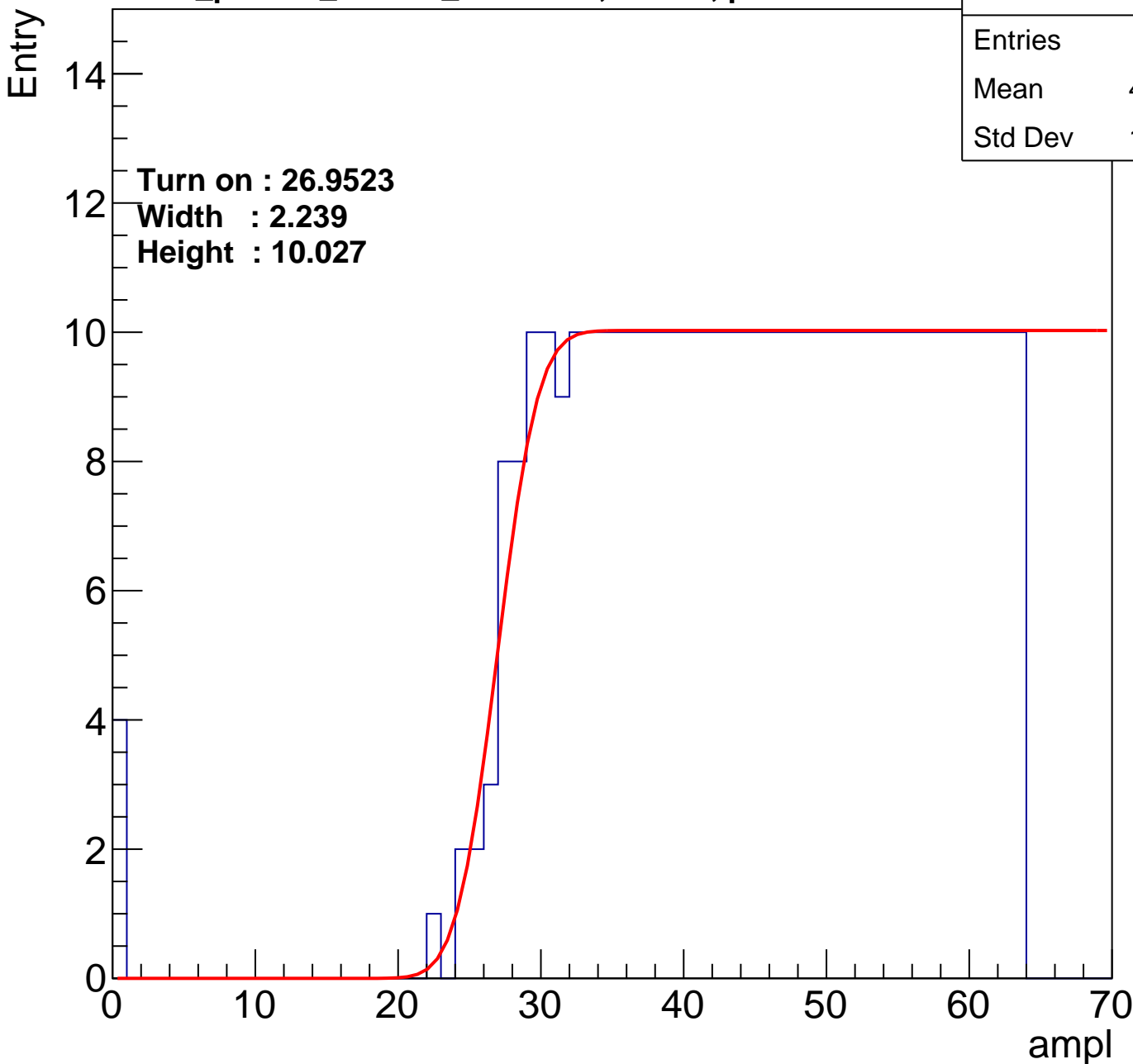


calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.32
Std Dev	11.74

Height : 10.027



B0L102S, U25-ch16

calib_packv5_042523_0143.root, FC#12, port B1

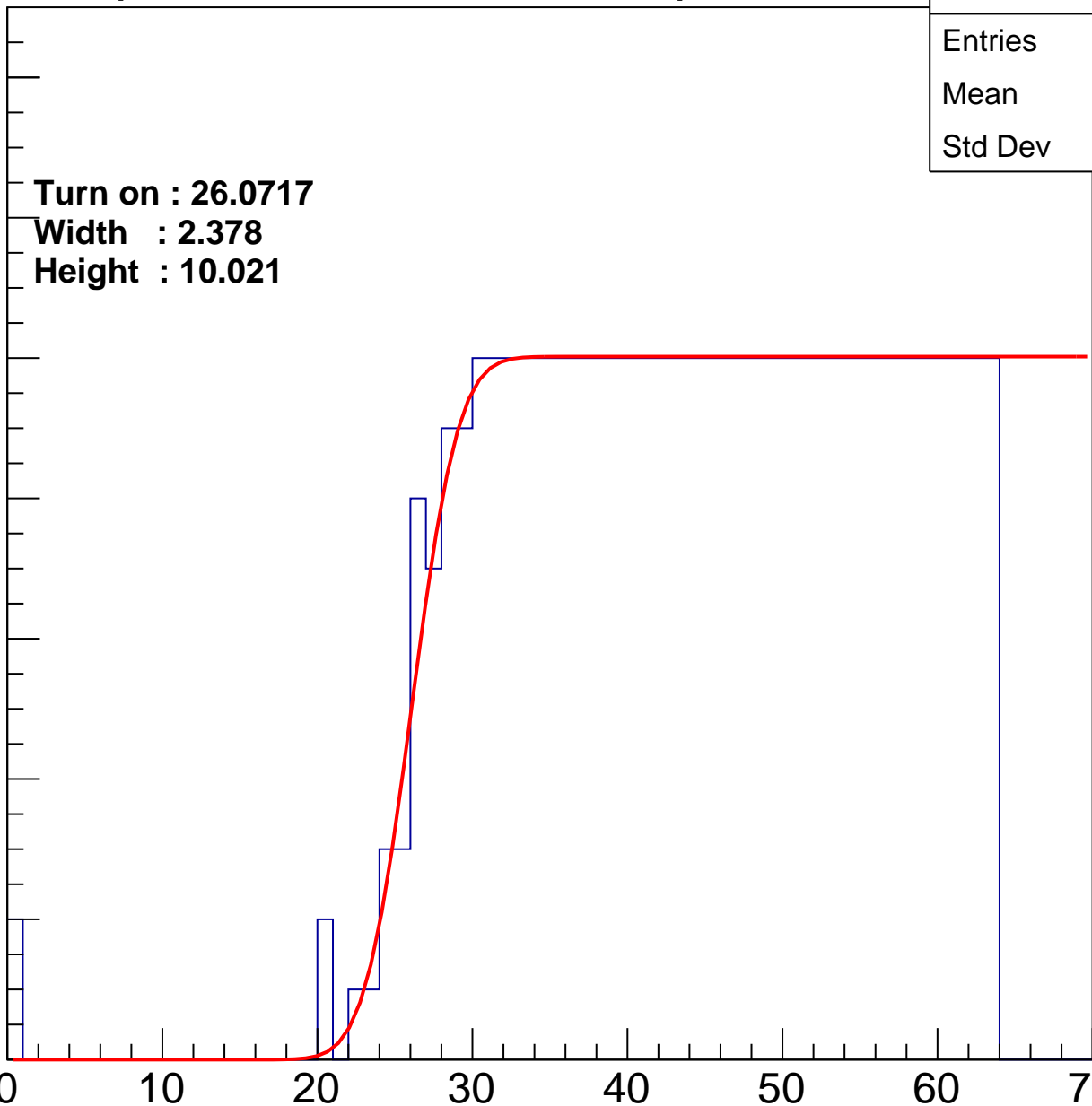
Entry

14
12
10
8
6
4
2
0

Turn on : 26.0717
Width : 2.378
Height : 10.021

Entries	385
Mean	44.03
Std Dev	11.62

ampl



B0L102S, U25-ch17

calib_packv5_042523_0143.root, FC#12, port B1

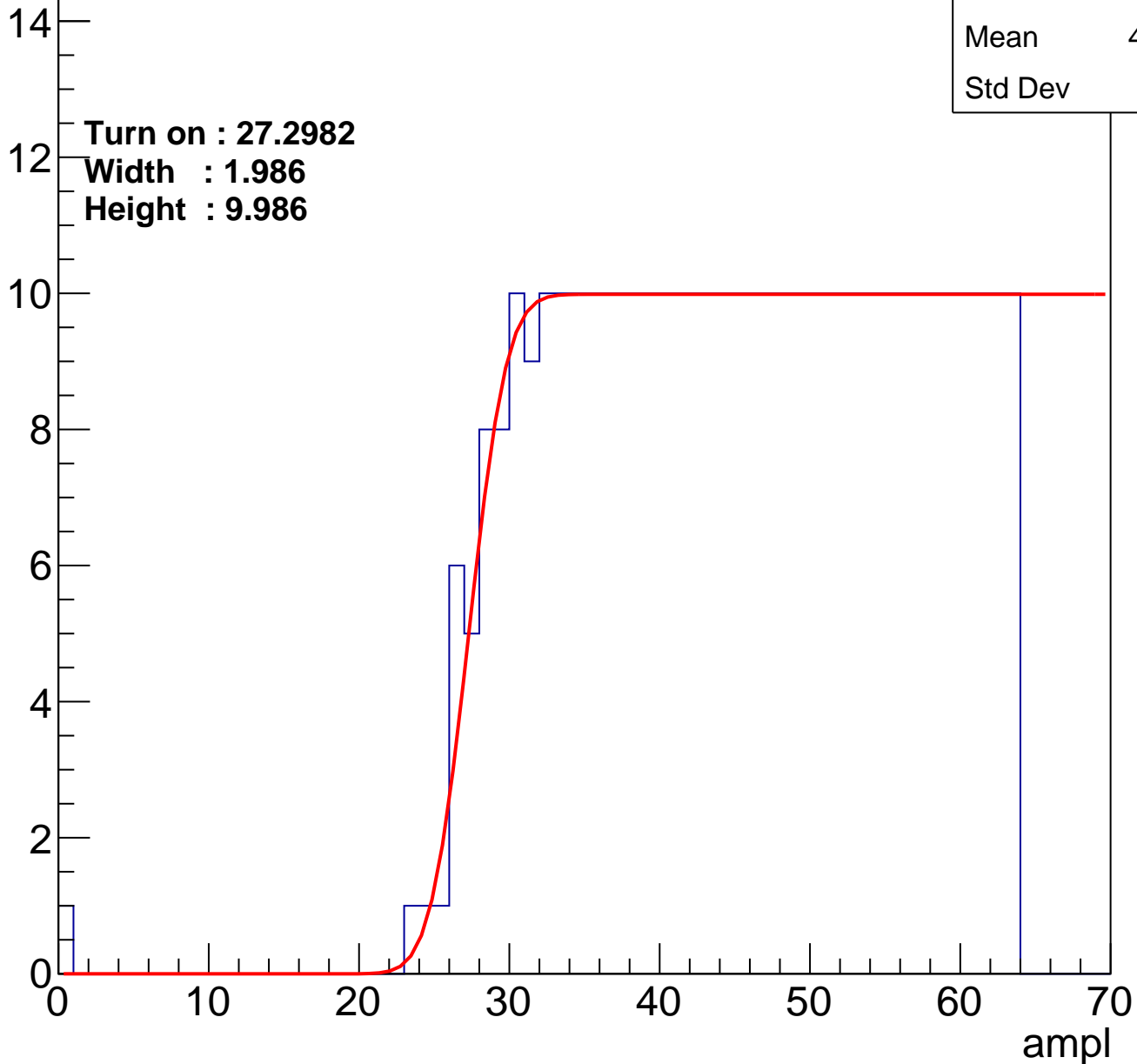
Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.2982

Width : 1.986

Height : 9.986

Entry



B0L102S, U25-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.2228

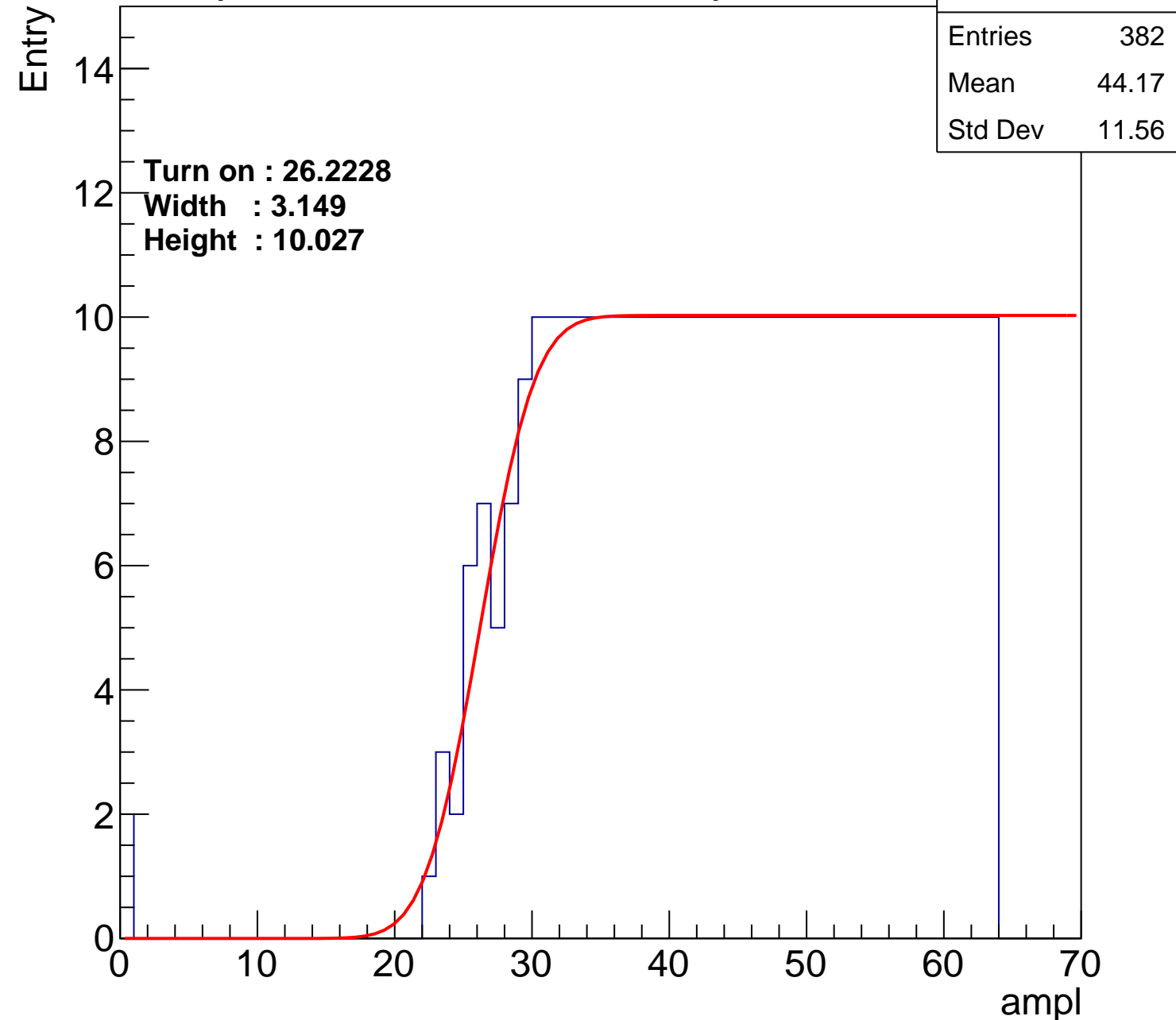
Width : 3.149

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch19

calib_packv5_042523_0143.root, FC#12, port B1

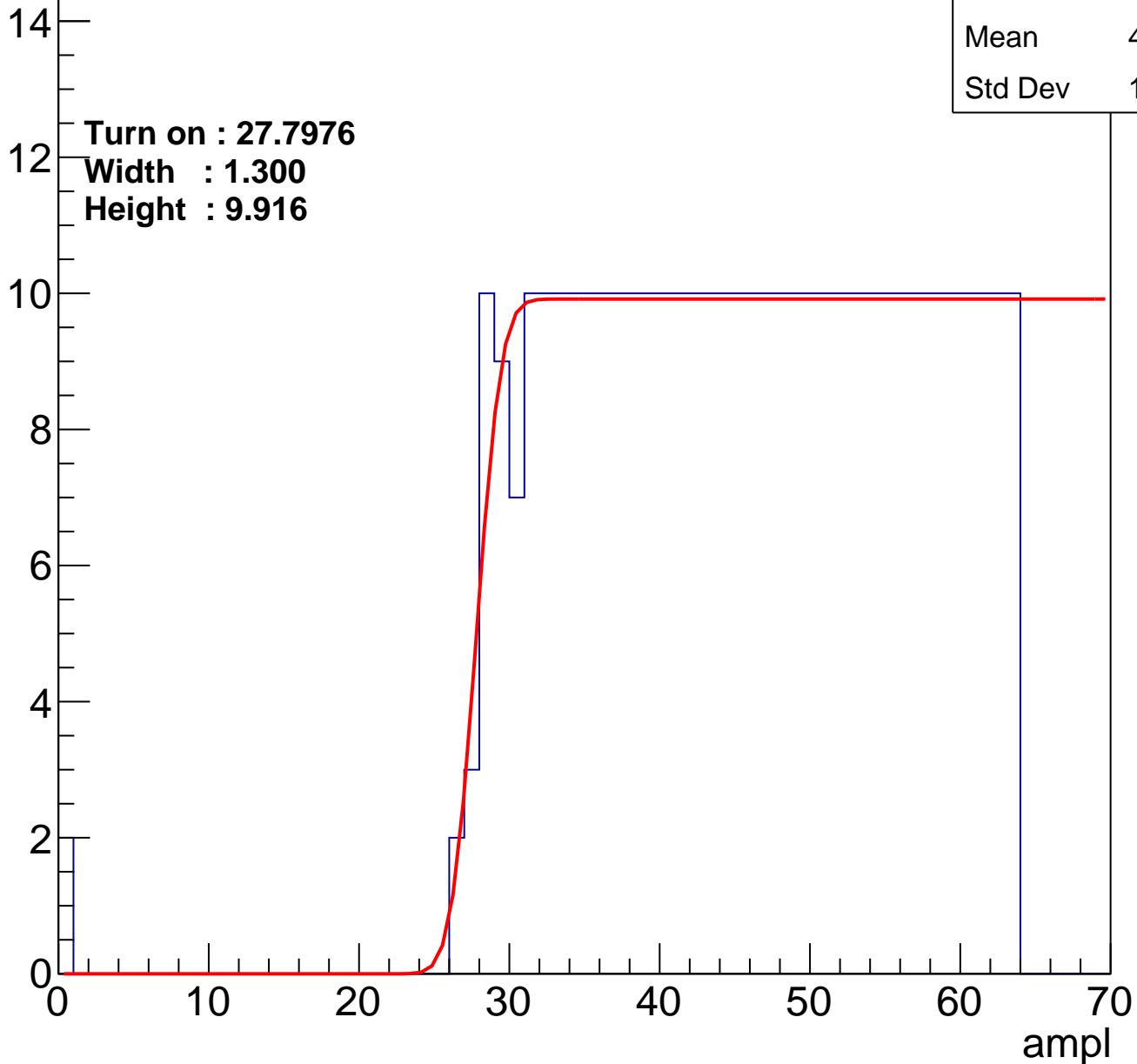
Entries	363
Mean	45.16
Std Dev	10.98

Turn on : 27.7976

Width : 1.300

Height : 9.916

Entry



B0L102S, U25-ch20

calib_packv5_042523_0143.root, FC#12, port B1

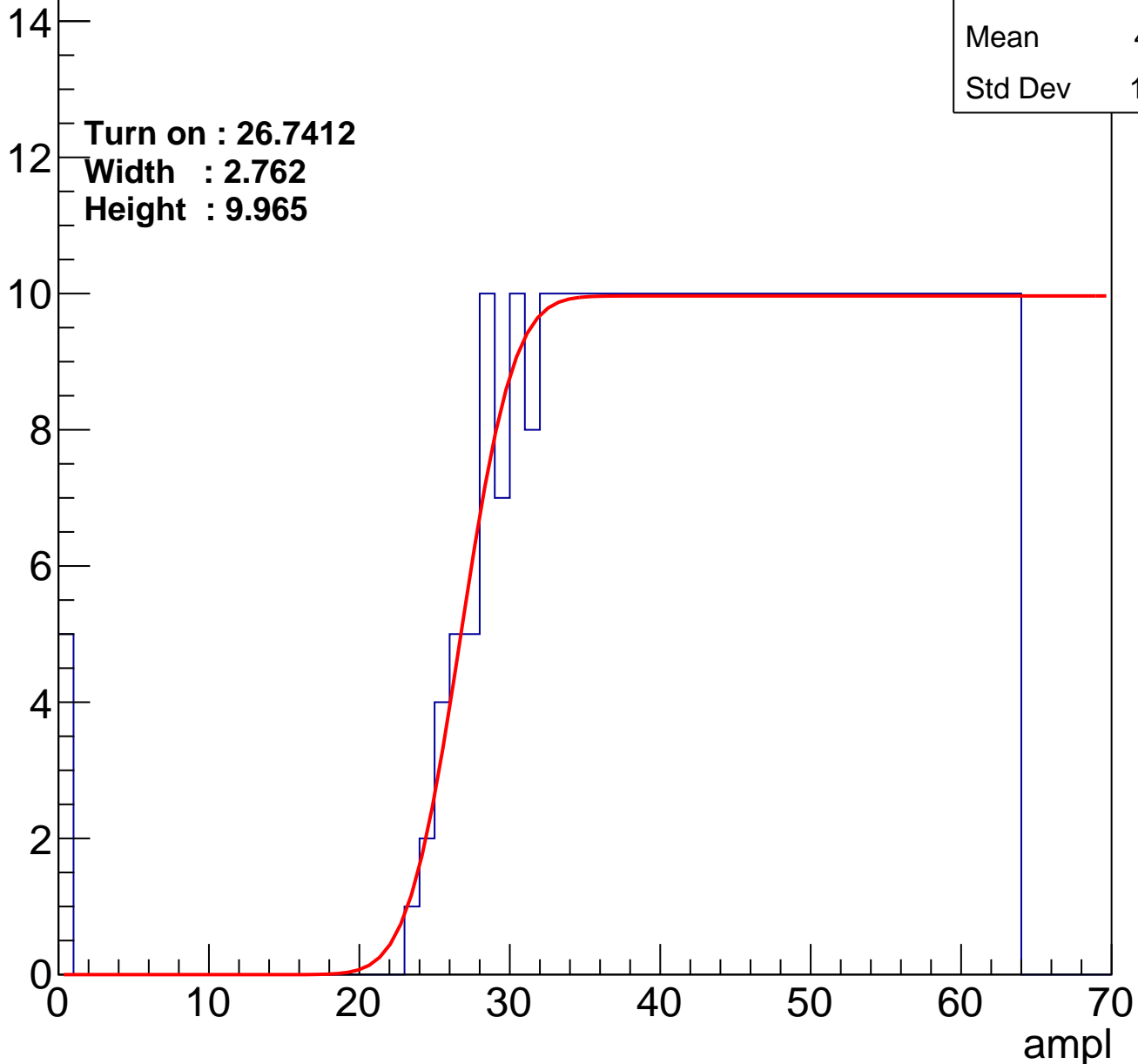
Entries	377
Mean	44.21
Std Dev	11.97

Turn on : 26.7412

Width : 2.762

Height : 9.965

Entry



B0L102S, U25-ch21

calib_packv5_042523_0143.root, FC#12, port B1

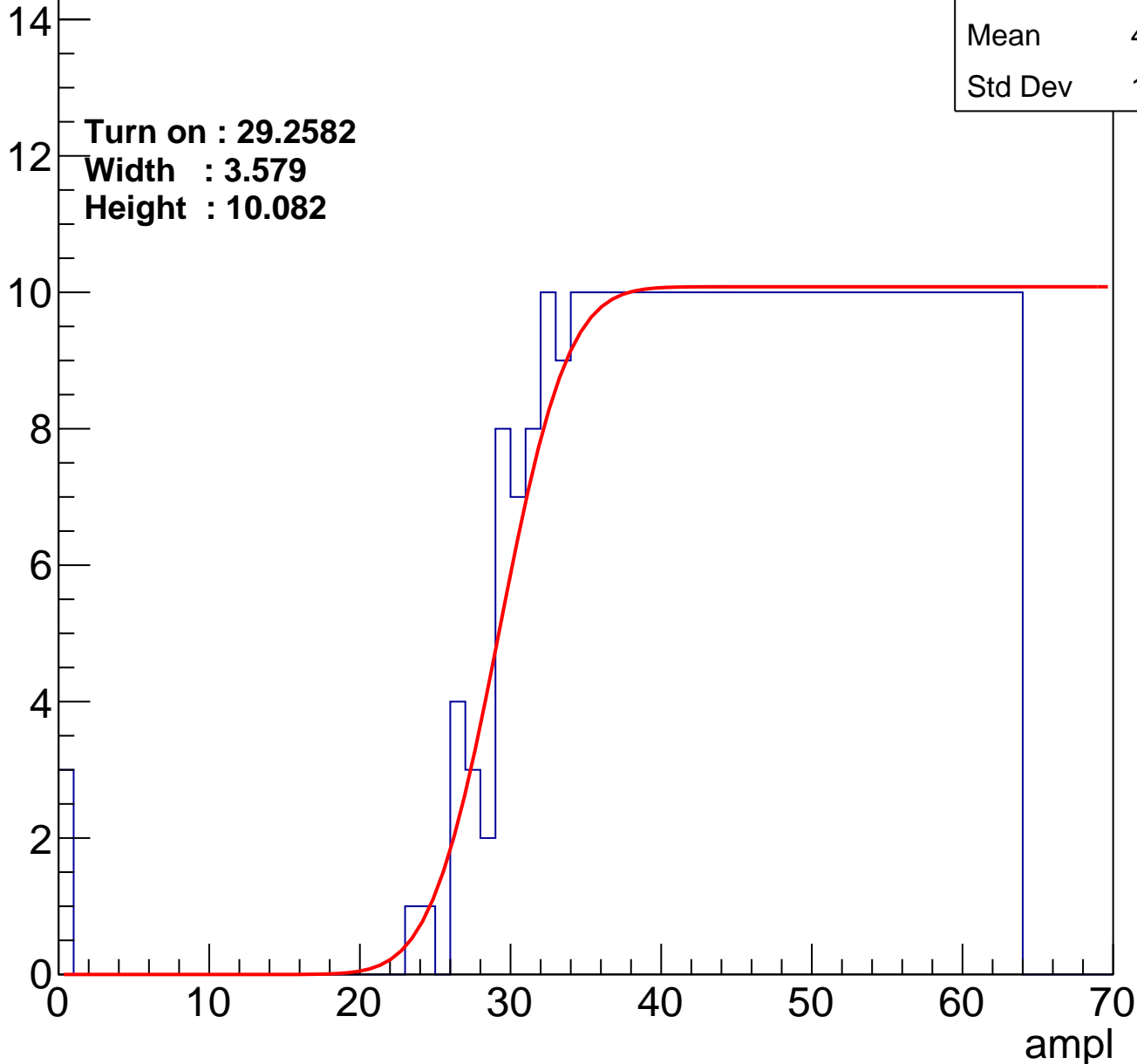
Entries	356
Mean	45.35
Std Dev	11.15

Turn on : 29.2582

Width : 3.579

Height : 10.082

Entry



B0L102S, U25-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.88
Std Dev	11.16

Turn on : 27.4916

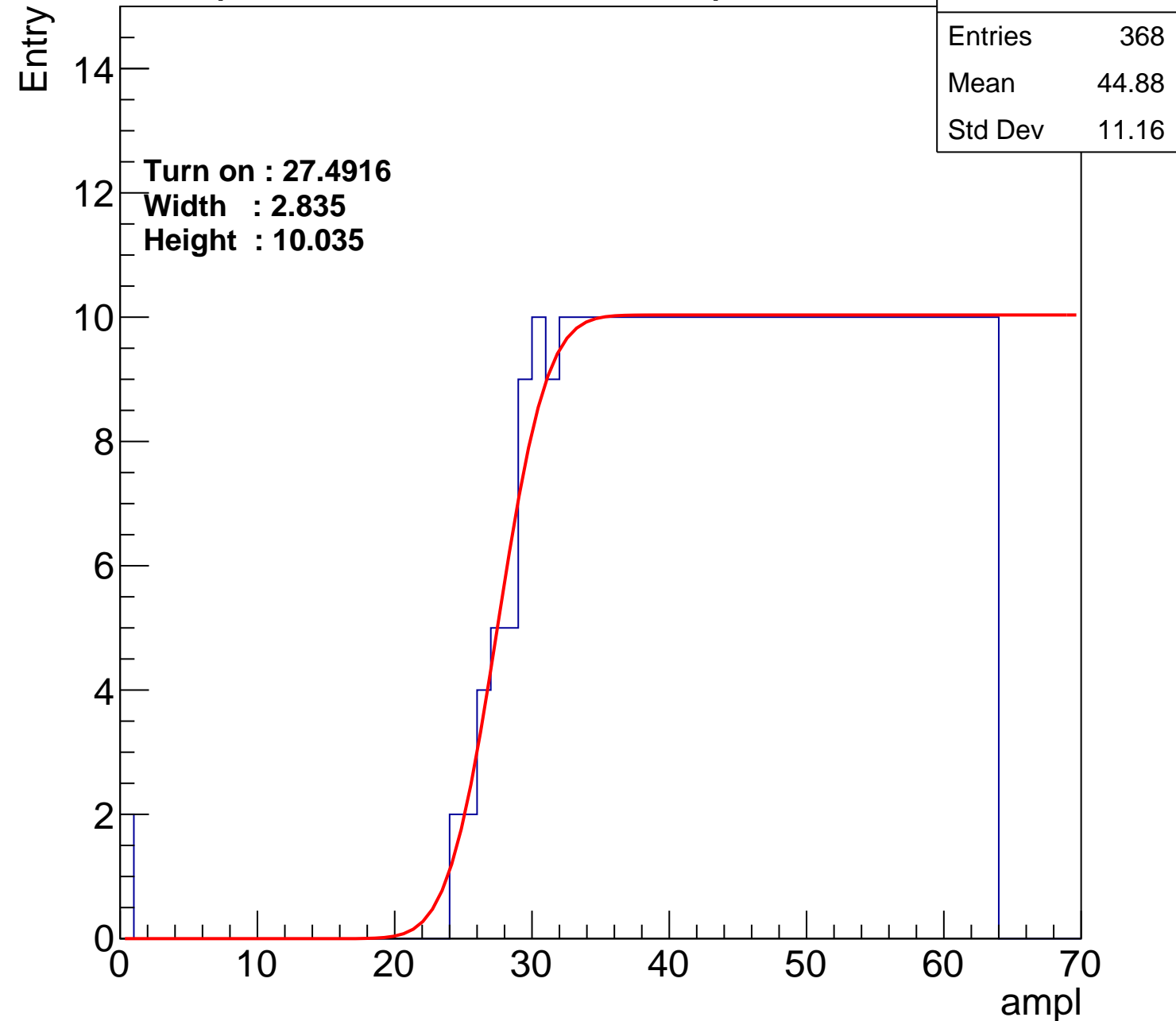
Width : 2.835

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.45
Std Dev	11.39

Turn on : 26.7562

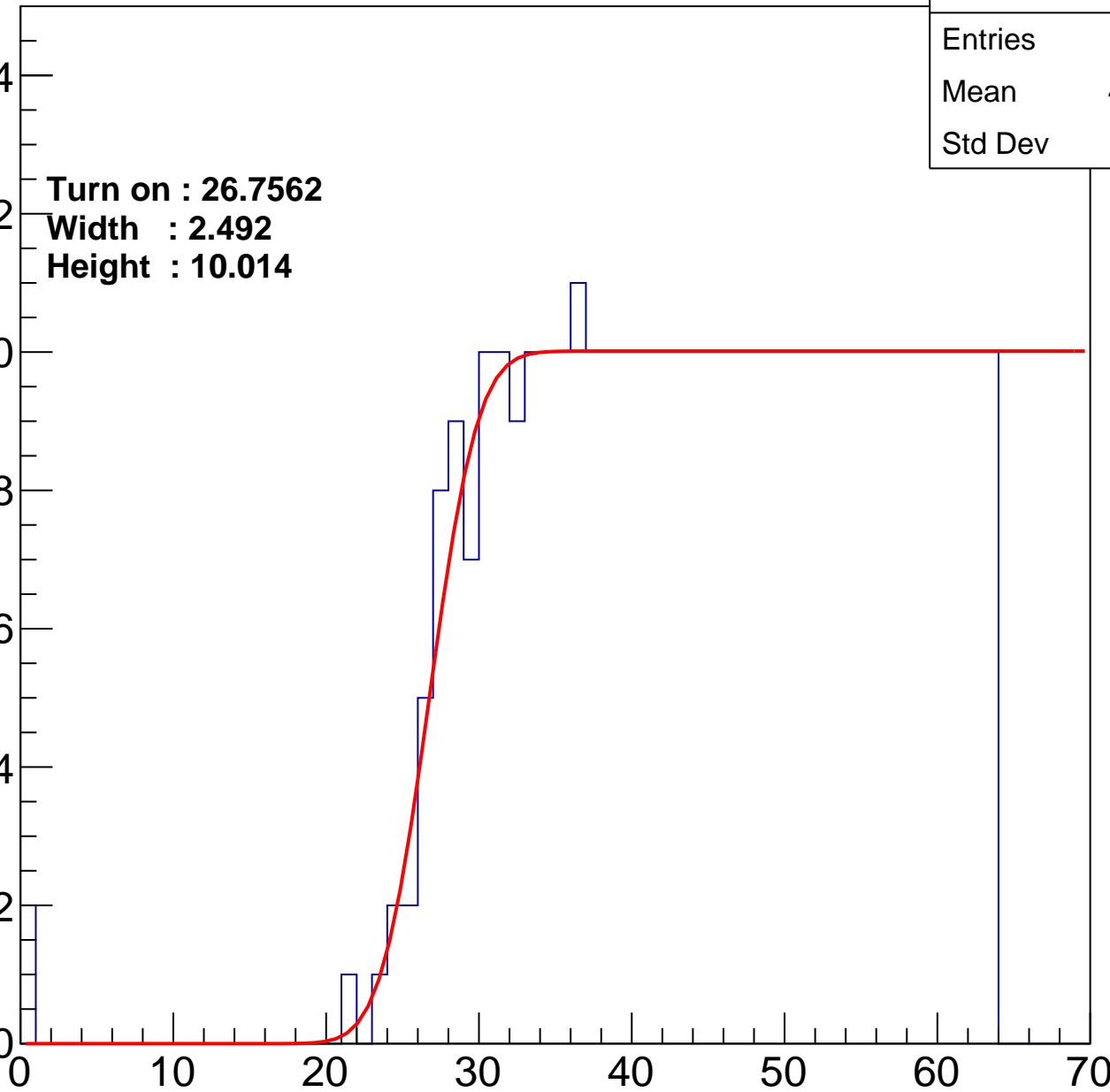
Width : 2.492

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.83
Std Dev	12.04

Turn on : 25.8171

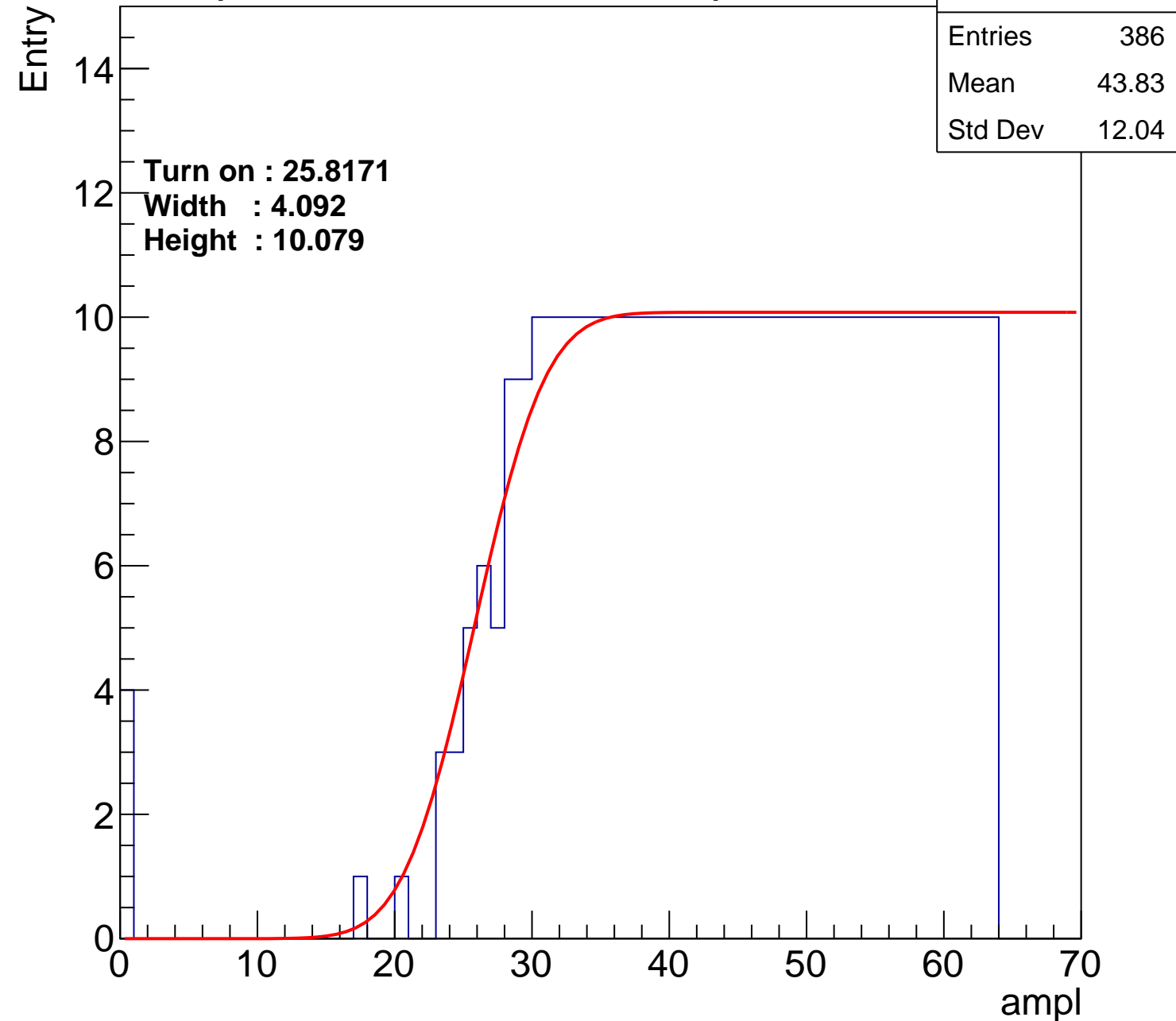
Width : 4.092

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.44
Std Dev	10.95

Turn on : 28.7164

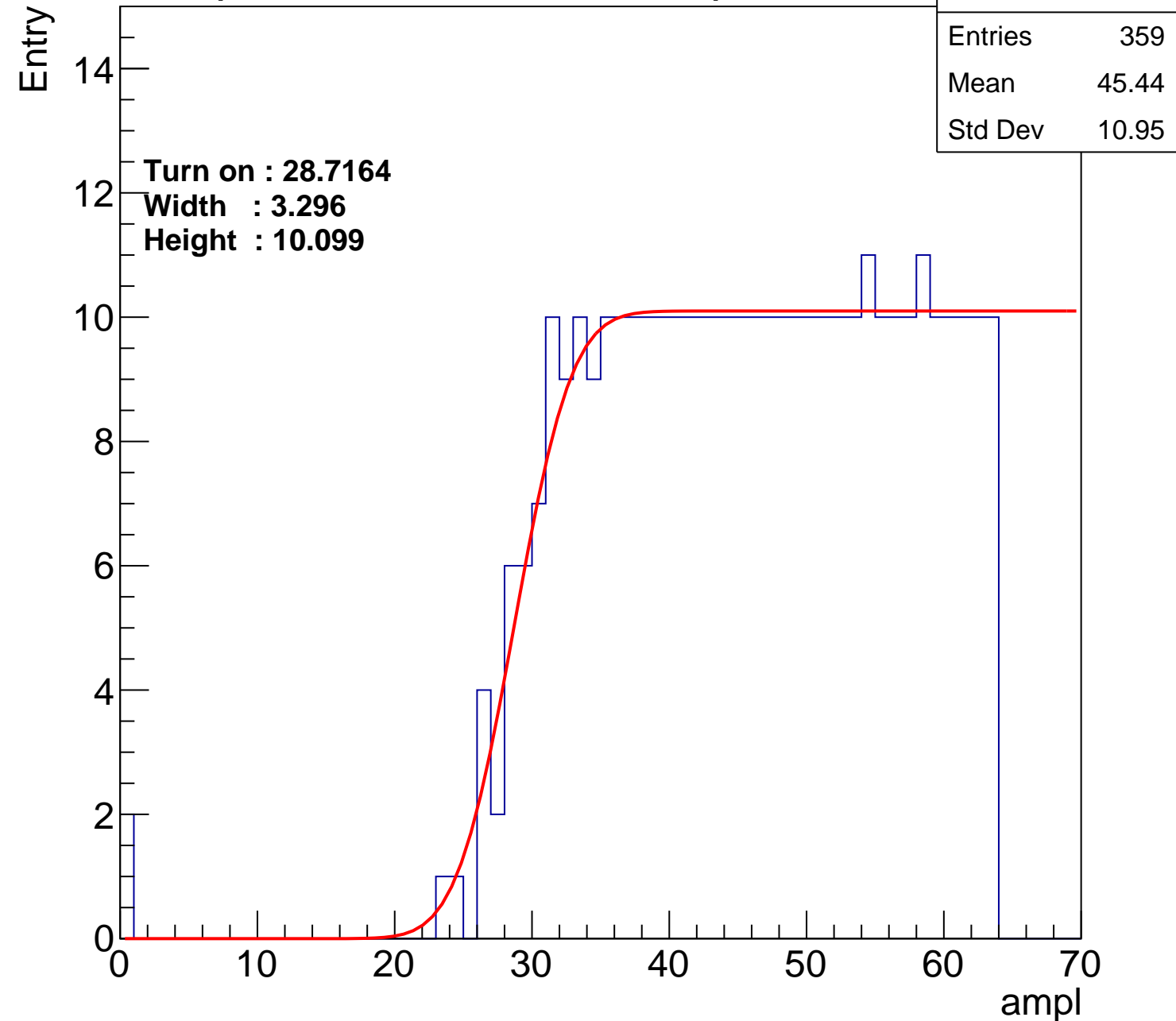
Width : 3.296

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.51
Std Dev	11.52

Turn on : 27.5094

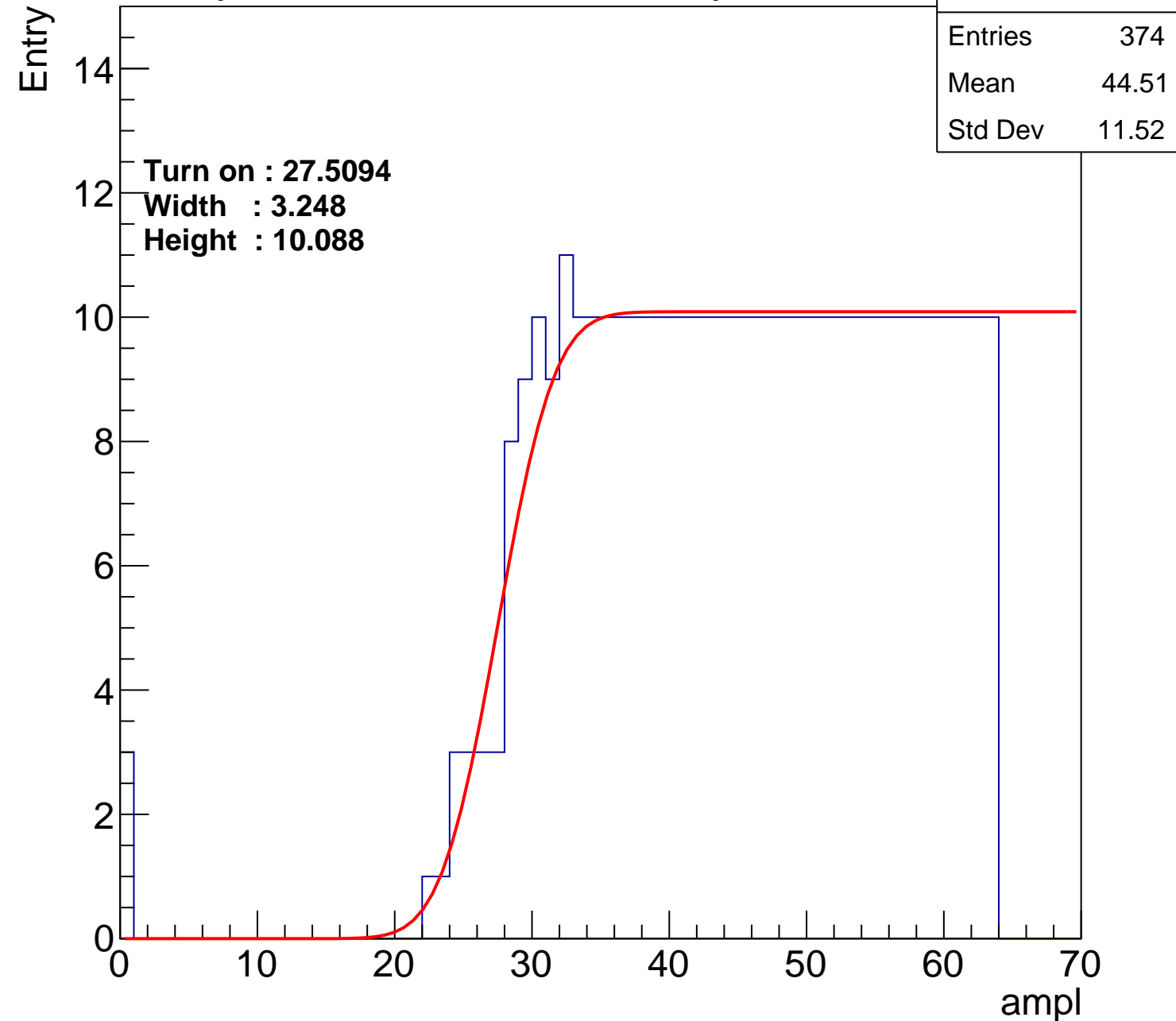
Width : 3.248

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 26.5346

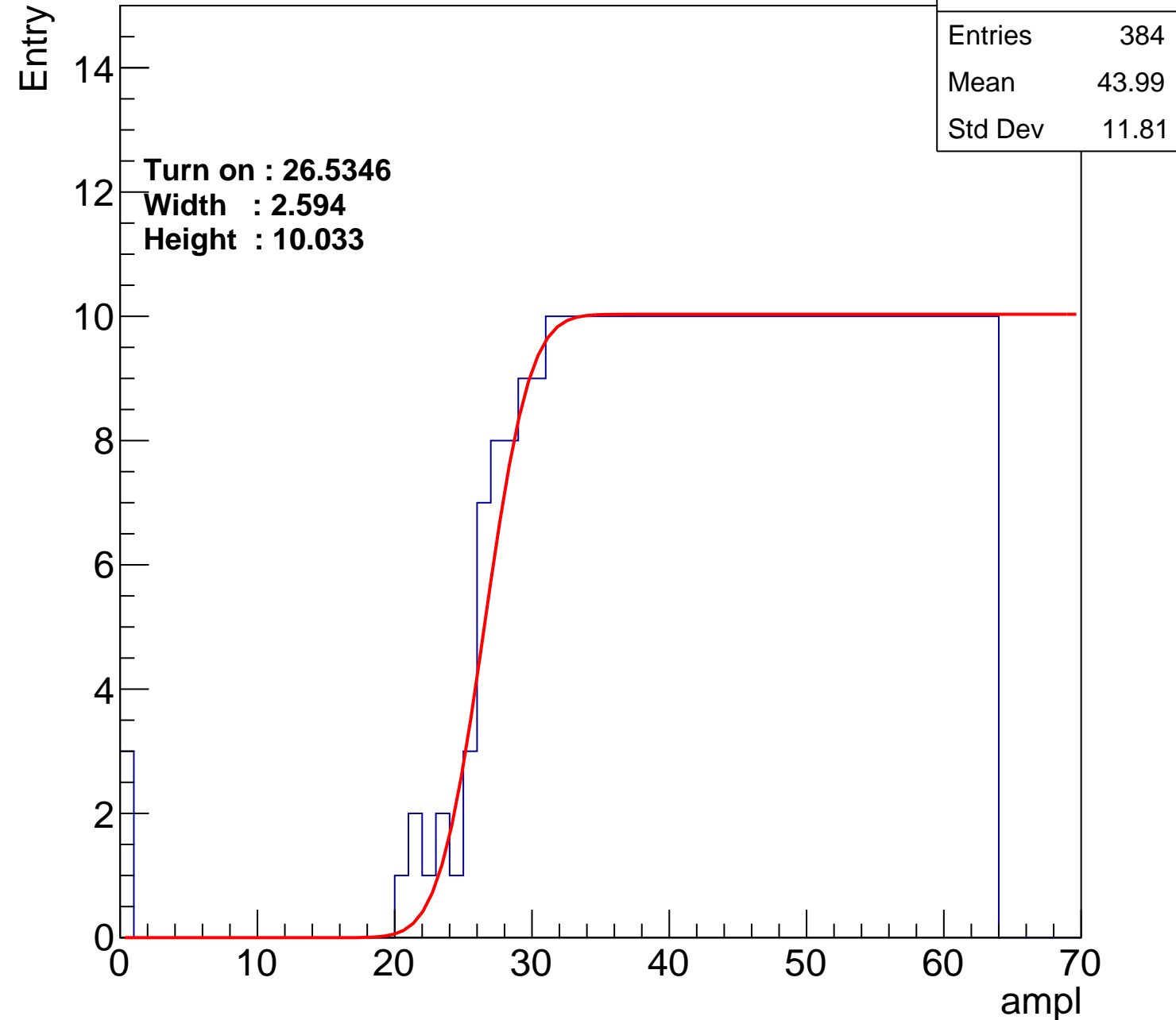
Width : 2.594

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.11
Std Dev	11.84

Turn on : 26.3352

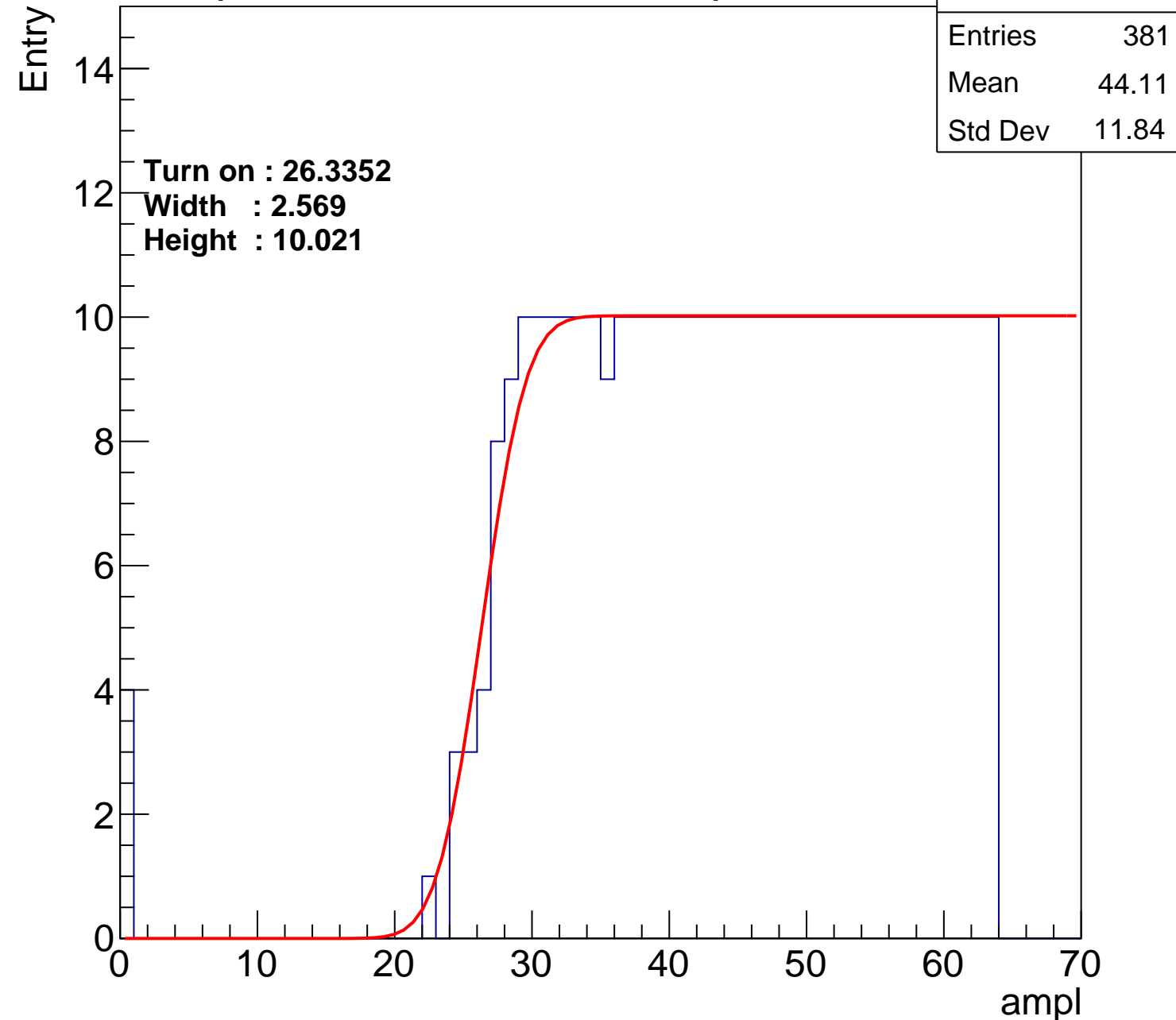
Width : 2.569

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch29

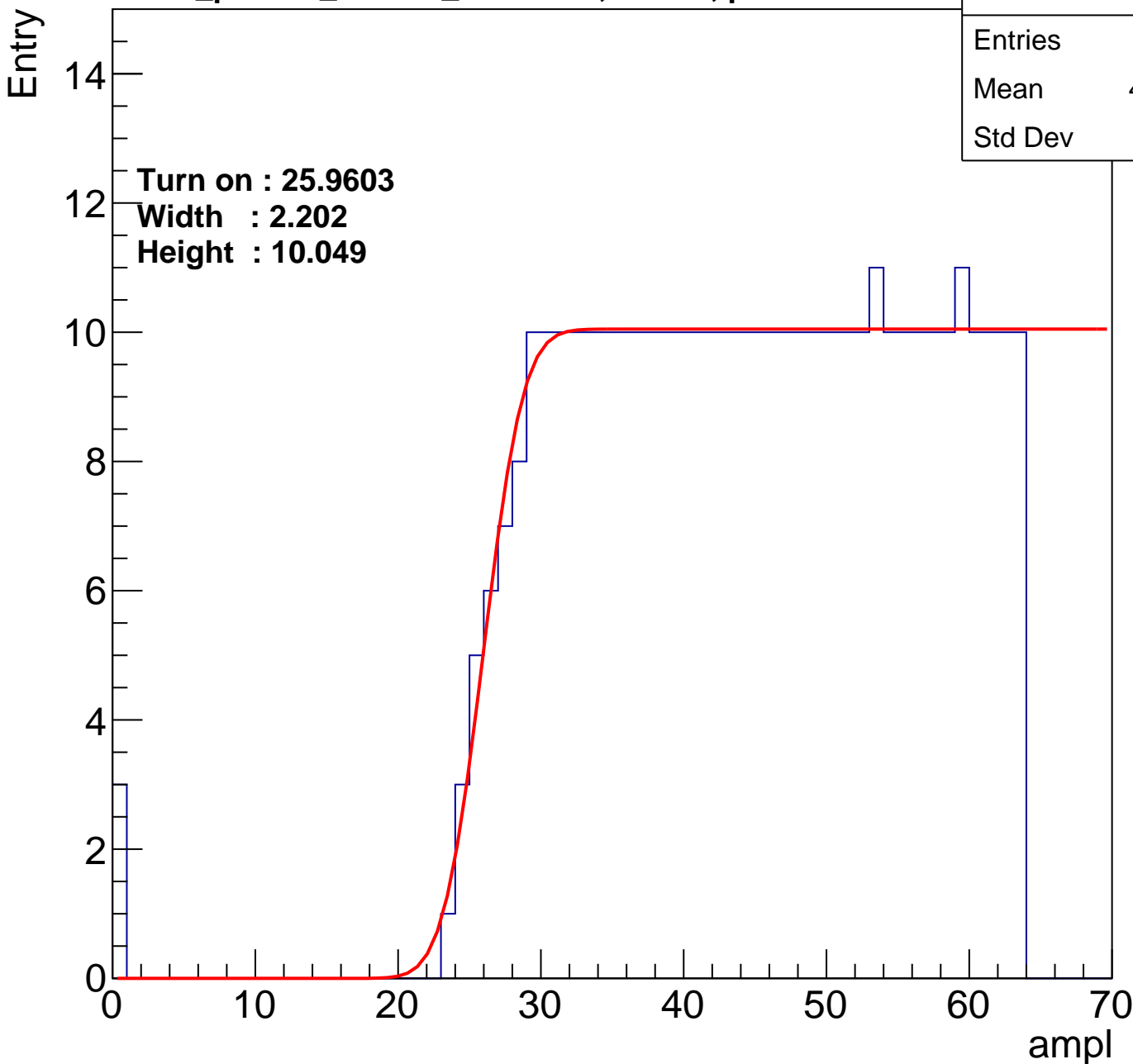
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.9603

Width : 2.202

Height : 10.049

Entries	385
Mean	44.16
Std Dev	11.7



B0L102S, U25-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.01
Std Dev	11.79

Turn on : 26.1759

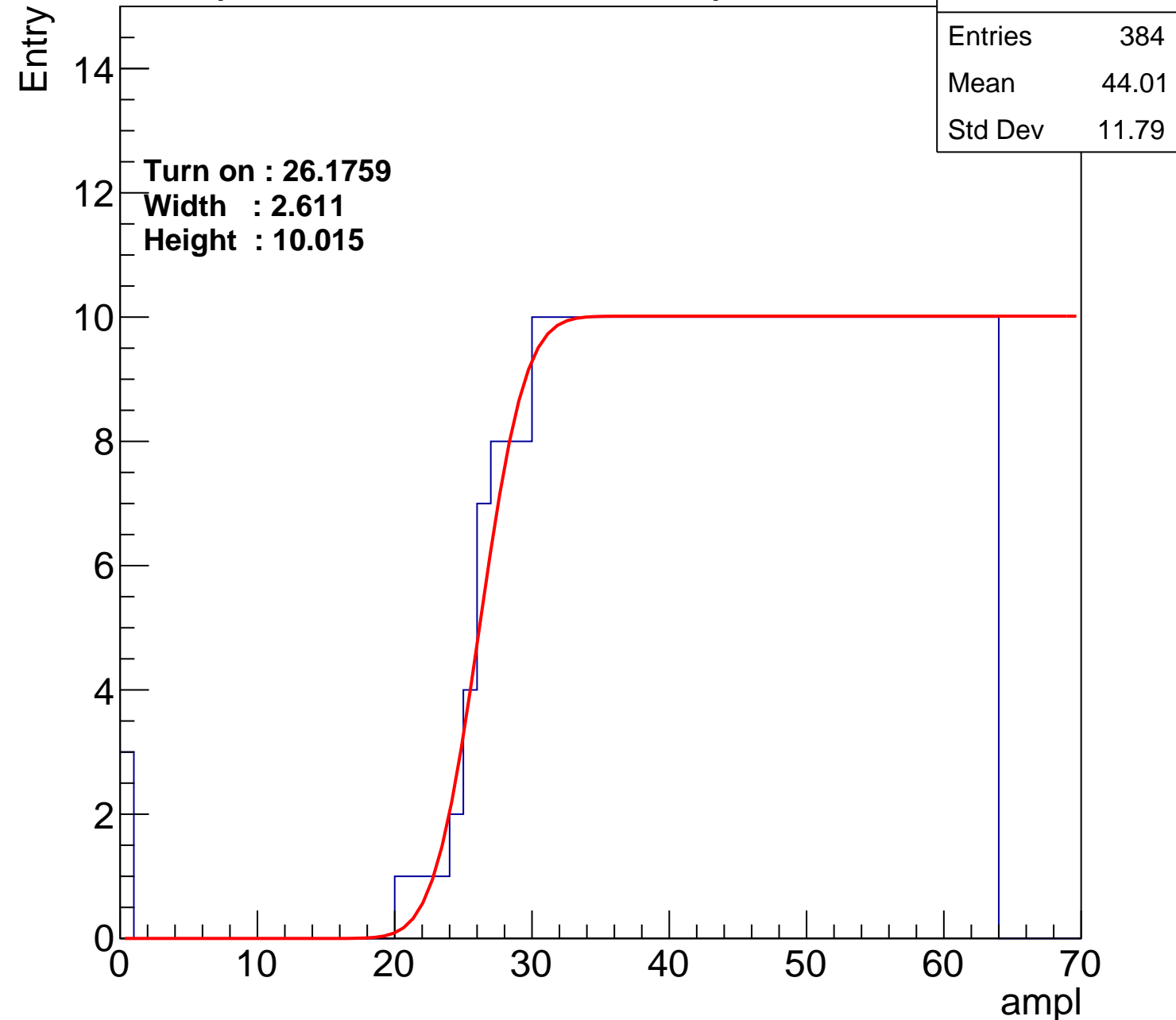
Width : 2.611

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.57
Std Dev	11.47

Turn on : 27.3766

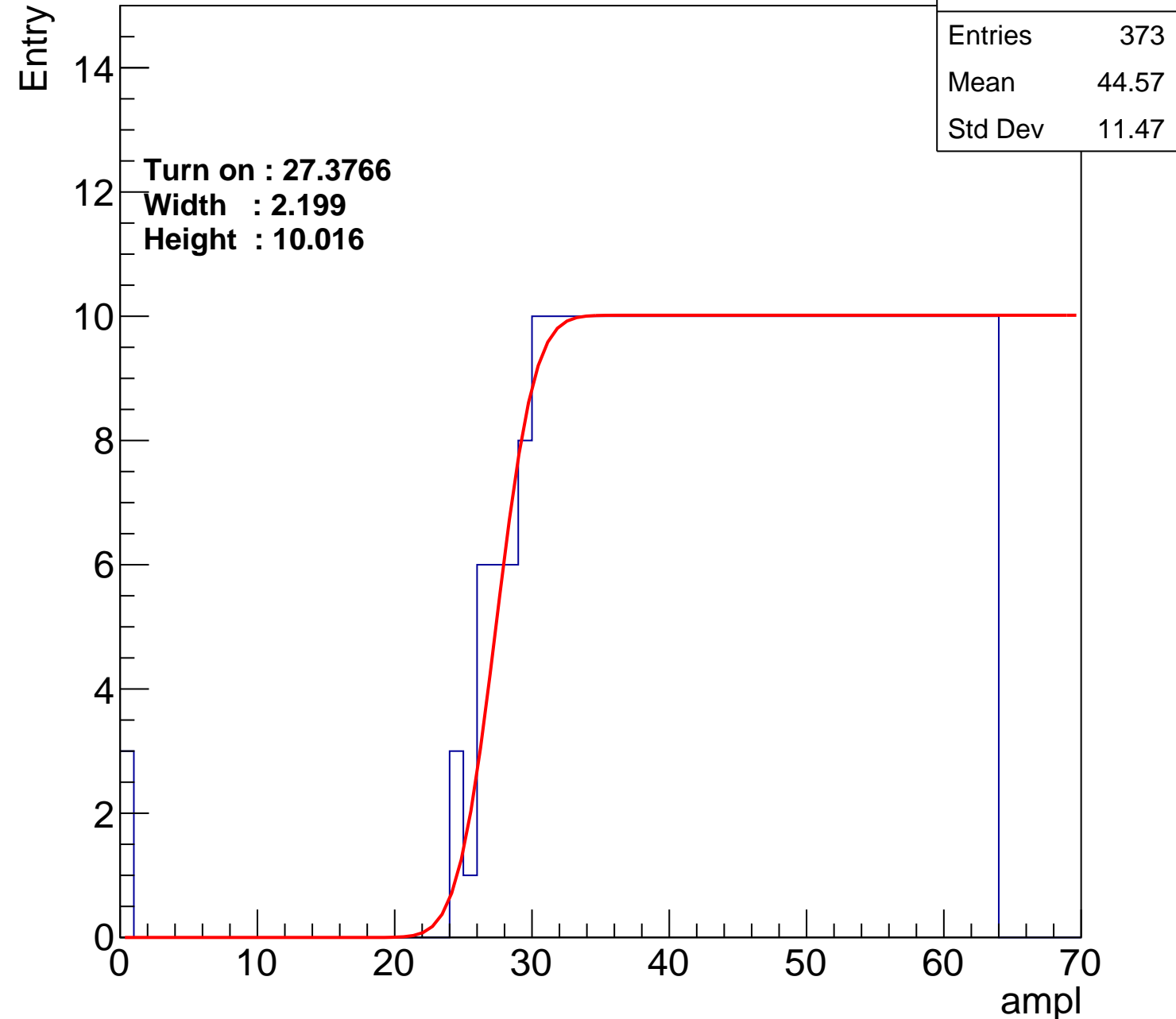
Width : 2.199

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.08
Std Dev	11.32

Turn on : 29.3428

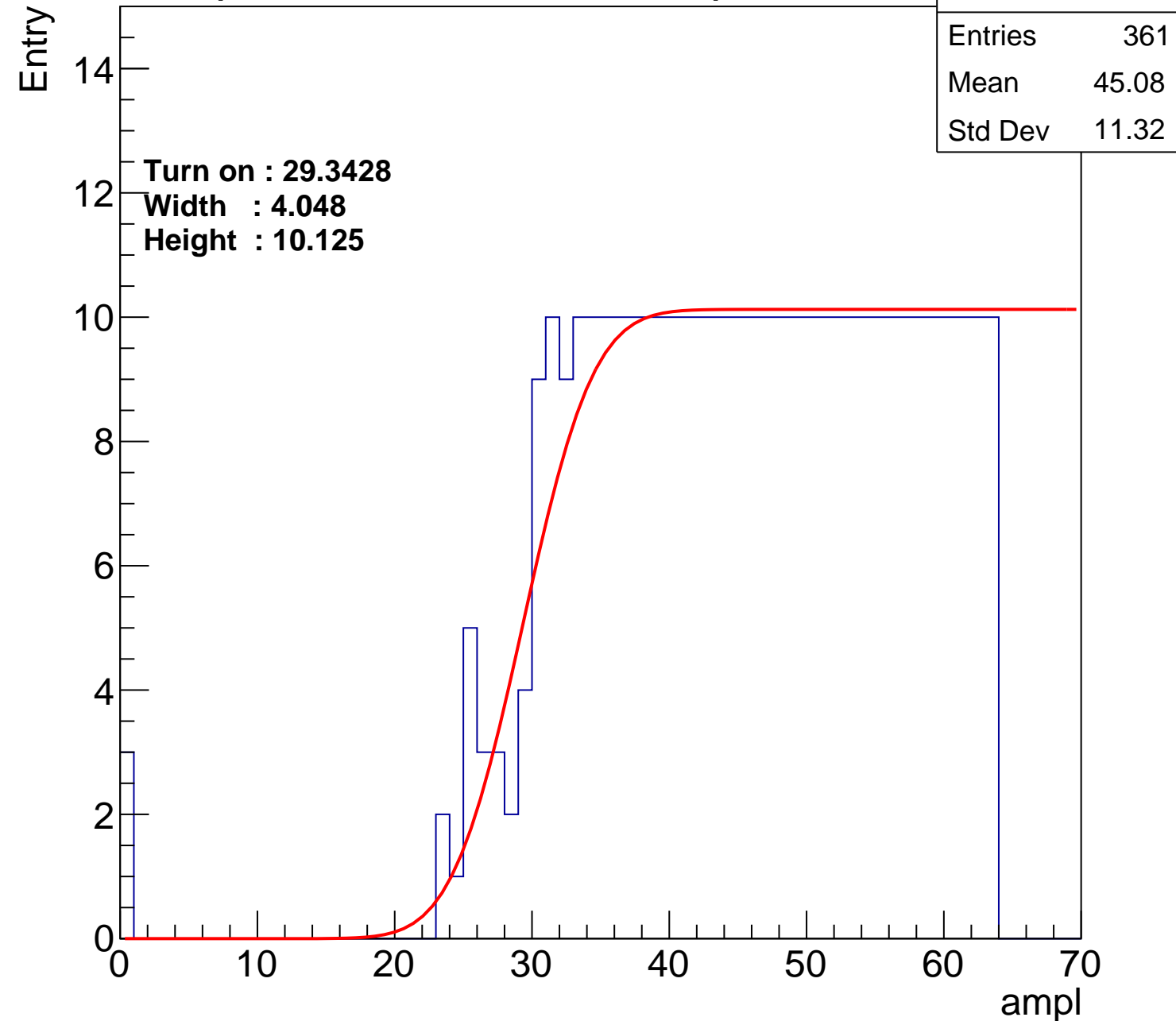
Width : 4.048

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.12
Std Dev	11.06

Turn on : 28.2974

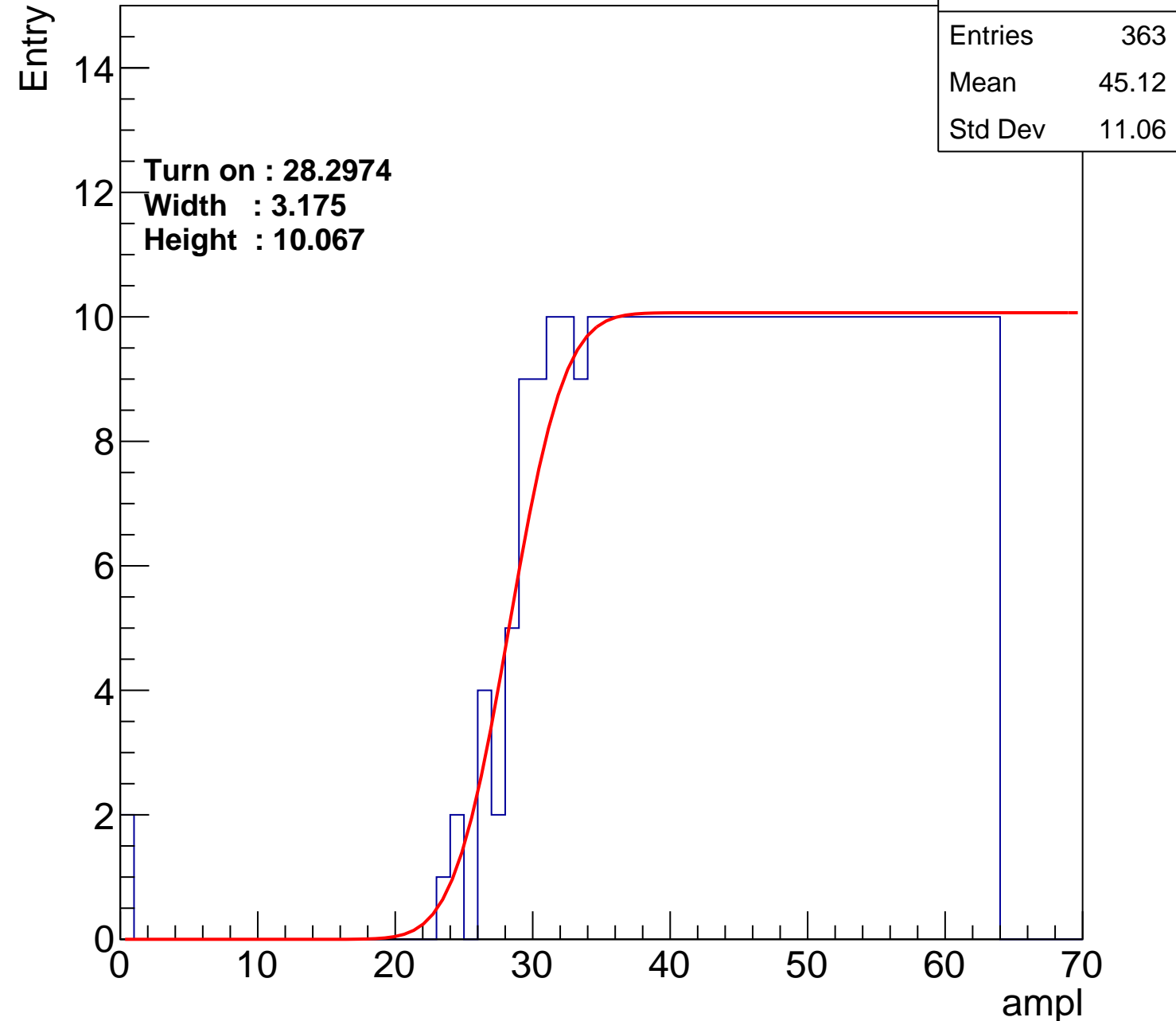
Width : 3.175

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch34

calib_packv5_042523_0143.root, FC#12, port B1

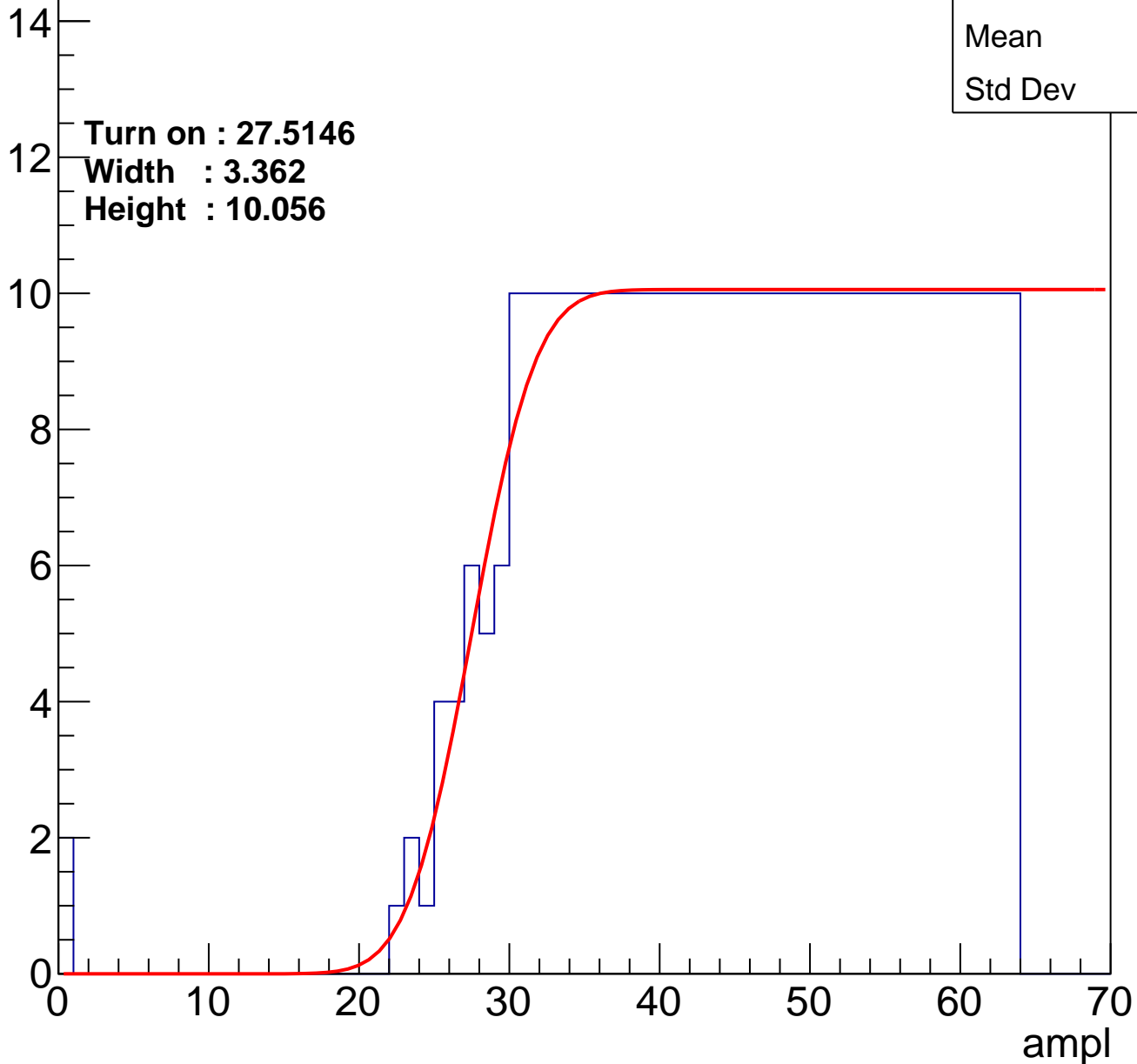
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.5146

Width : 3.362

Height : 10.056

Entry



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.4796
Width : 3.179
Height : 10.036



B0L102S, U25-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.47
Std Dev	11.66

Turn on : 26.6541

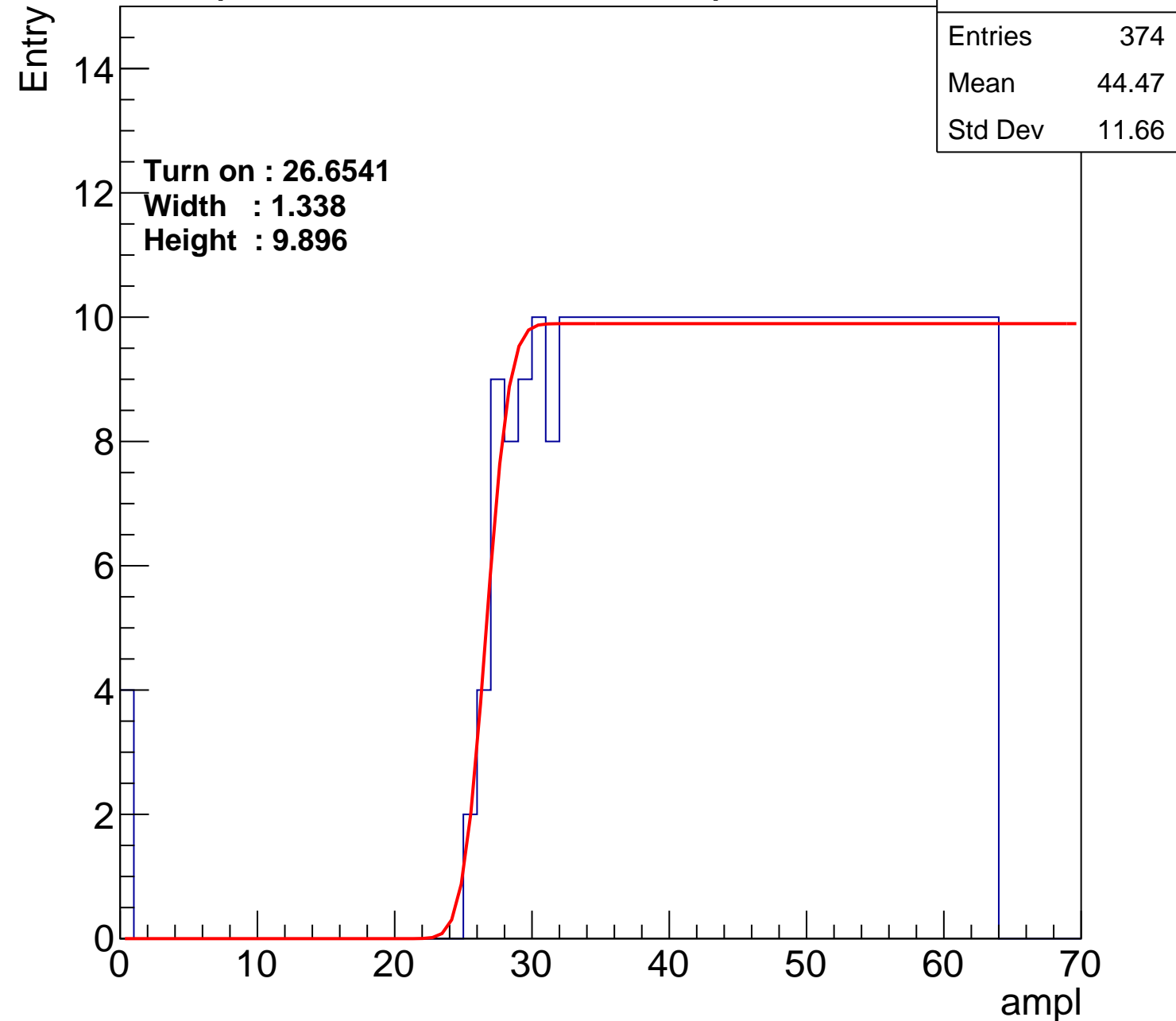
Width : 1.338

Height : 9.896

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.31
Std Dev	11.44

Turn on : 26.4152

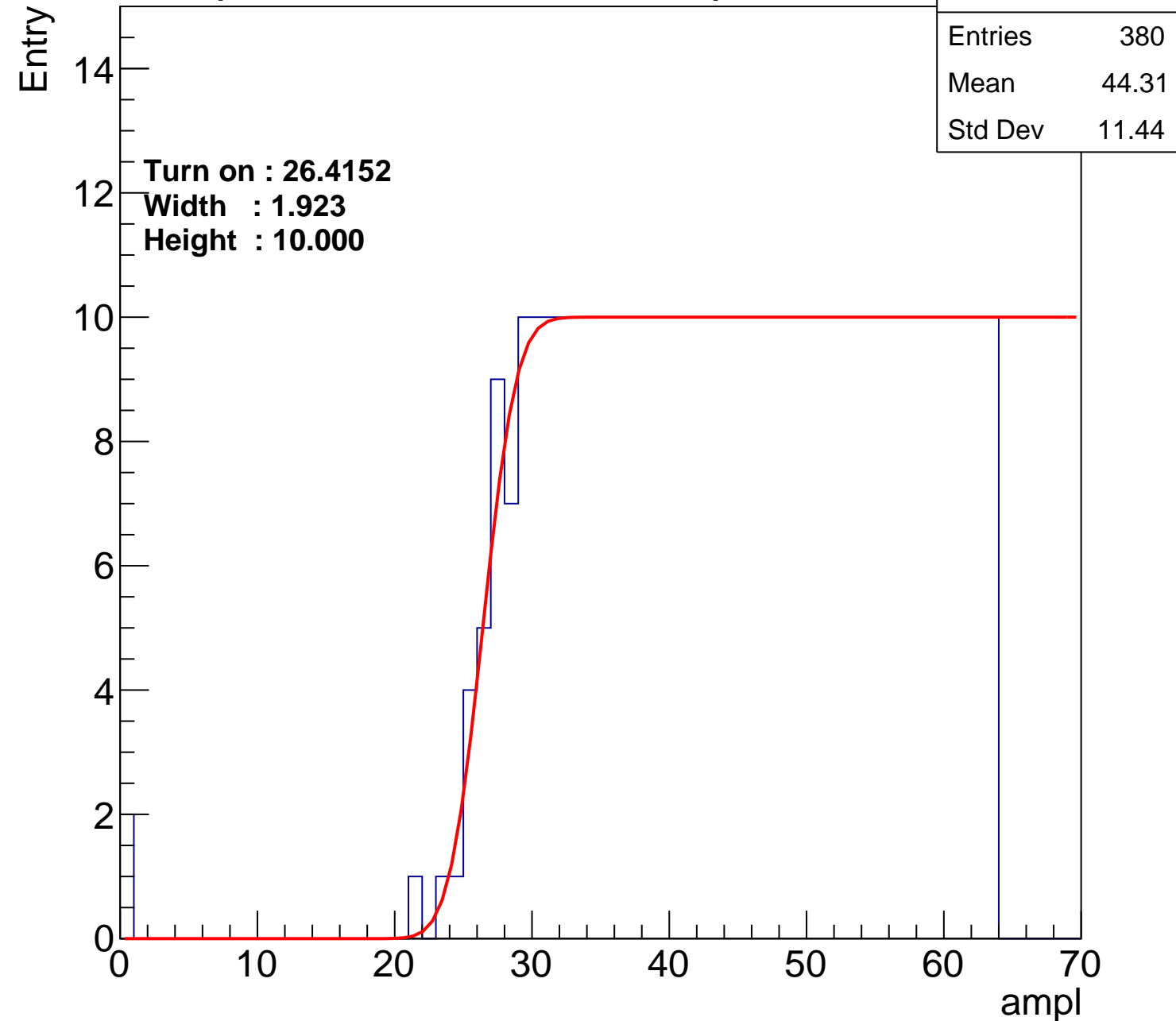
Width : 1.923

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch38

calib_packv5_042523_0143.root, FC#12, port B1

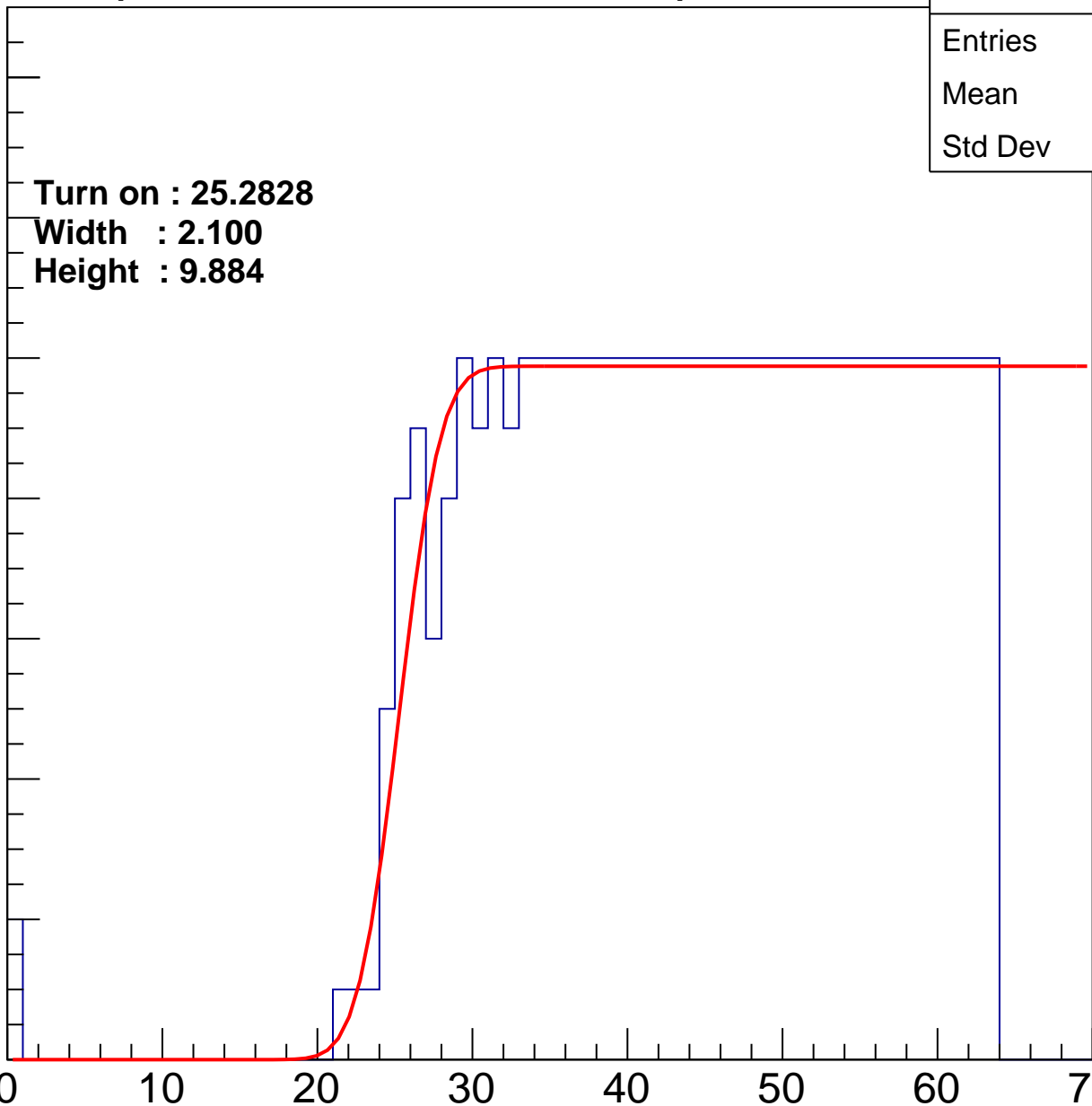
Entry

14
12
10
8
6
4
2
0

Turn on : 25.2828
Width : 2.100
Height : 9.884

Entries	389
Mean	43.81
Std Dev	11.74

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 27.2270
Width : 1.963
Height : 9.977



B0L102S, U25-ch40

calib_packv5_042523_0143.root, FC#12, port B1

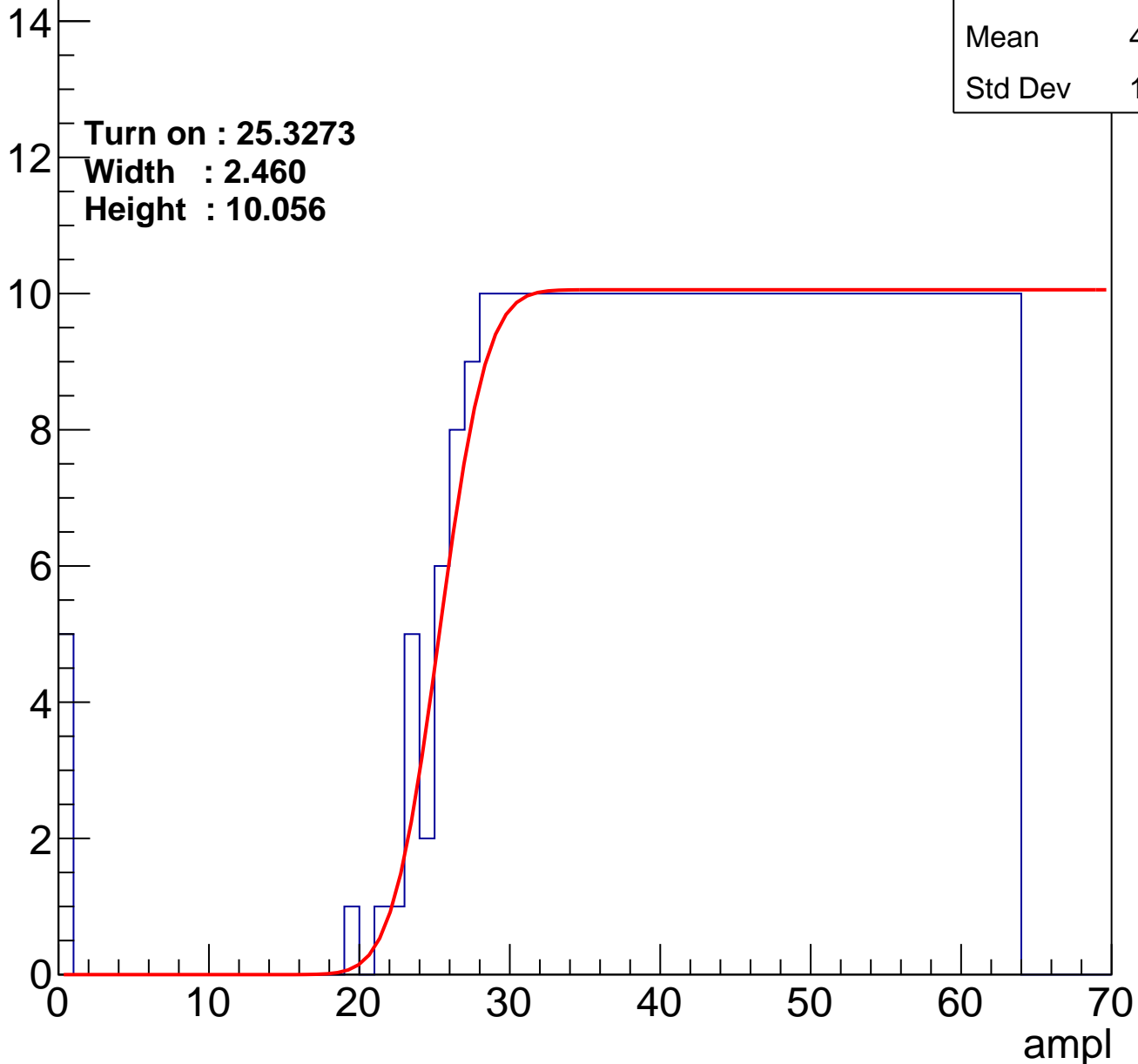
Entries	398
Mean	43.23
Std Dev	12.39

Turn on : 25.3273

Width : 2.460

Height : 10.056

Entry



B0L102S, U25-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	357
Mean	45.28
Std Dev	11.23

Turn on : 28.8339

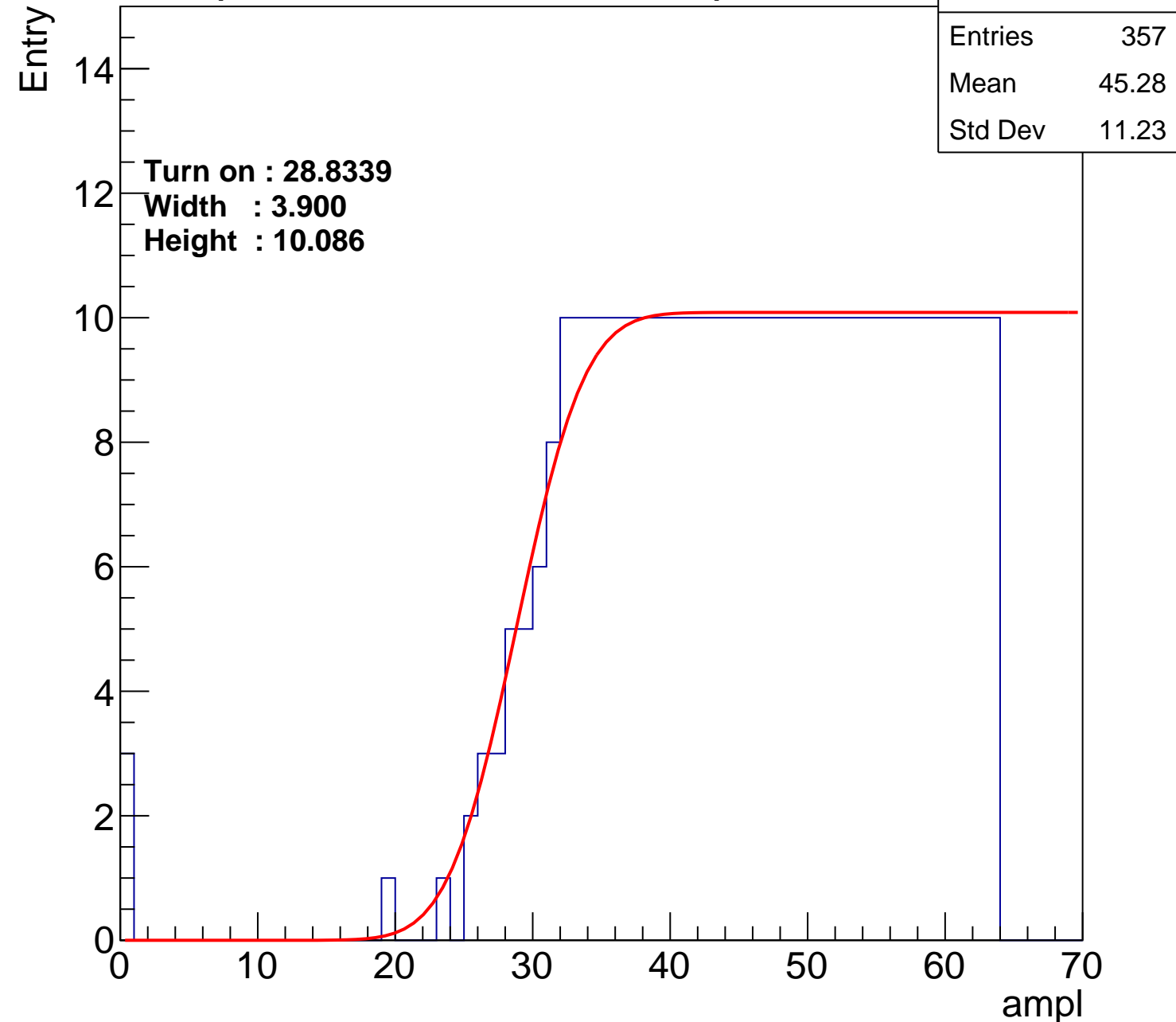
Width : 3.900

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.02
Std Dev	11.65

Turn on : 25.9716

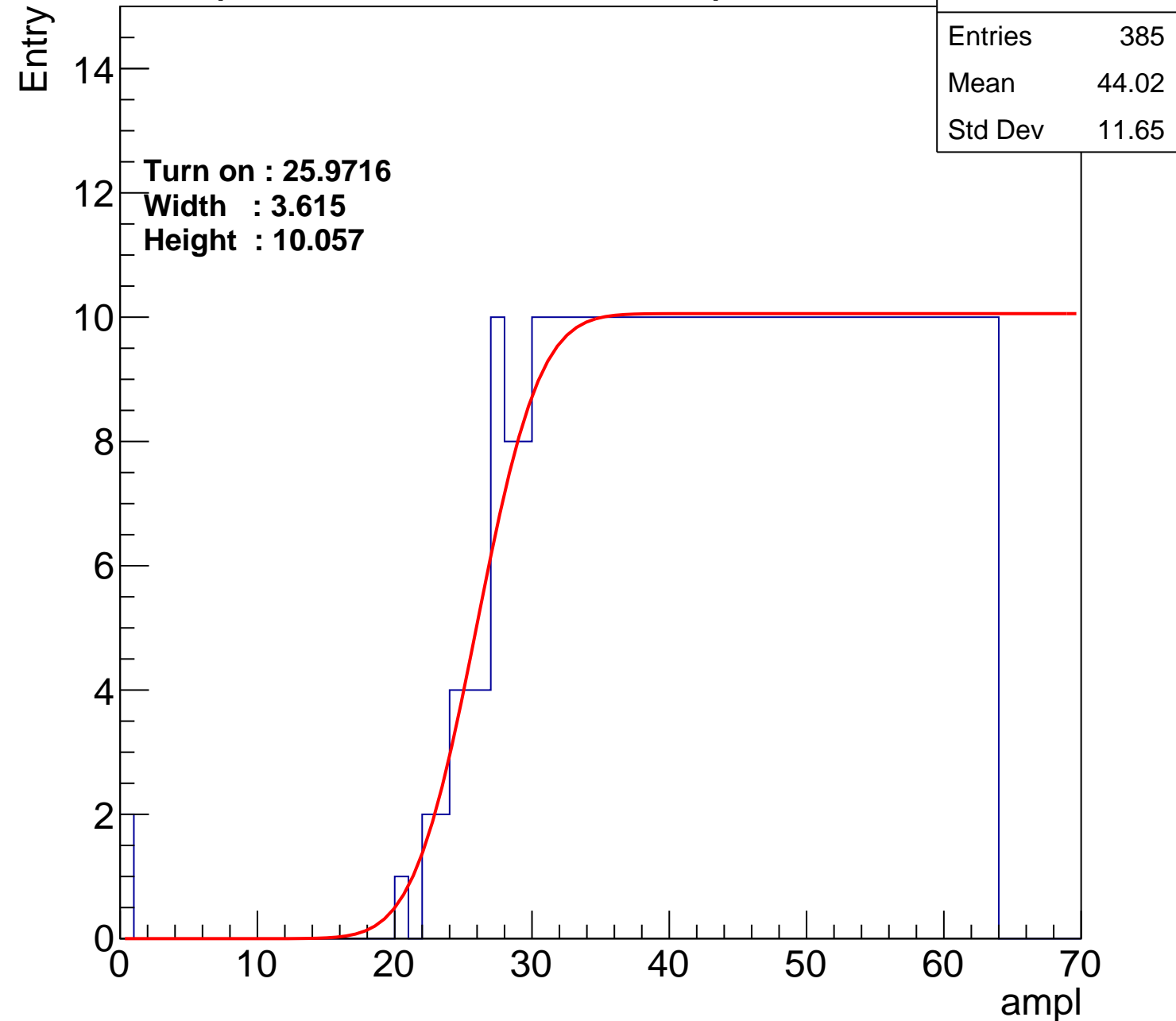
Width : 3.615

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 27.9062

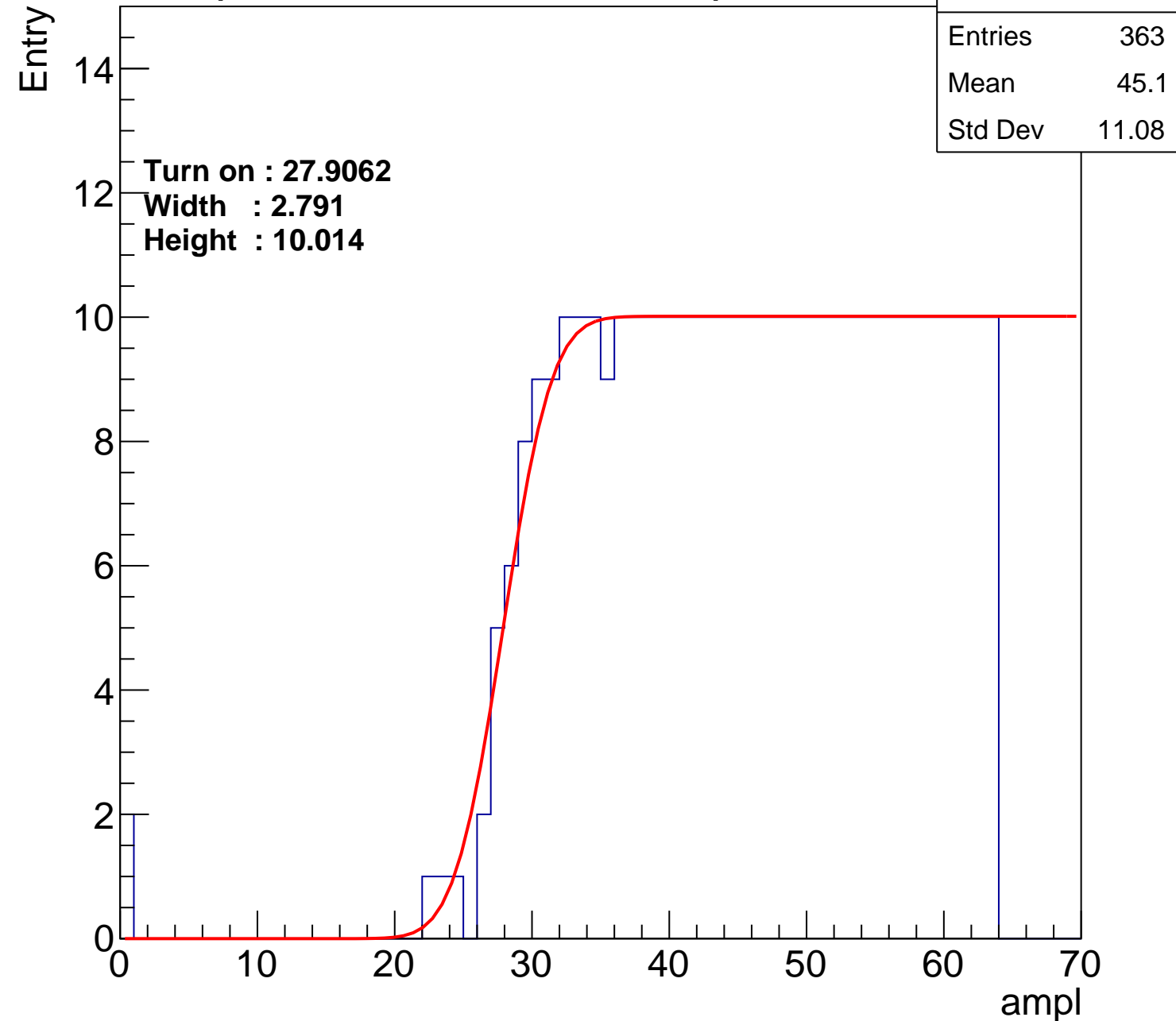
Width : 2.791

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.57
Std Dev	12

Turn on : 25.4218

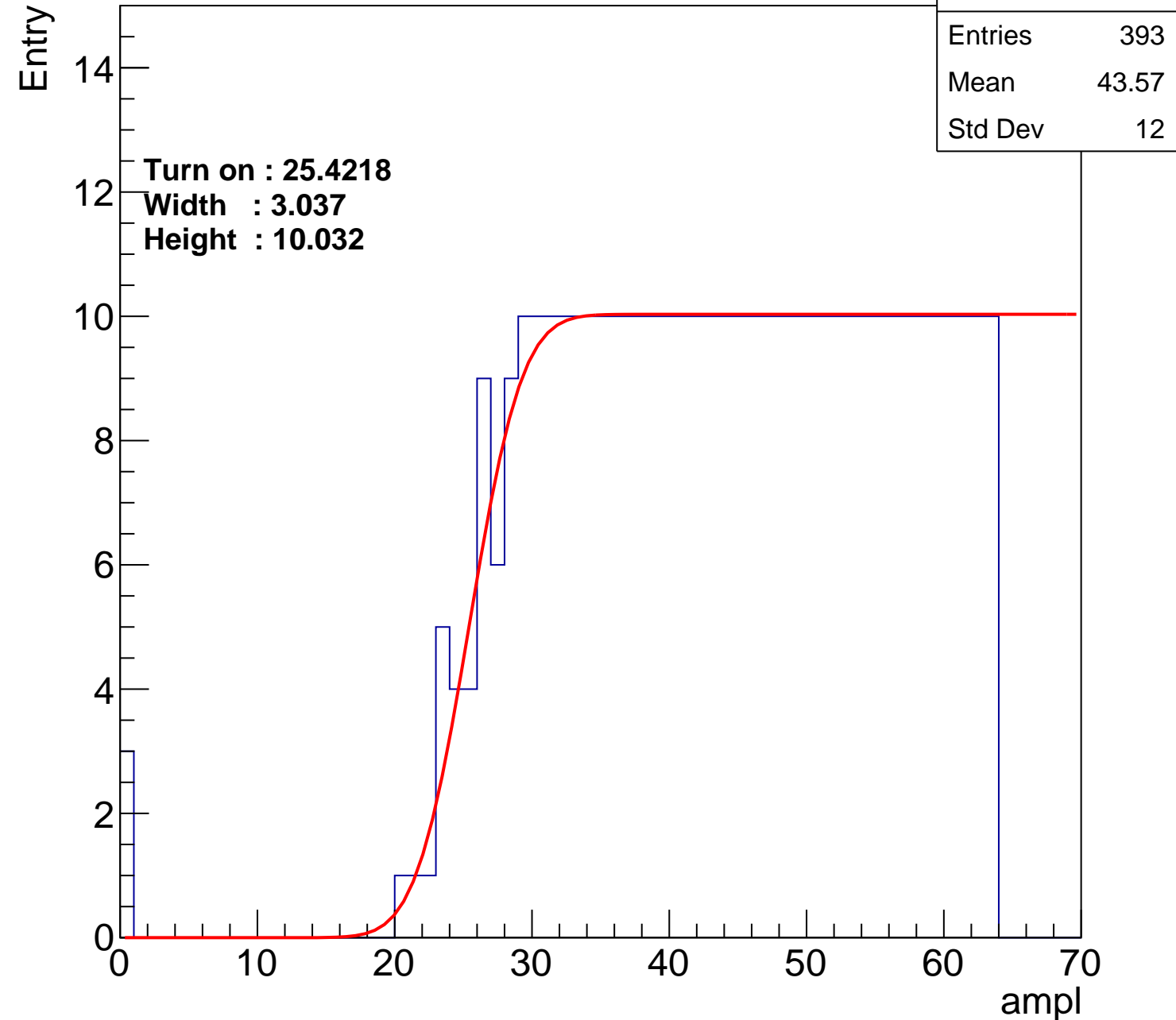
Width : 3.037

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.55
Std Dev	12.15

Turn on : 25.3916

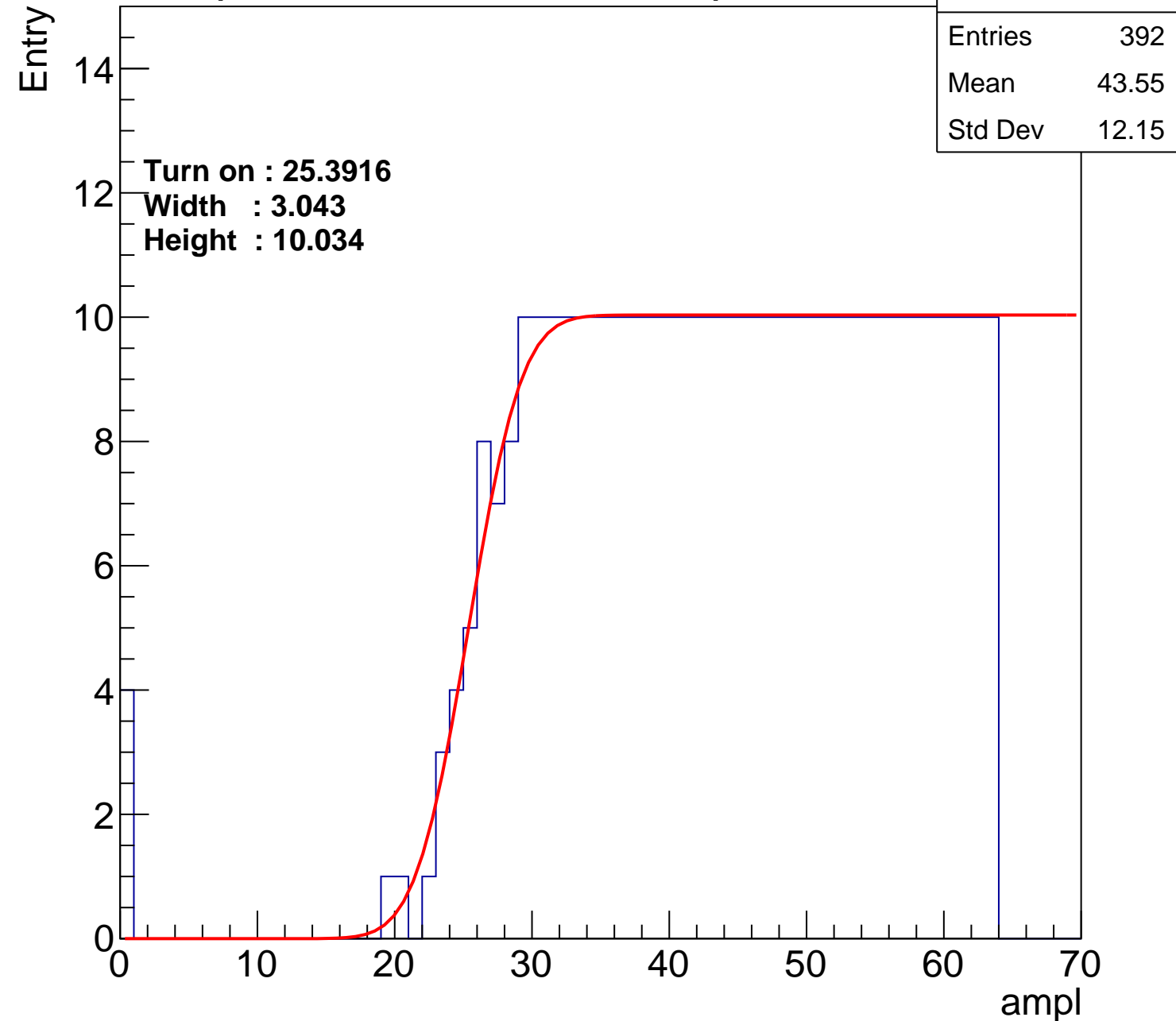
Width : 3.043

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	353
Mean	45.49
Std Dev	11.09

Turn on : 29.4418

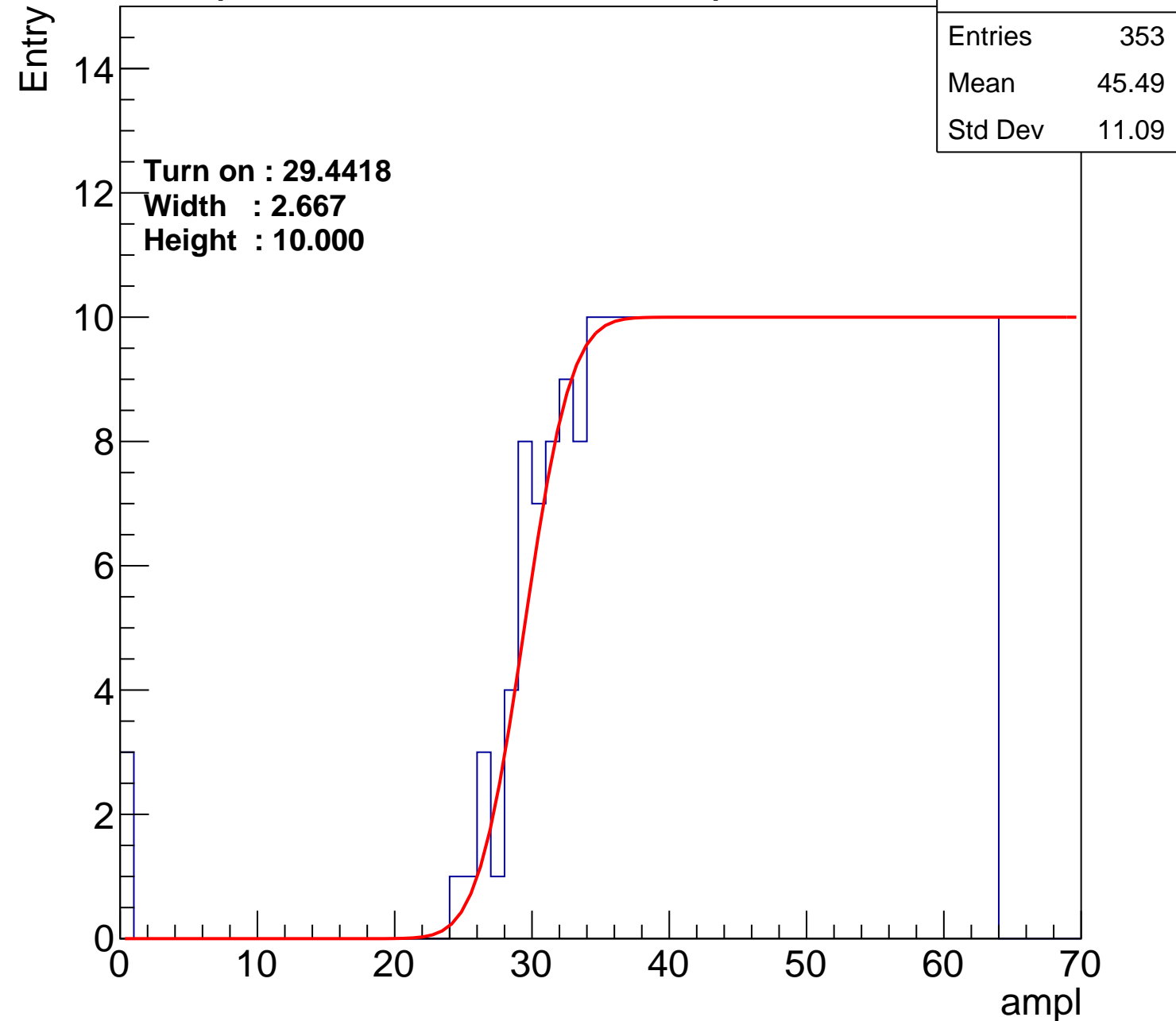
Width : 2.667

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	362
Mean	45.25
Std Dev	10.8

Turn on : 27.9381

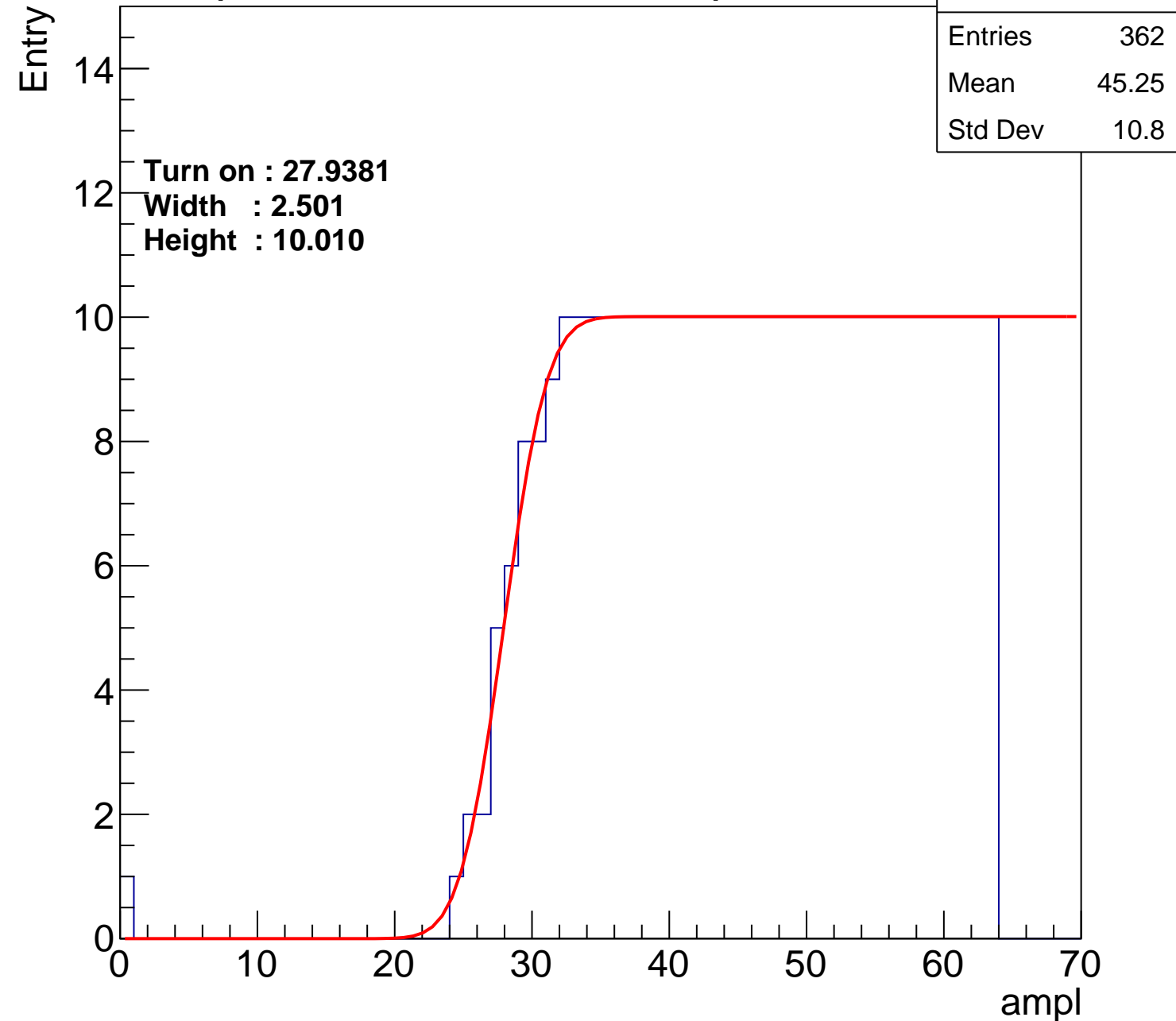
Width : 2.501

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.97
Std Dev	11.96

Turn on : 26.3106

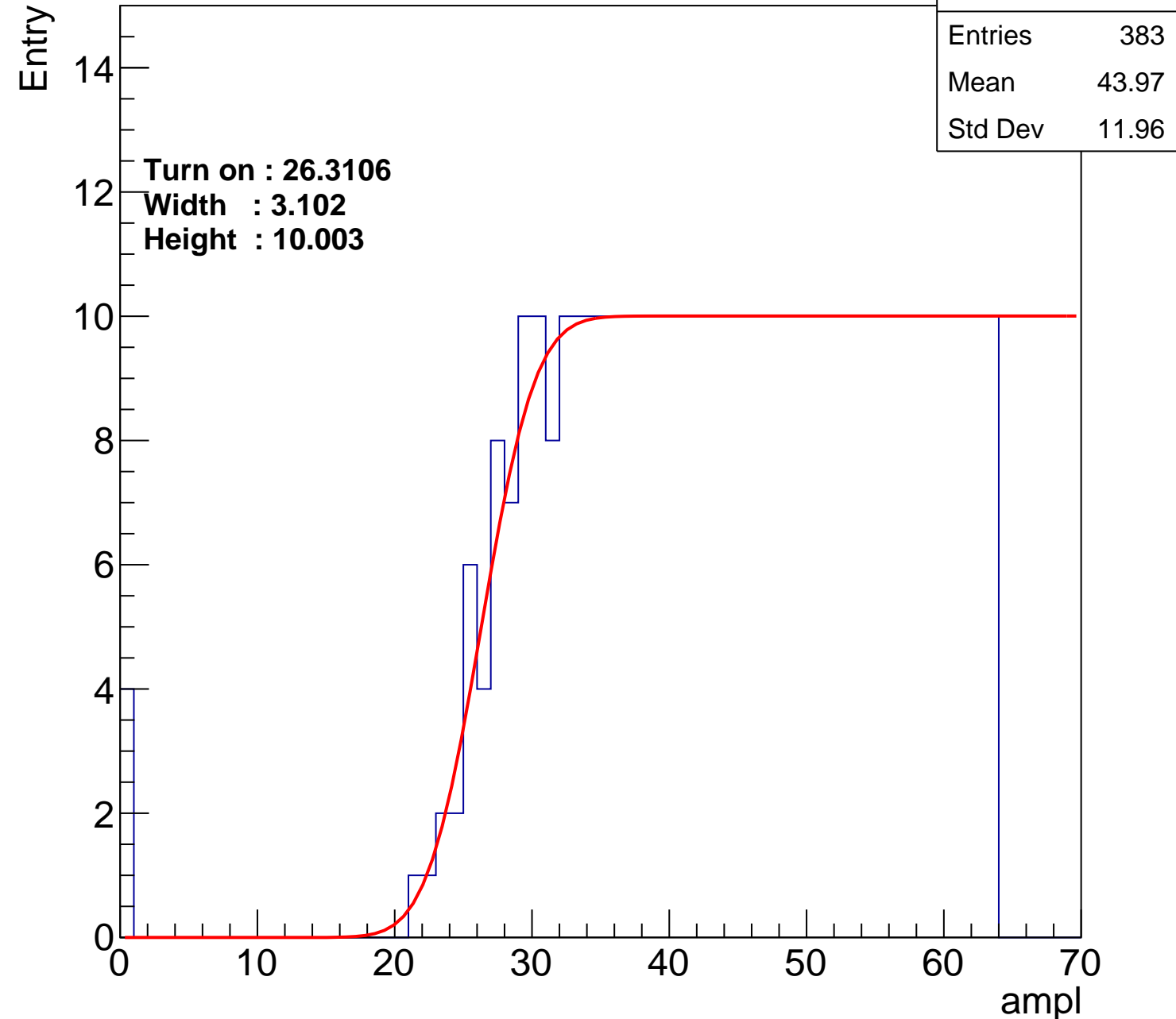
Width : 3.102

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	356
Mean	45.36
Std Dev	11.13

Turn on : 28.8021

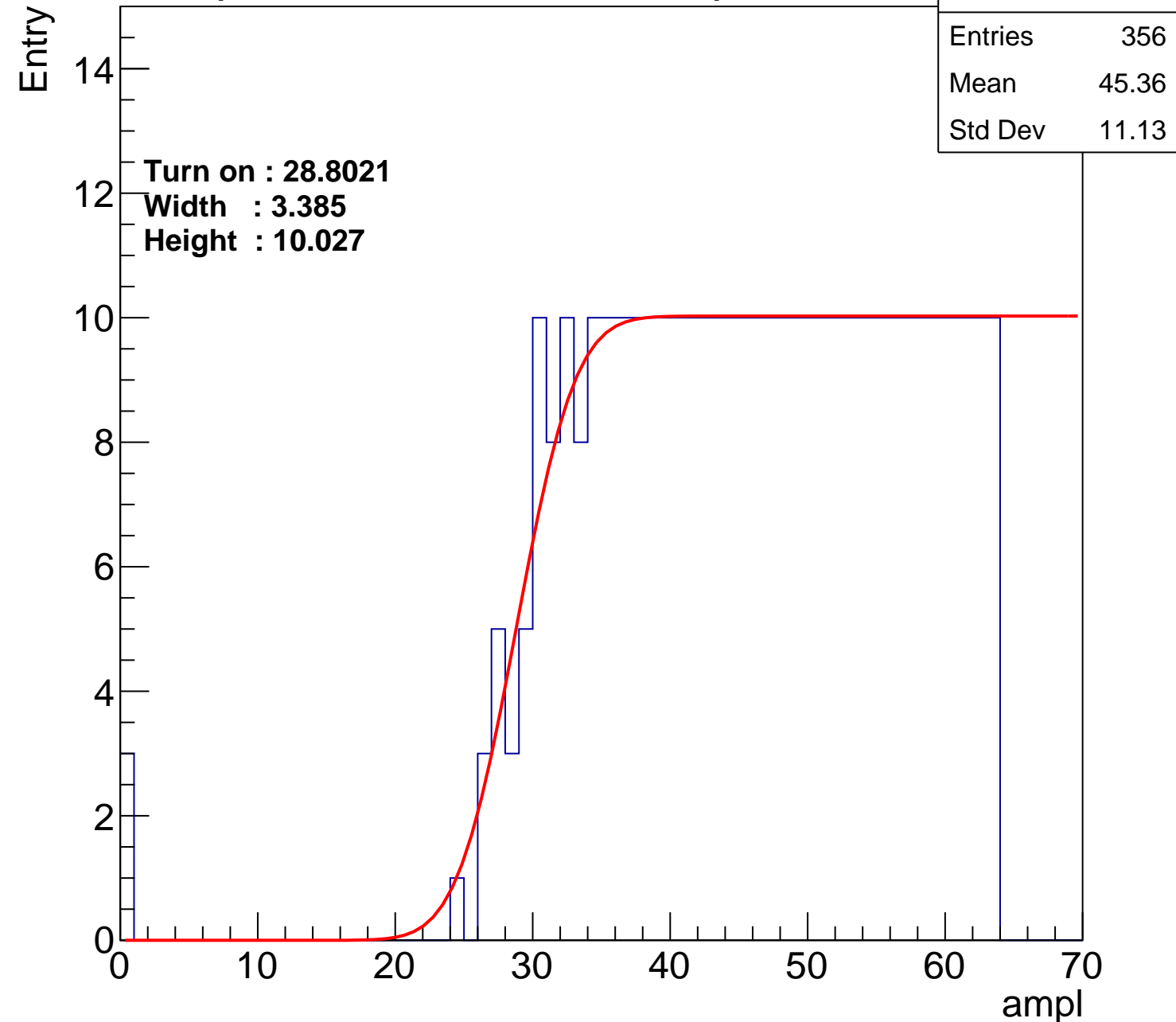
Width : 3.385

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.93
Std Dev	11.33

Turn on : 28.9209

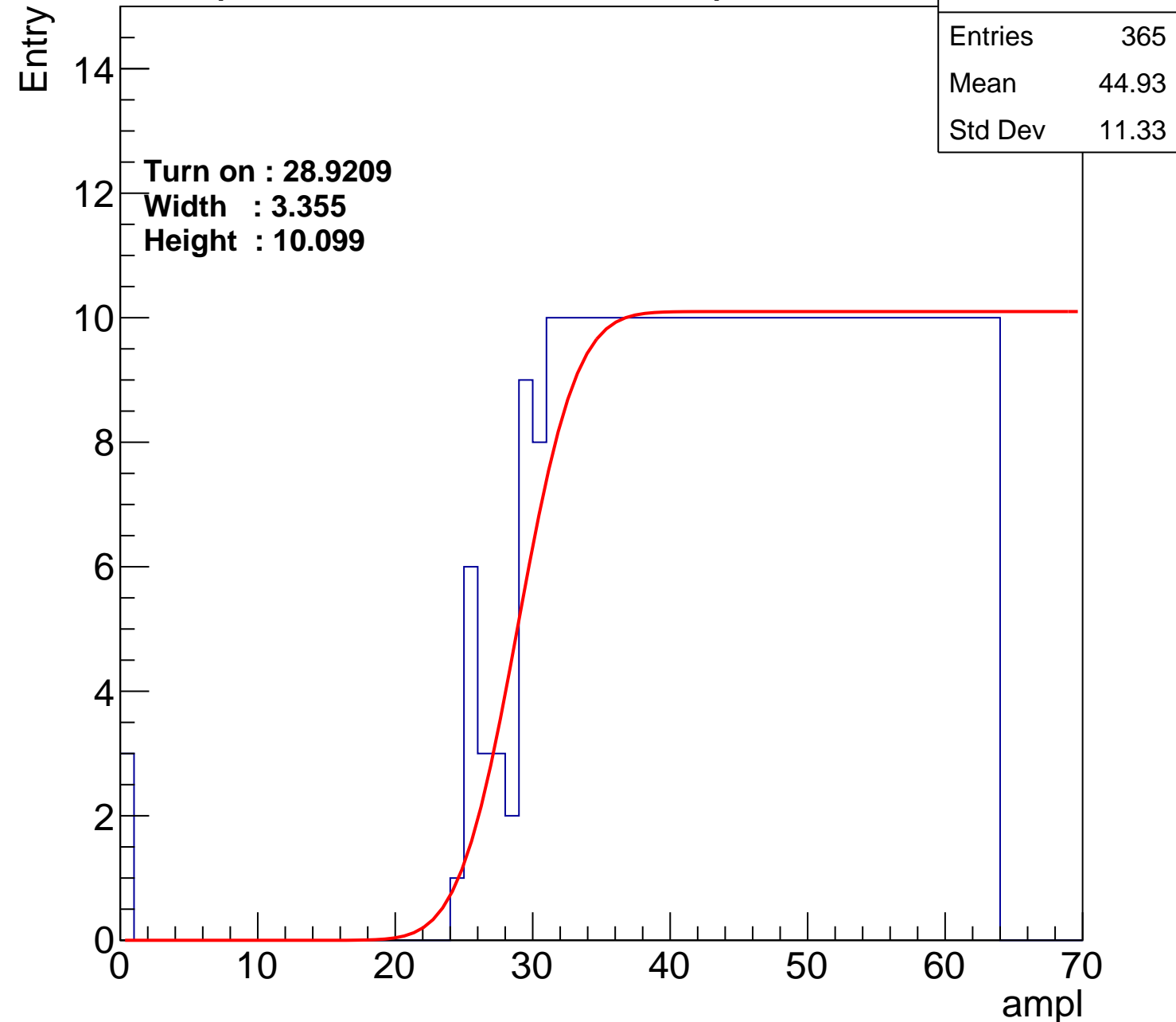
Width : 3.355

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch51

calib_packv5_042523_0143.root, FC#12, port B1

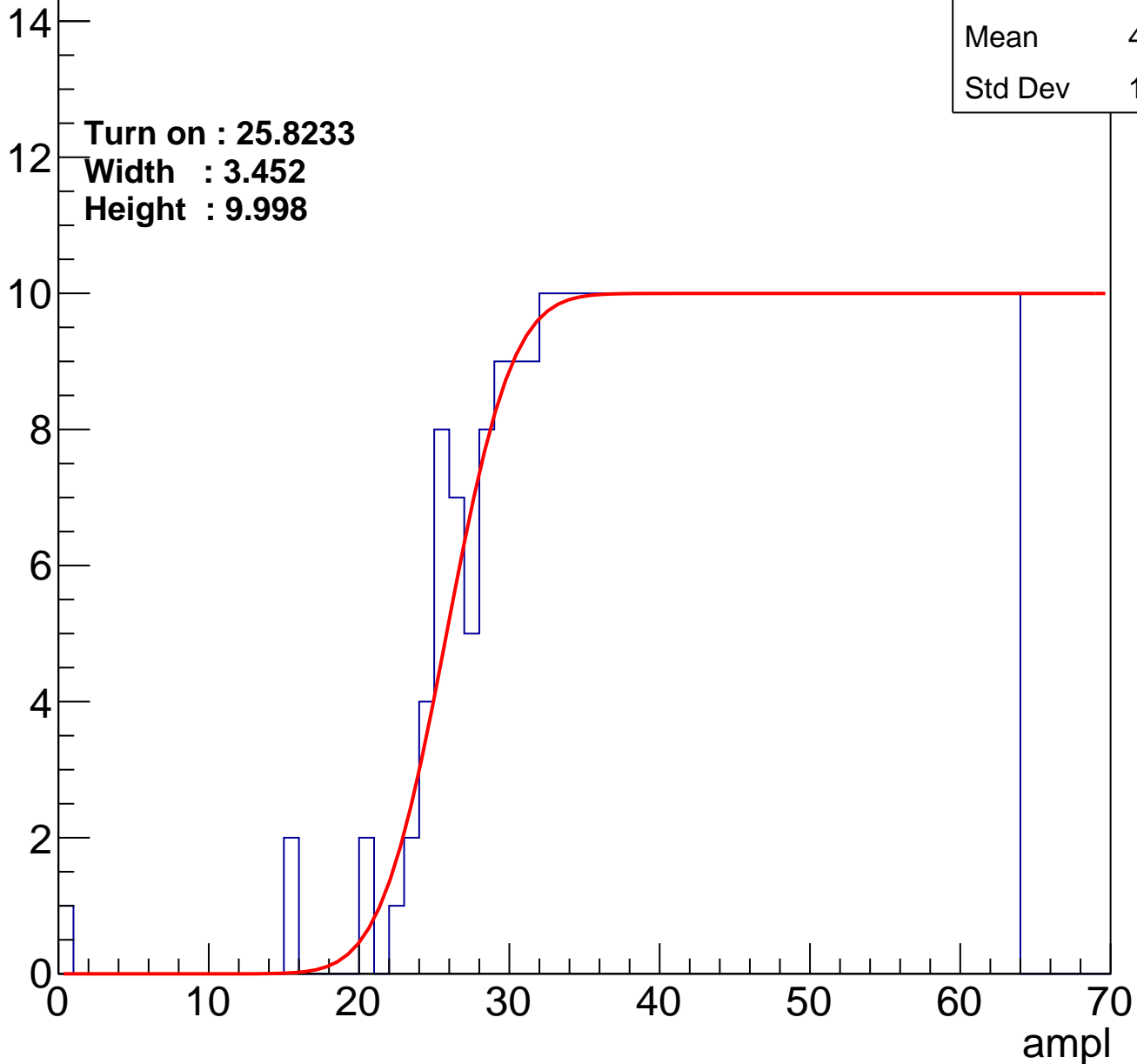
Entries	387
Mean	43.89
Std Dev	11.69

Turn on : 25.8233

Width : 3.452

Height : 9.998

Entry



B0L102S, U25-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.62
Std Dev	11.45

Turn on : 27.4820

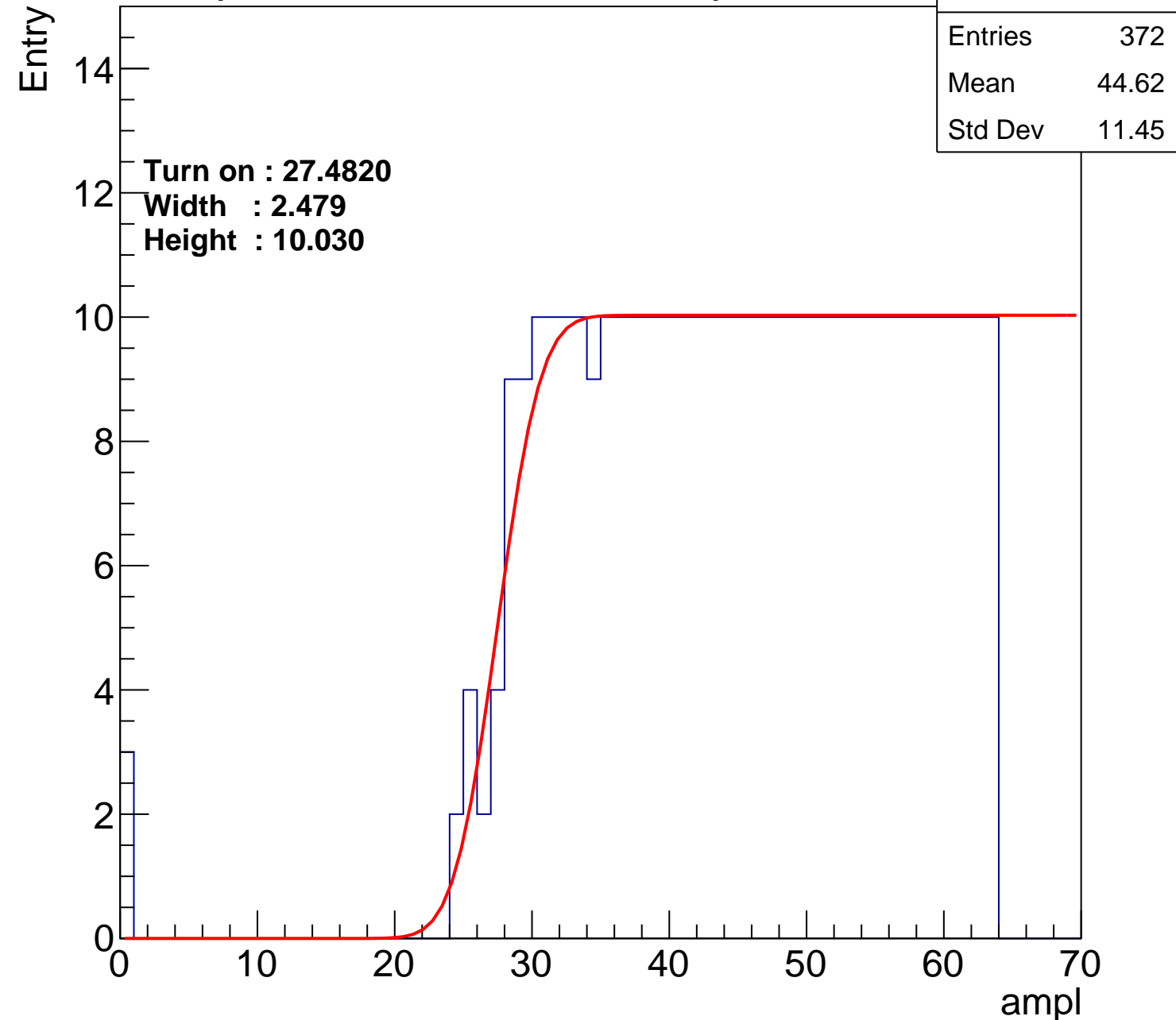
Width : 2.479

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.35
Std Dev	12.22

Turn on : 24.9840

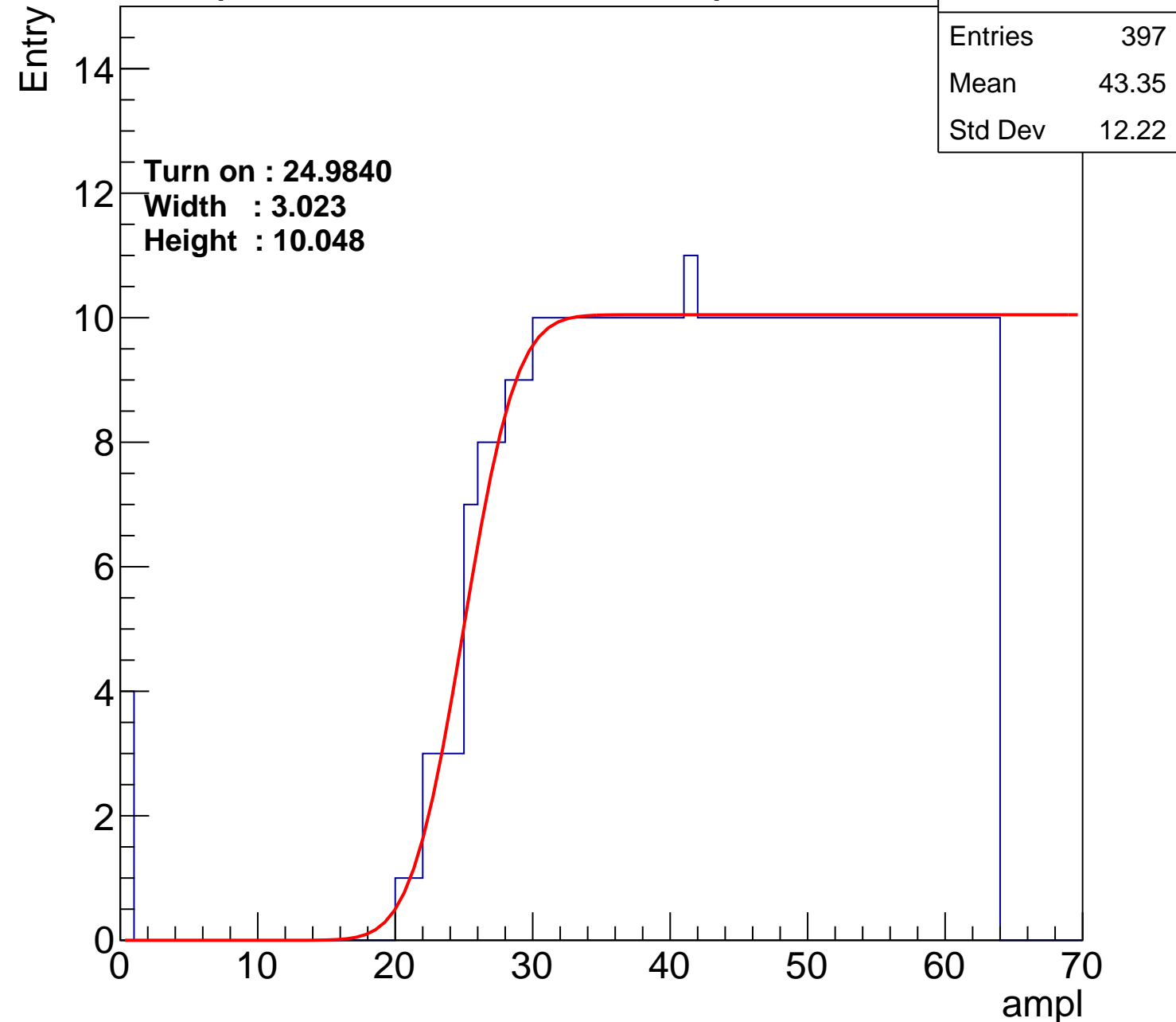
Width : 3.023

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.8507

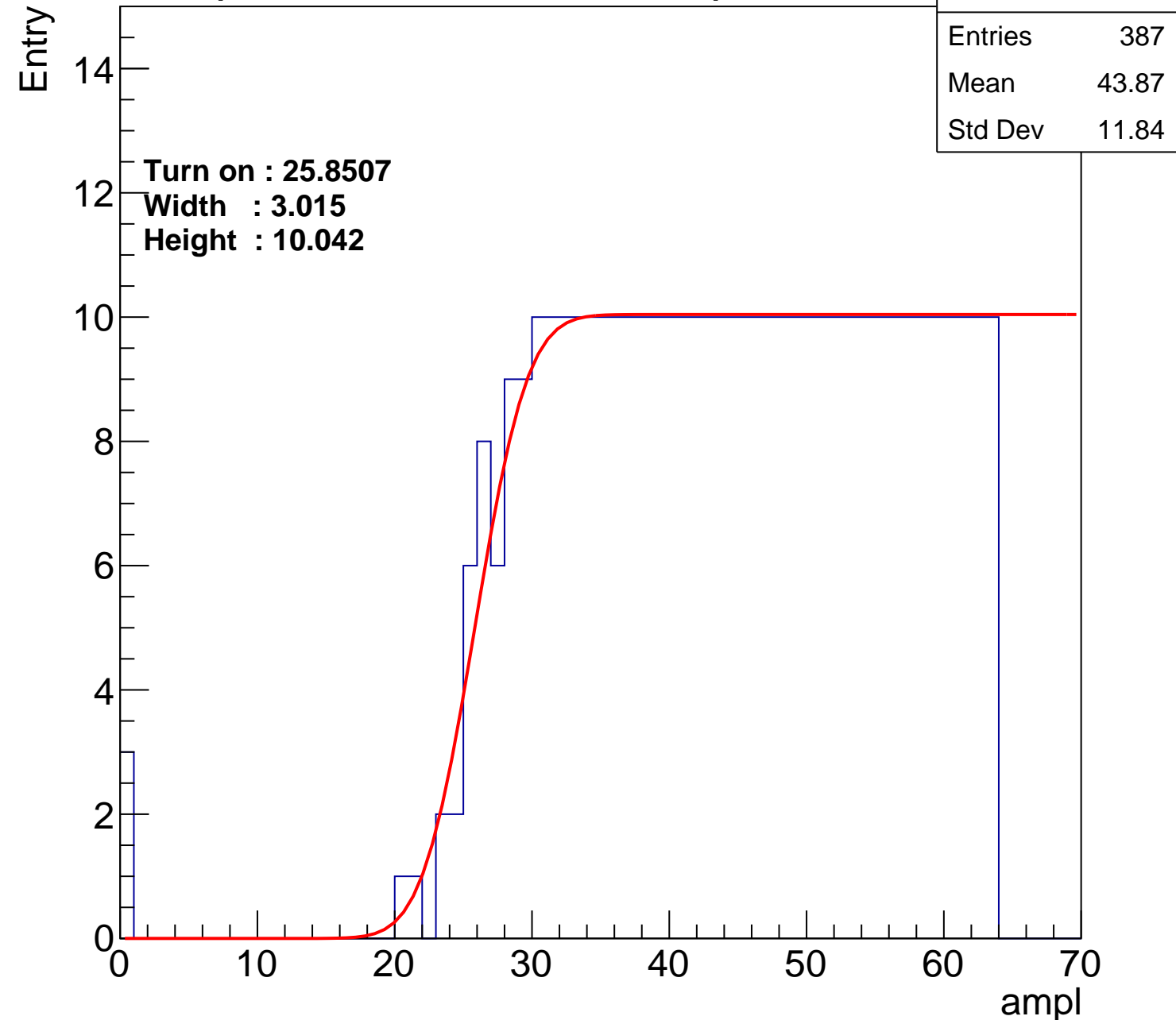
Width : 3.015

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.21
Std Dev	11.52

Turn on : 26.0780

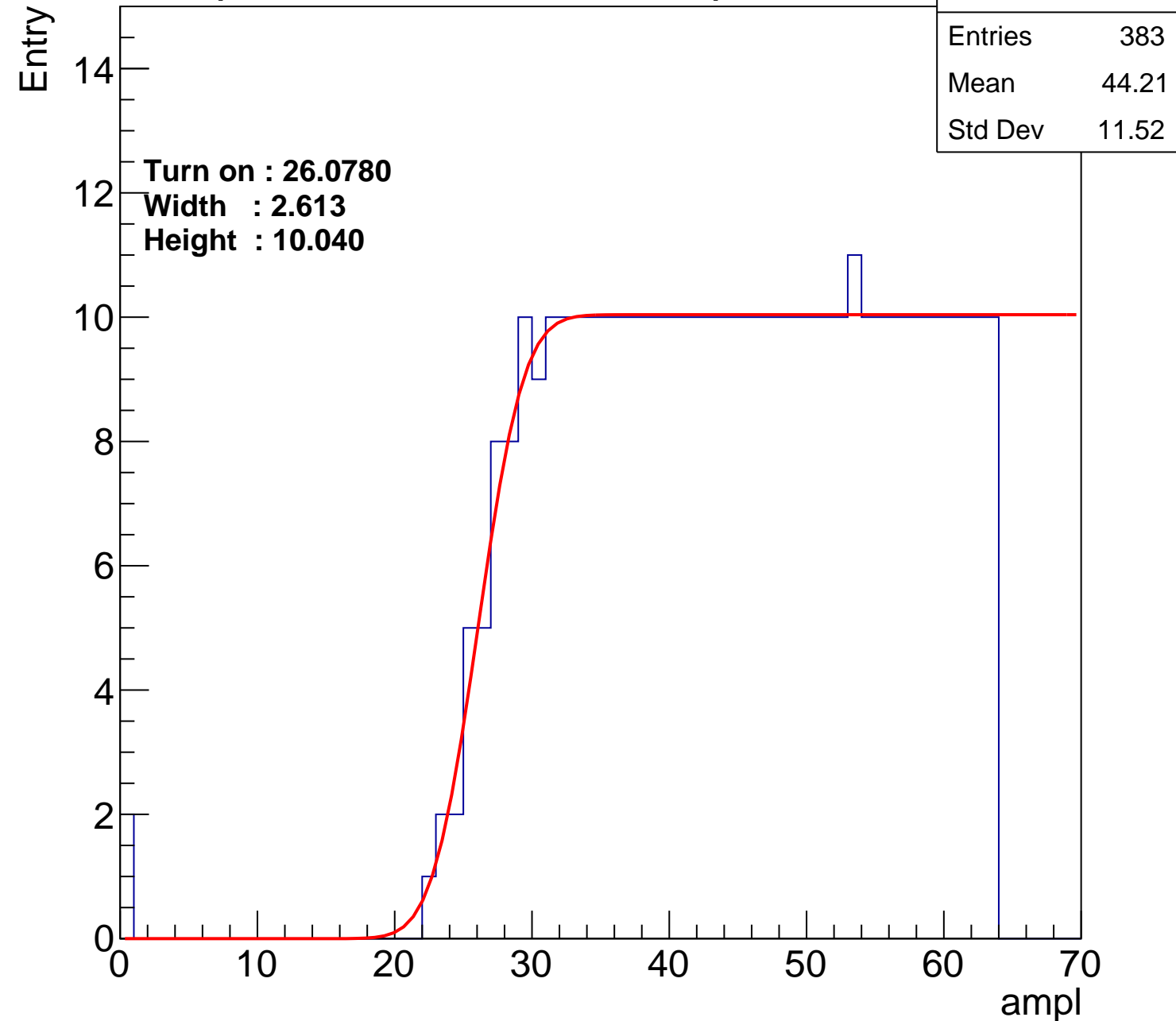
Width : 2.613

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch56

calib_packv5_042523_0143.root, FC#12, port B1

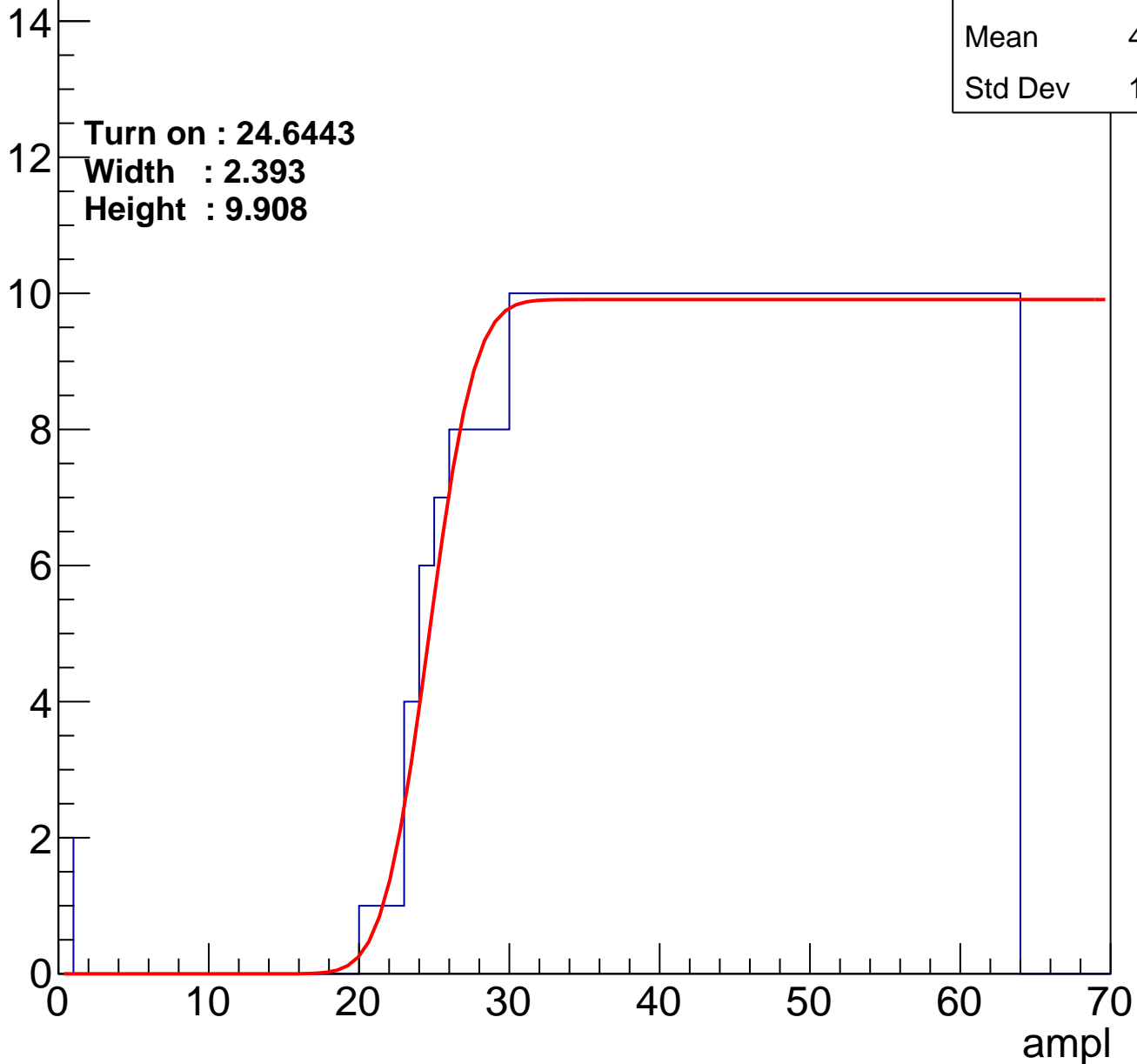
Entries	394
Mean	43.56
Std Dev	11.89

Turn on : 24.6443

Width : 2.393

Height : 9.908

Entry



B0L102S, U25-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.12
Std Dev	11.53

Turn on : 25.6709

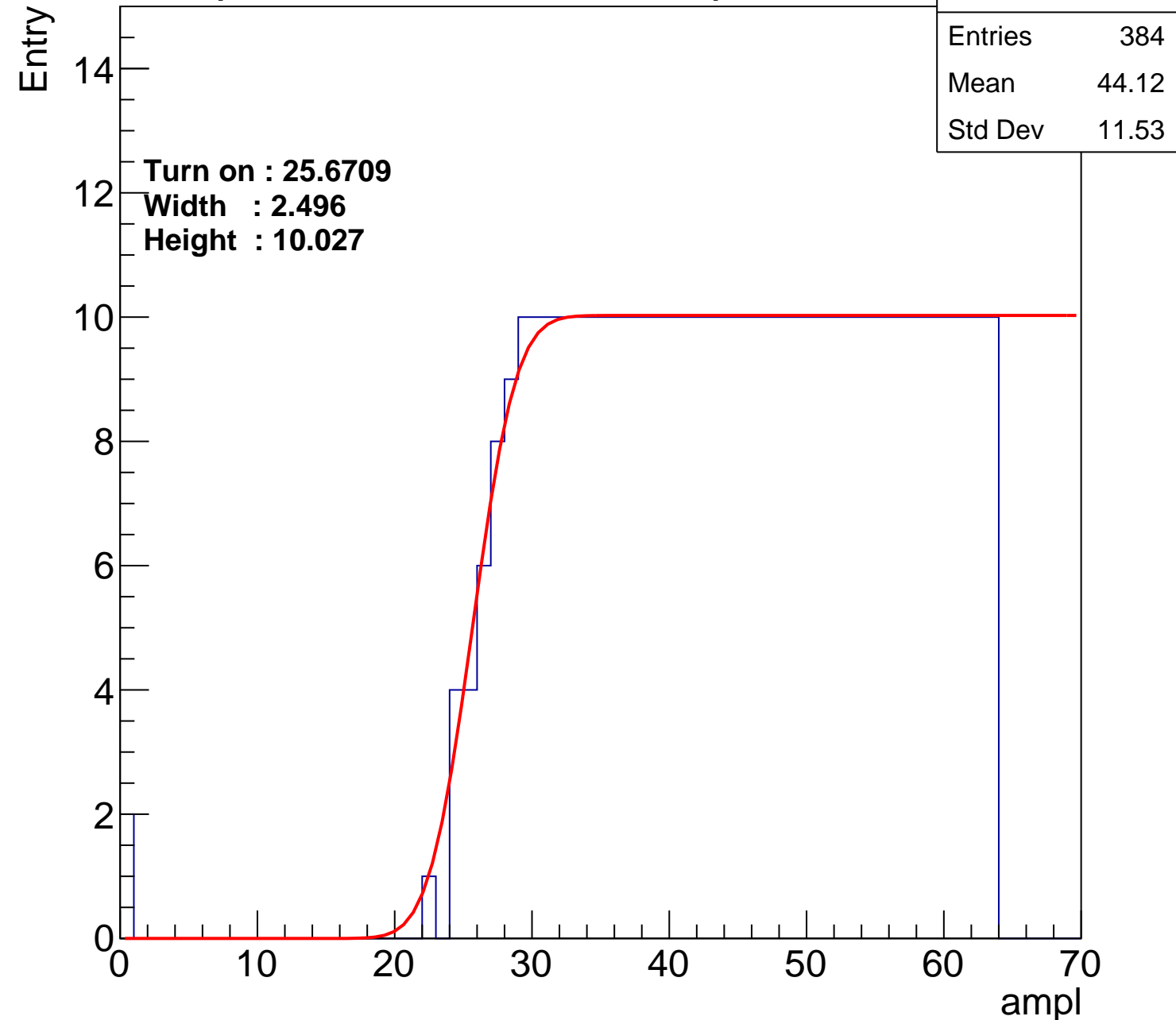
Width : 2.496

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch58

calib_packv5_042523_0143.root, FC#12, port B1

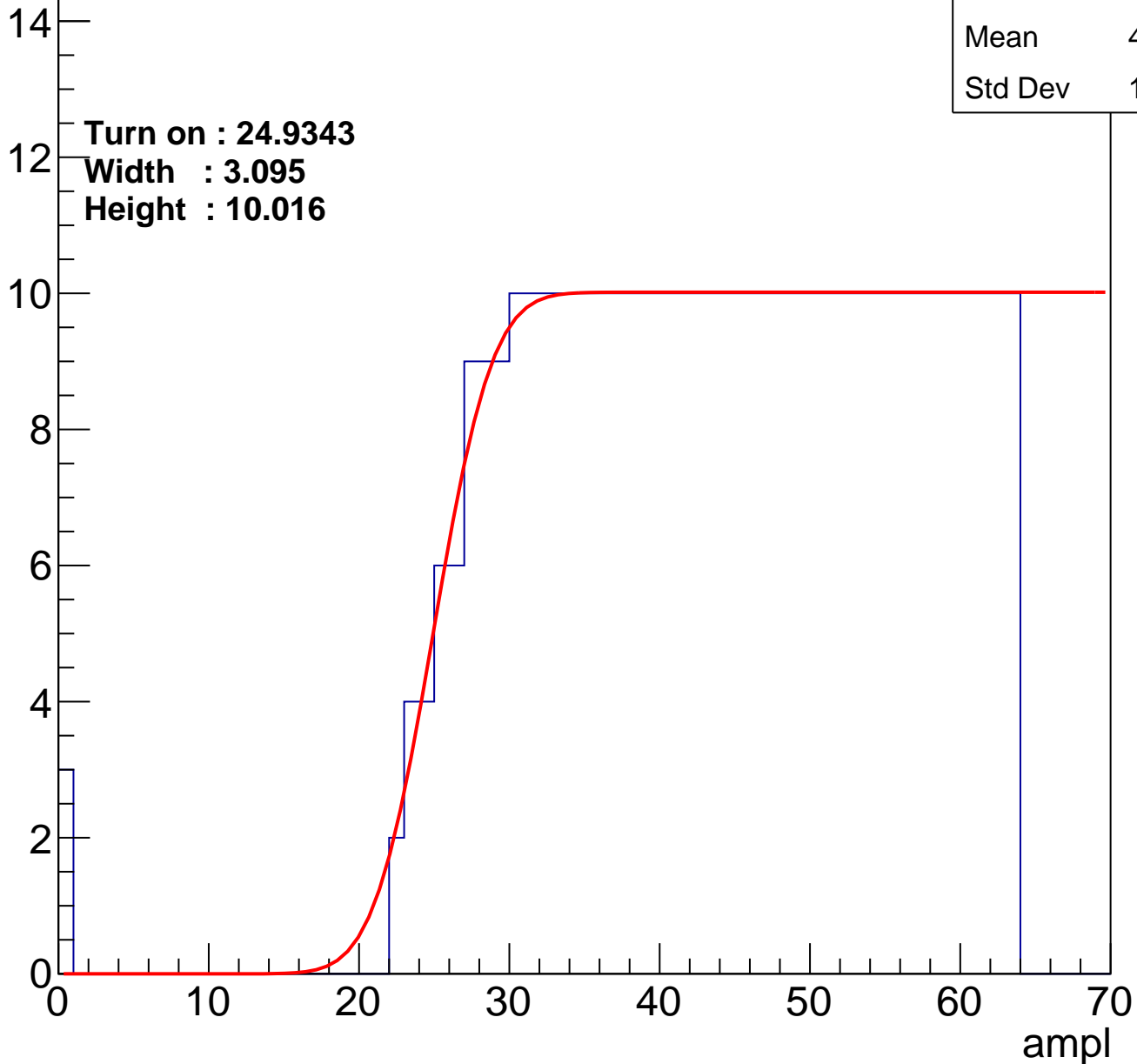
Entries	392
Mean	43.63
Std Dev	11.95

Turn on : 24.9343

Width : 3.095

Height : 10.016

Entry



B0L102S, U25-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.58
Std Dev	11.7

Turn on : 27.8276

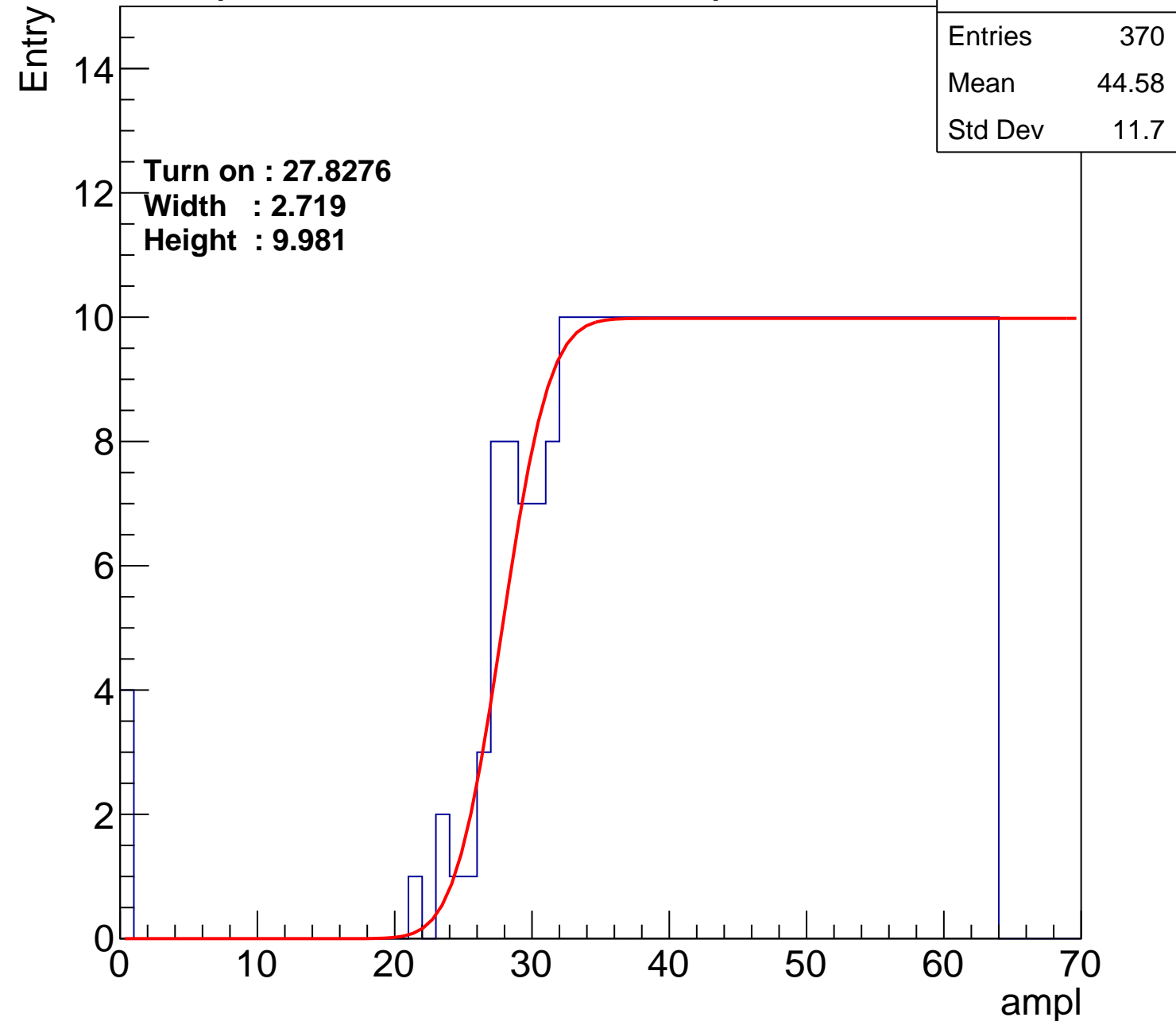
Width : 2.719

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.29
Std Dev	10.8

Turn on : 28.7383

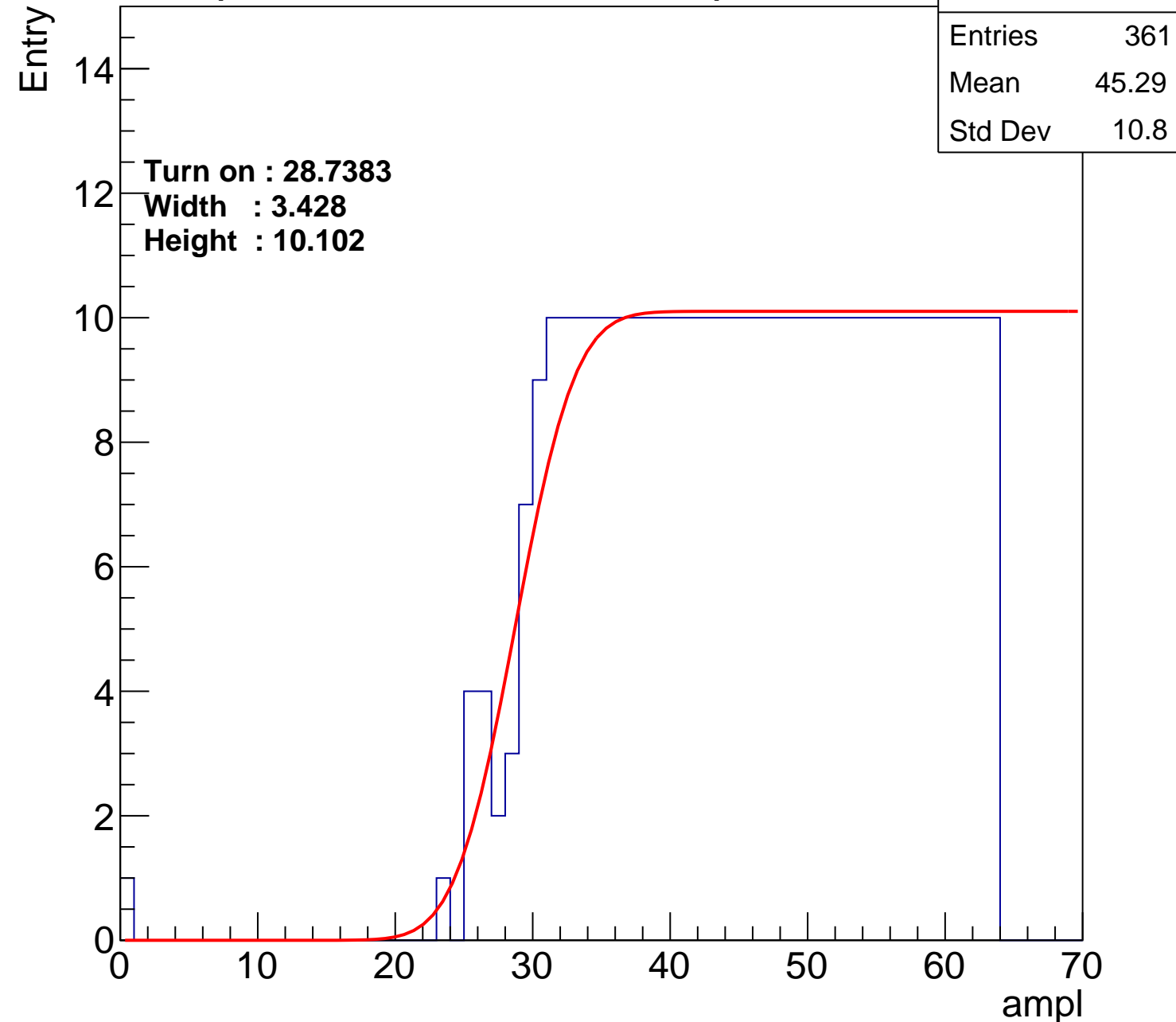
Width : 3.428

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch61

calib_packv5_042523_0143.root, FC#12, port B1

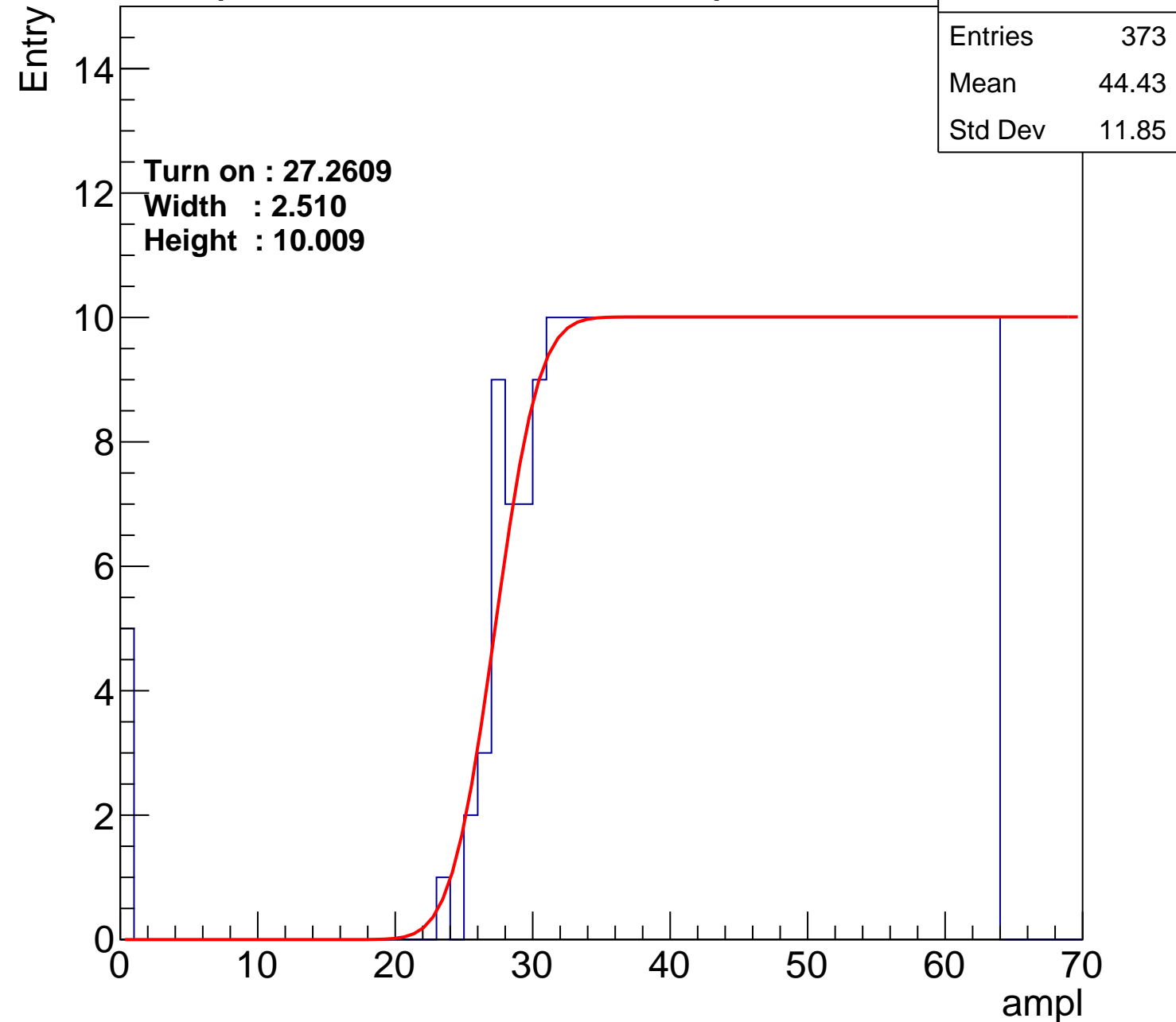
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2609
Width : 2.510
Height : 10.009

Entries	373
Mean	44.43
Std Dev	11.85

ampl



B0L102S, U25-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.2209

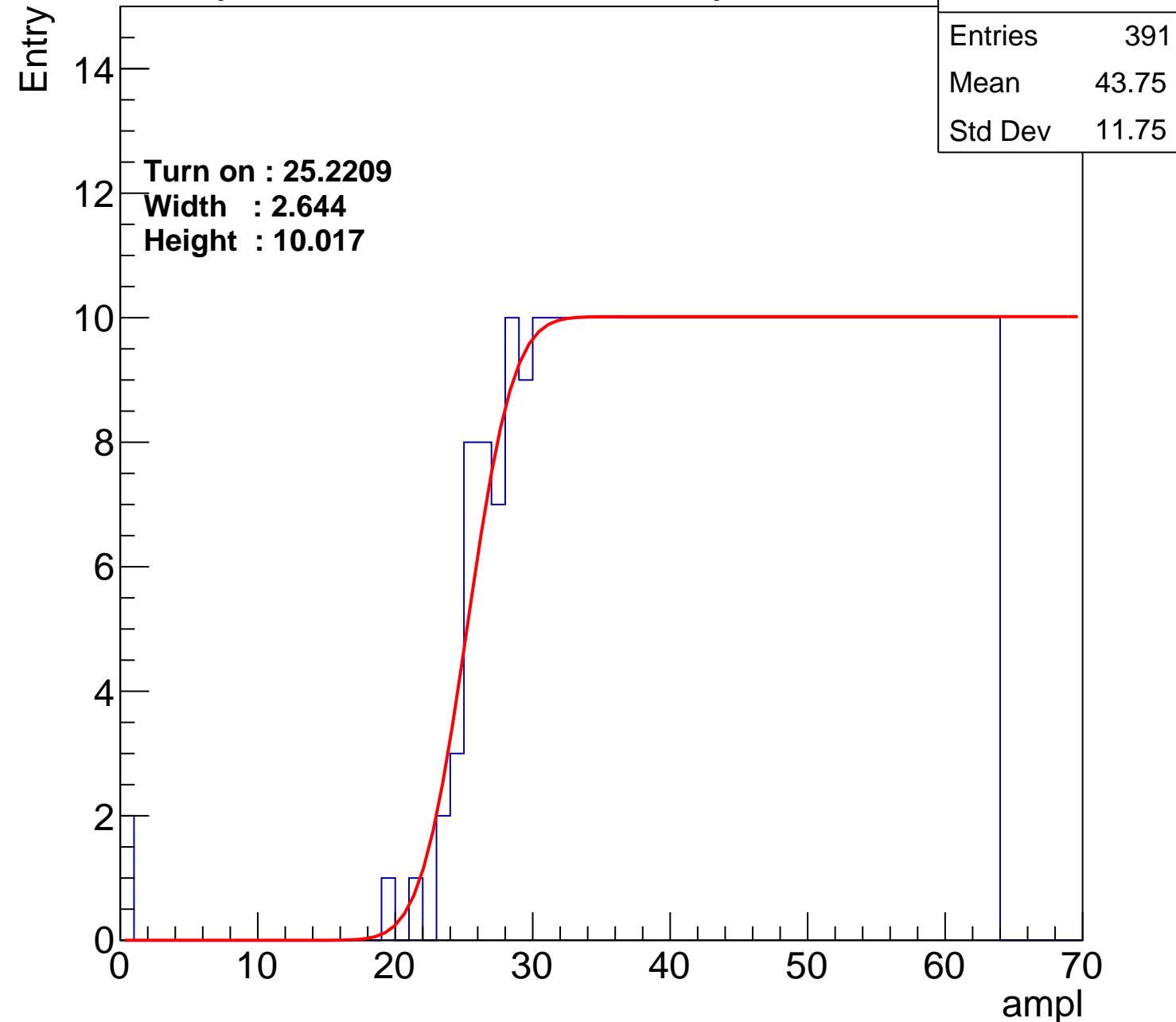
Width : 2.644

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.8788

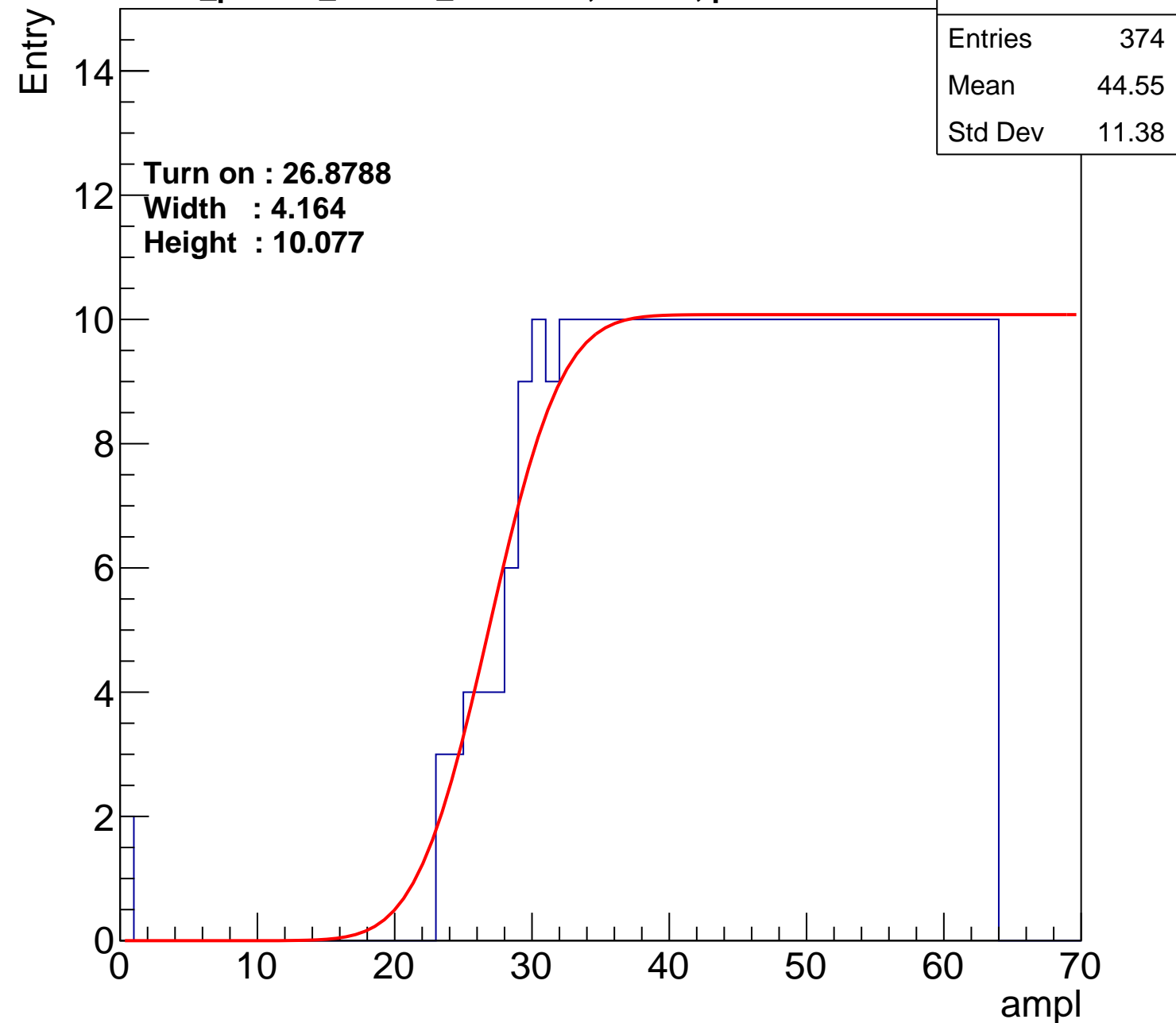
Width : 4.164

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch64

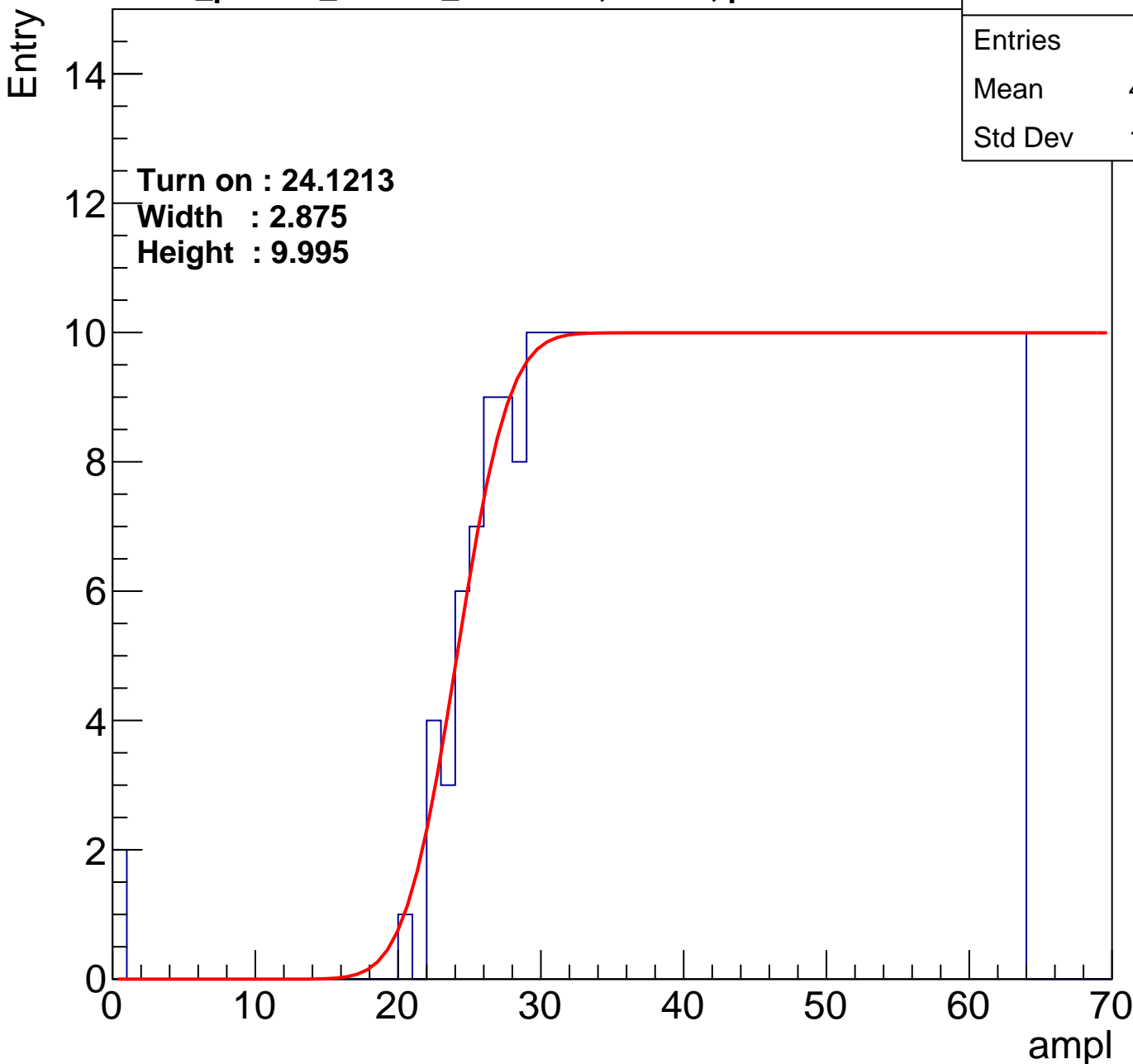
calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.35
Std Dev	11.97

Turn on : 24.1213

Width : 2.875

Height : 9.995



B0L102S, U25-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.22
Std Dev	11.46

Turn on : 26.1902

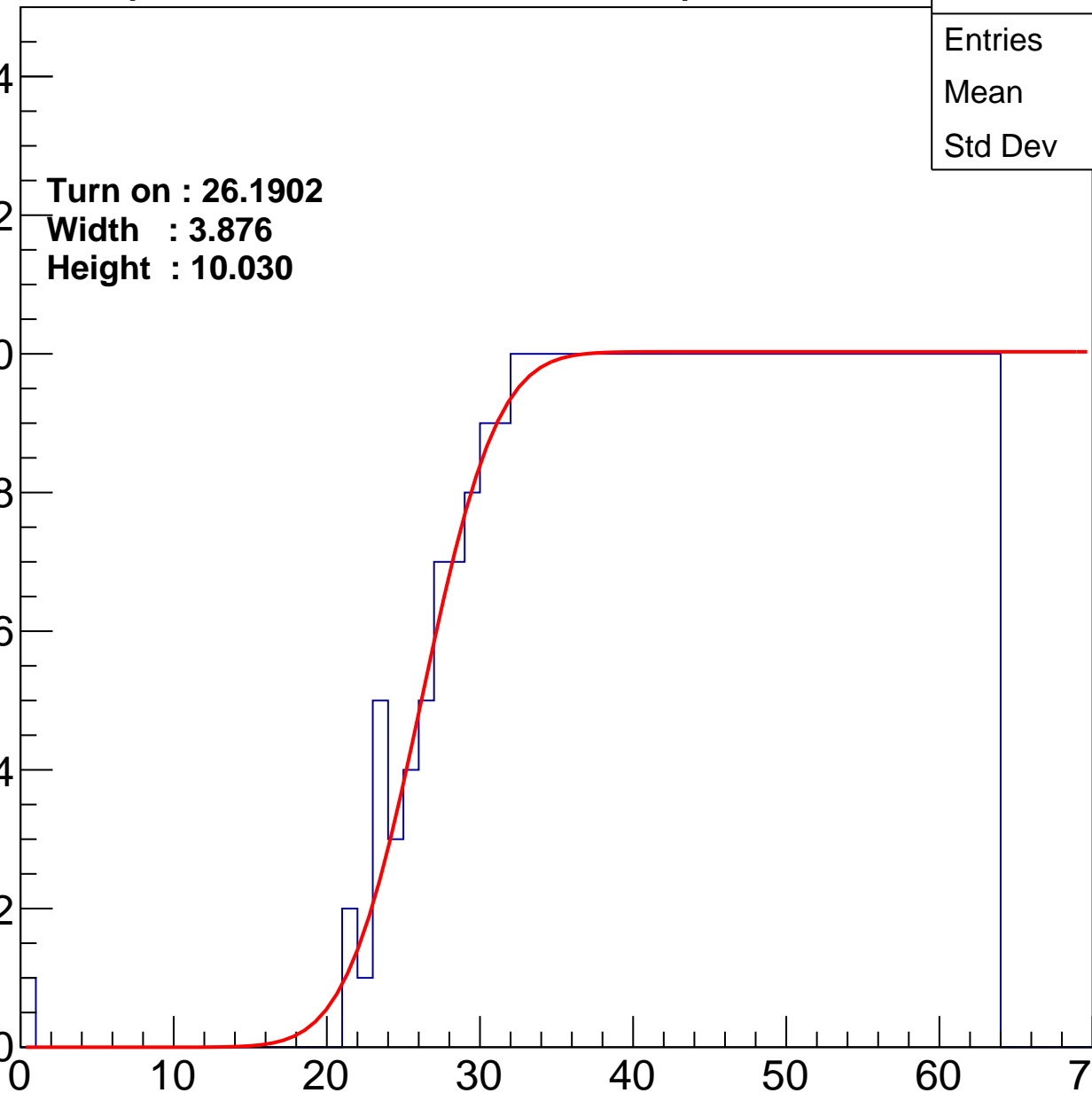
Width : 3.876

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch66

calib_packv5_042523_0143.root, FC#12, port B1

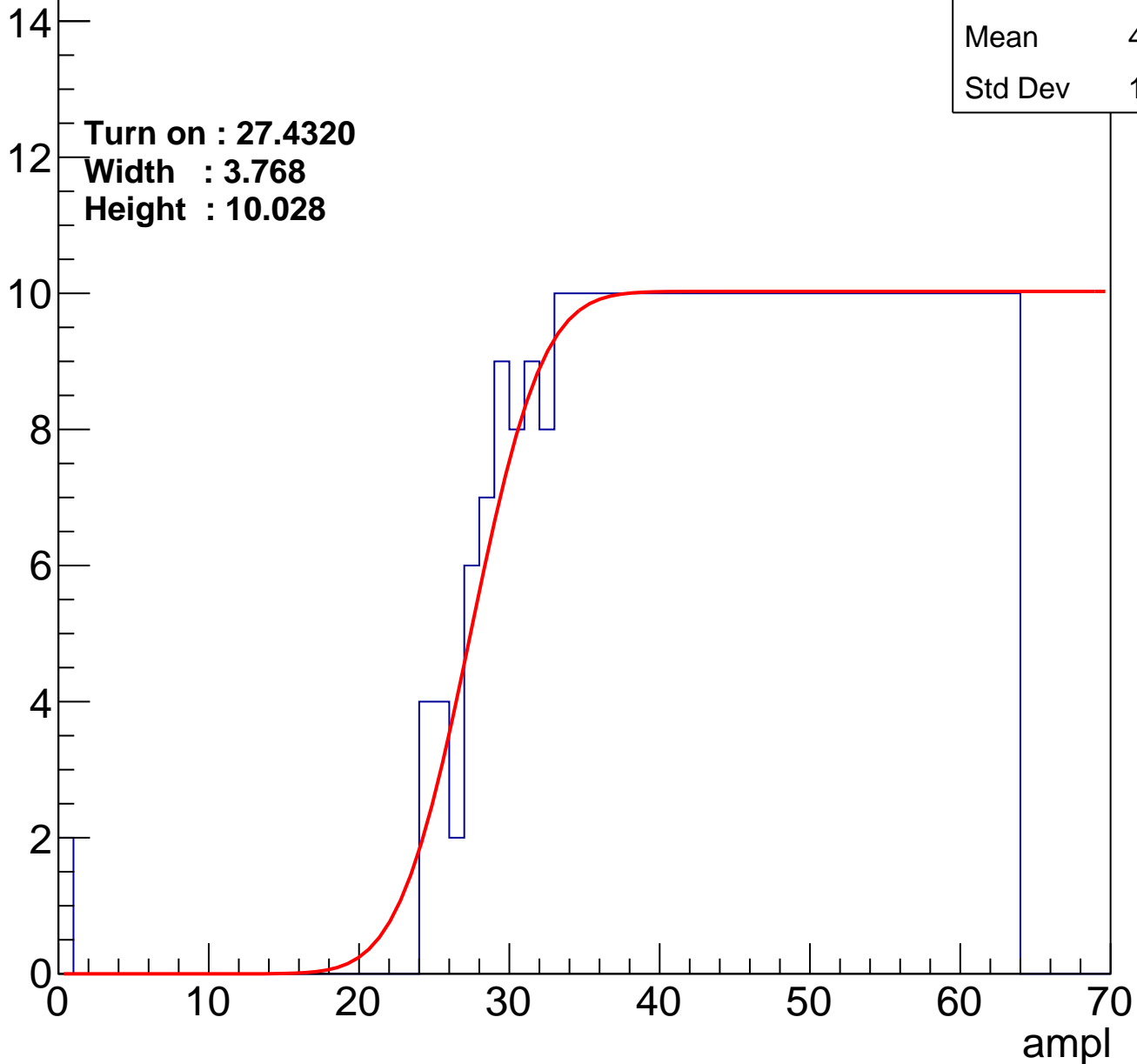
Entries	369
Mean	44.78
Std Dev	11.27

Turn on : 27.4320

Width : 3.768

Height : 10.028

Entry



B0L102S, U25-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.1309

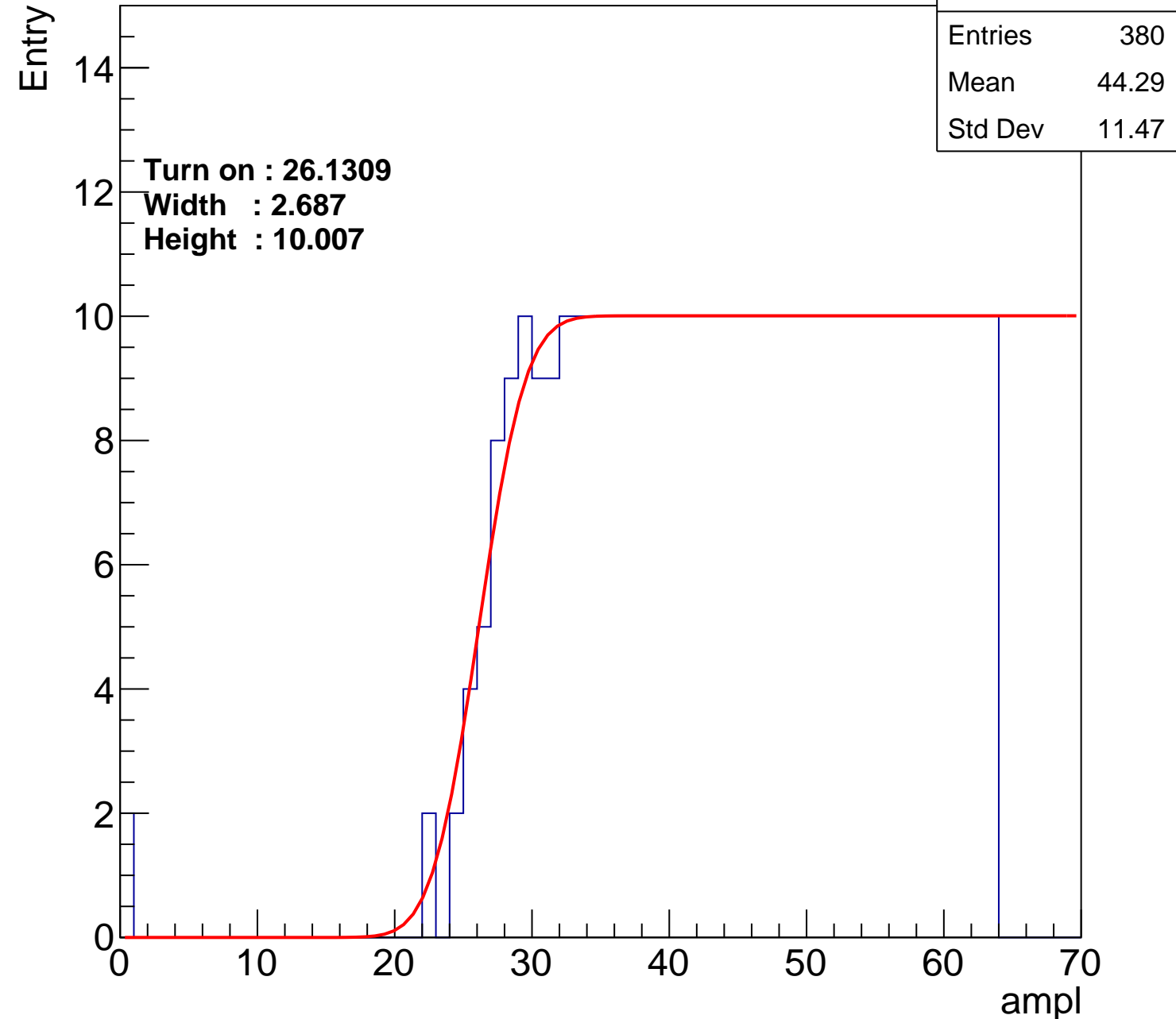
Width : 2.687

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.71
Std Dev	12.22

Turn on : 26.2478

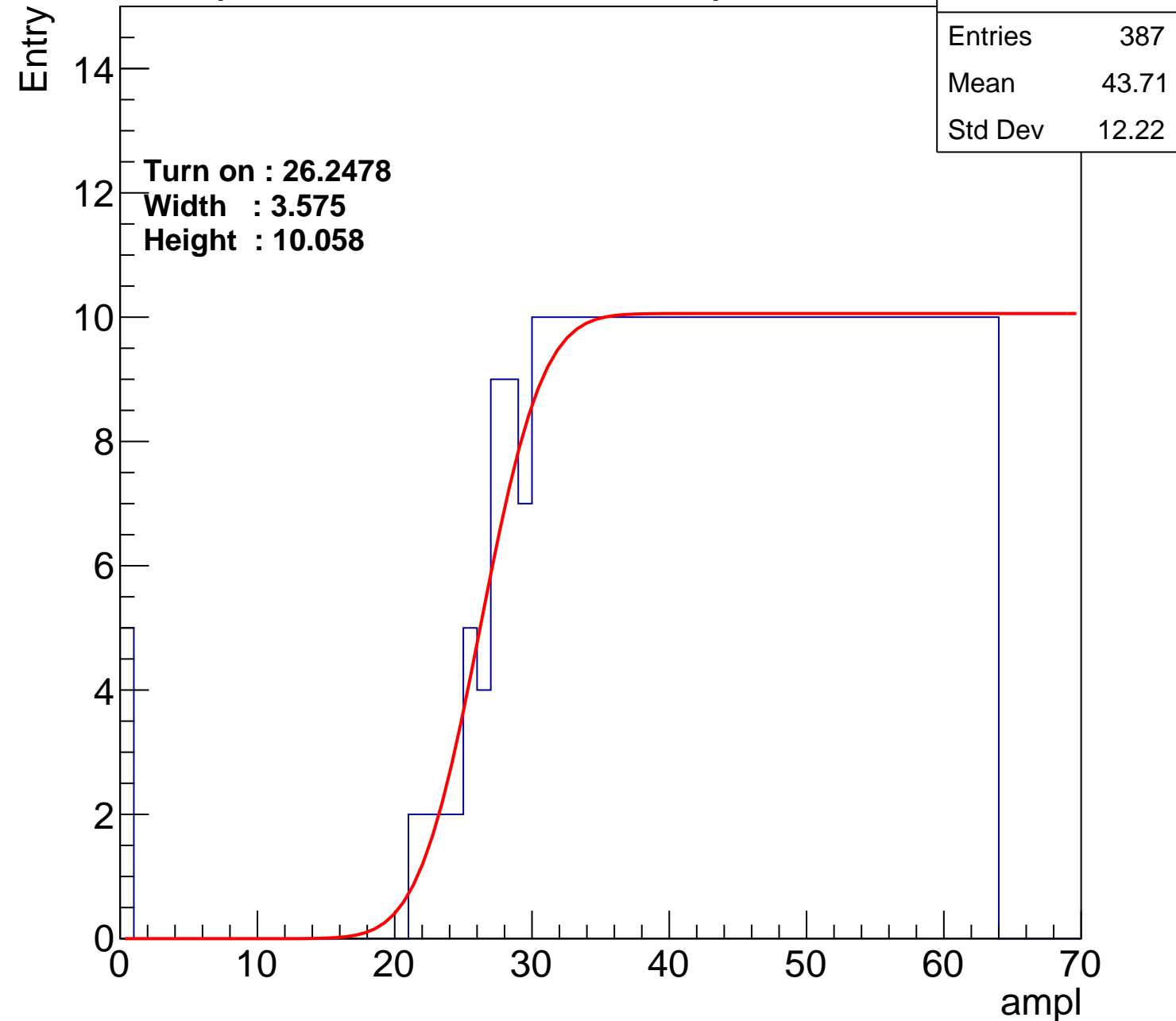
Width : 3.575

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.65
Std Dev	11.18

Turn on : 26.7009

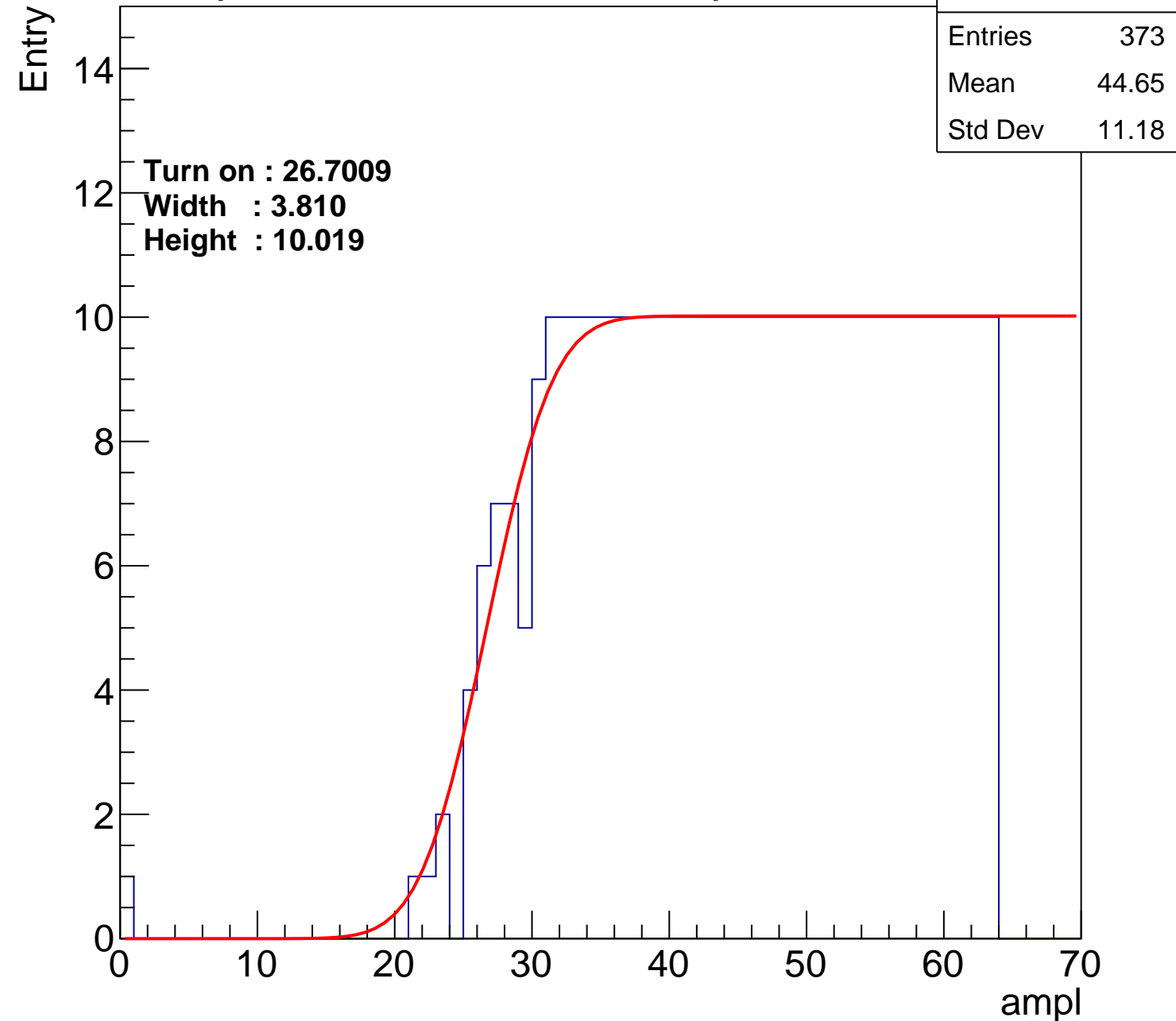
Width : 3.810

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.89
Std Dev	11.81

Turn on : 25.6326

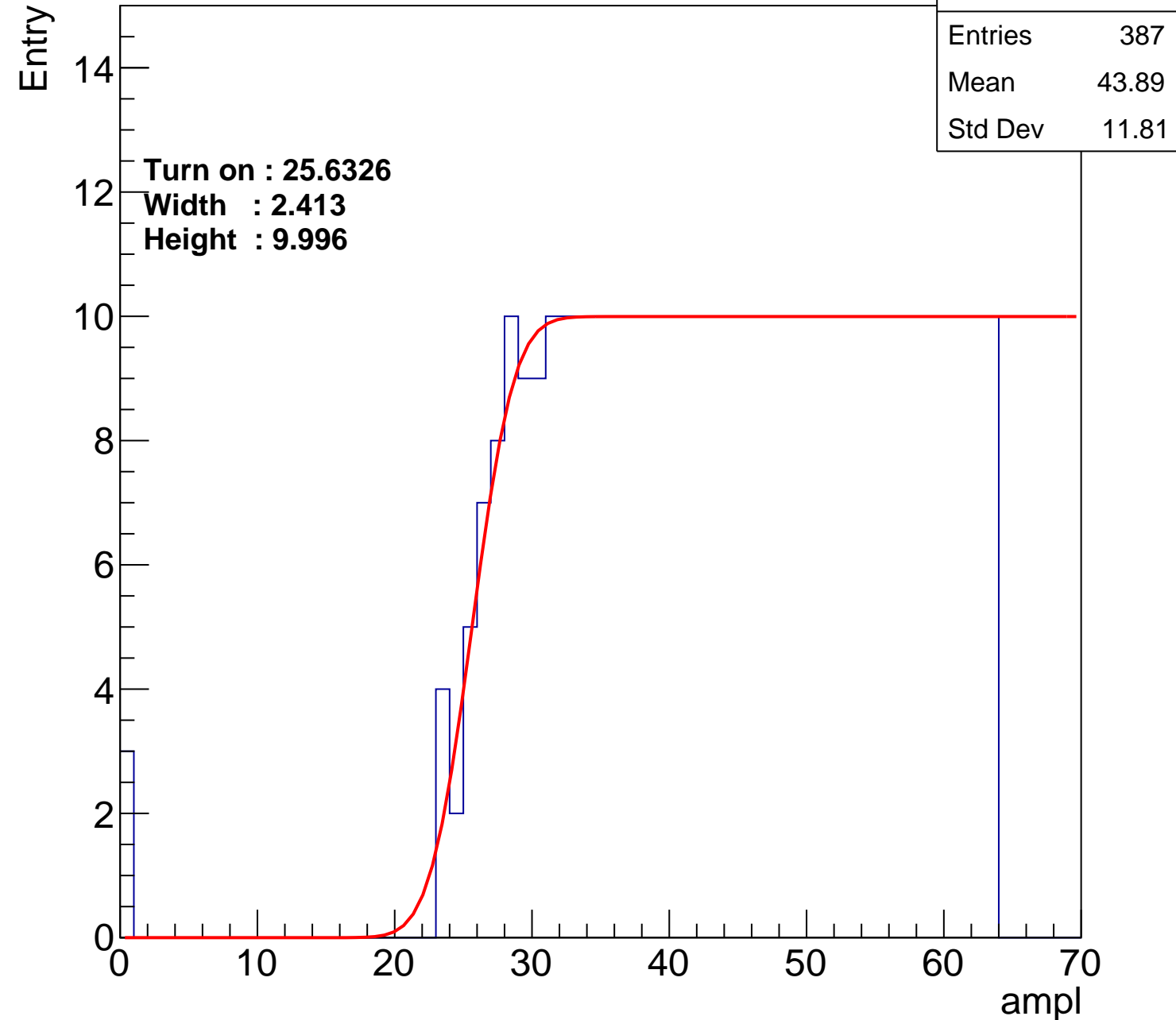
Width : 2.413

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch71

calib_packv5_042523_0143.root, FC#12, port B1

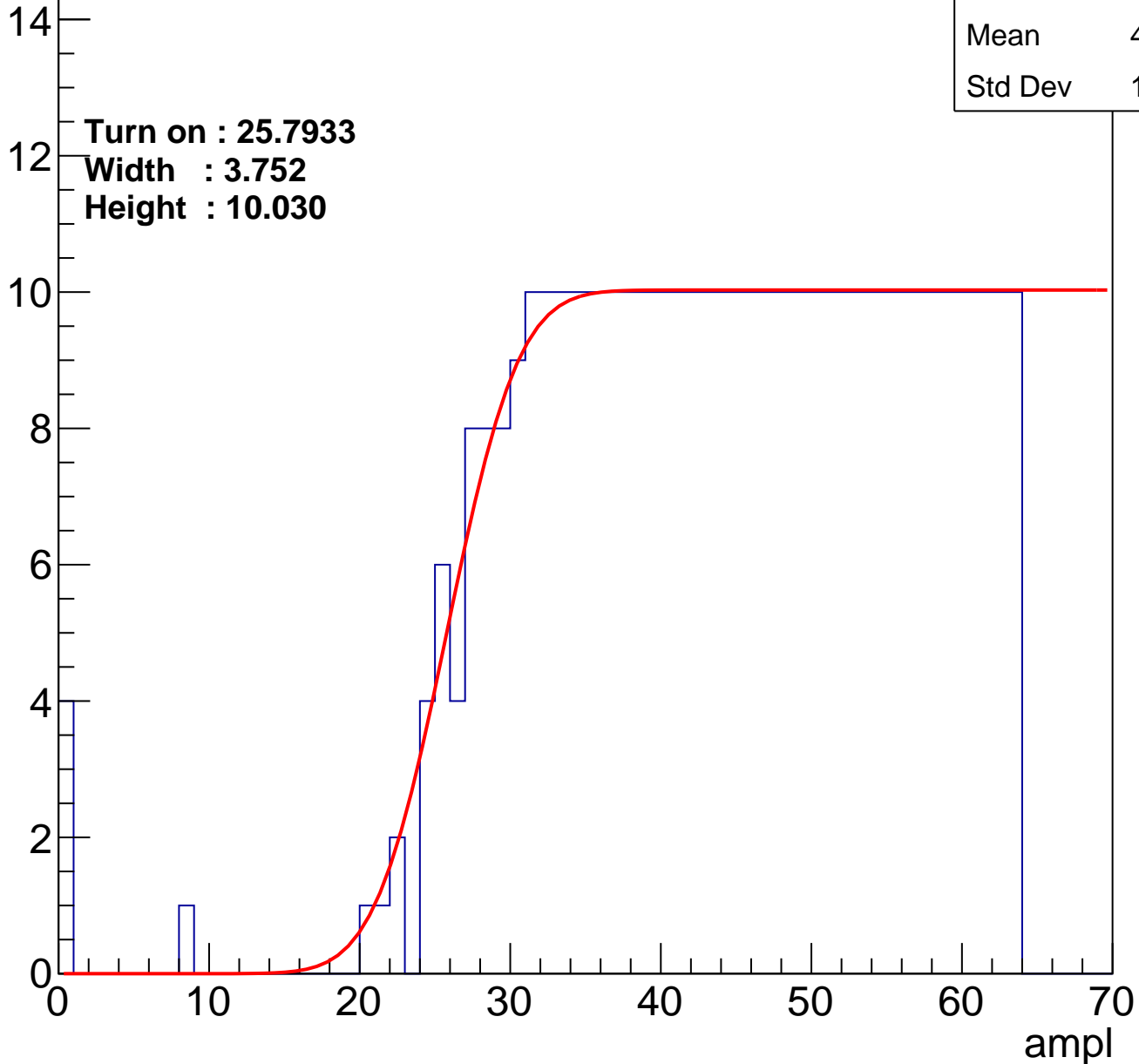
Entries	386
Mean	43.77
Std Dev	12.15

Turn on : 25.7933

Width : 3.752

Height : 10.030

Entry



B0L102S, U25-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.56
Std Dev	12.2

Turn on : 25.4505

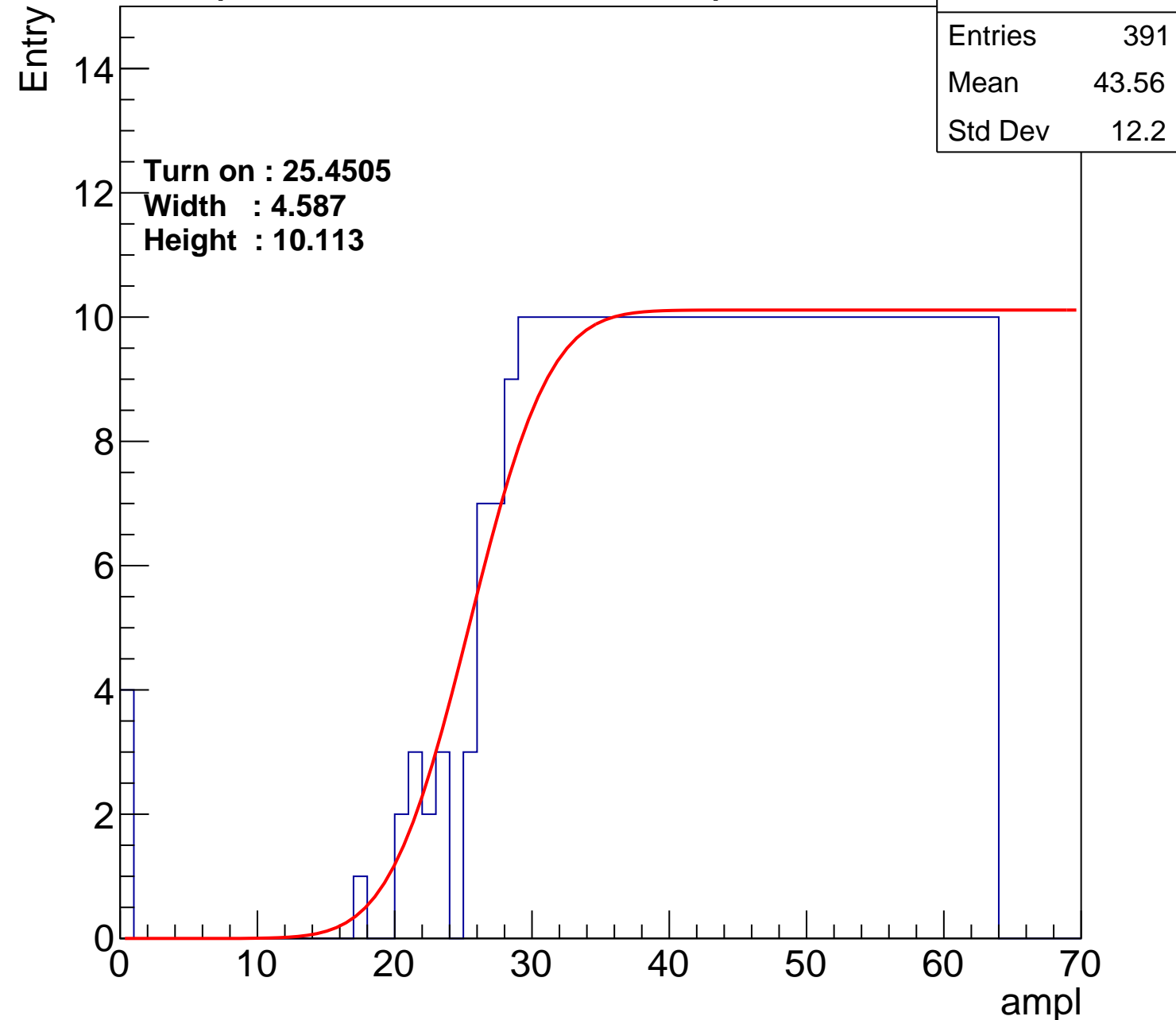
Width : 4.587

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 27.0027

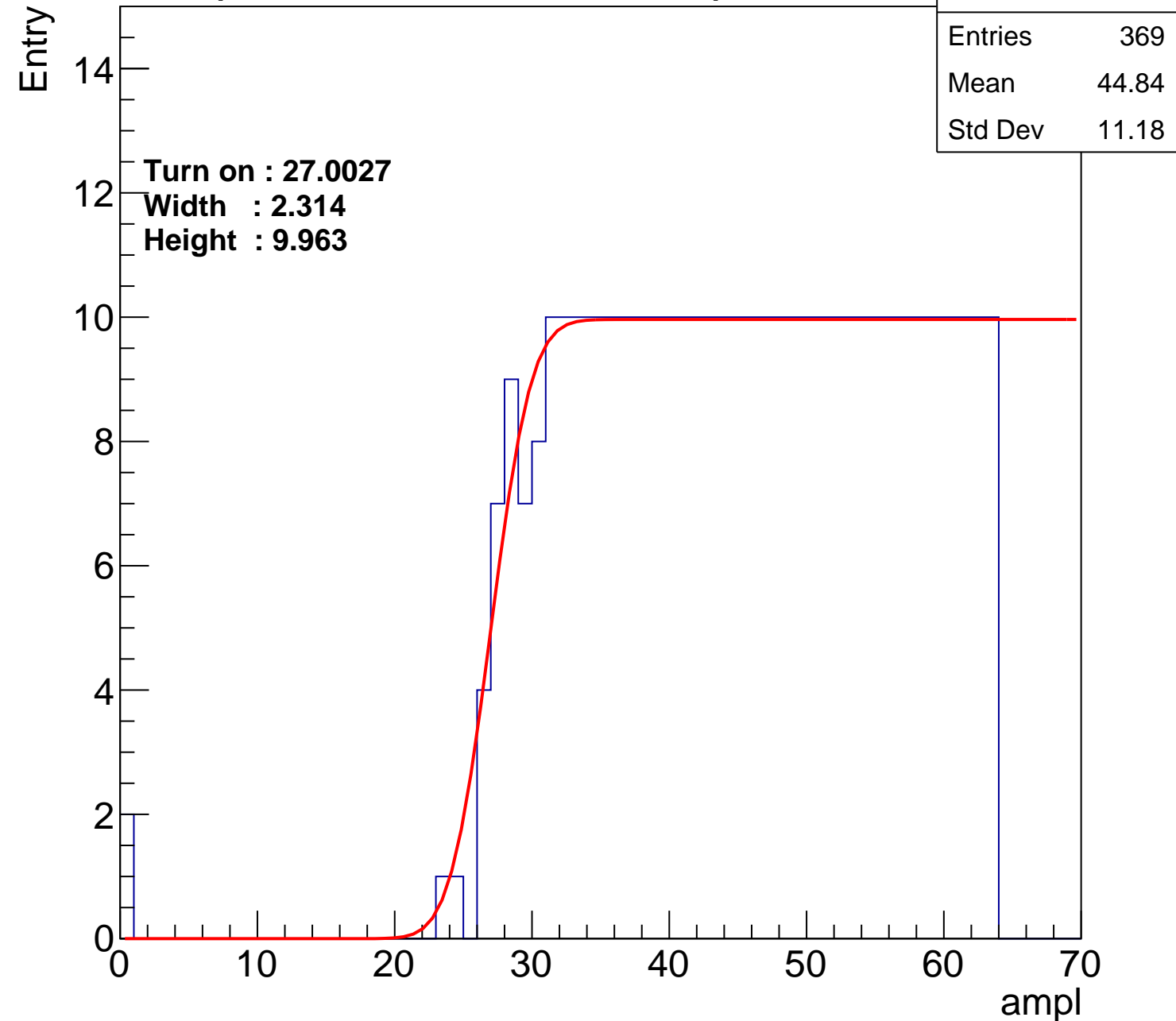
Width : 2.314

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.7515

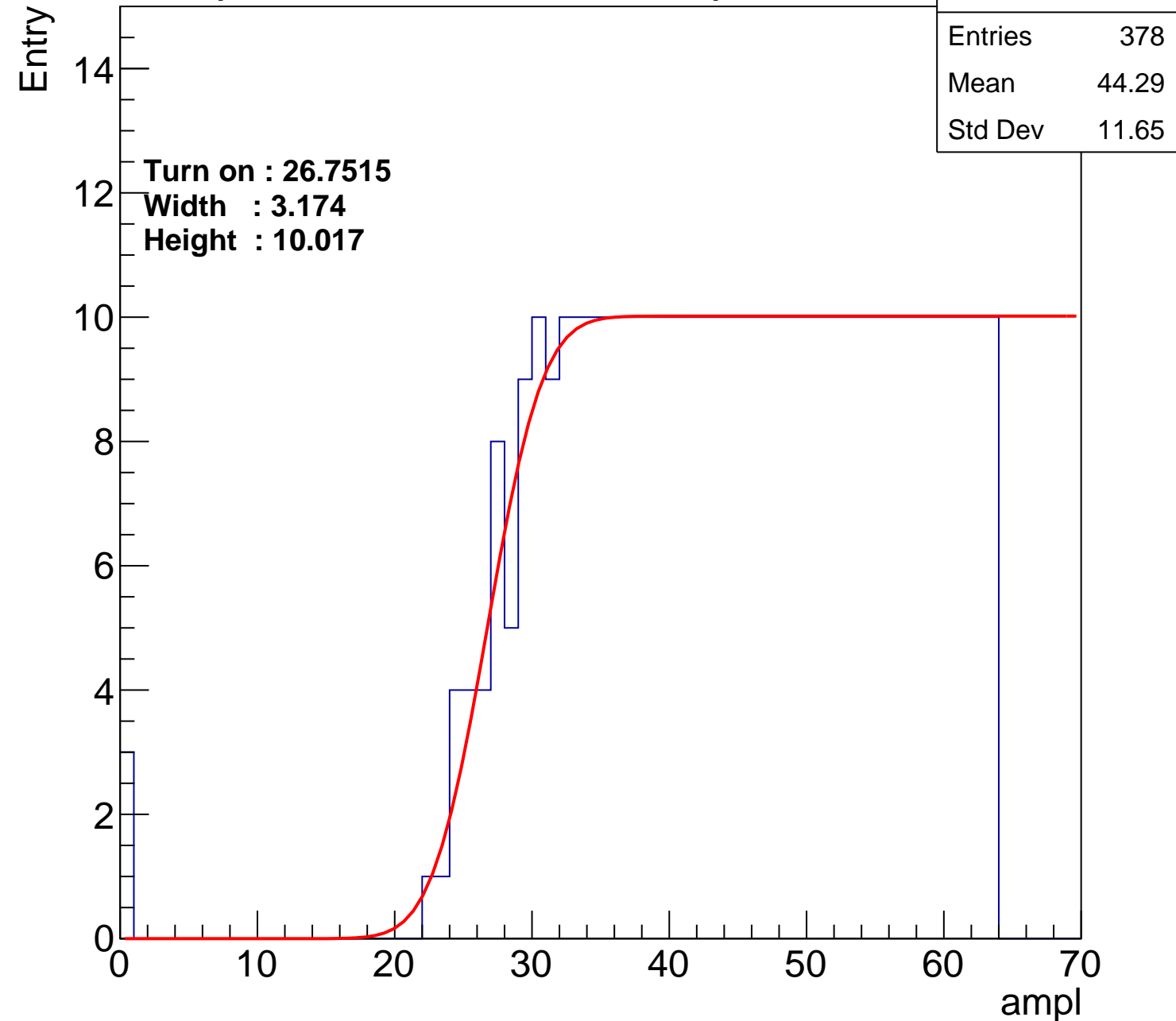
Width : 3.174

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.69
Std Dev	11.44

Turn on : 27.6965

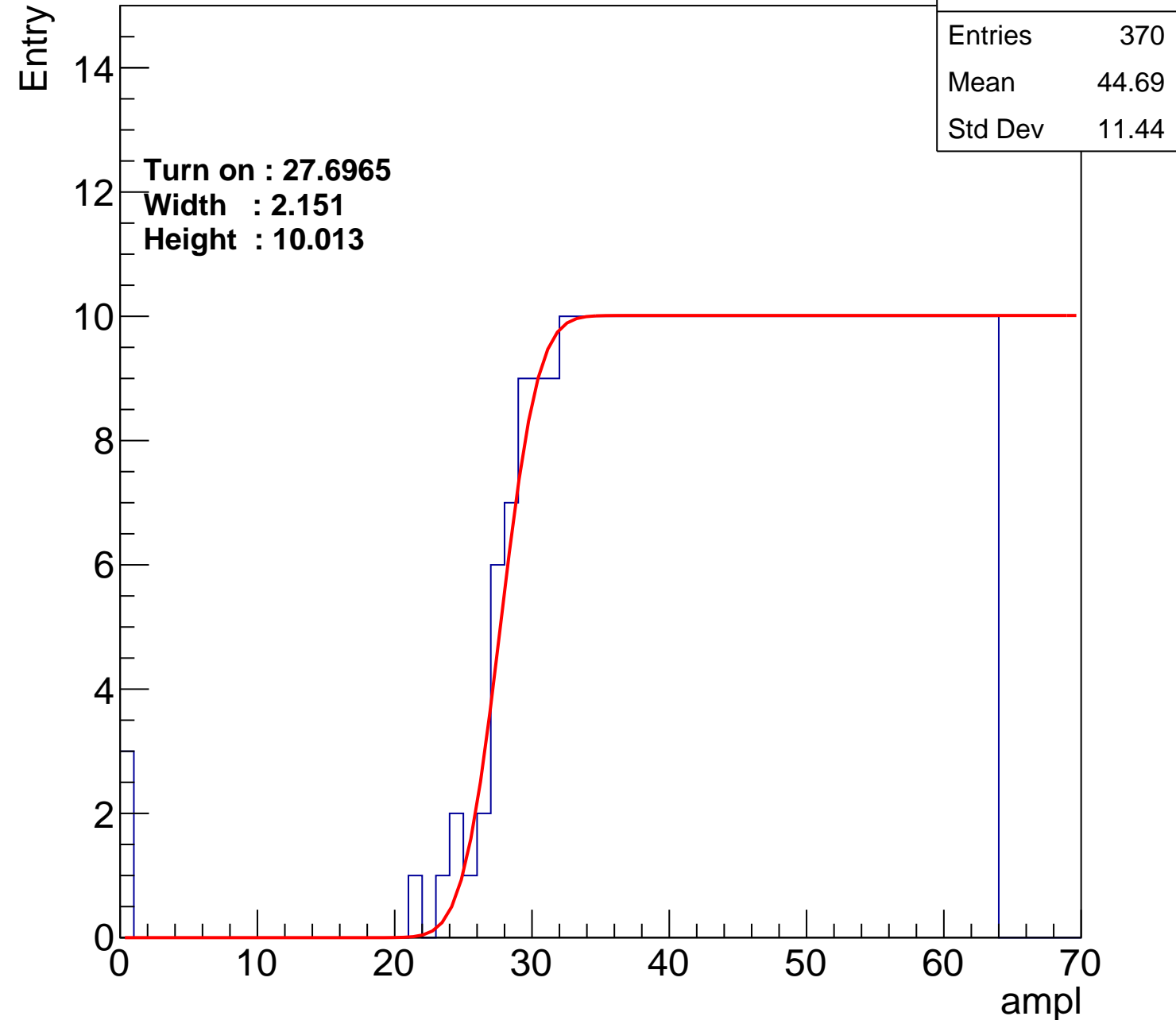
Width : 2.151

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.54
Std Dev	11.84

Turn on : 27.7575

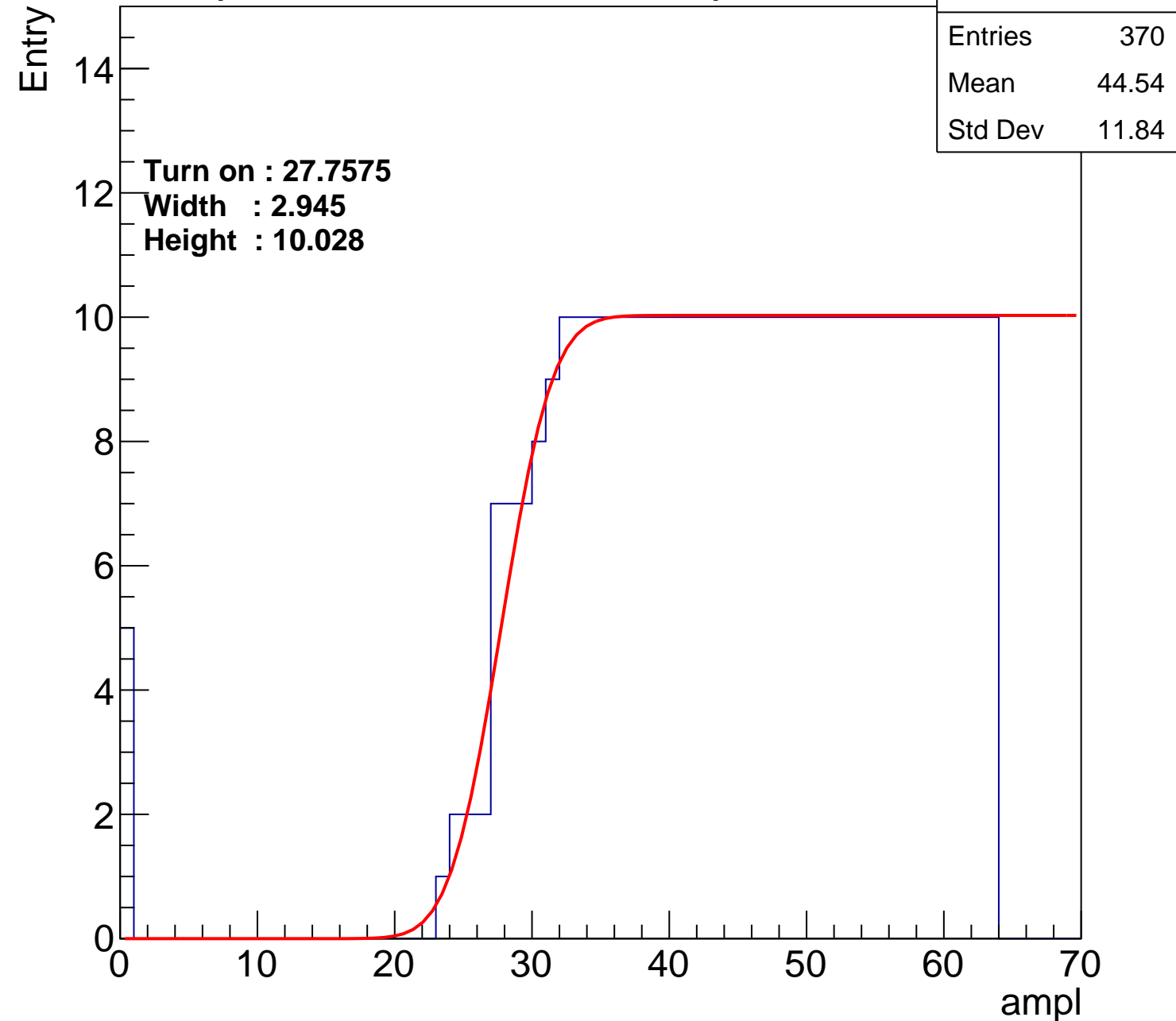
Width : 2.945

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.8087

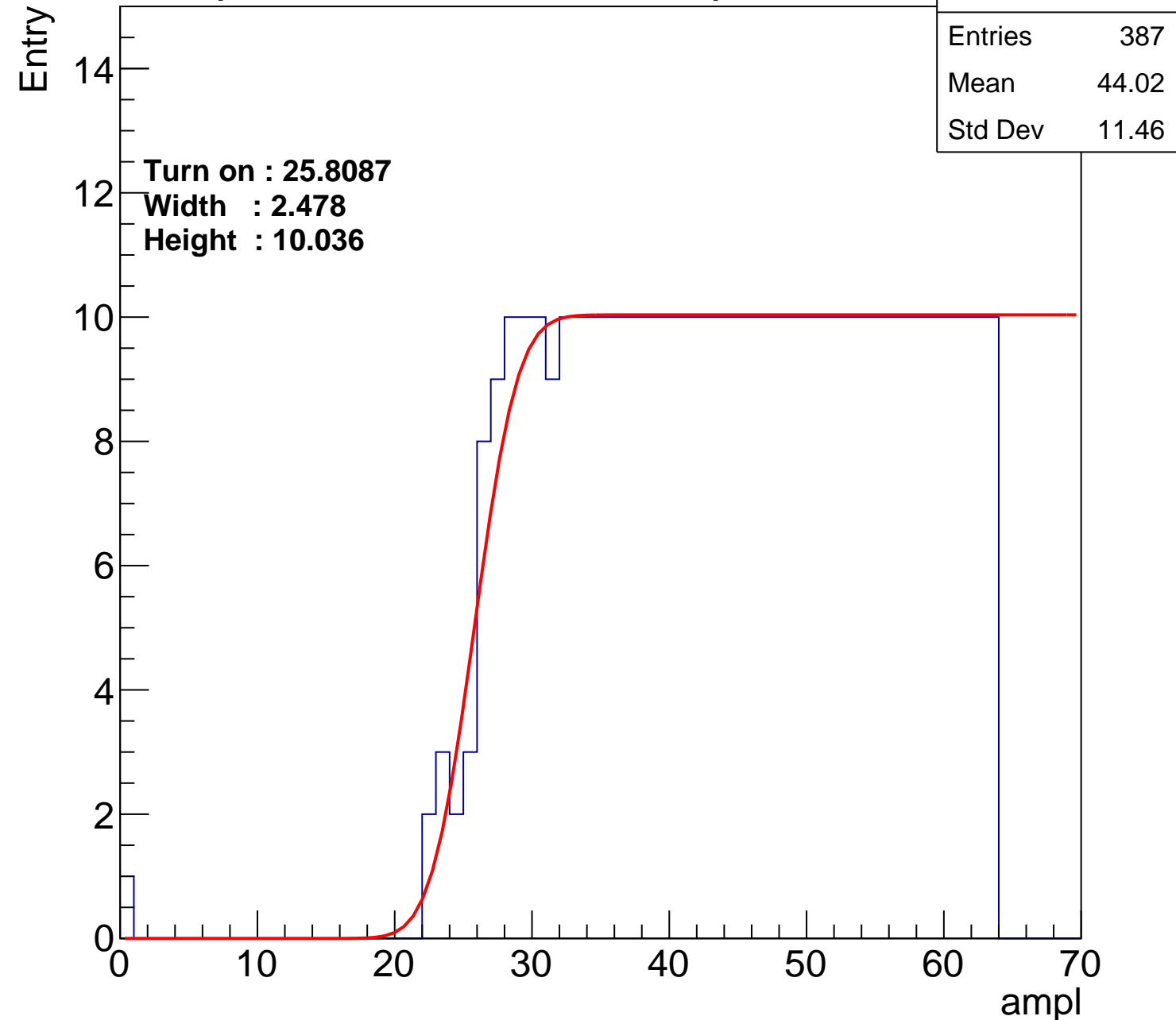
Width : 2.478

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.63
Std Dev	11.48

Turn on : 27.0599

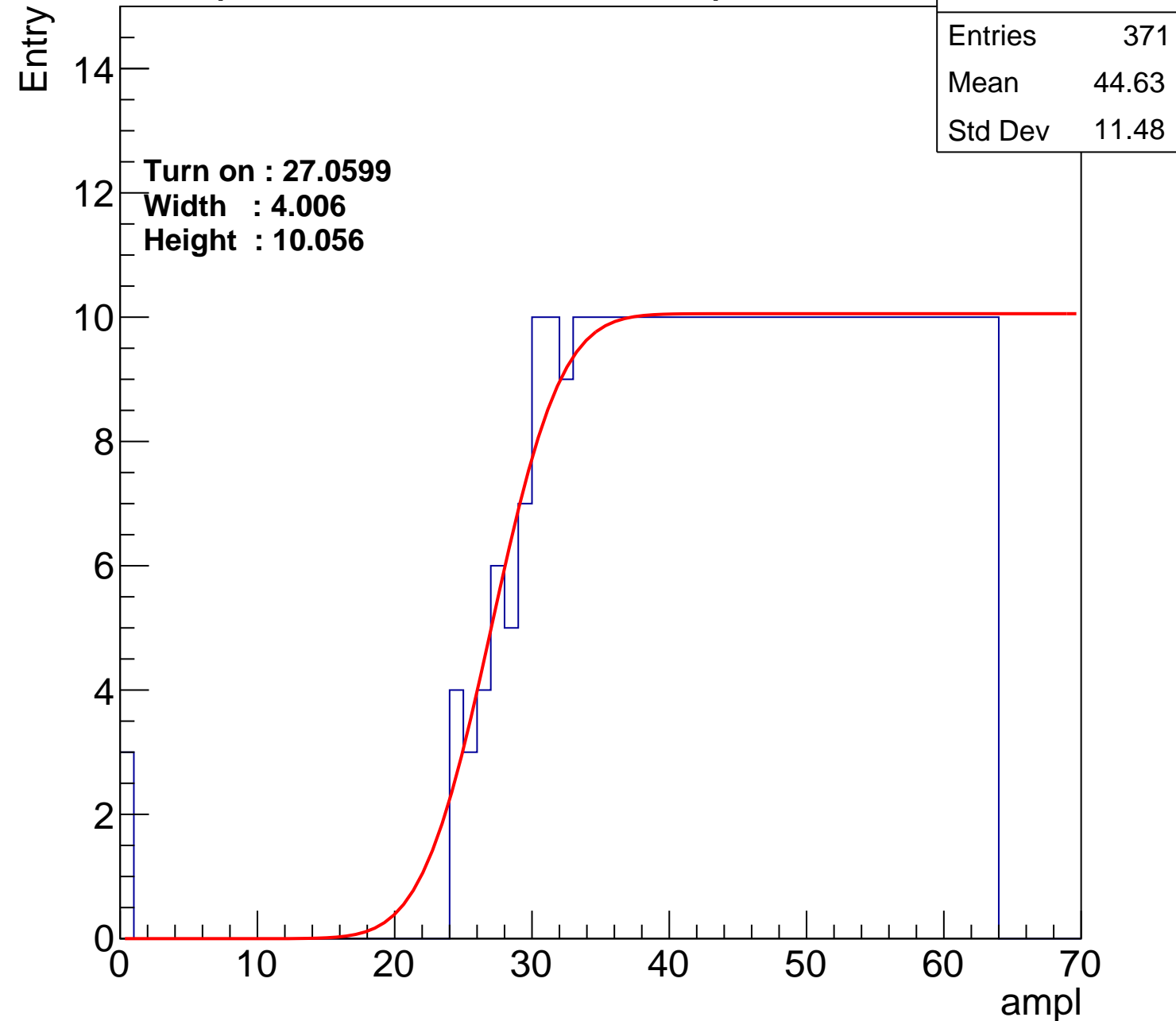
Width : 4.006

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 26.1596

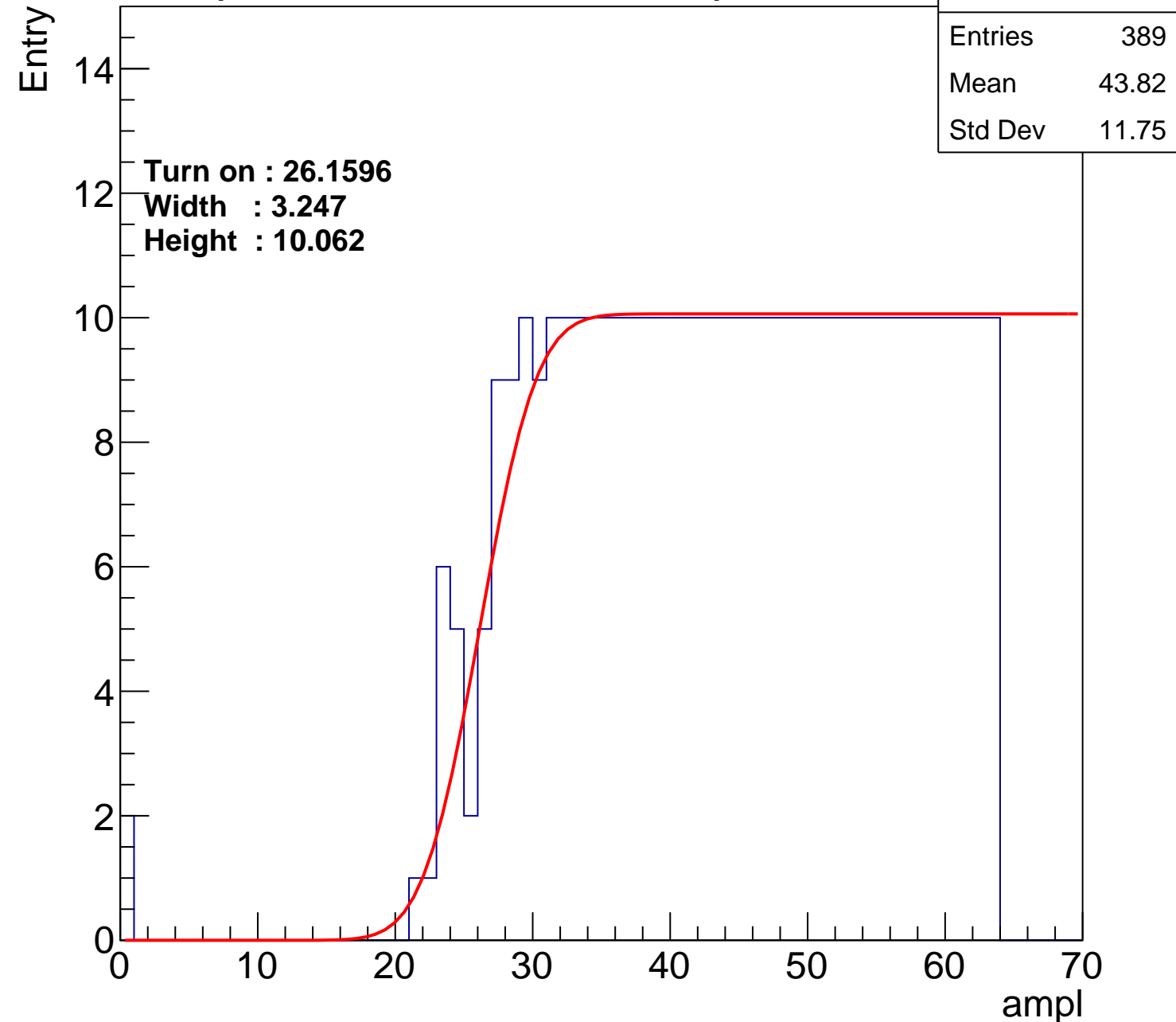
Width : 3.247

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.95
Std Dev	12.31

Turn on : 23.5368

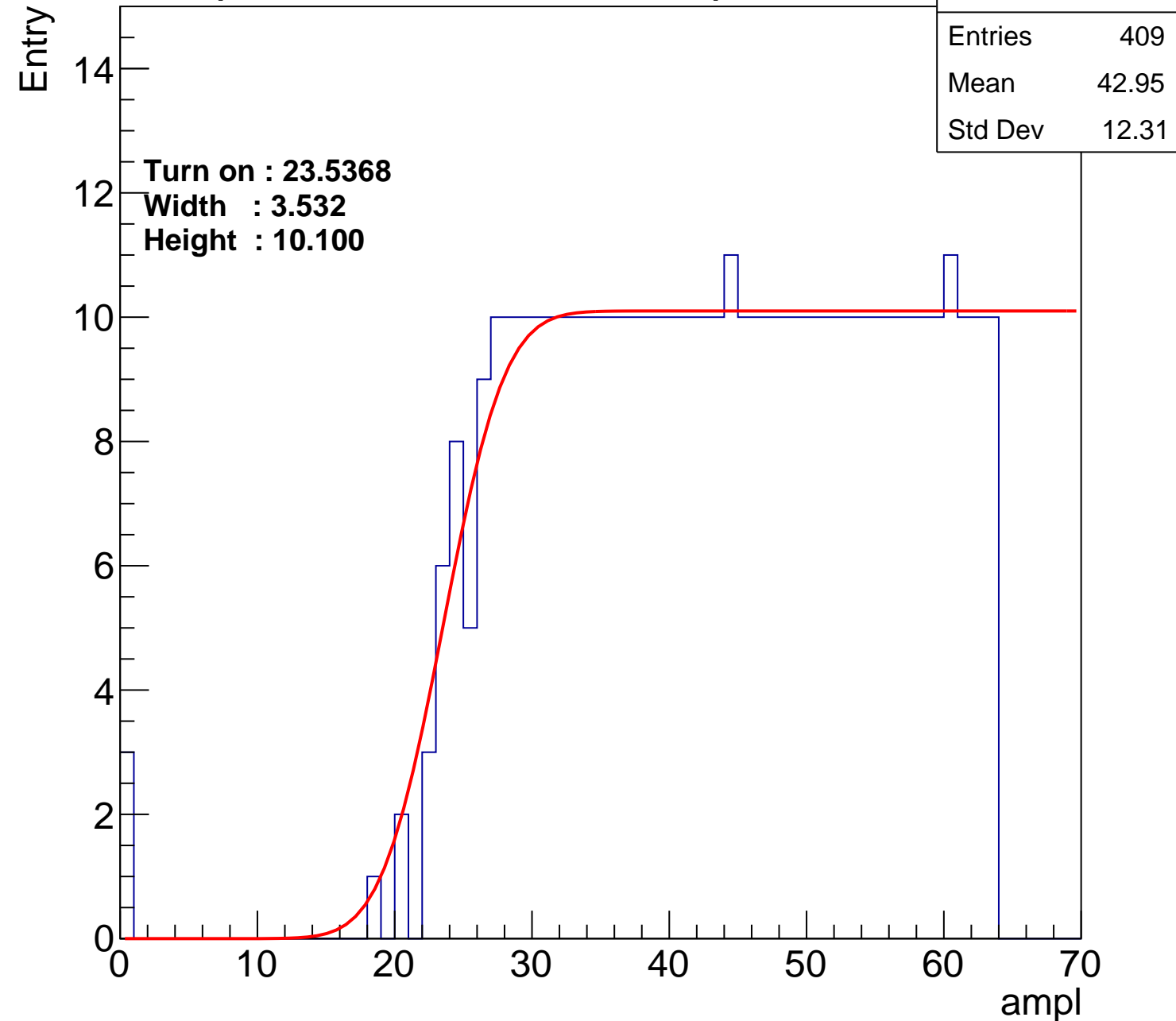
Width : 3.532

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch81

calib_packv5_042523_0143.root, FC#12, port B1

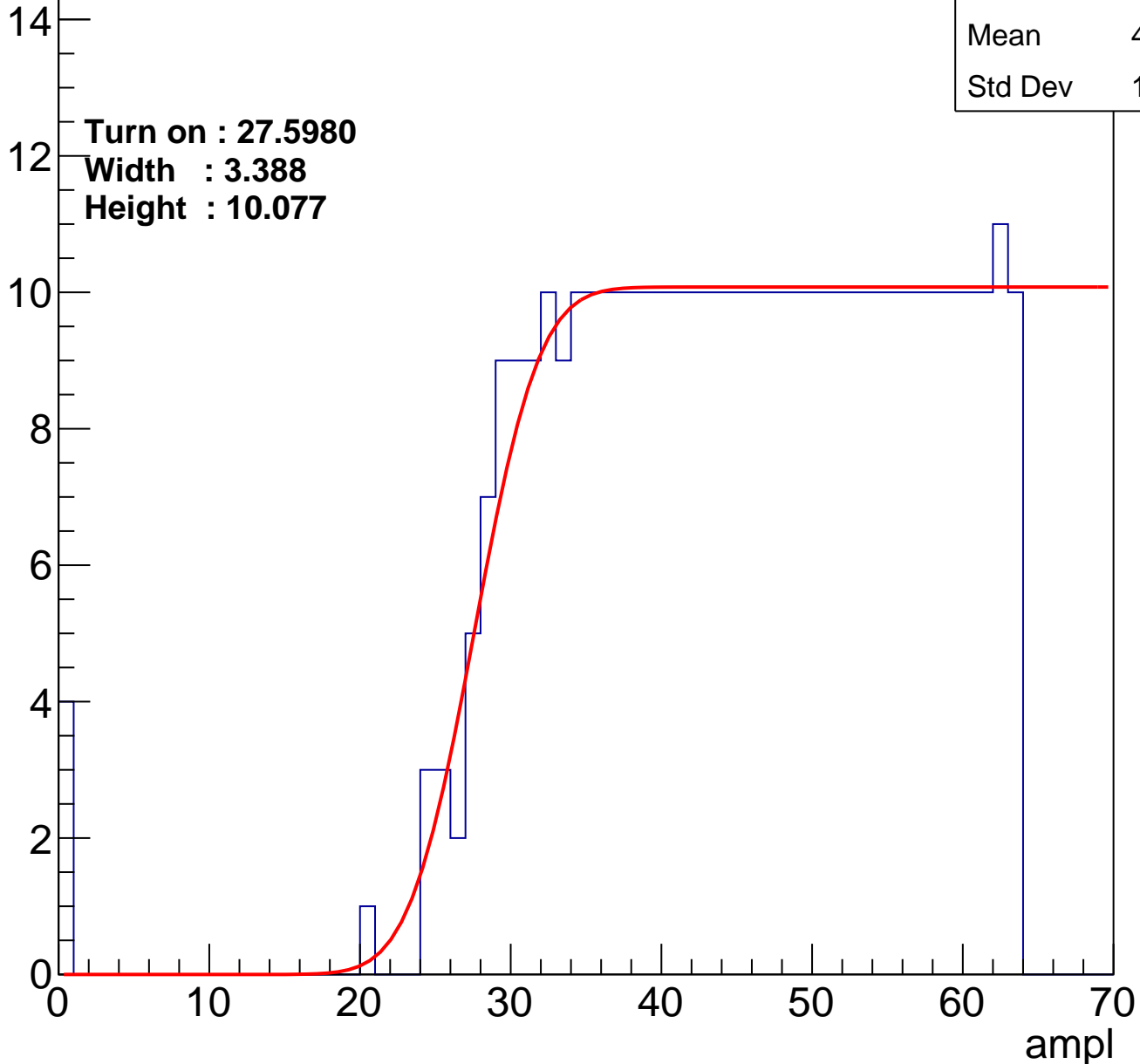
Entries	372
Mean	44.59
Std Dev	11.72

Turn on : 27.5980

Width : 3.388

Height : 10.077

Entry



B0L102S, U25-ch82

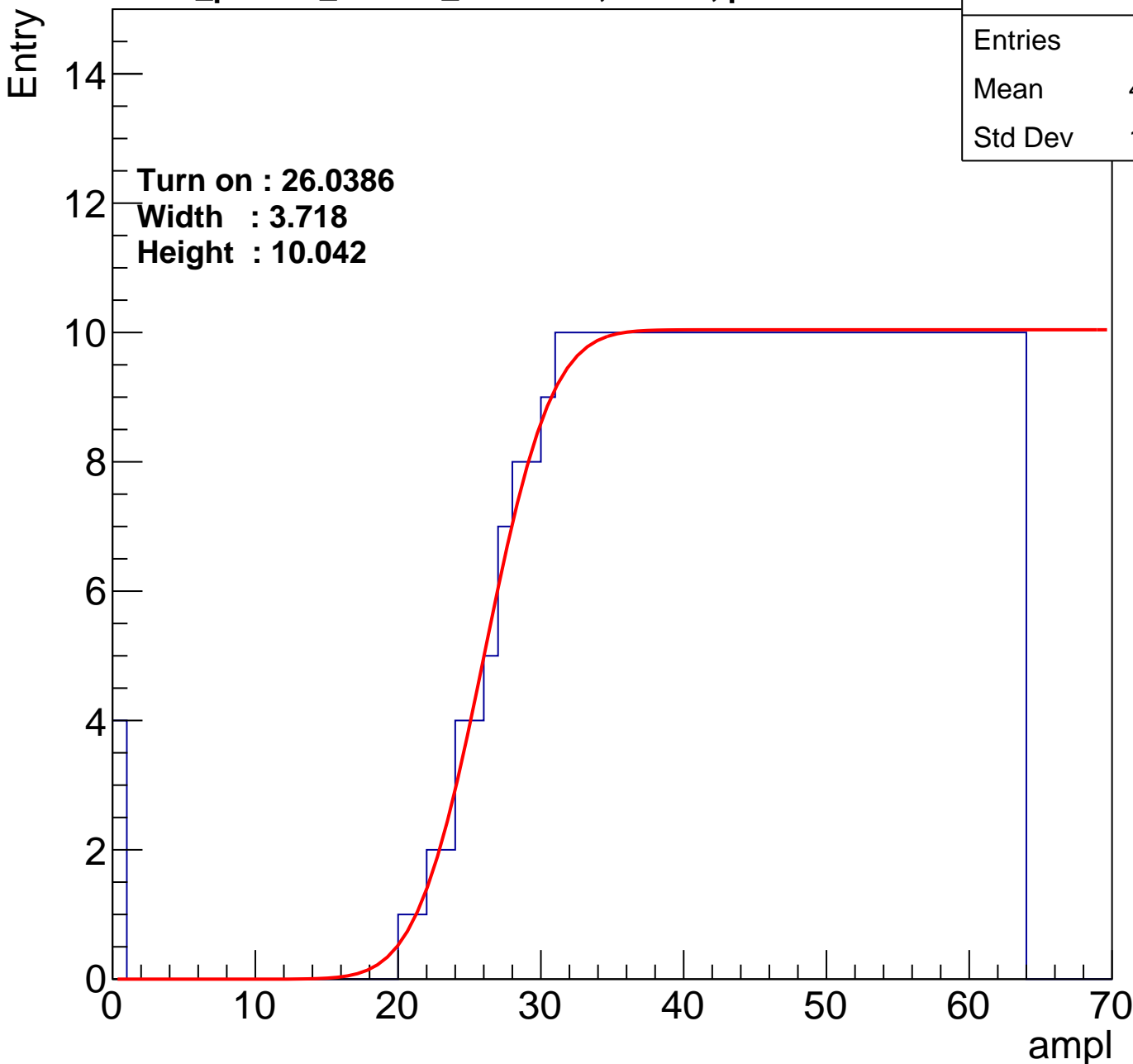
calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.85
Std Dev	12.05

Turn on : 26.0386

Width : 3.718

Height : 10.042



B0L102S, U25-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.73
Std Dev	12.19

Turn on : 26.0898

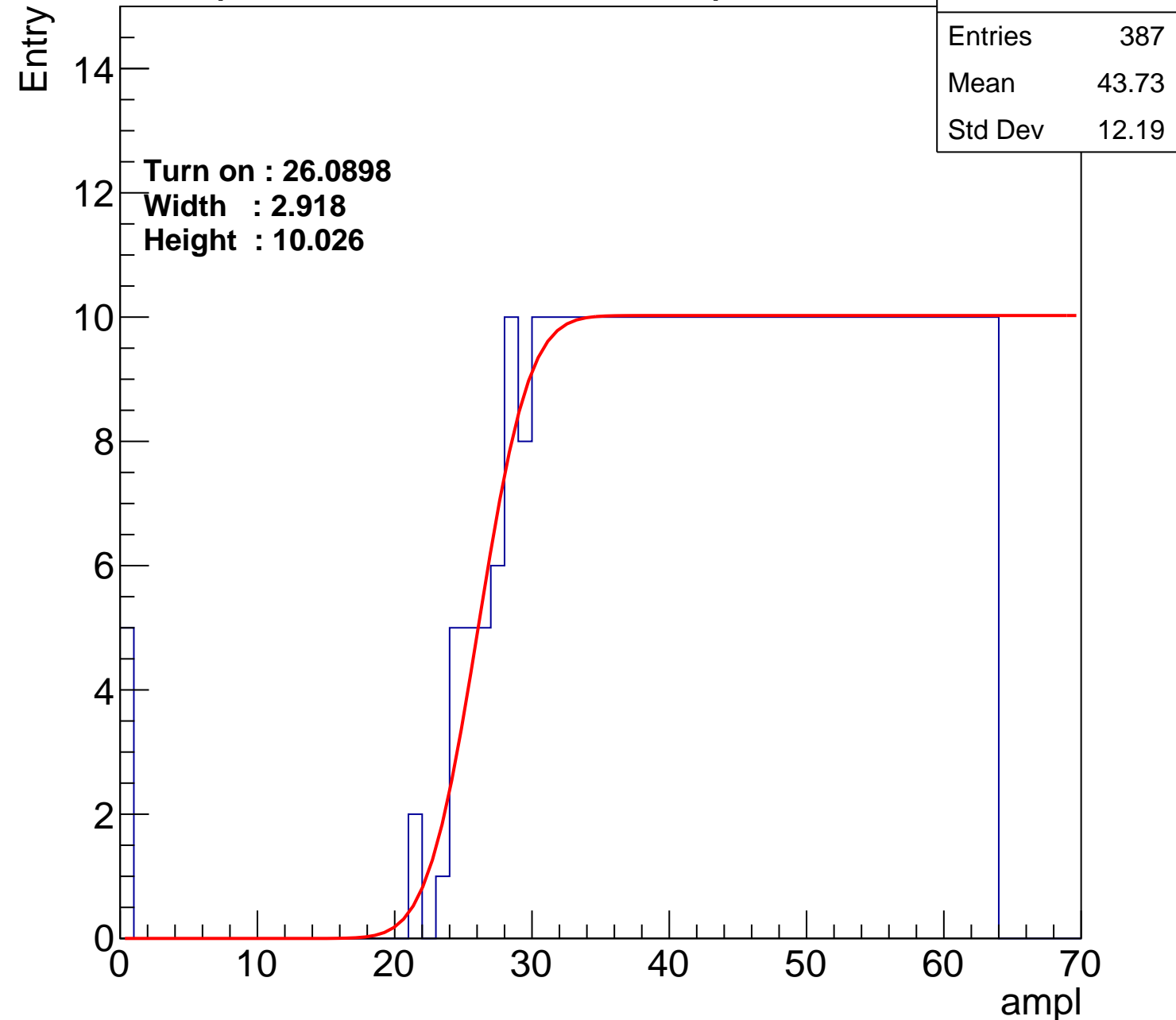
Width : 2.918

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.54
Std Dev	11.58

Turn on : 27.8842

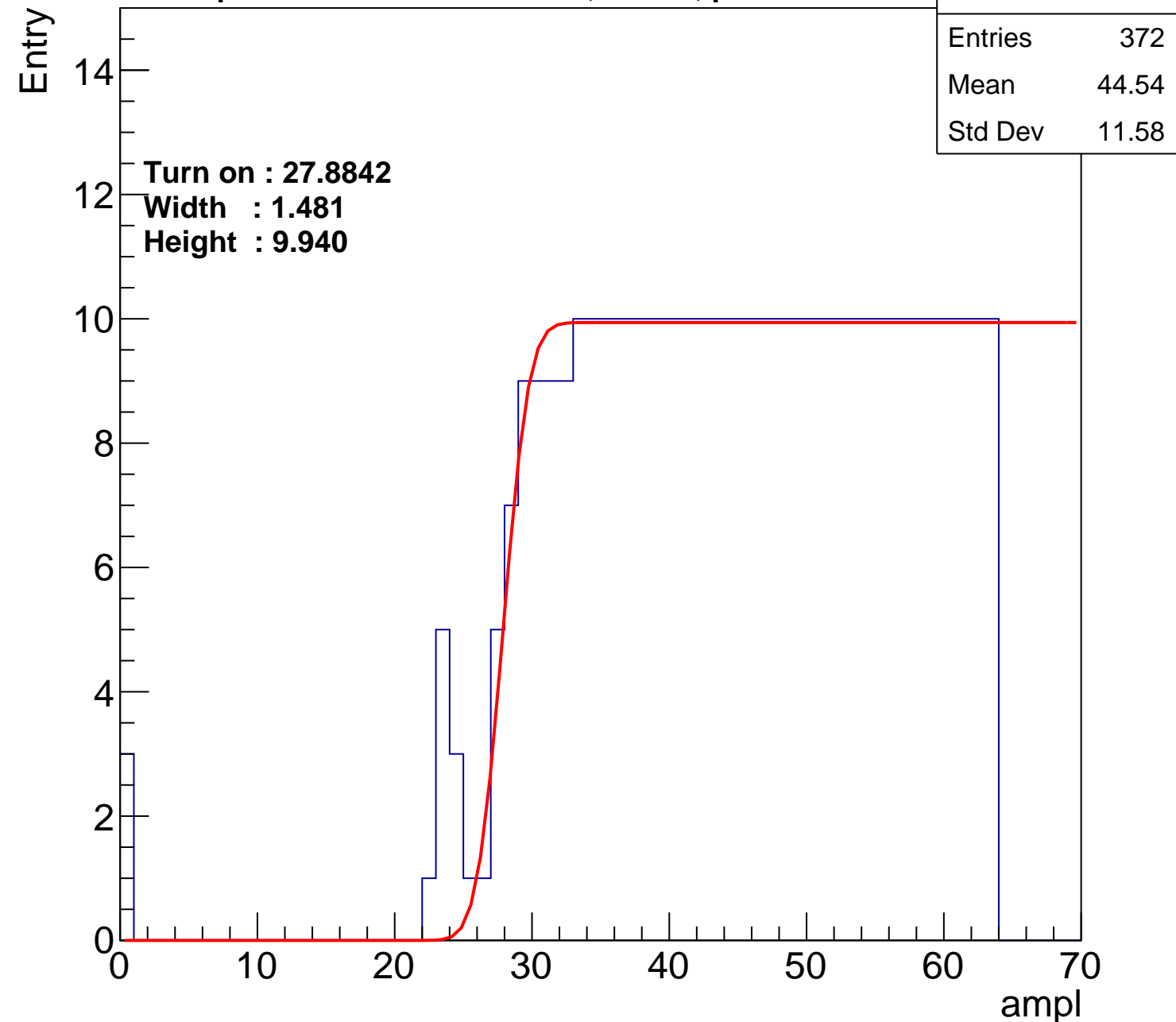
Width : 1.481

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch85

calib_packv5_042523_0143.root, FC#12, port B1

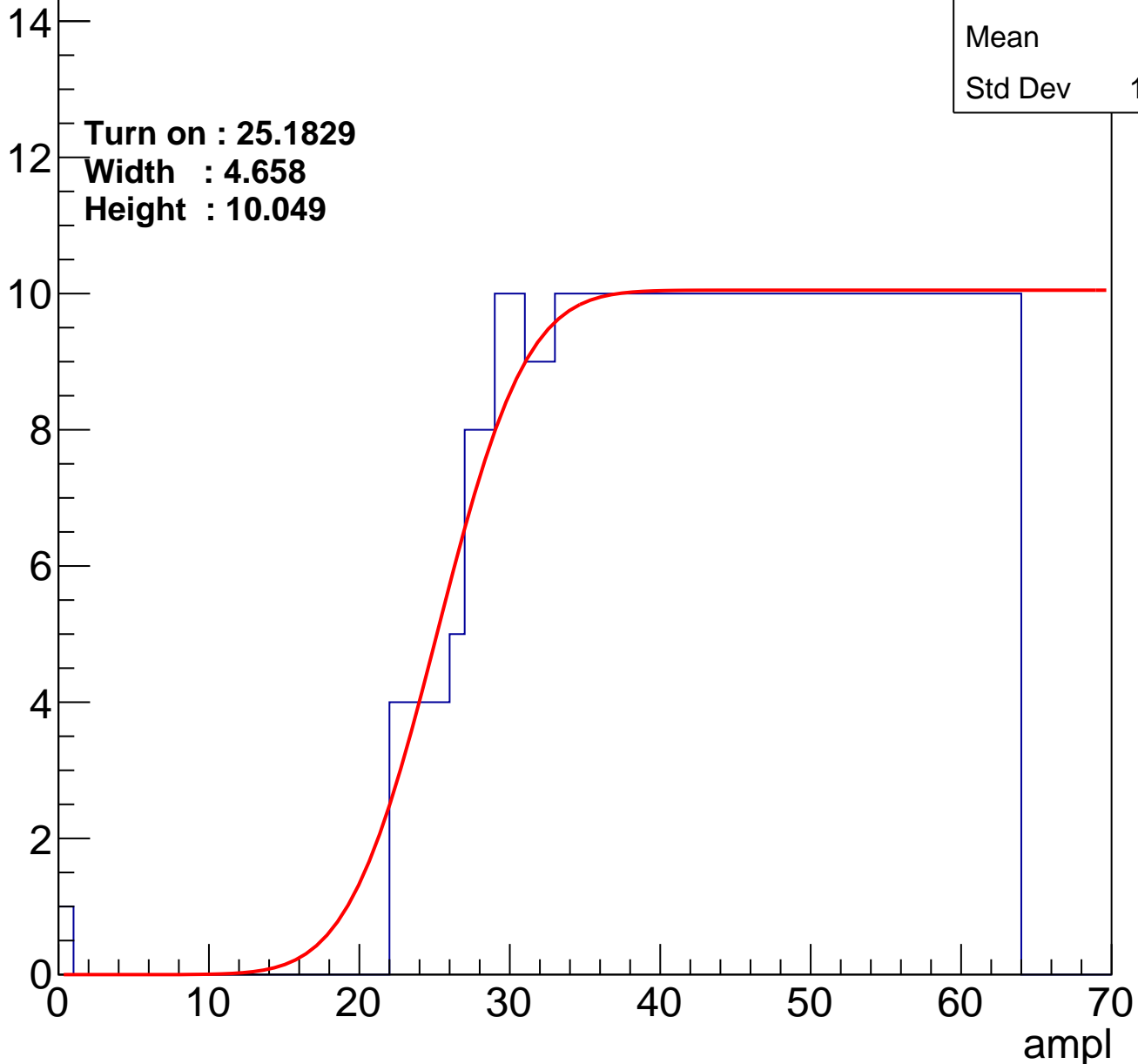
Entries	386
Mean	44
Std Dev	11.54

Turn on : 25.1829

Width : 4.658

Height : 10.049

Entry



B0L102S, U25-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.88
Std Dev	11.59

Turn on : 25.2813

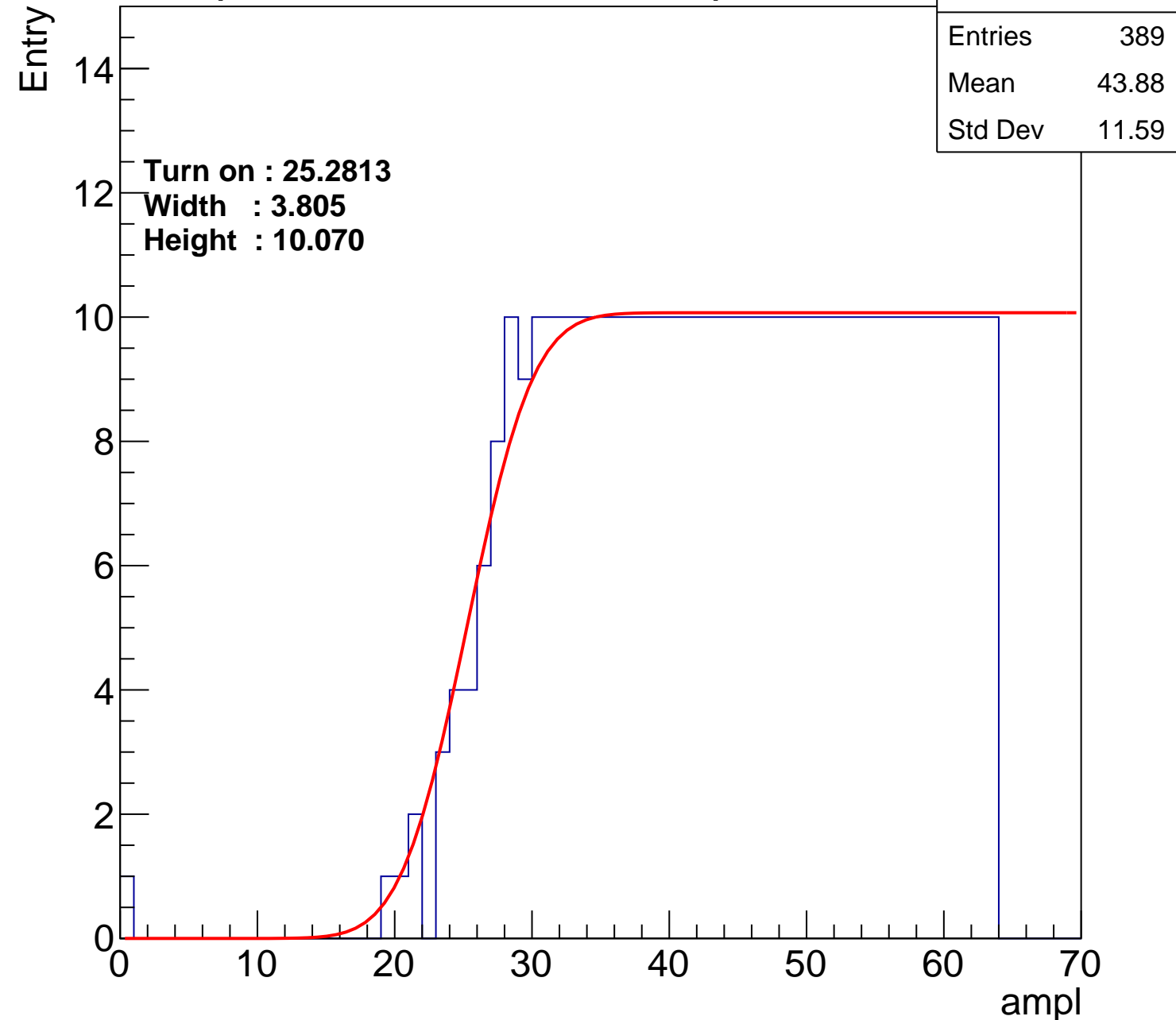
Width : 3.805

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.96
Std Dev	11.69

Turn on : 25.8176

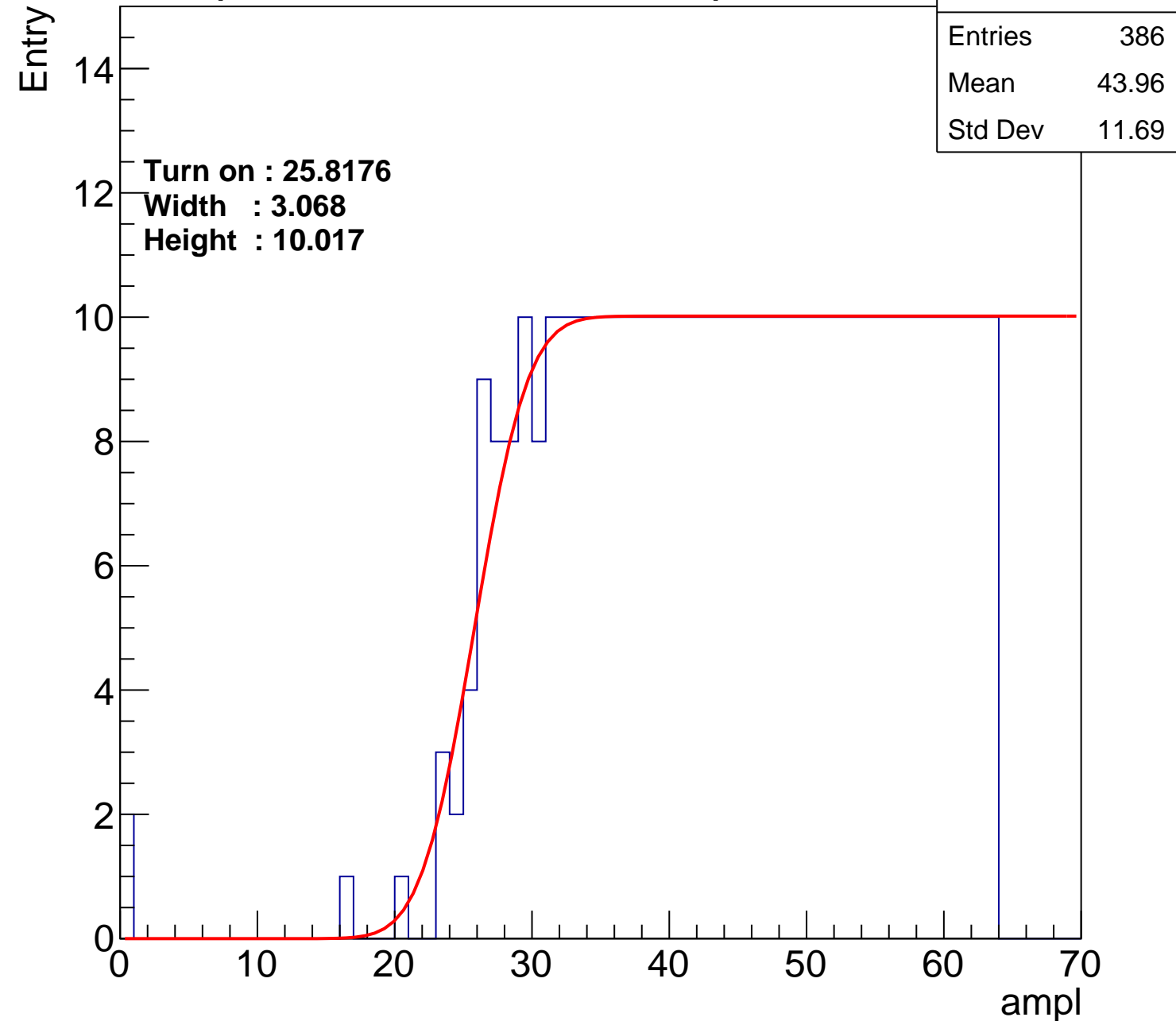
Width : 3.068

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.07
Std Dev	12.06

Turn on : 24.6868

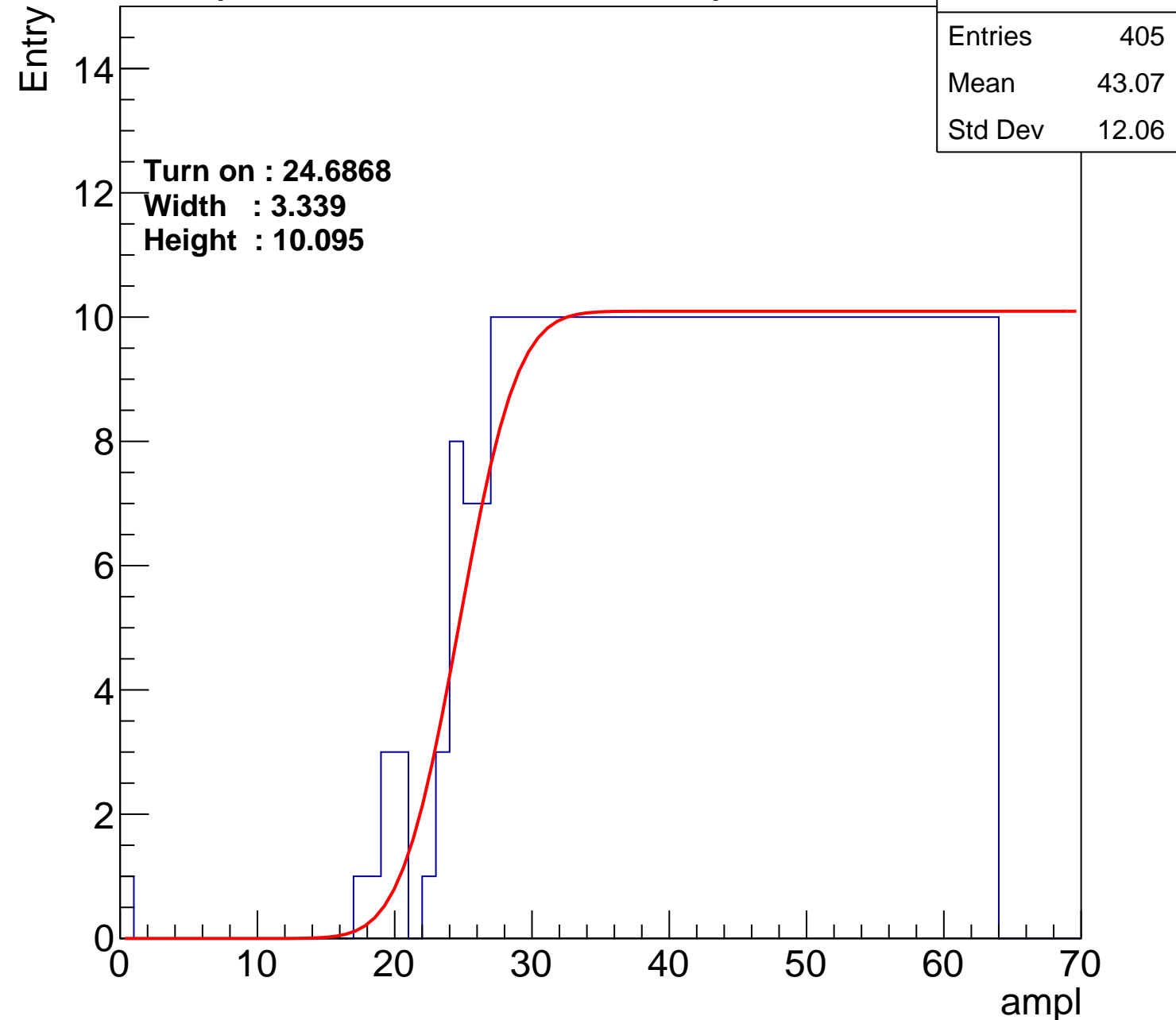
Width : 3.339

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.04
Std Dev	11.79

Turn on : 26.1343

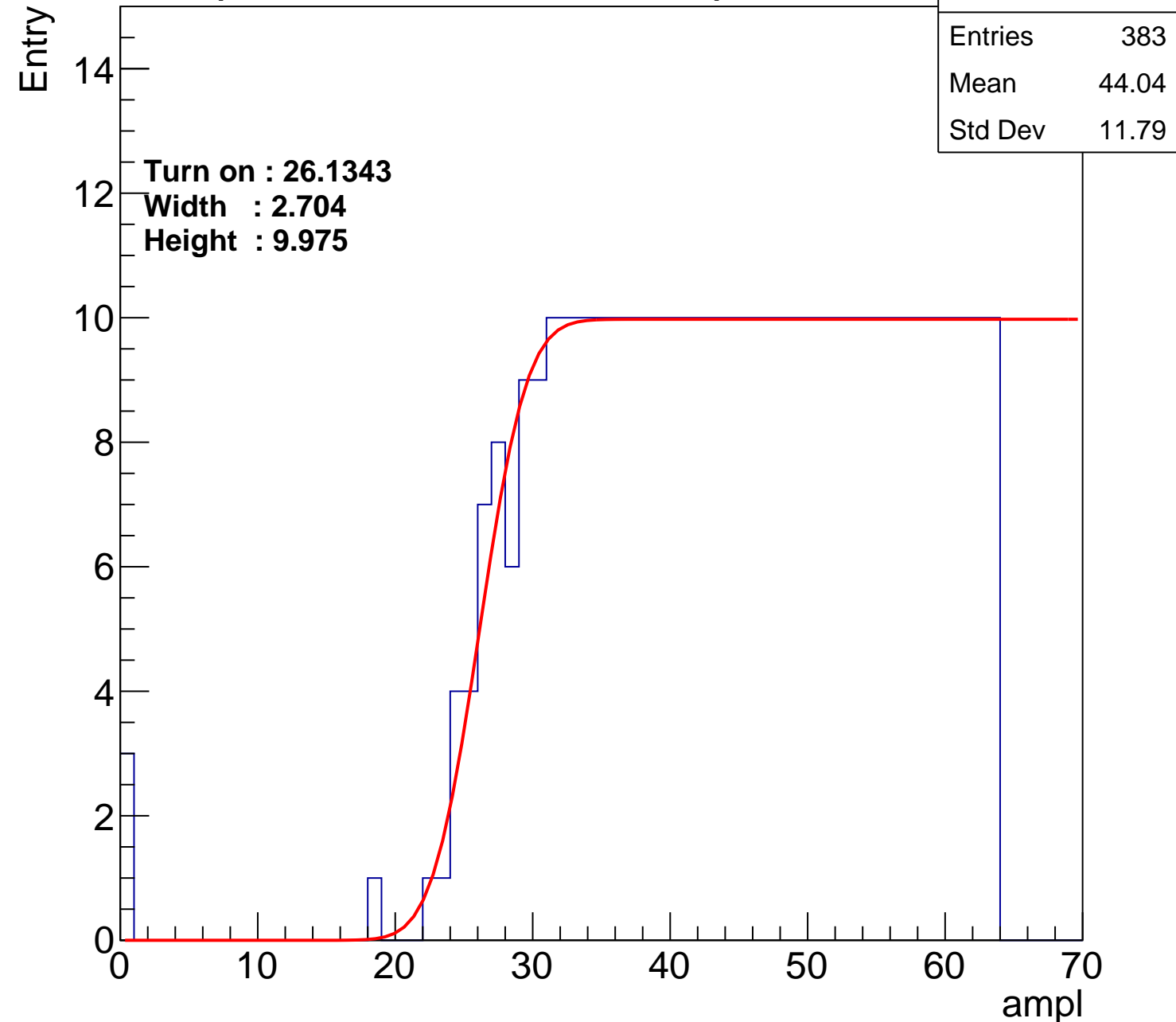
Width : 2.704

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.68
Std Dev	11.3

Turn on : 27.4018

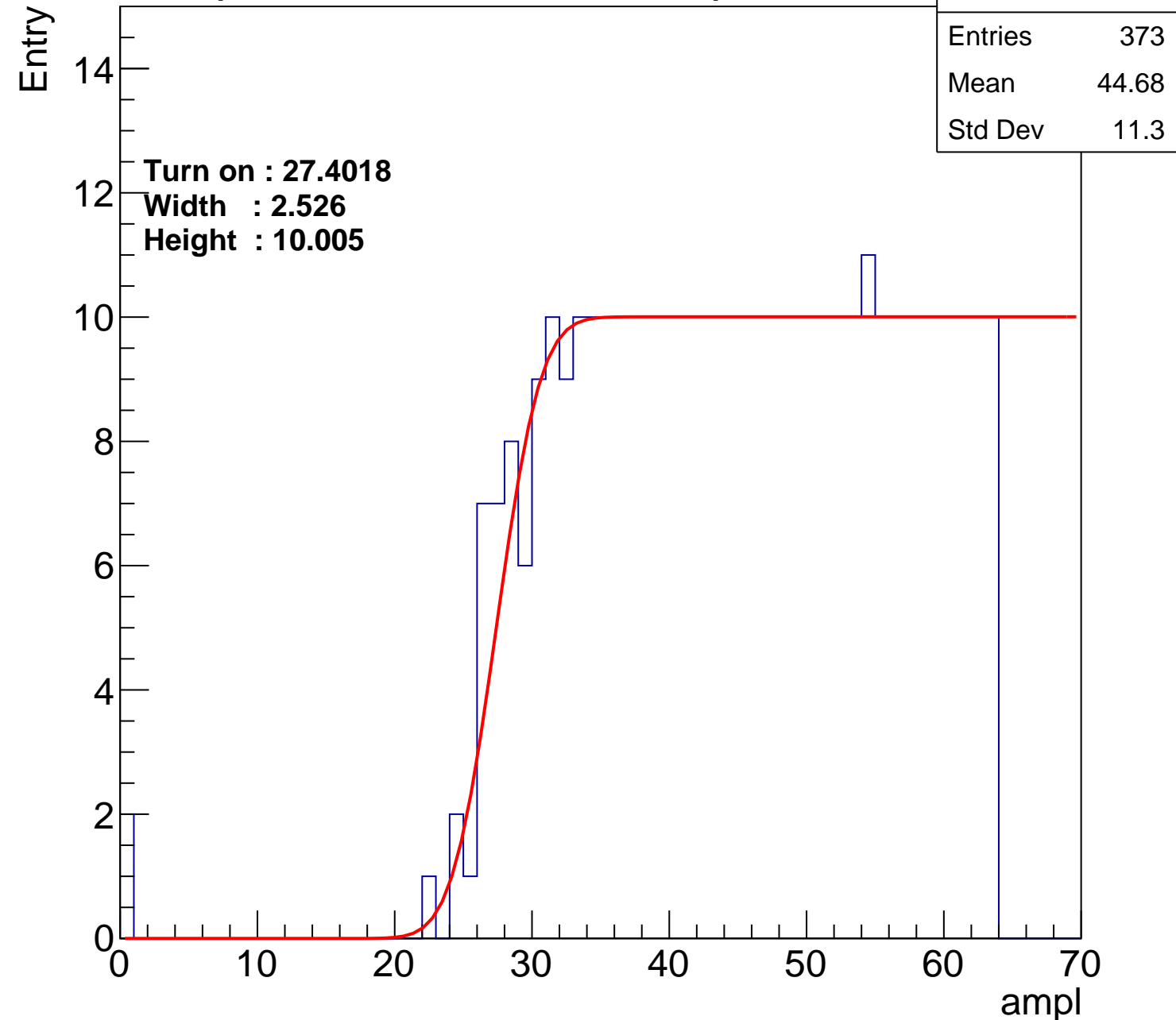
Width : 2.526

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch91

calib_packv5_042523_0143.root, FC#12, port B1

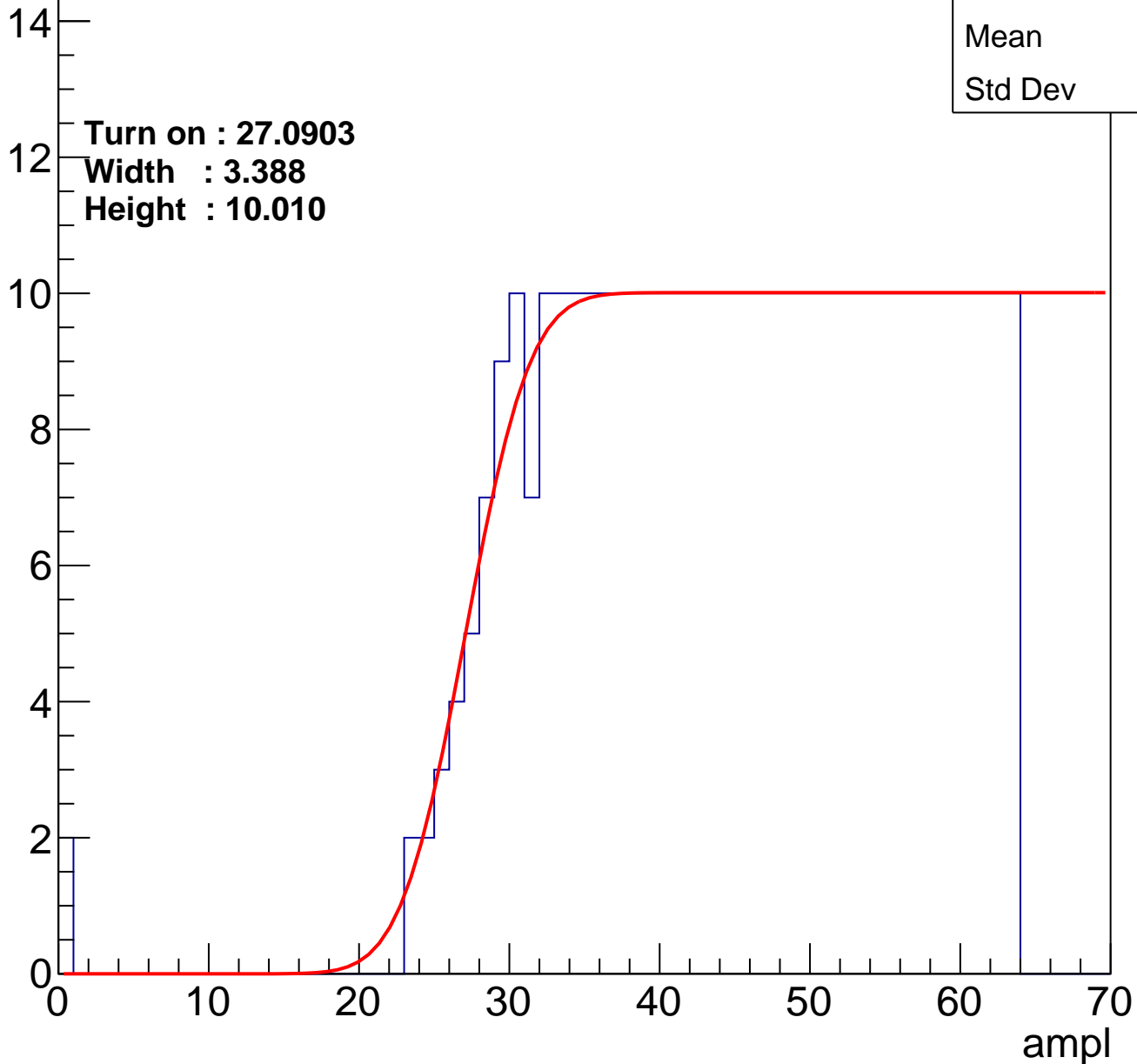
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.0903

Width : 3.388

Height : 10.010

Entry



B0L102S, U25-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.72
Std Dev	12.05

Turn on : 26.5993

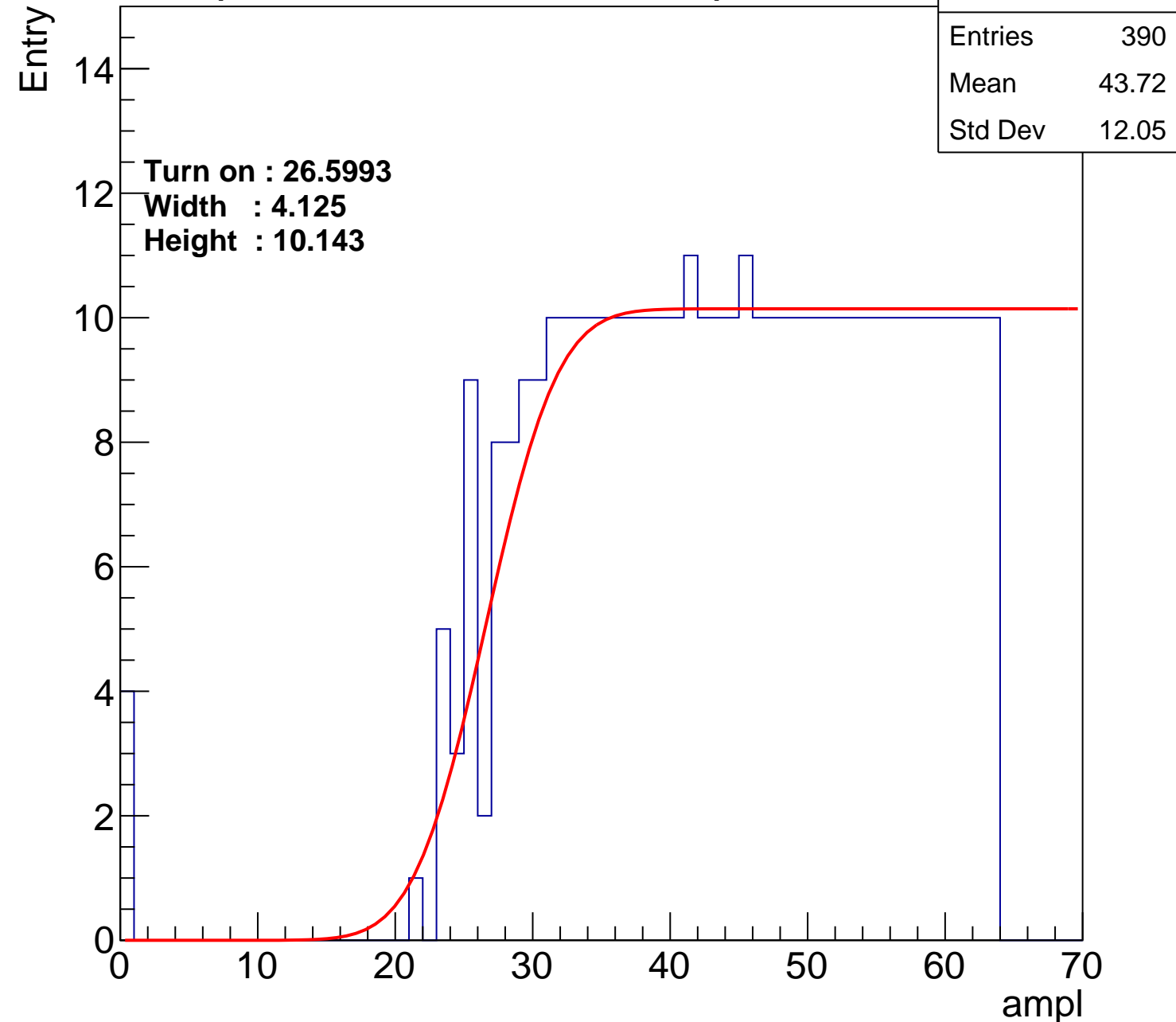
Width : 4.125

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.27
Std Dev	12.27

Turn on : 24.9549

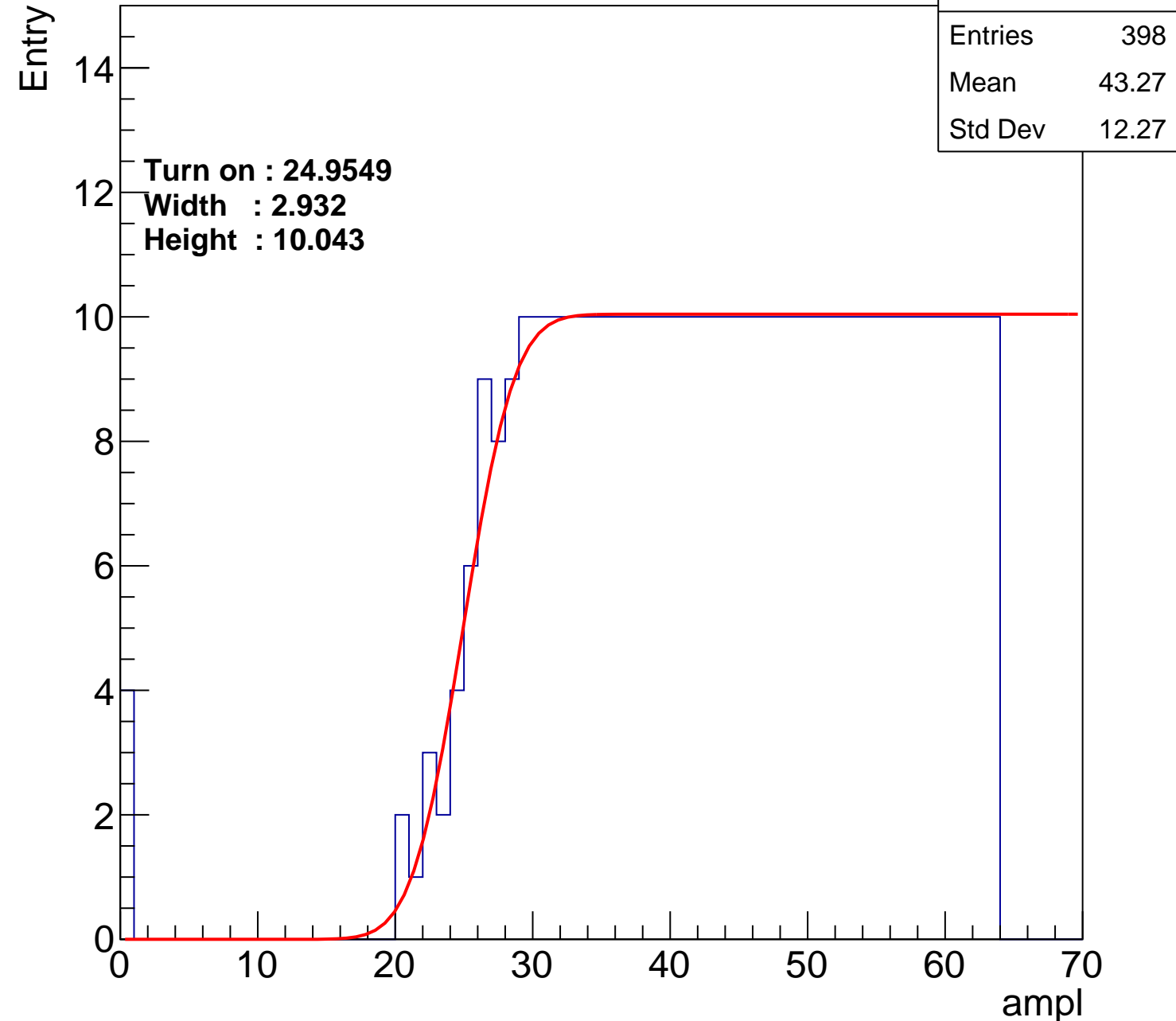
Width : 2.932

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch94

calib_packv5_042523_0143.root, FC#12, port B1

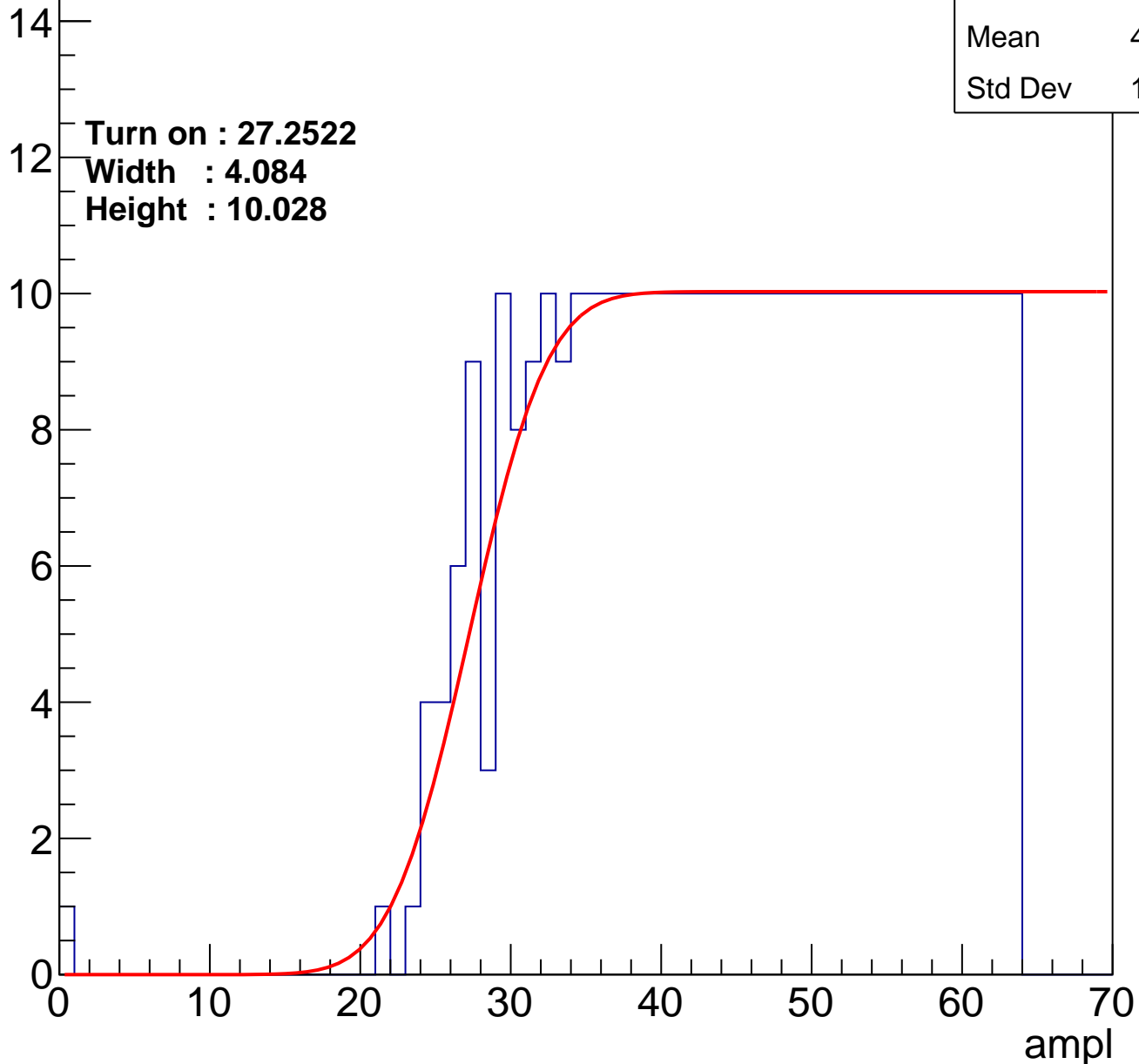
Entries	375
Mean	44.53
Std Dev	11.26

Turn on : 27.2522

Width : 4.084

Height : 10.028

Entry



B0L102S, U25-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	44.81
Std Dev	11.74

Turn on : 28.3833

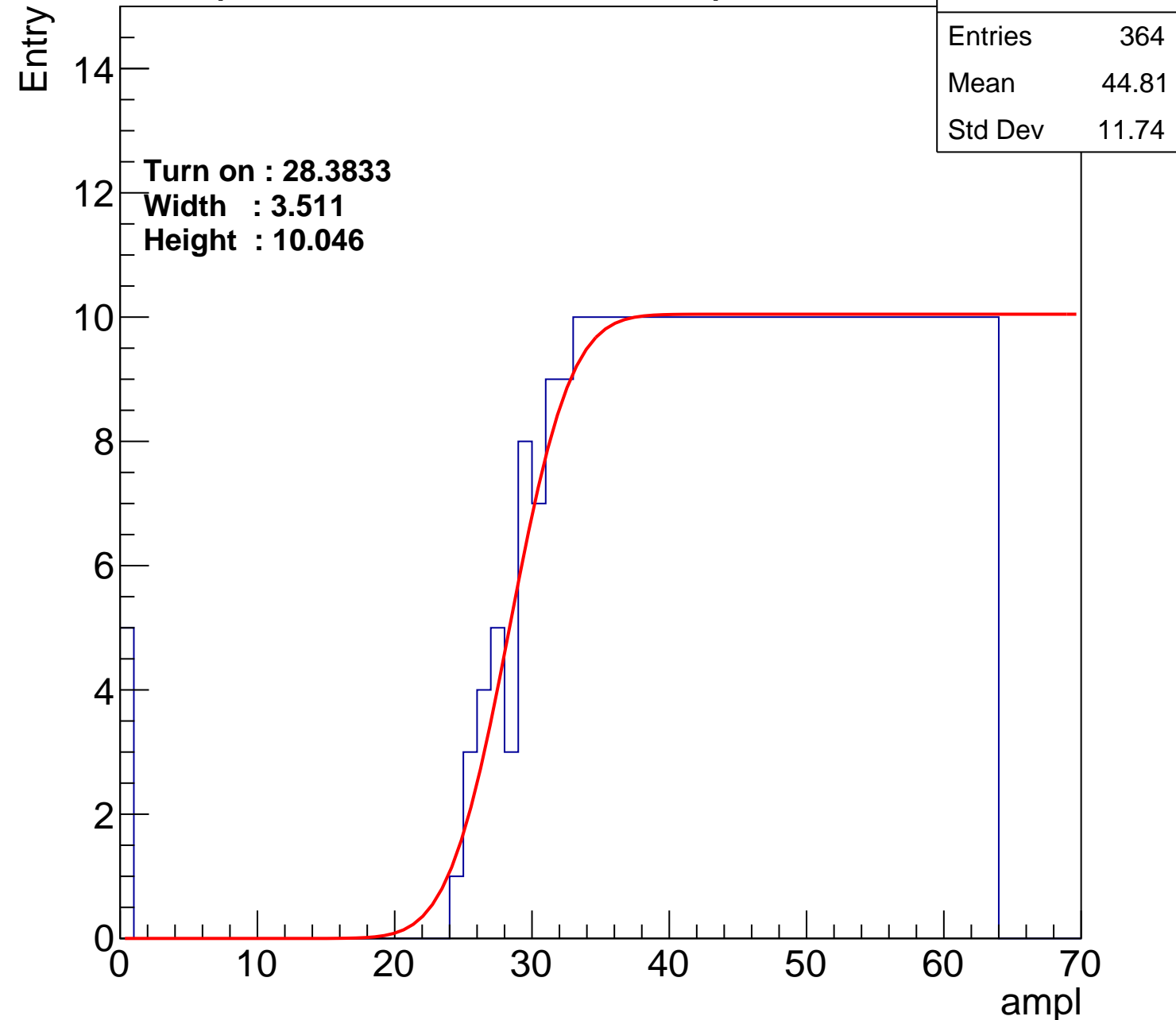
Width : 3.511

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.66
Std Dev	11.91

Turn on : 24.9934

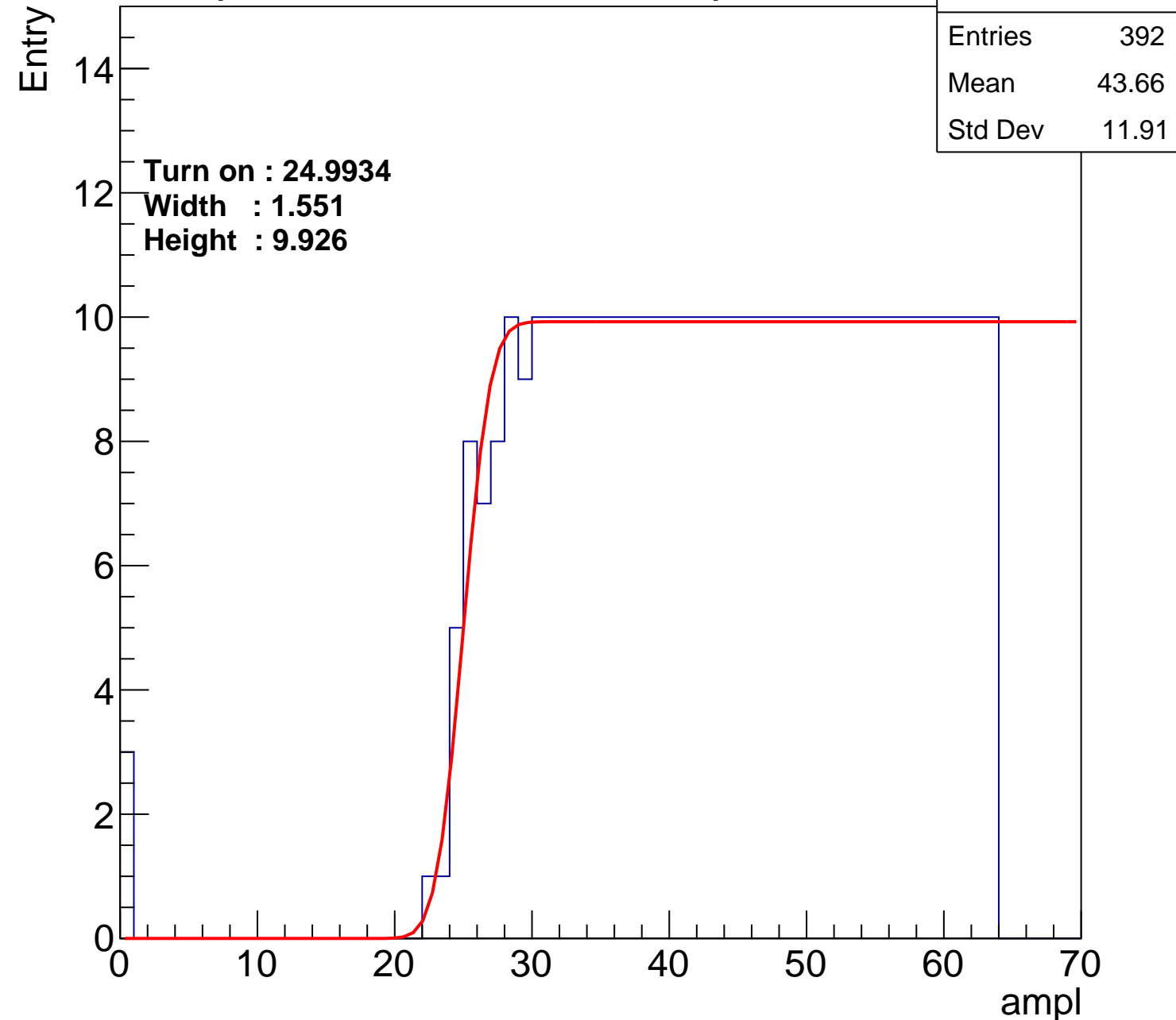
Width : 1.551

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch97

calib_packv5_042523_0143.root, FC#12, port B1

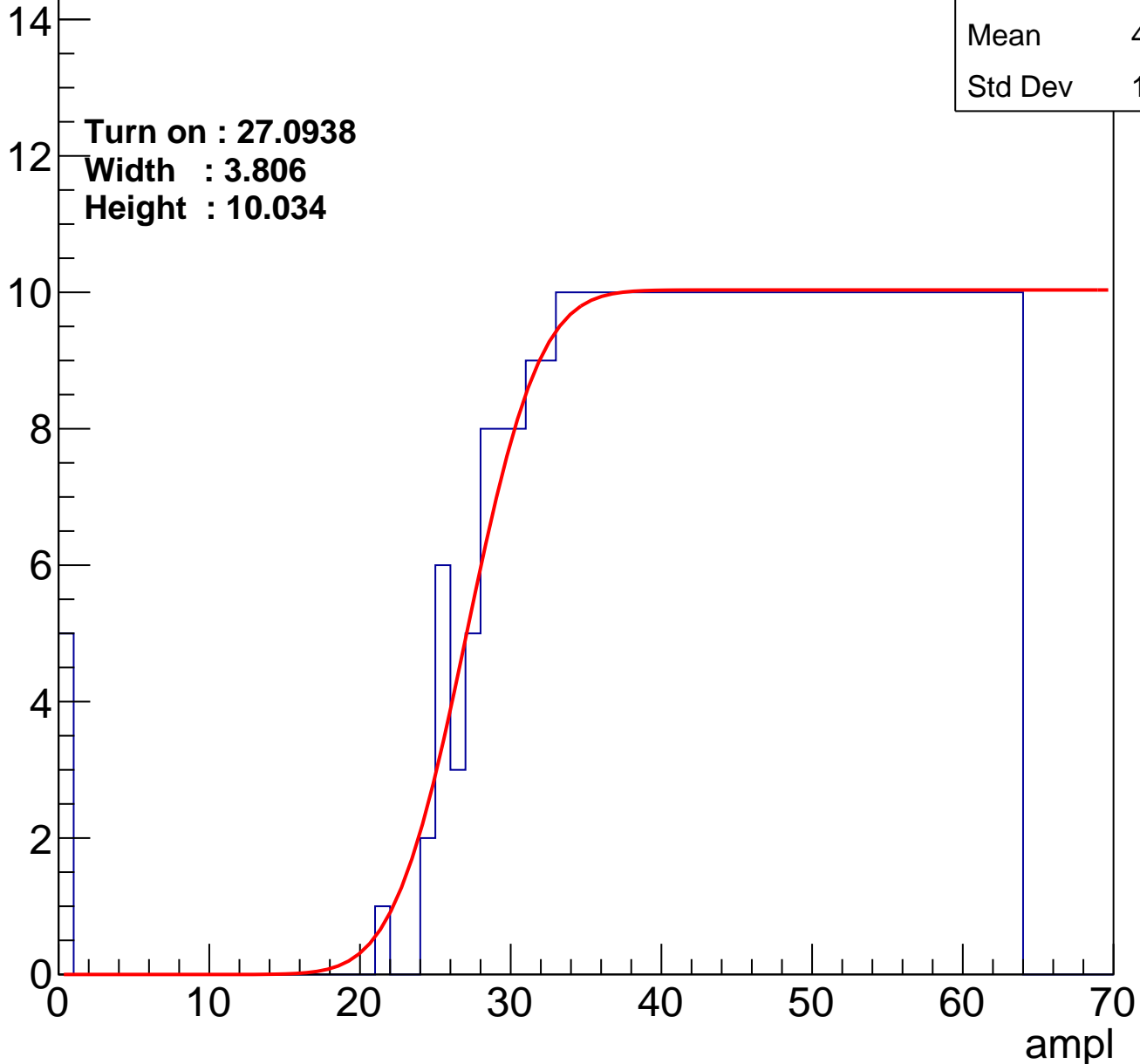
Entries	374
Mean	44.32
Std Dev	11.97

Turn on : 27.0938

Width : 3.806

Height : 10.034

Entry



B0L102S, U25-ch98

calib_packv5_042523_0143.root, FC#12, port B1

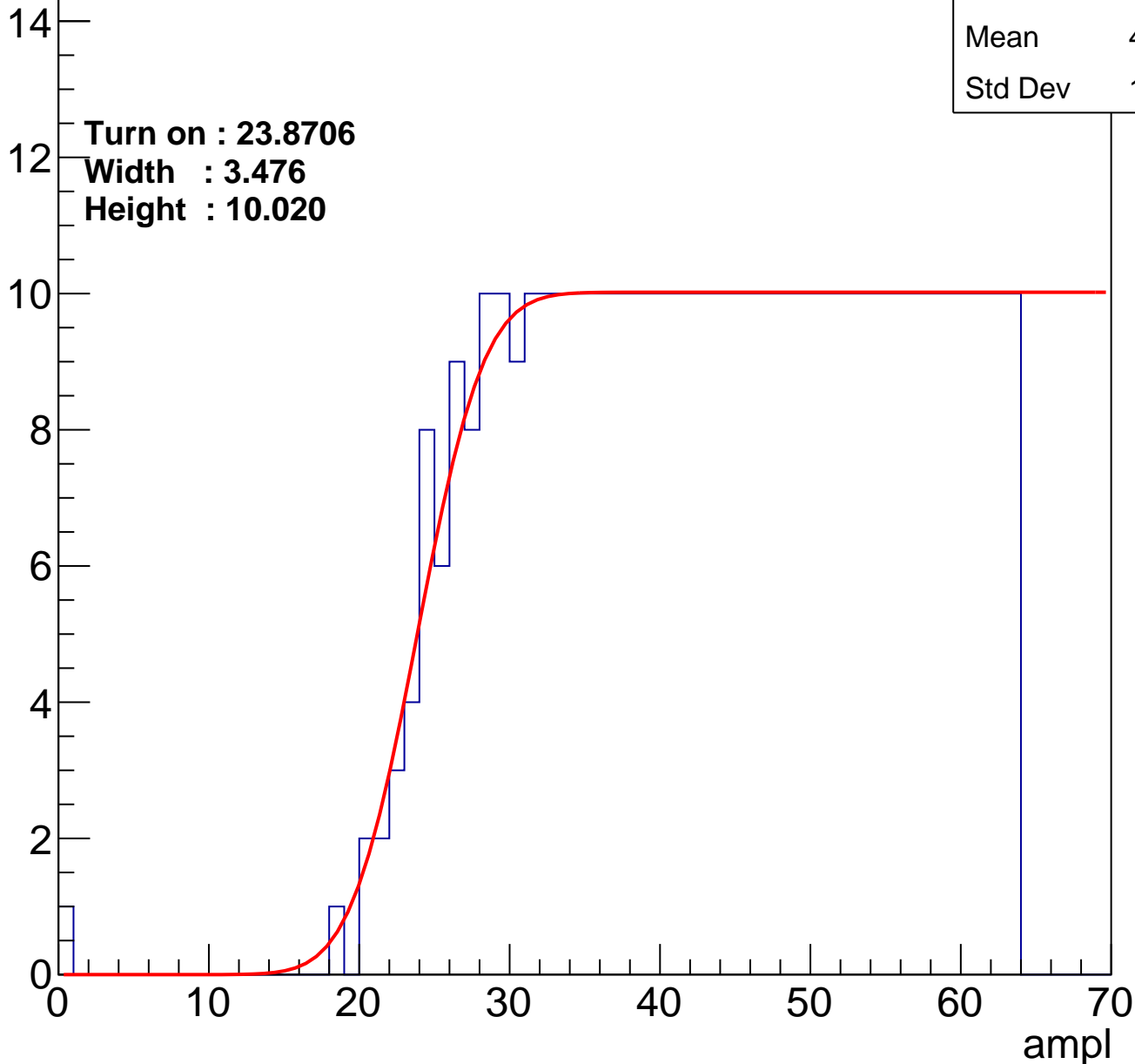
Entries	403
Mean	43.18
Std Dev	11.98

Turn on : 23.8706

Width : 3.476

Height : 10.020

Entry



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.7589
Width : 2.333
Height : 9.953



B0L102S, U25-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 25.3371

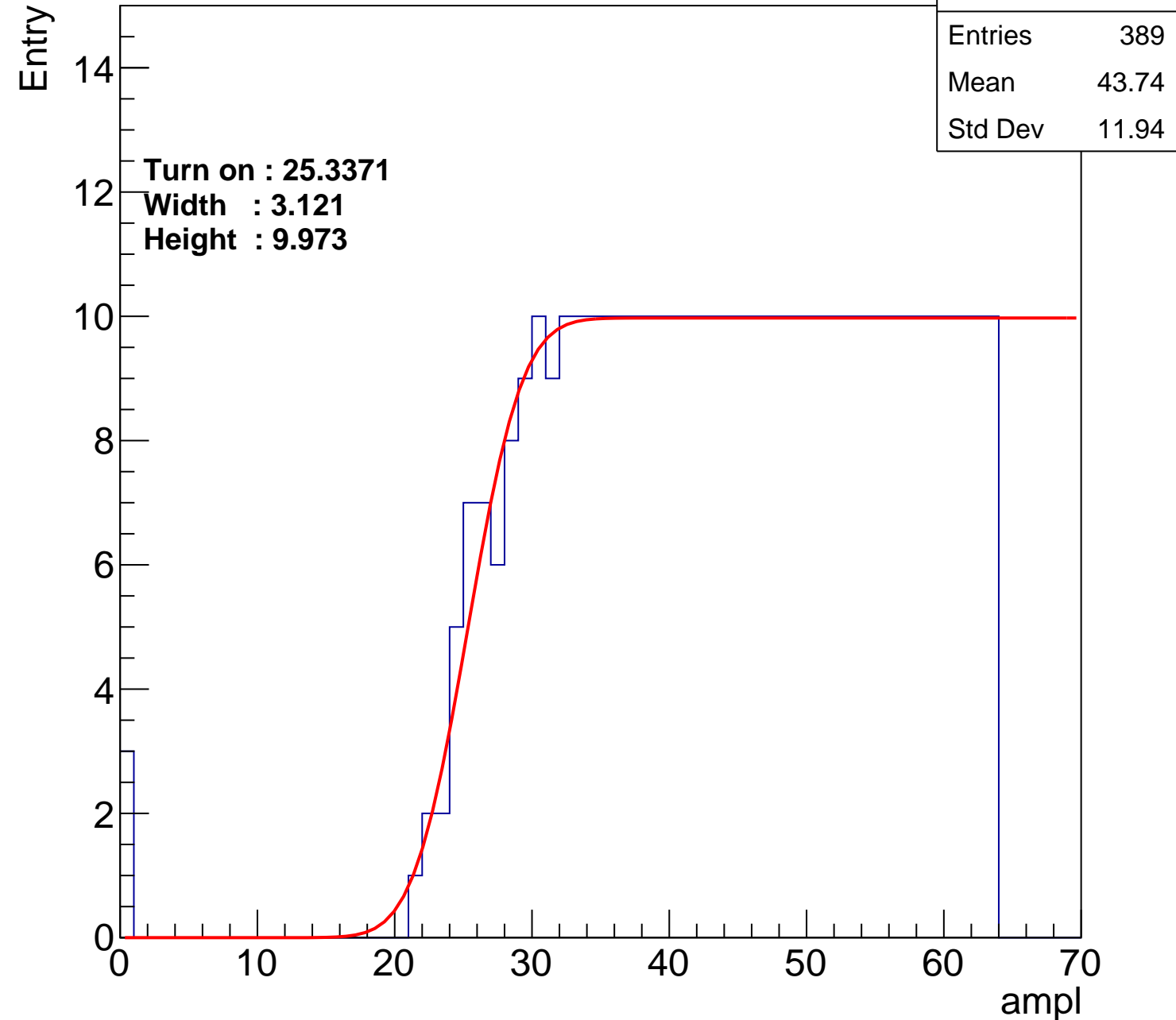
Width : 3.121

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 26.7850

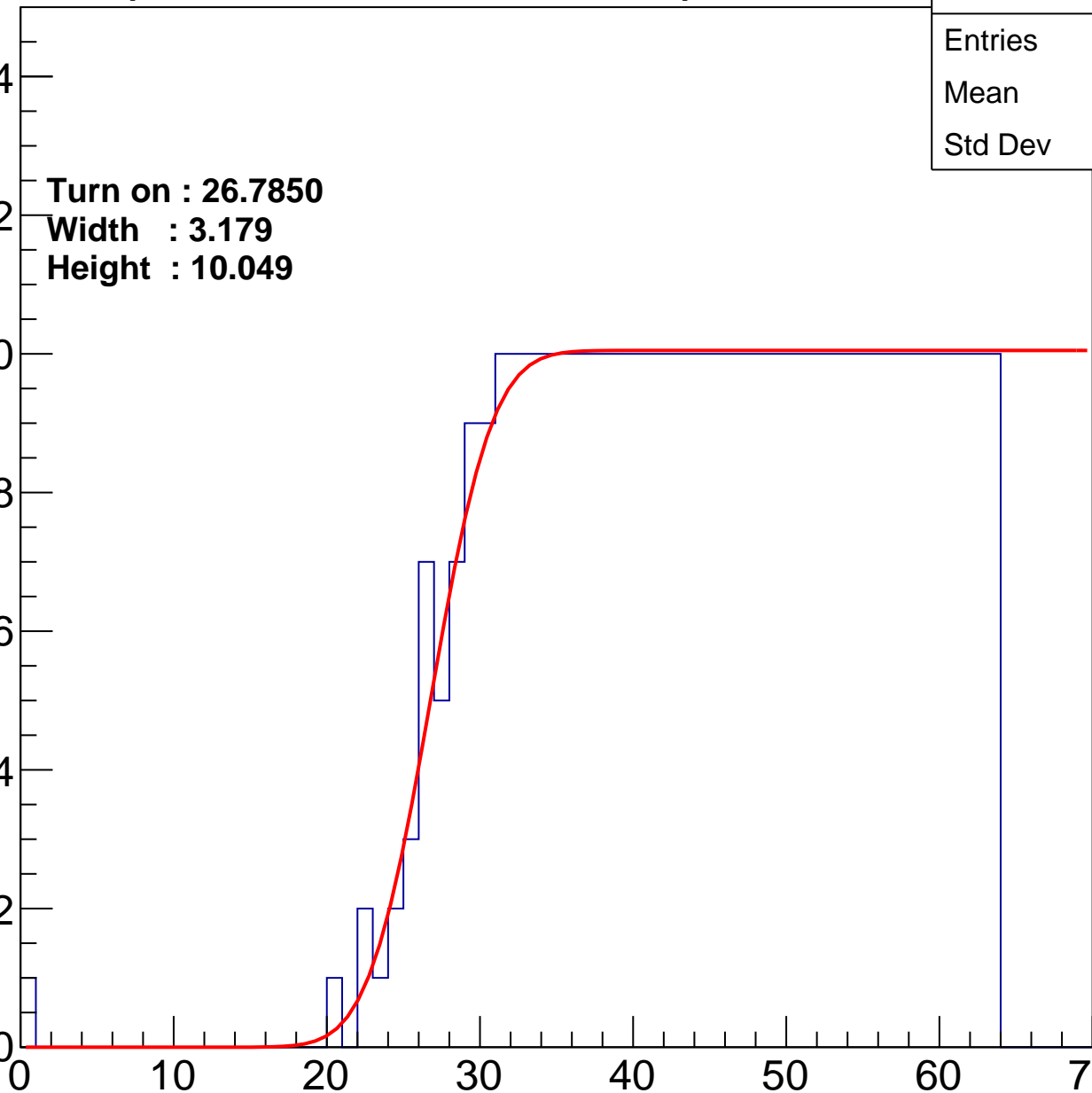
Width : 3.179

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.94
Std Dev	11.71

Turn on : 25.8201

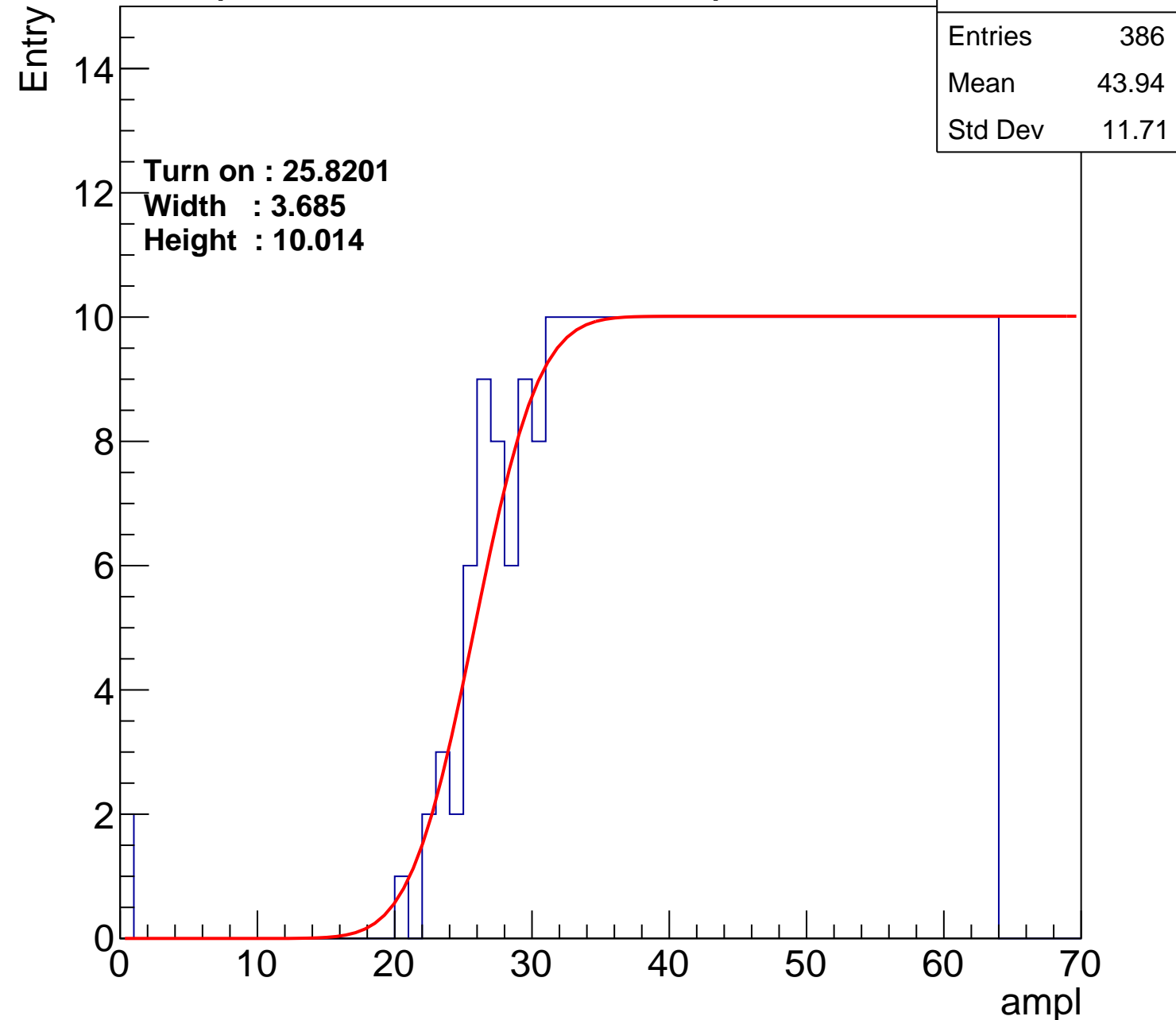
Width : 3.685

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.03
Std Dev	11.93

Turn on : 26.4958

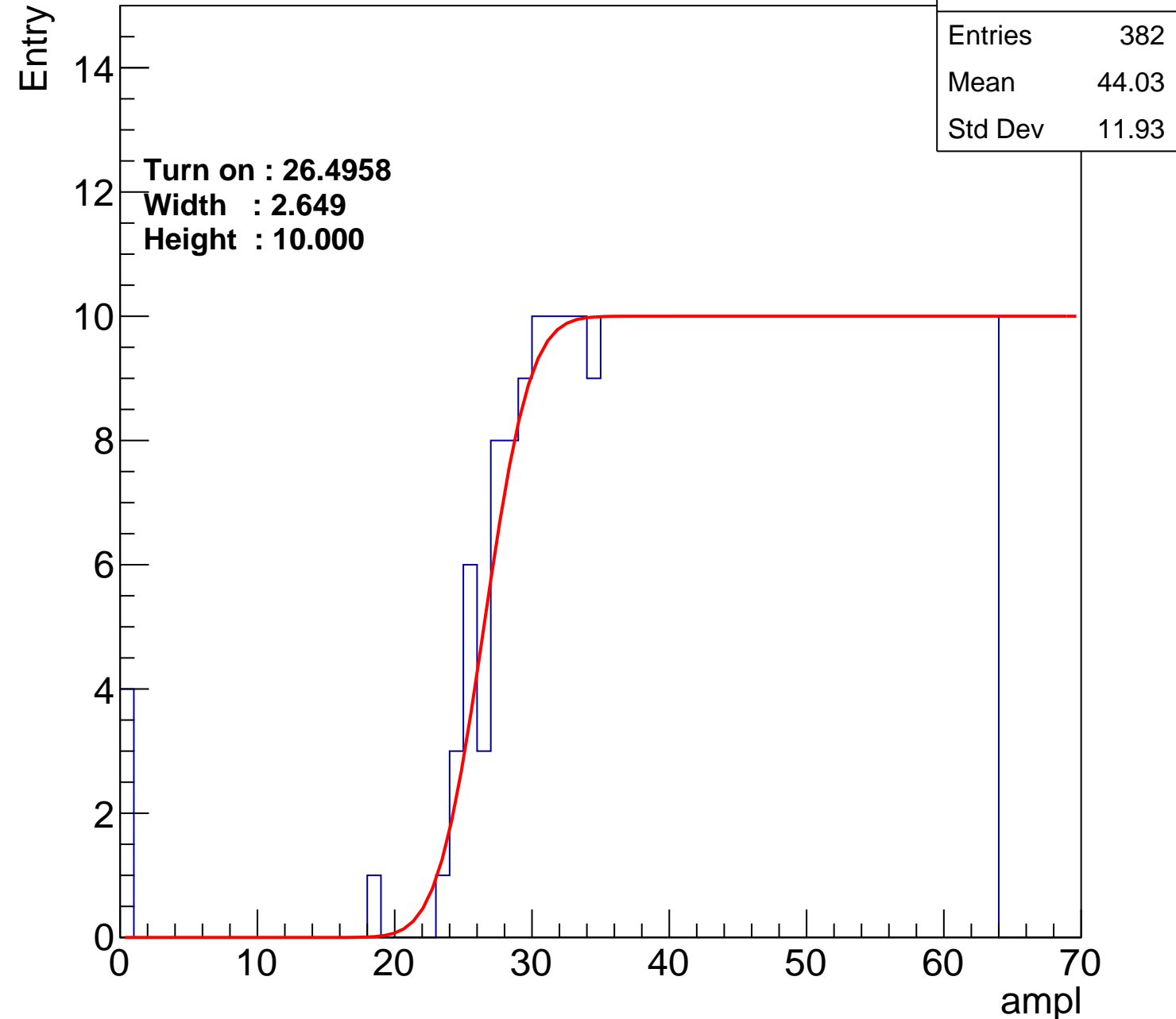
Width : 2.649

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.87
Std Dev	12.42

Turn on : 24.6201

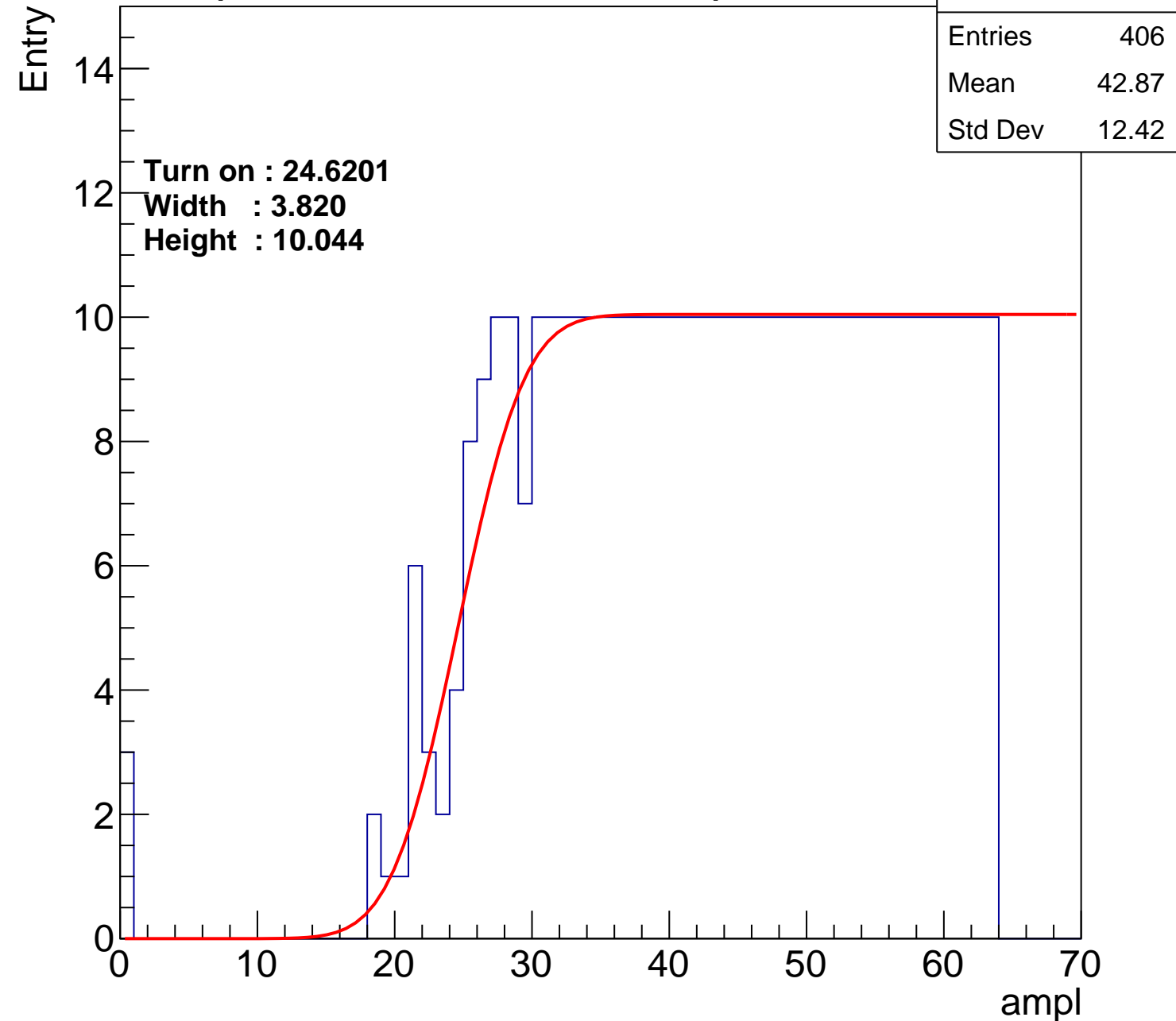
Width : 3.820

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.33
Std Dev	11.38

Turn on : 26.4712

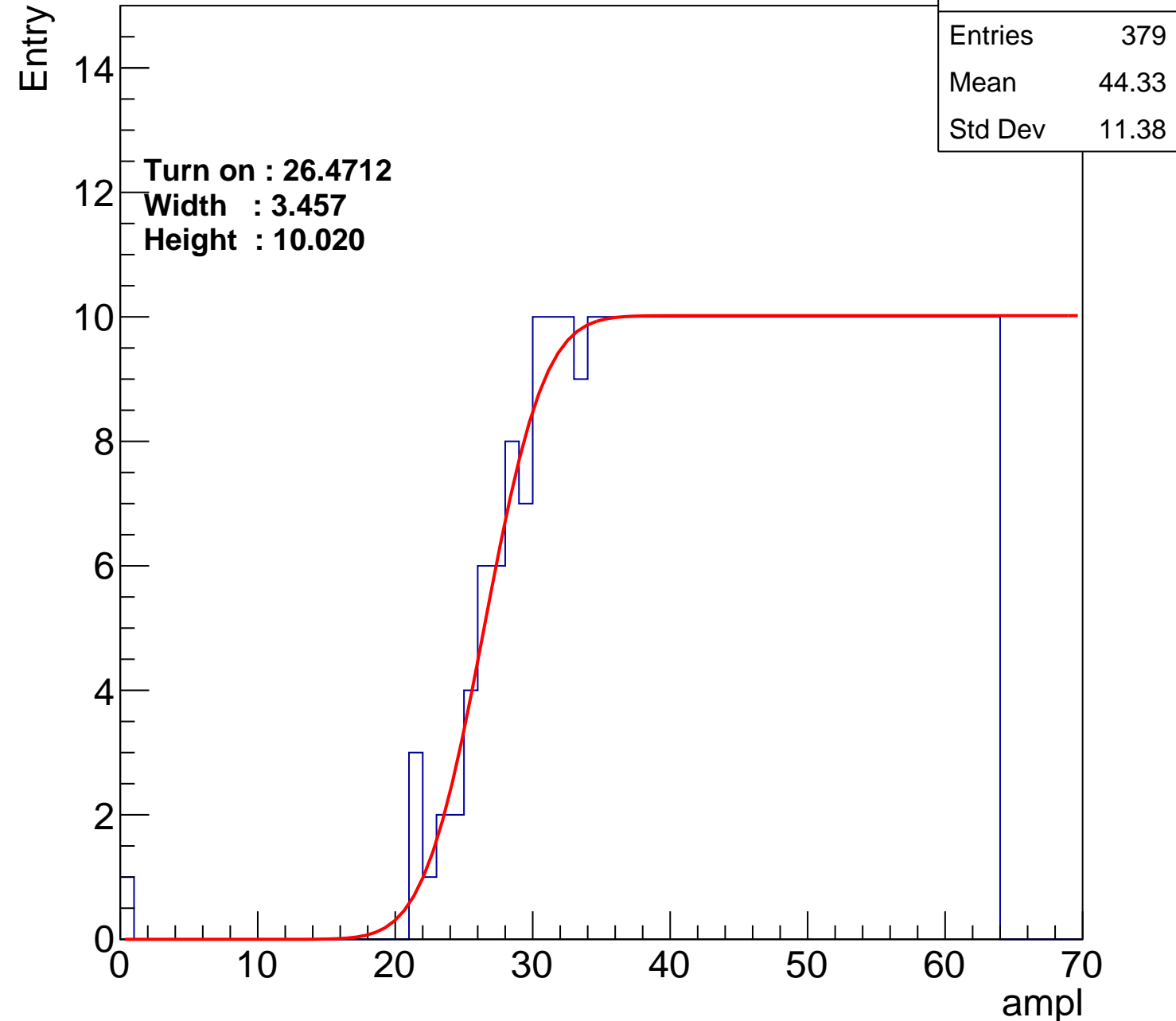
Width : 3.457

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch106

calib_packv5_042523_0143.root, FC#12, port B1

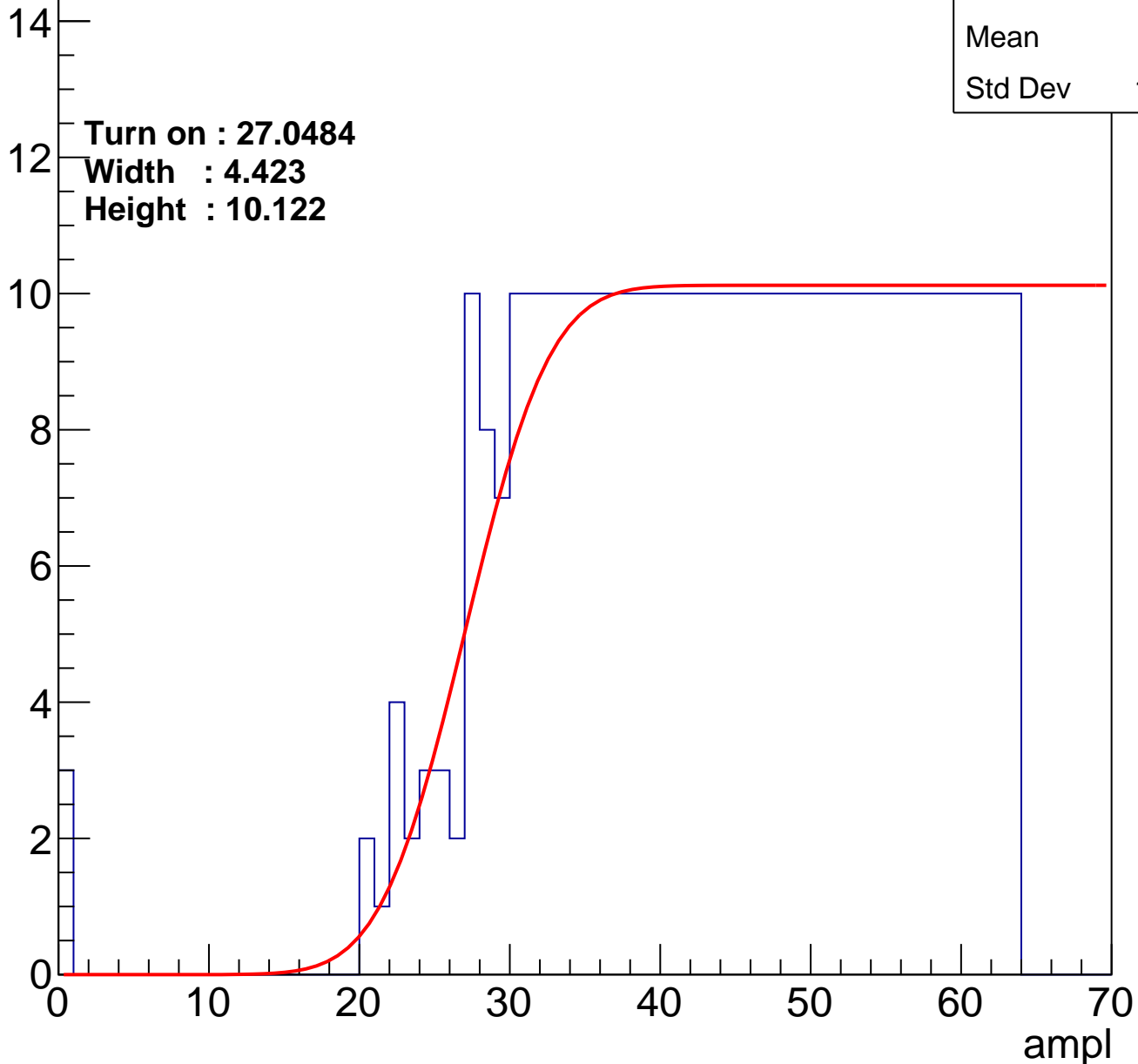
Entries	385
Mean	43.9
Std Dev	11.91

Turn on : 27.0484

Width : 4.423

Height : 10.122

Entry



B0L102S, U25-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.41
Std Dev	12.8

Turn on : 26.1520

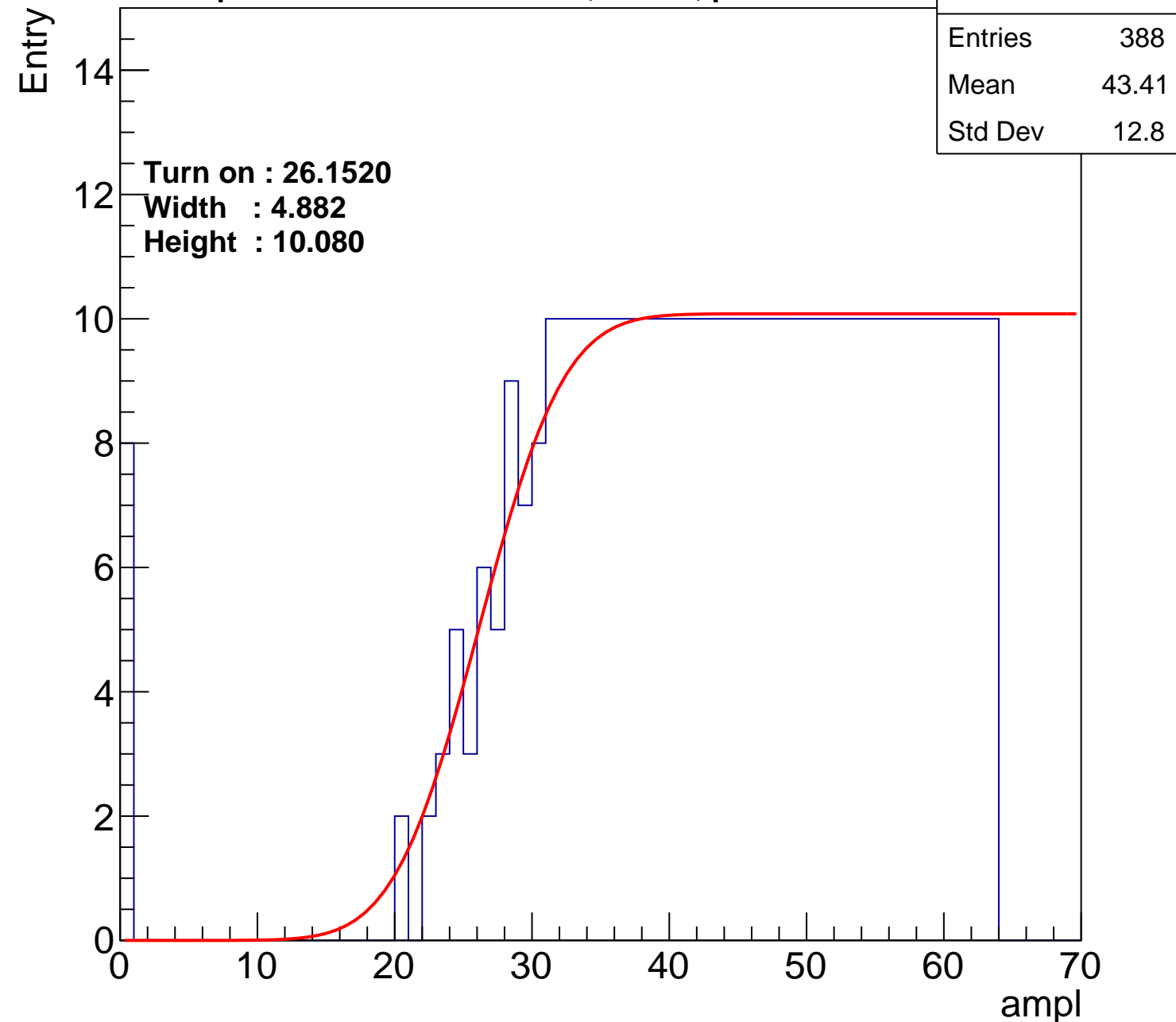
Width : 4.882

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.81
Std Dev	11.65

Turn on : 25.5604

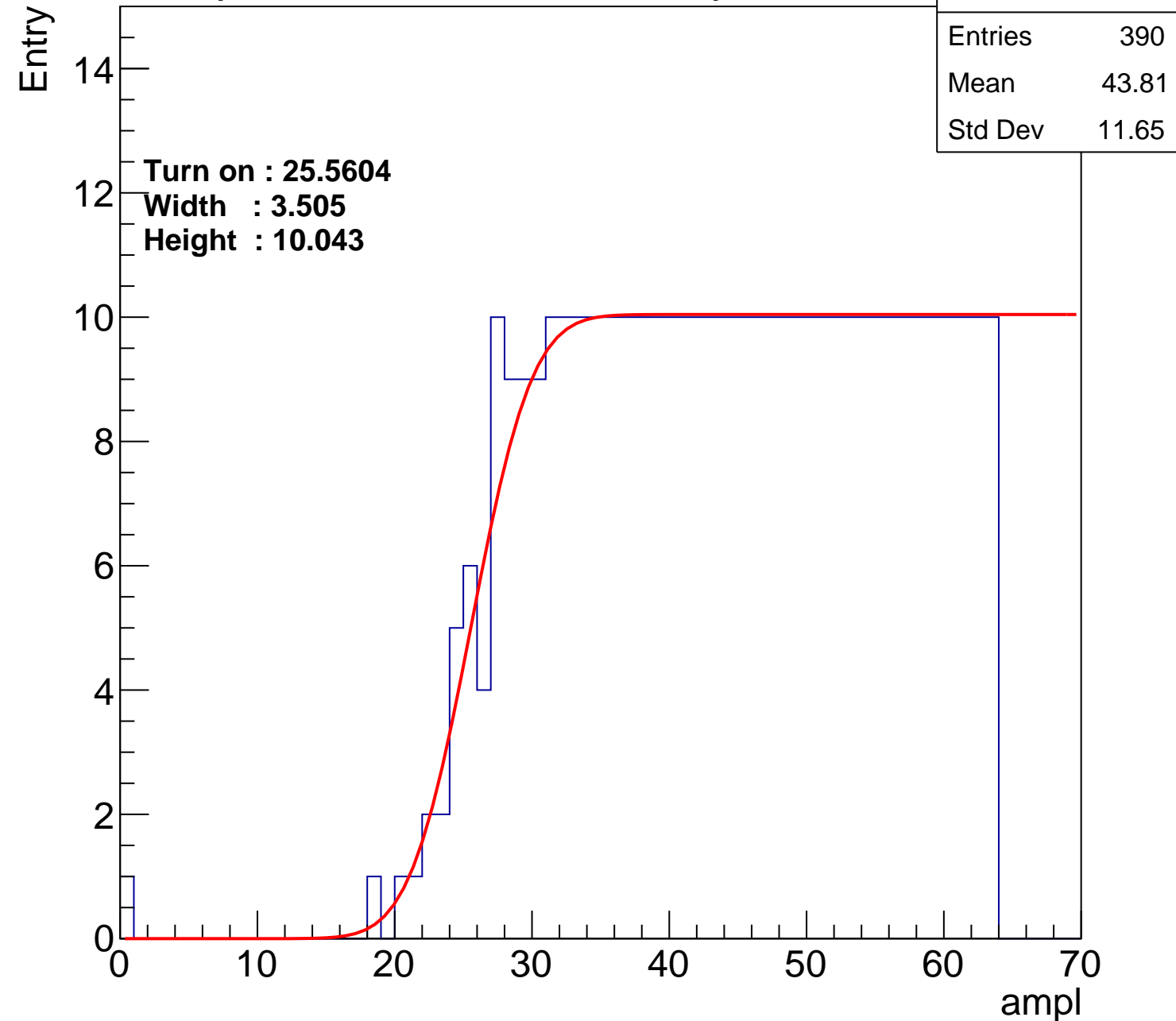
Width : 3.505

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.27
Std Dev	11.18

Turn on : 28.5268

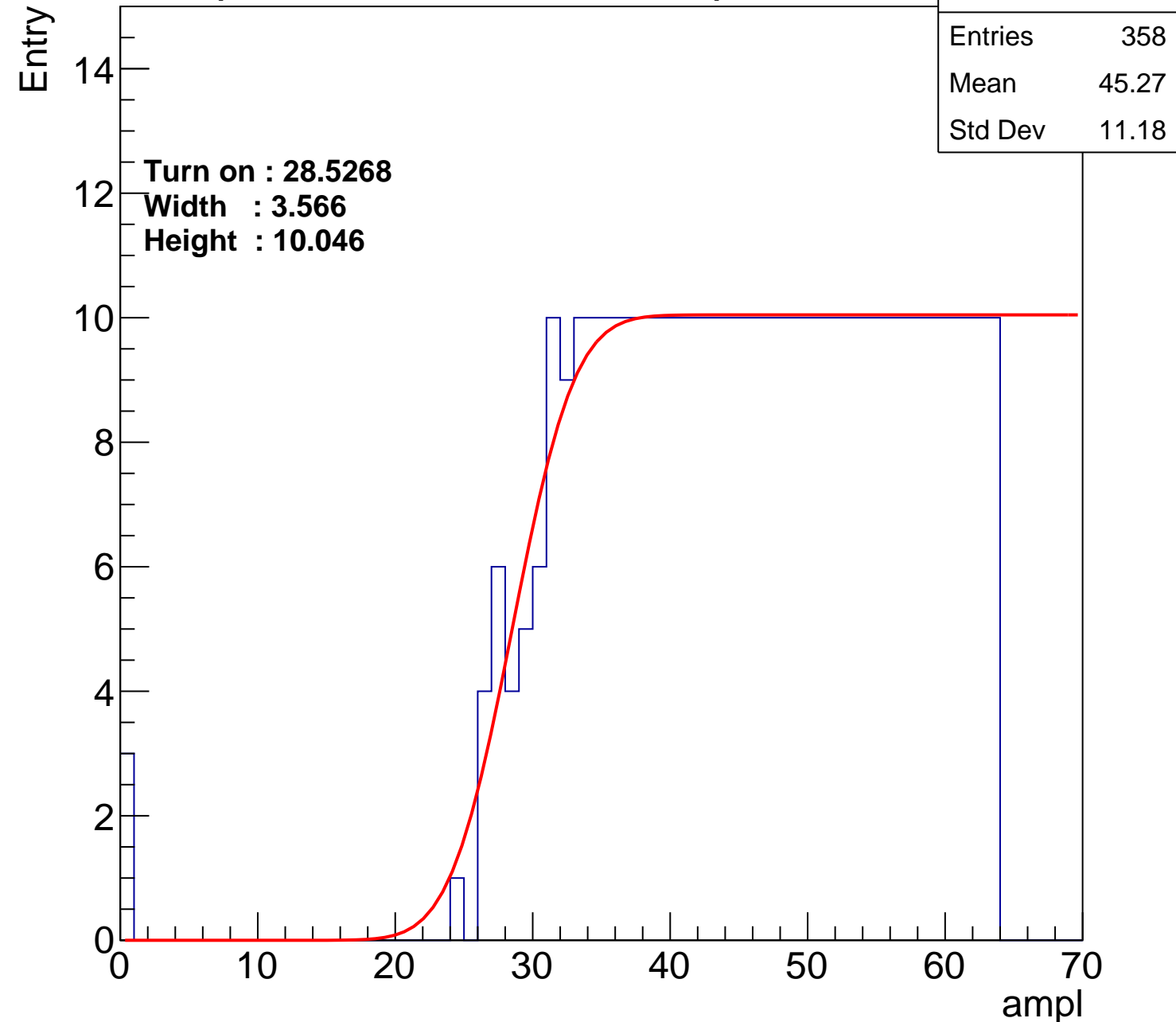
Width : 3.566

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.38
Std Dev	12.03

Turn on : 24.5073

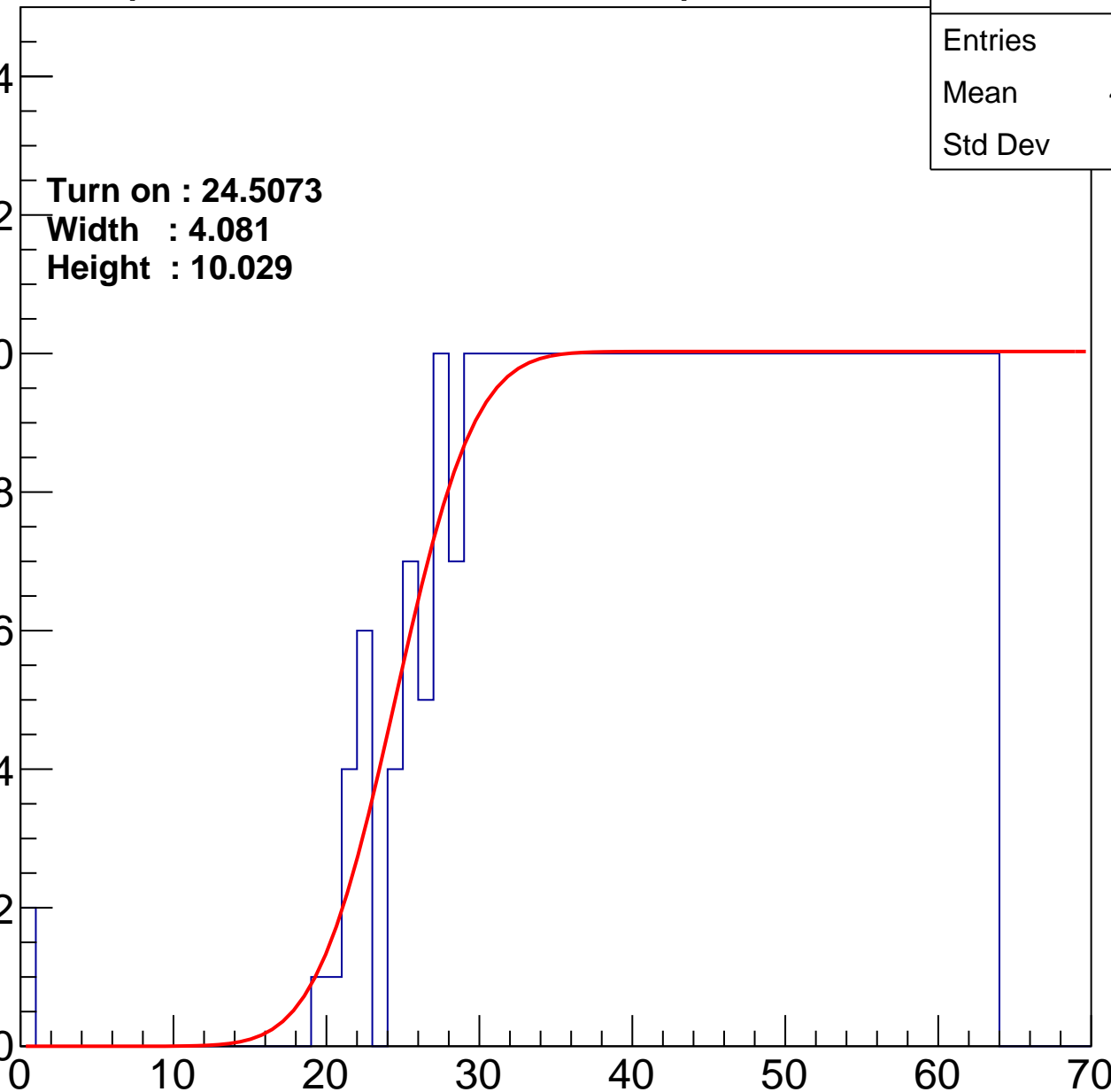
Width : 4.081

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.91
Std Dev	11.91

Turn on : 26.2103

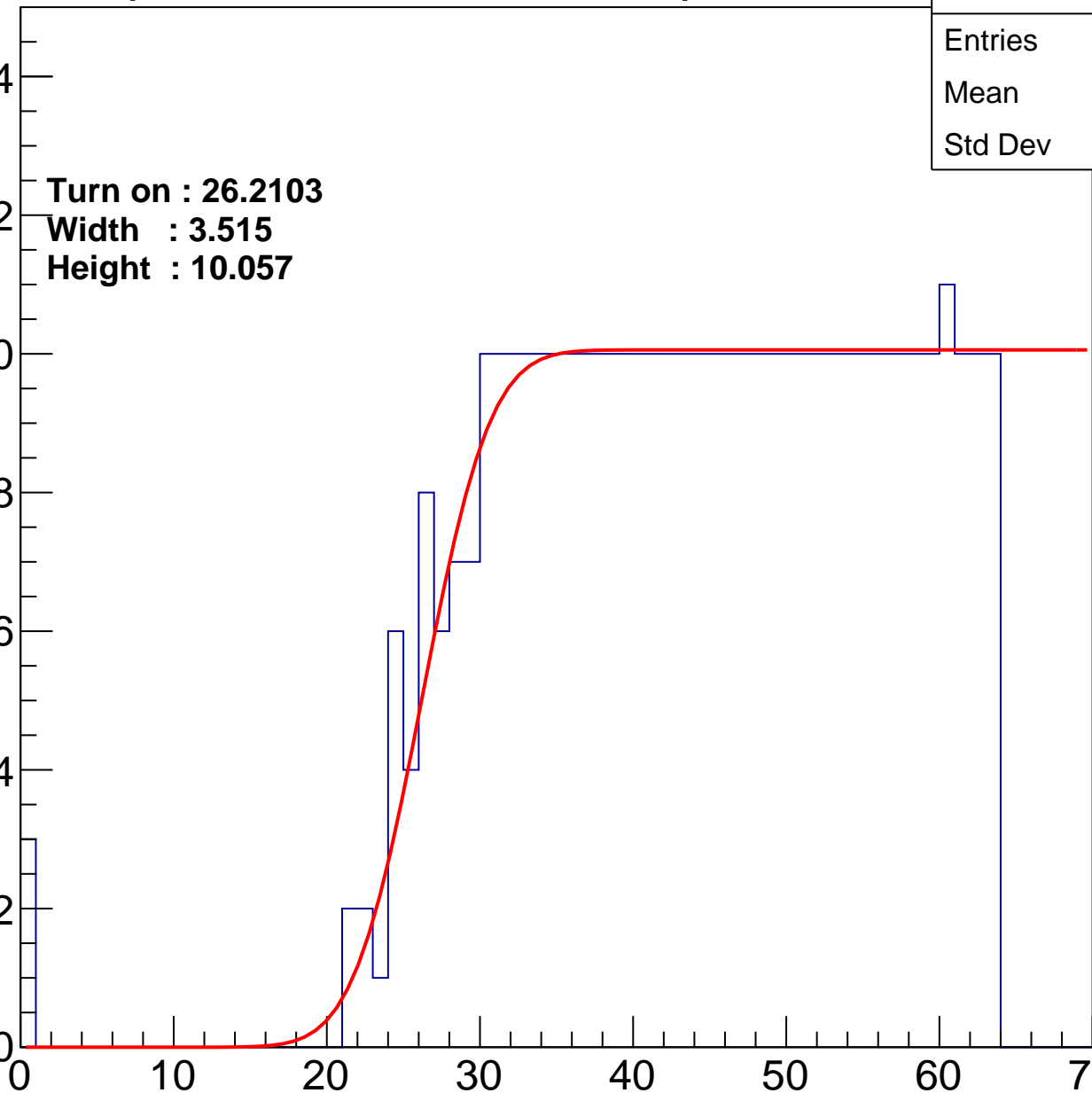
Width : 3.515

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.25
Std Dev	11.91

Turn on : 23.9985

Width : 1.719

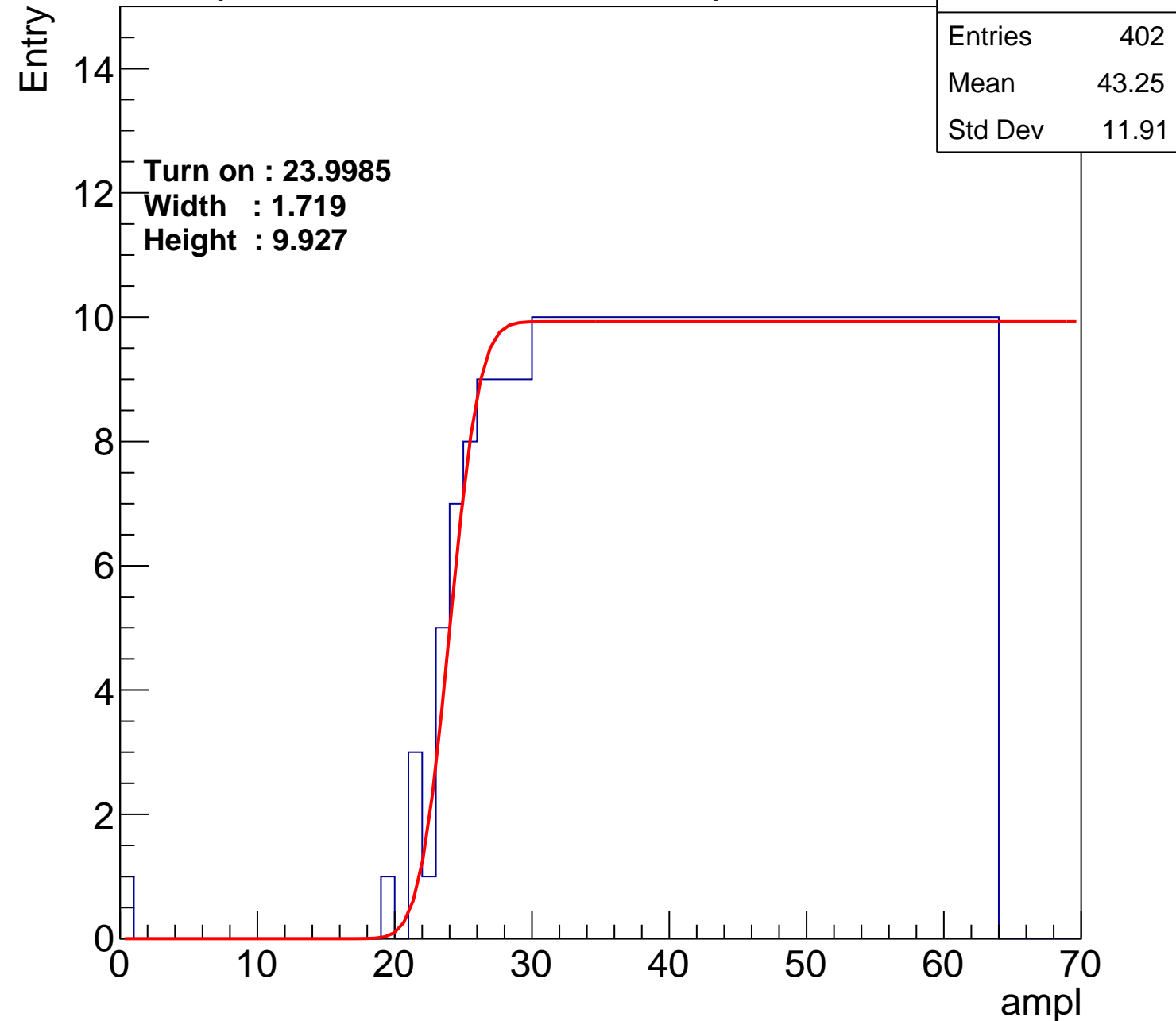
Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U25-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 25.8904

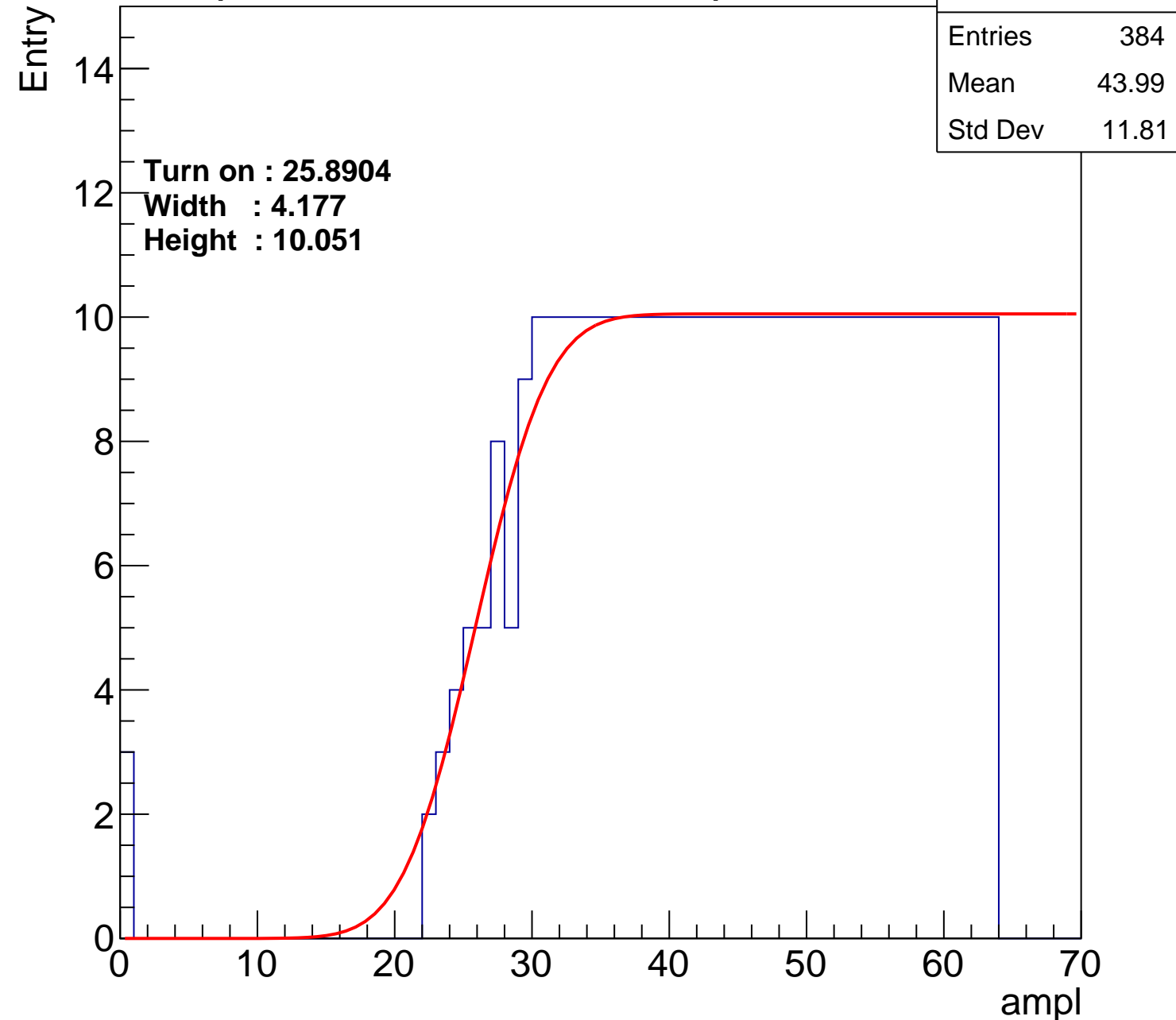
Width : 4.177

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.86
Std Dev	11.24

Turn on : 27.3994

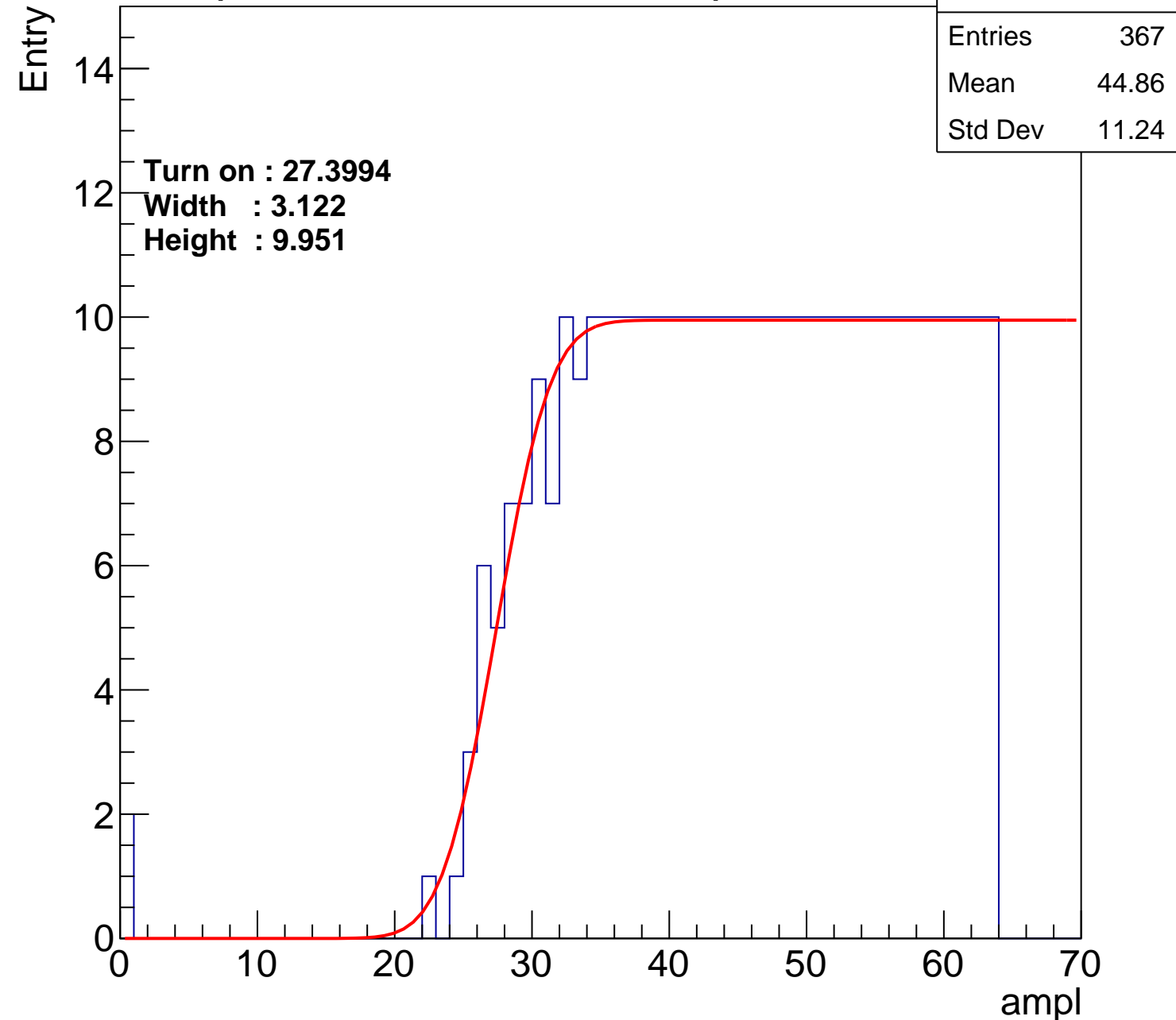
Width : 3.122

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch115

calib_packv5_042523_0143.root, FC#12, port B1

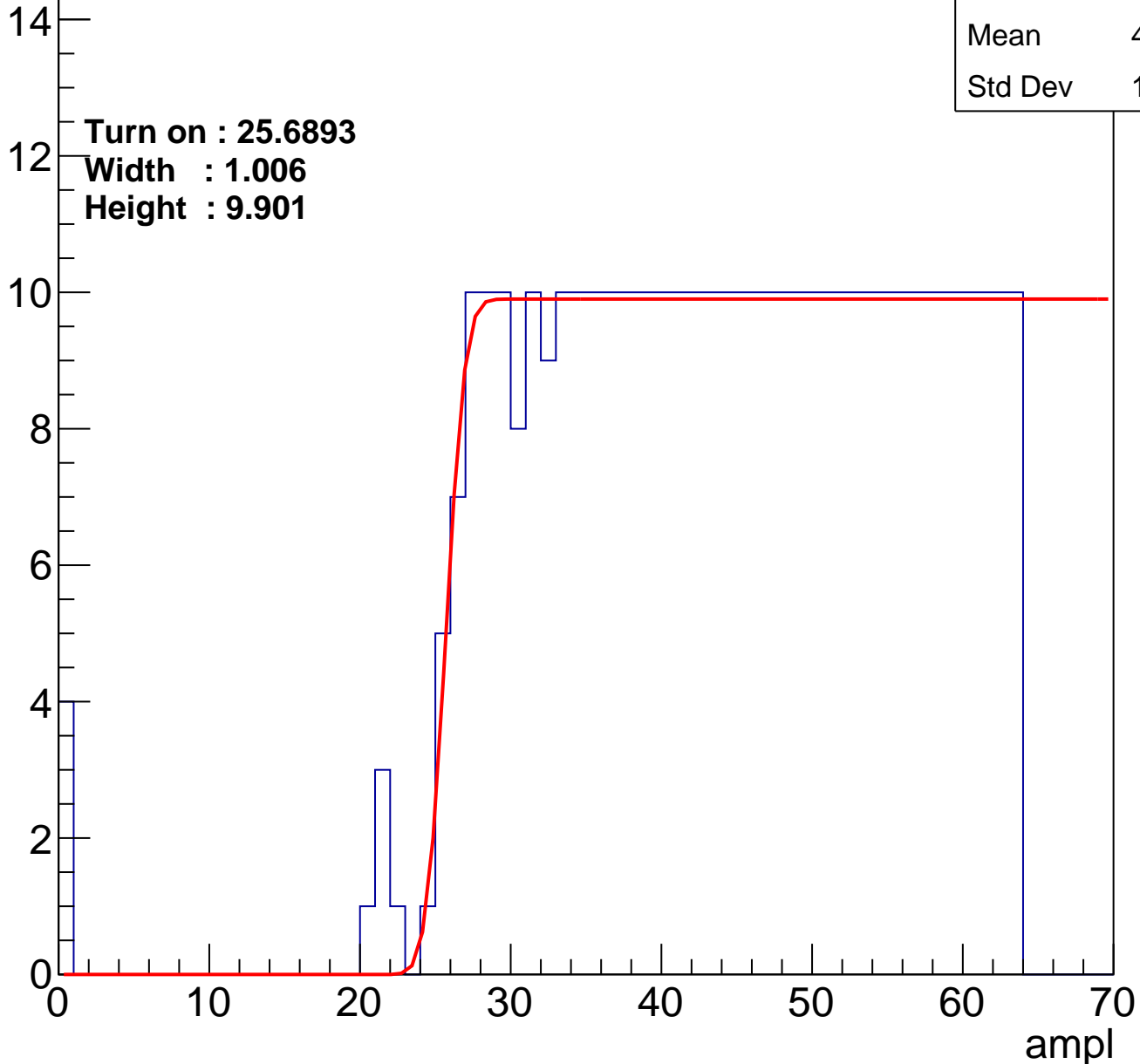
Entries	389
Mean	43.69
Std Dev	12.09

Turn on : 25.6893

Width : 1.006

Height : 9.901

Entry



B0L102S, U25-ch116

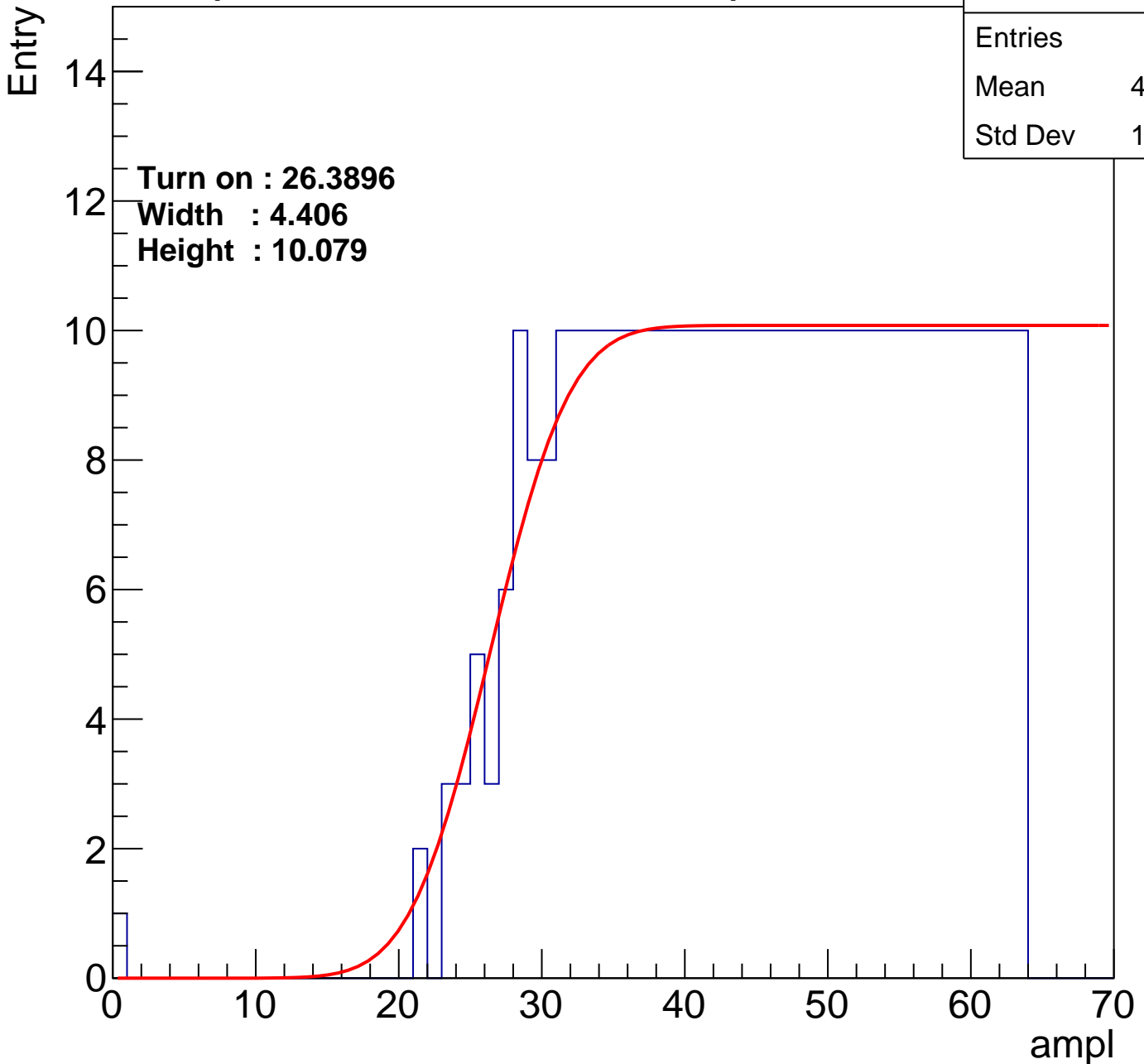
calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.35
Std Dev	11.35

Turn on : 26.3896

Width : 4.406

Height : 10.079



B0L102S, U25-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.81
Std Dev	12.17

Turn on : 26.2562

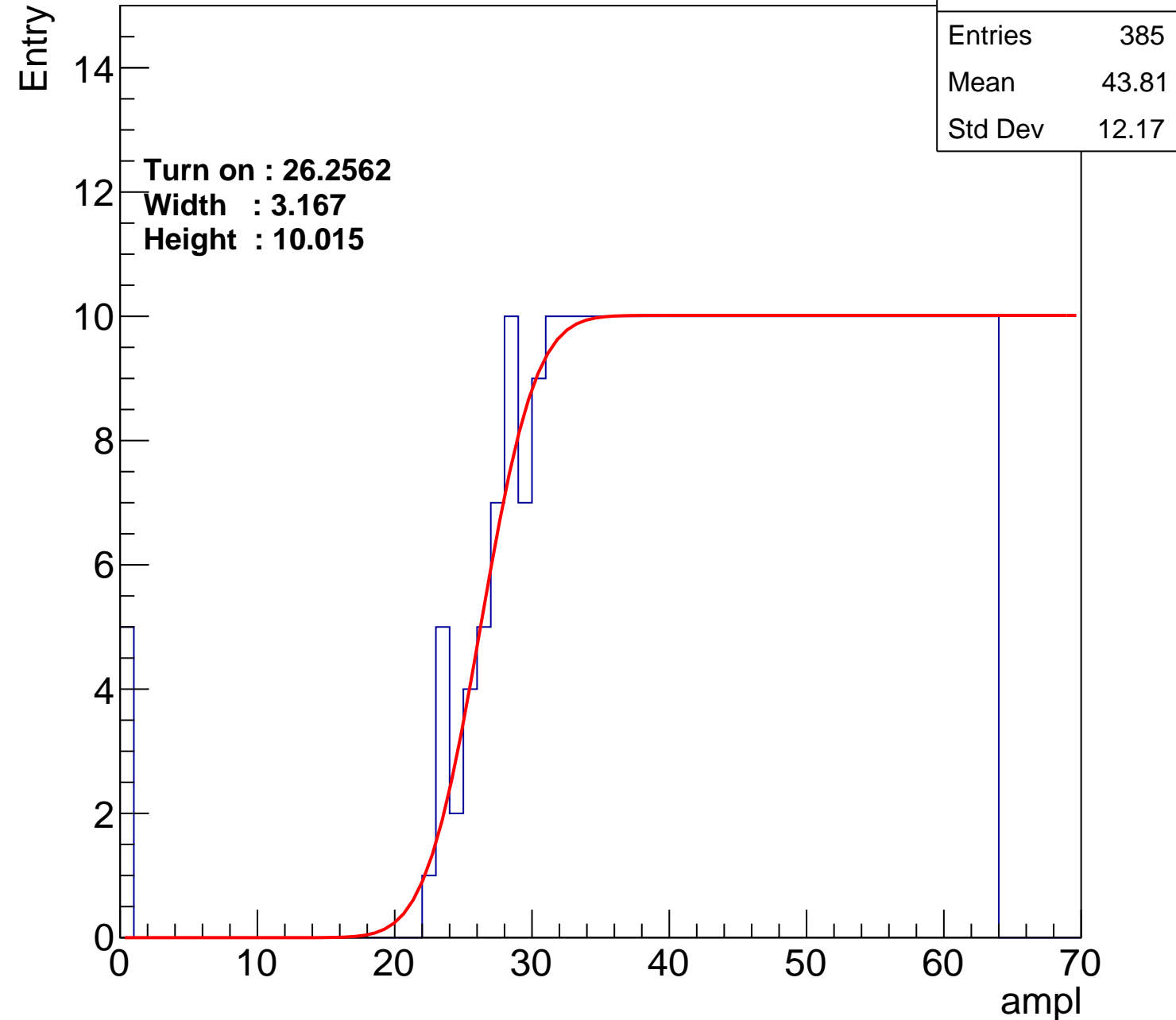
Width : 3.167

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.86
Std Dev	11.74

Turn on : 25.3454

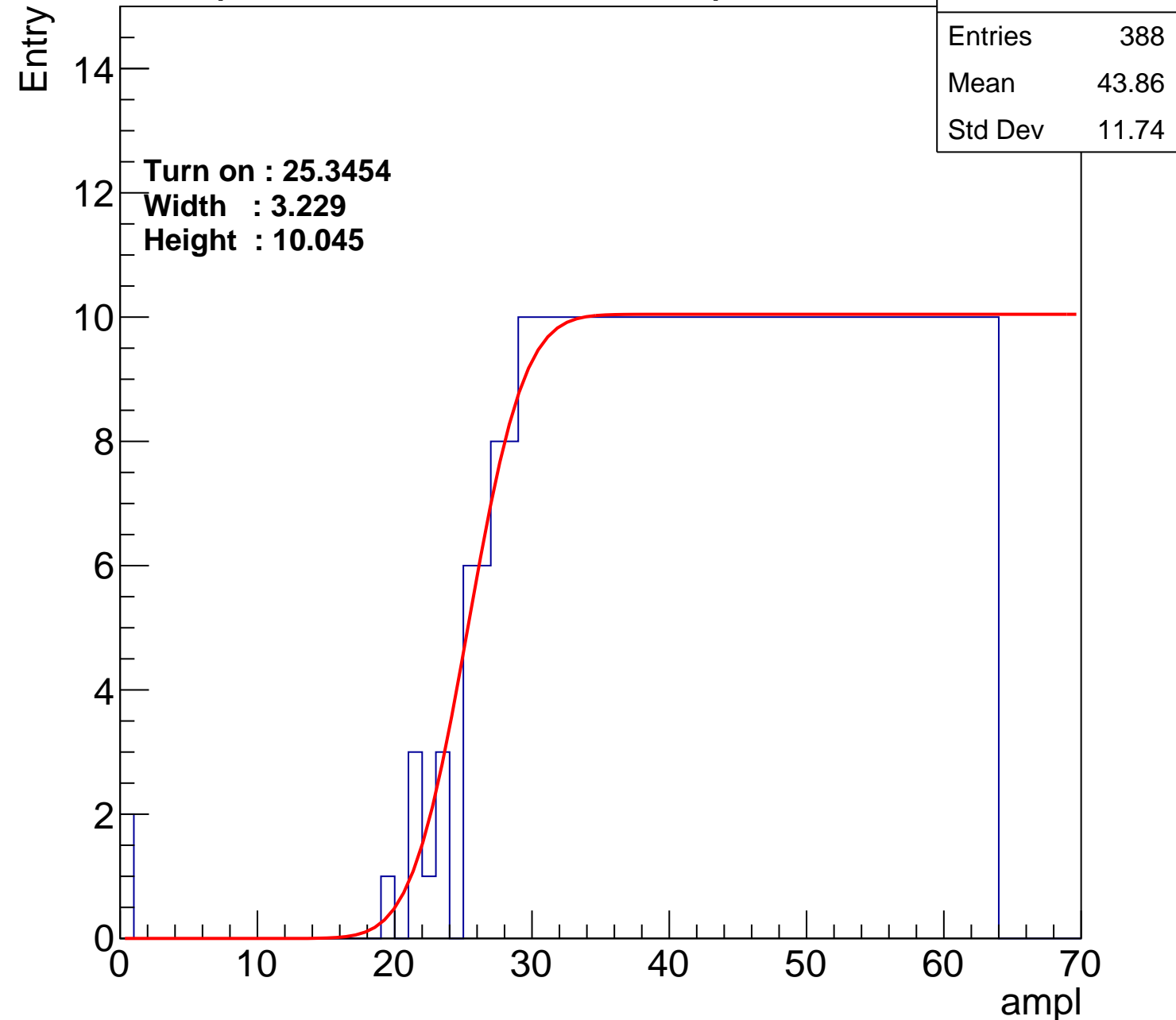
Width : 3.229

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch119

calib_packv5_042523_0143.root, FC#12, port B1

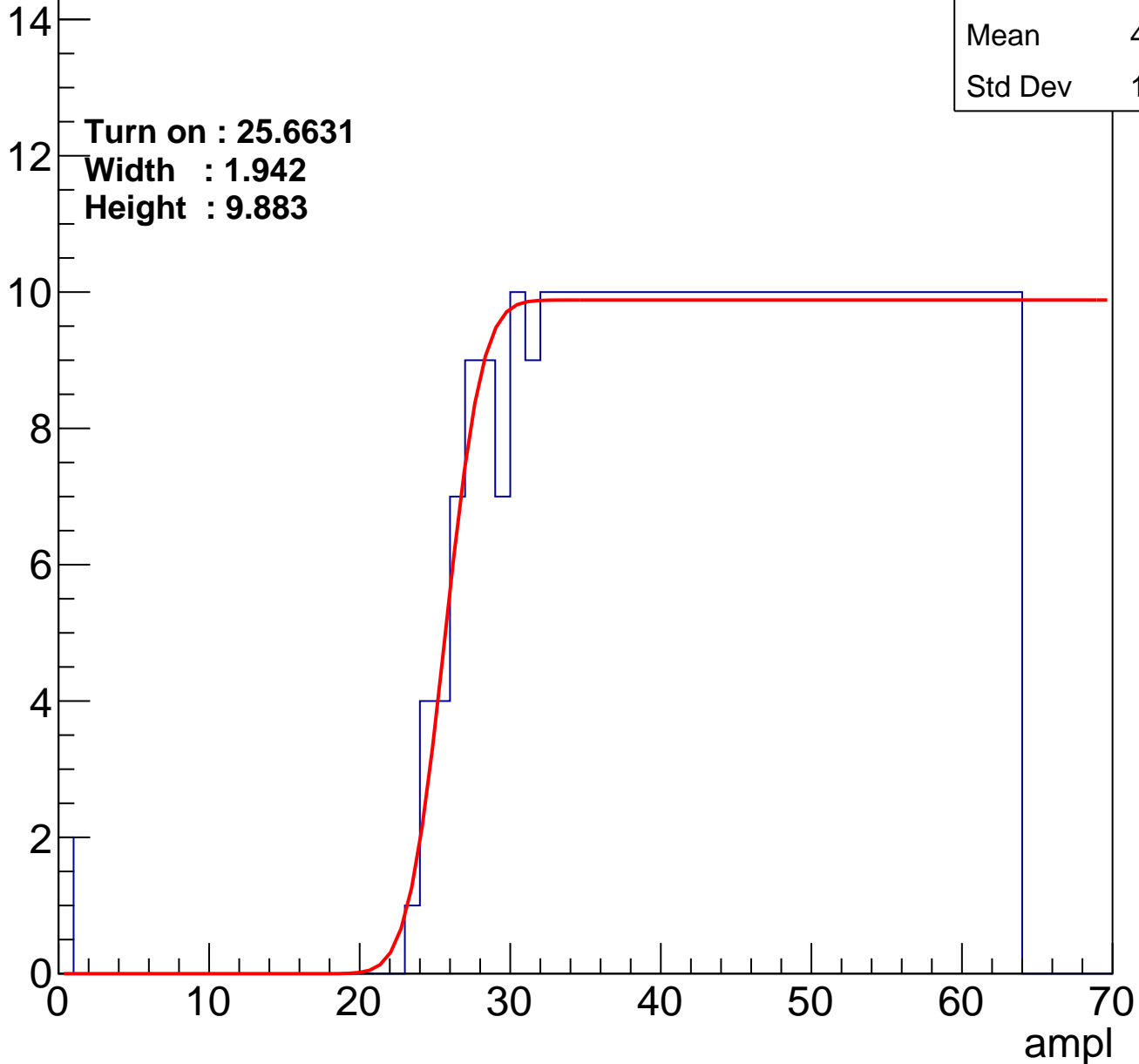
Entries	382
Mean	44.18
Std Dev	11.53

Turn on : 25.6631

Width : 1.942

Height : 9.883

Entry



B0L102S, U25-ch120

calib_packv5_042523_0143.root, FC#12, port B1

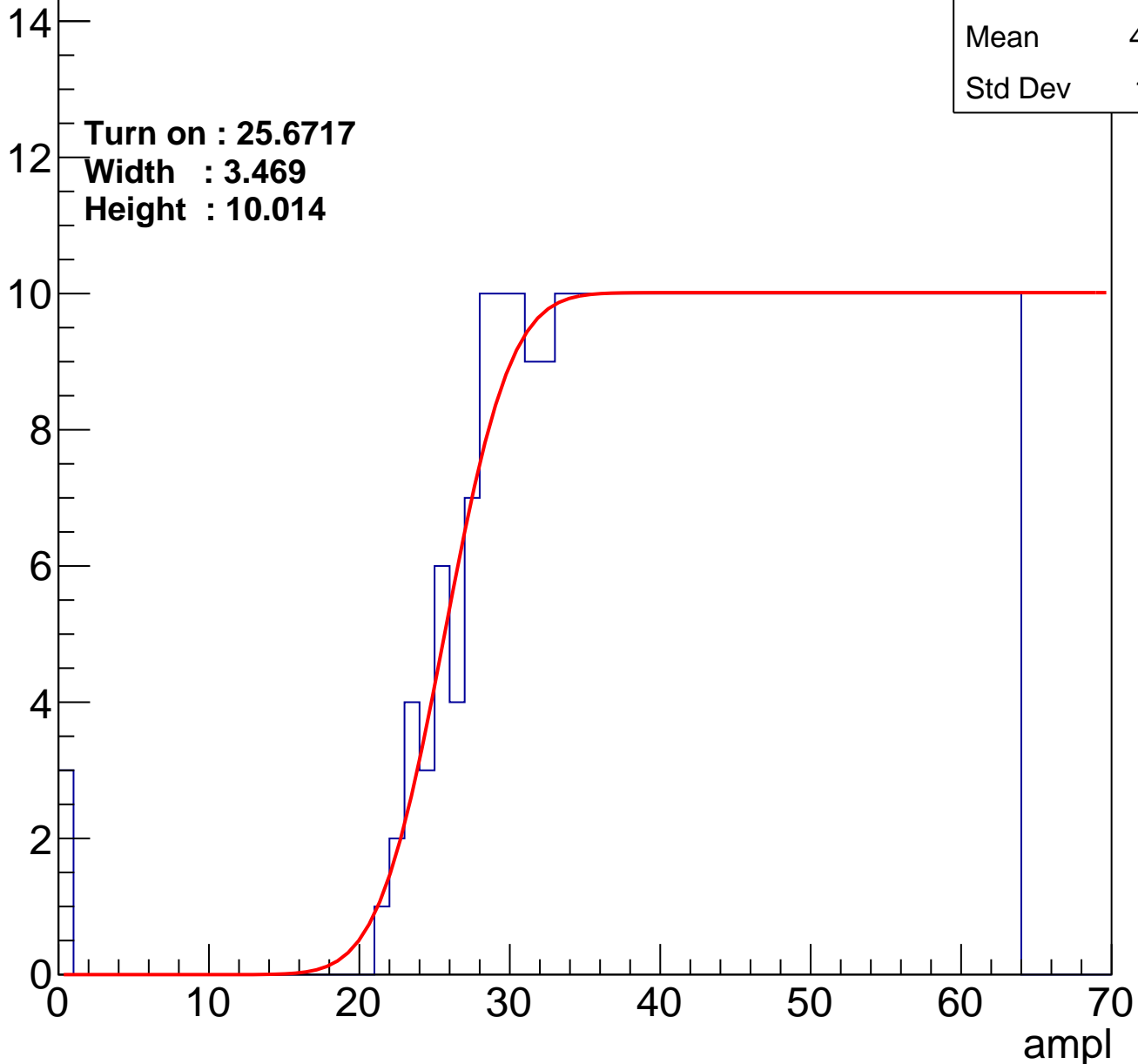
Entries	388
Mean	43.79
Std Dev	11.91

Turn on : 25.6717

Width : 3.469

Height : 10.014

Entry



B0L102S, U25-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.77
Std Dev	12.13

Turn on : 25.4575

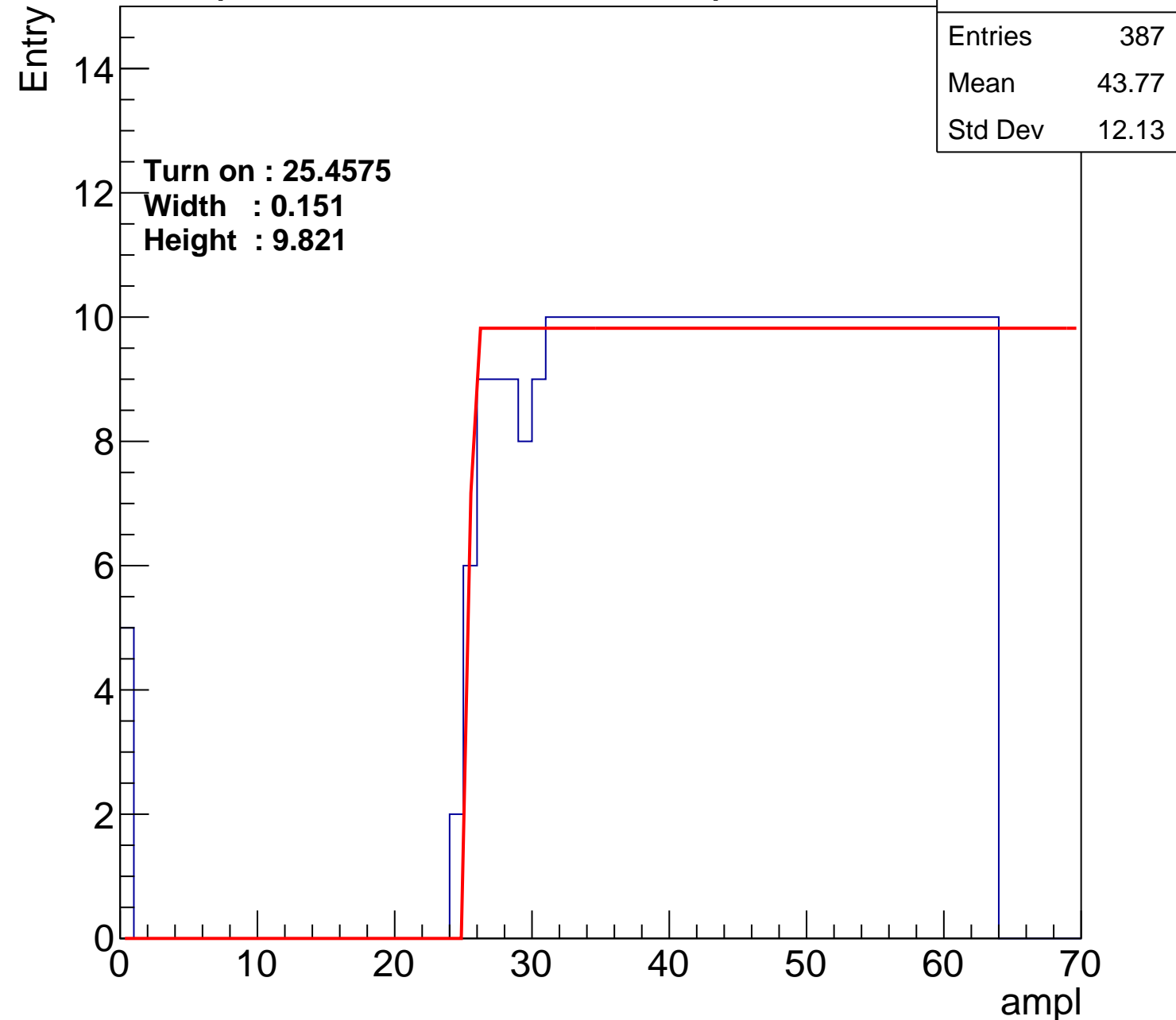
Width : 0.151

Height : 9.821

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch122

calib_packv5_042523_0143.root, FC#12, port B1

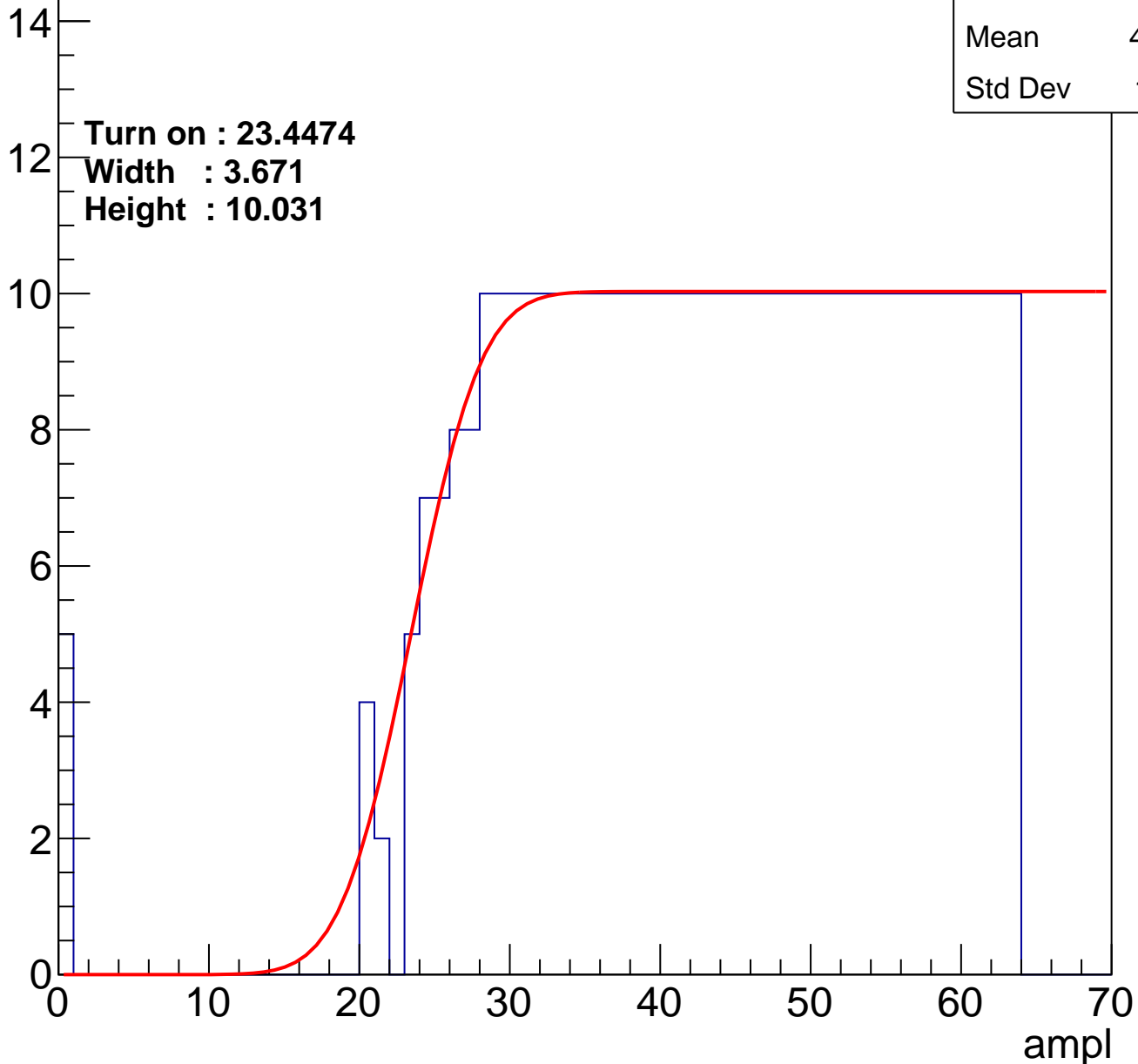
Entries	406
Mean	42.82
Std Dev	12.61

Turn on : 23.4474

Width : 3.671

Height : 10.031

Entry



B0L102S, U25-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.83
Std Dev	11.9

Turn on : 25.2058

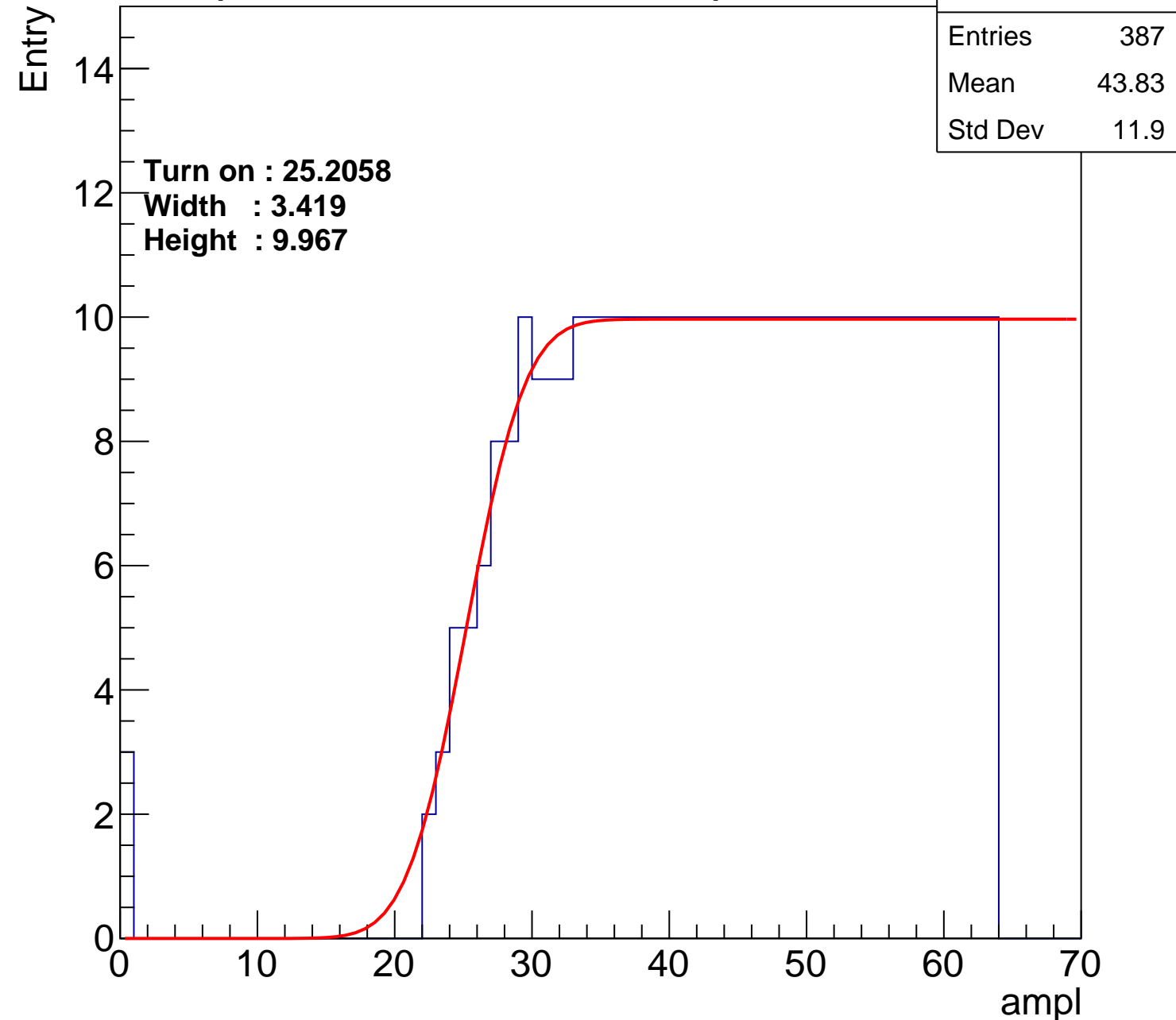
Width : 3.419

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Turn on : 25.4130

Width : 3.172

Height : 10.069

Entries	393
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Mean	43.57
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Std Dev	12
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B0L102S, U25-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.44
Std Dev	11.51

Turn on : 27.2257

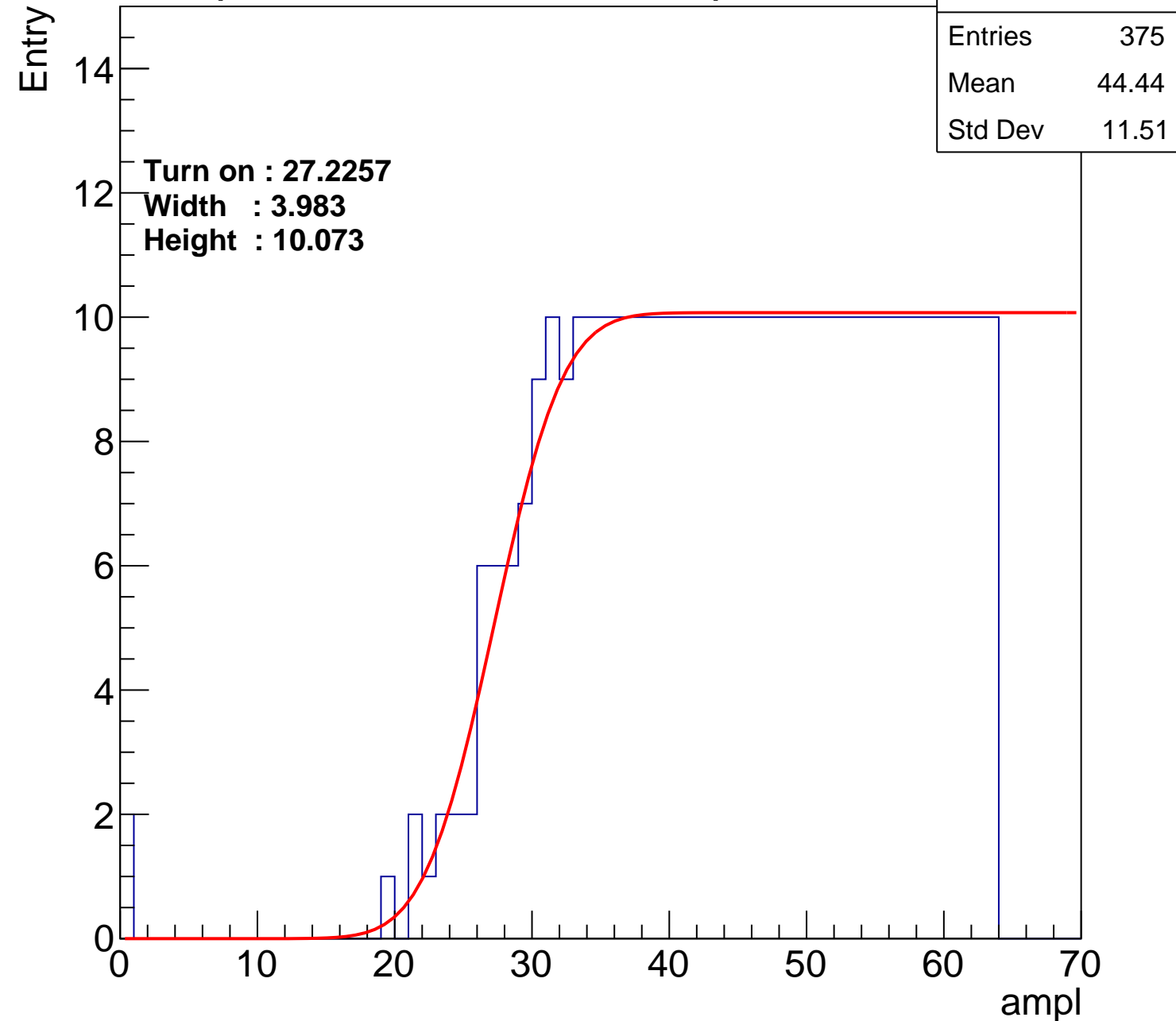
Width : 3.983

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U25-ch126

calib_packv5_042523_0143.root, FC#12, port B1

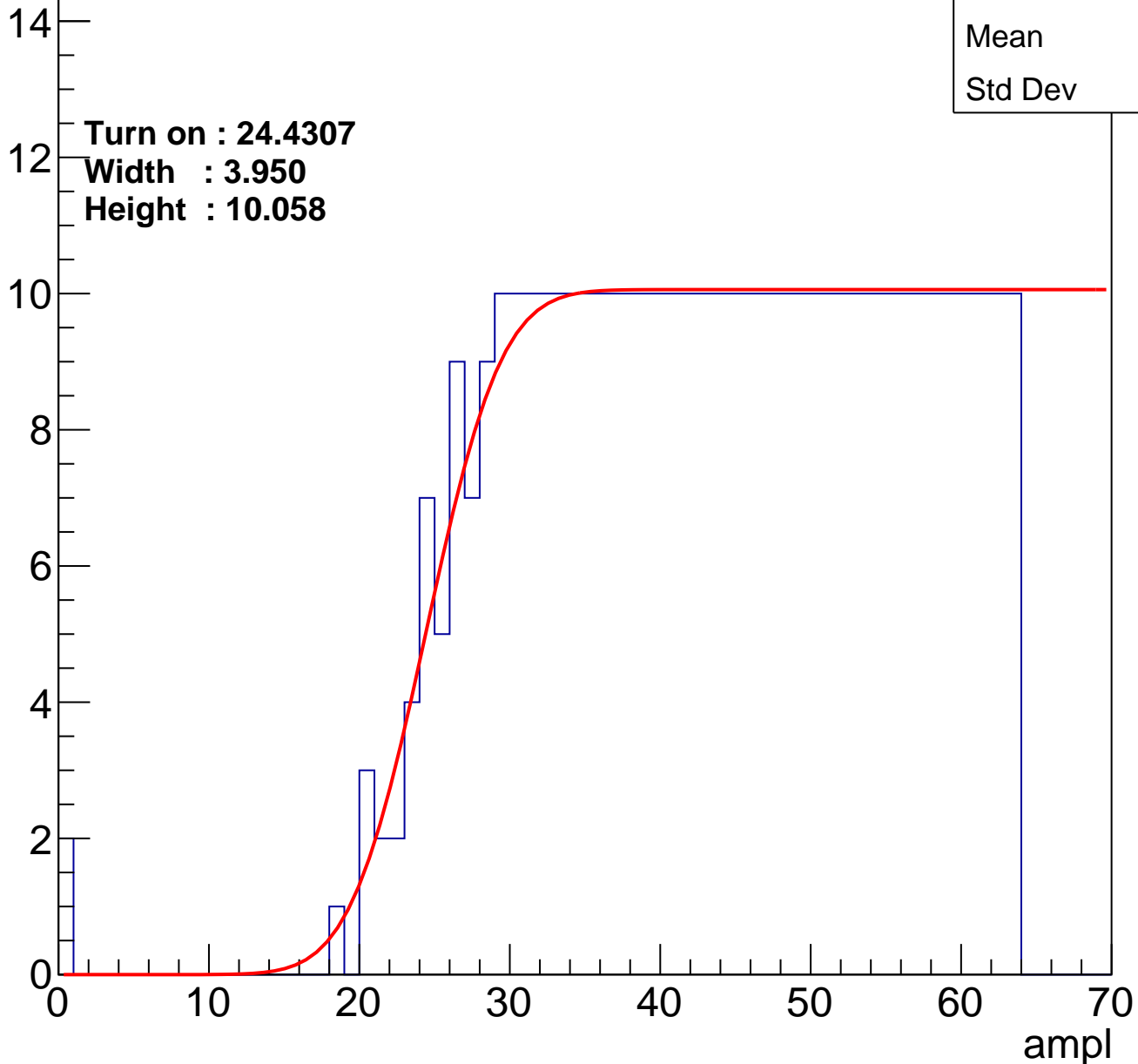
Entries	401
Mean	43.2
Std Dev	12.1

Turn on : 24.4307

Width : 3.950

Height : 10.058

Entry



B0L102S, U25-ch127

calib_packv5_042523_0143.root, FC#12, port B1

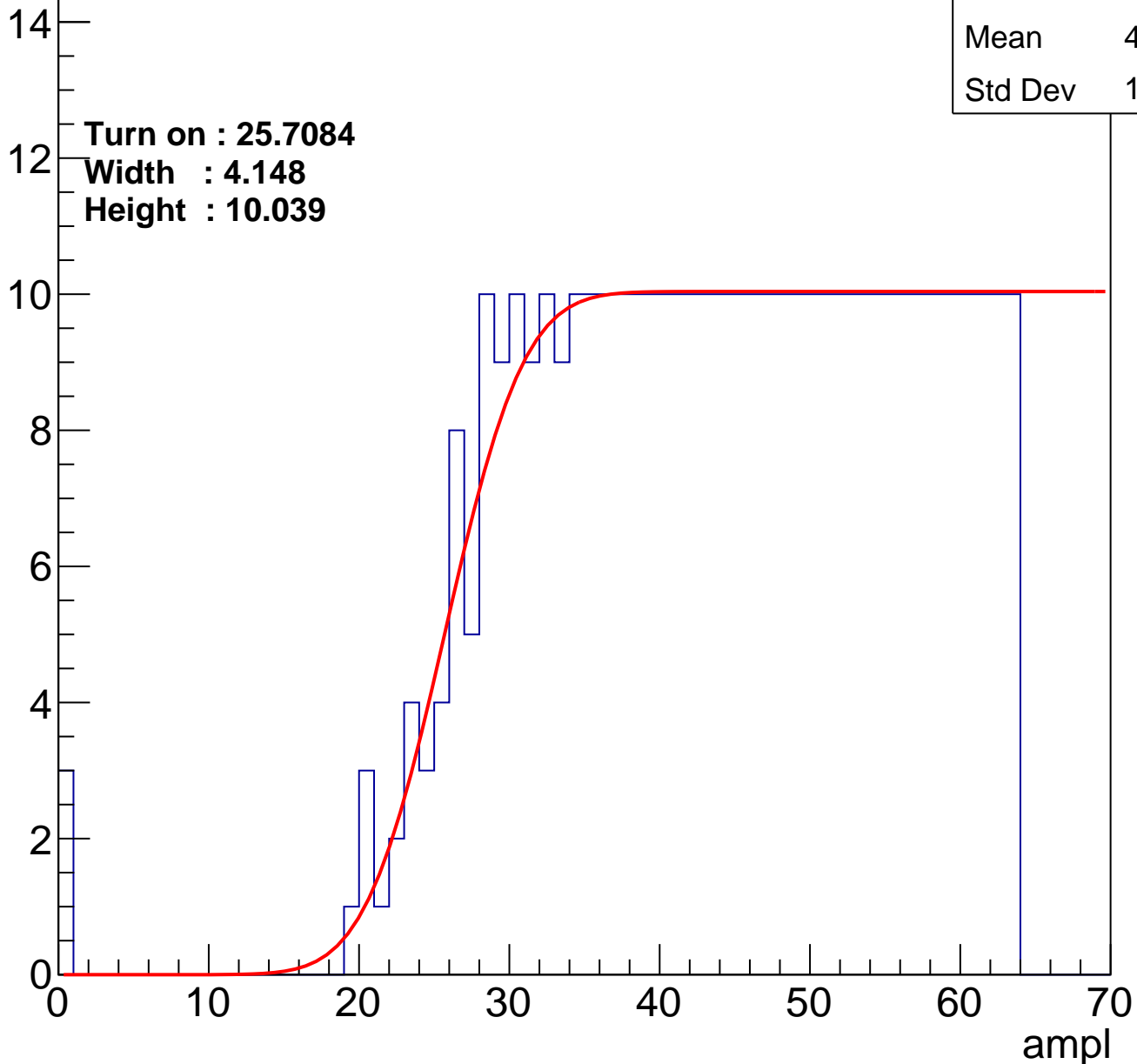
Entries	391
Mean	43.58
Std Dev	12.09

Turn on : 25.7084

Width : 4.148

Height : 10.039

Entry



B0L102S, U25-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.58
Std Dev	12.09

Turn on : 25.7084

Width : 4.148

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

