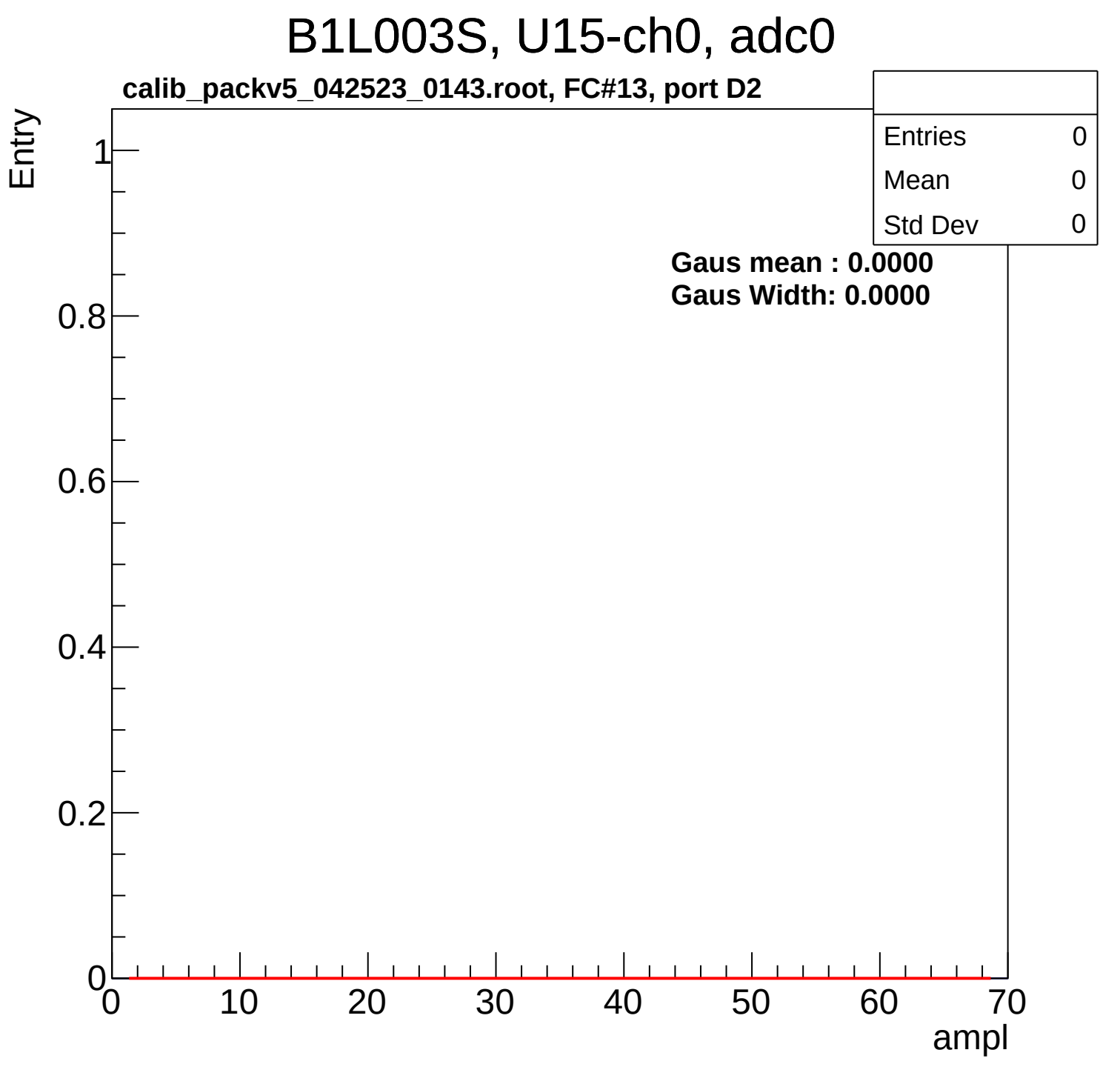
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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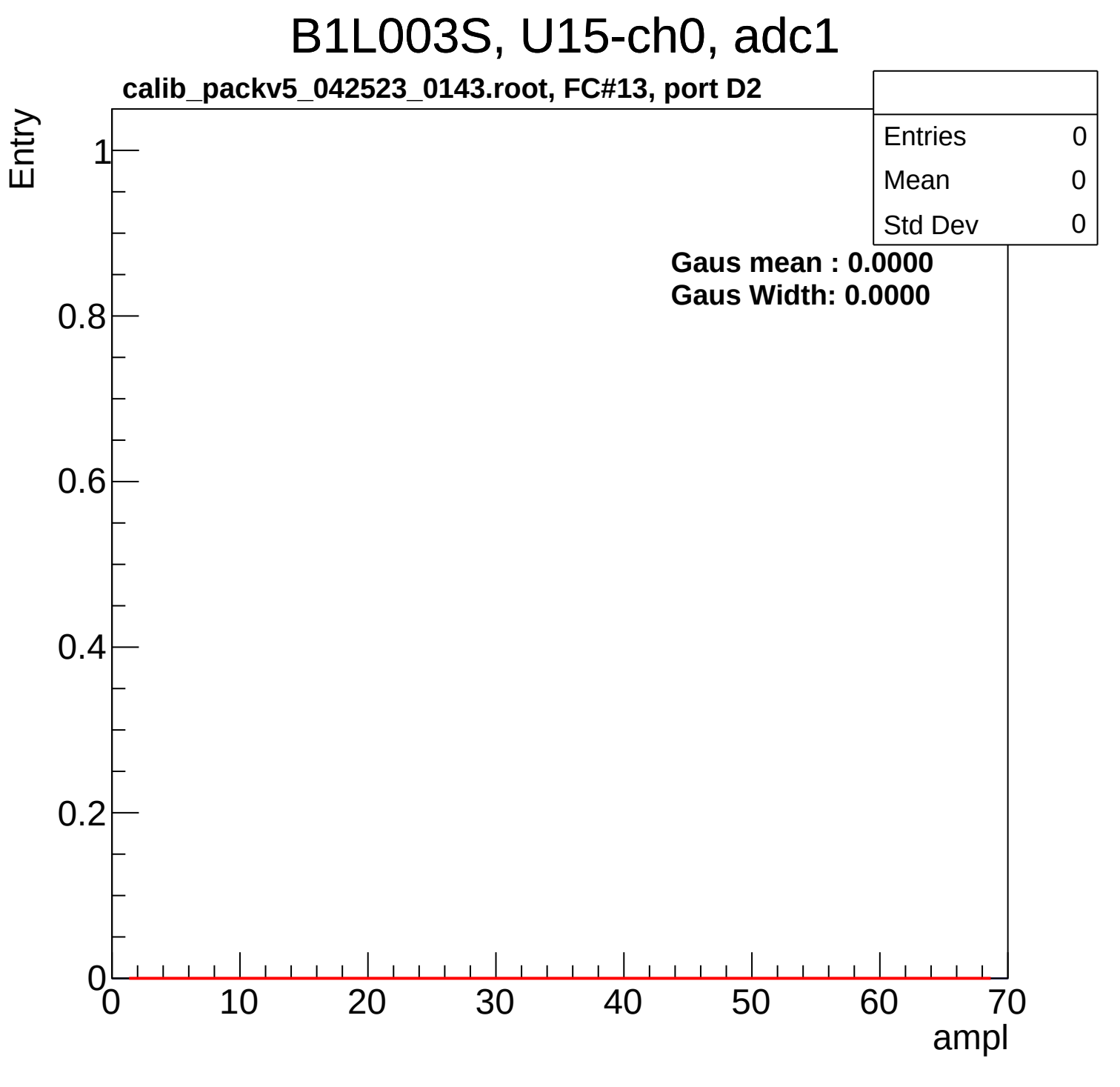
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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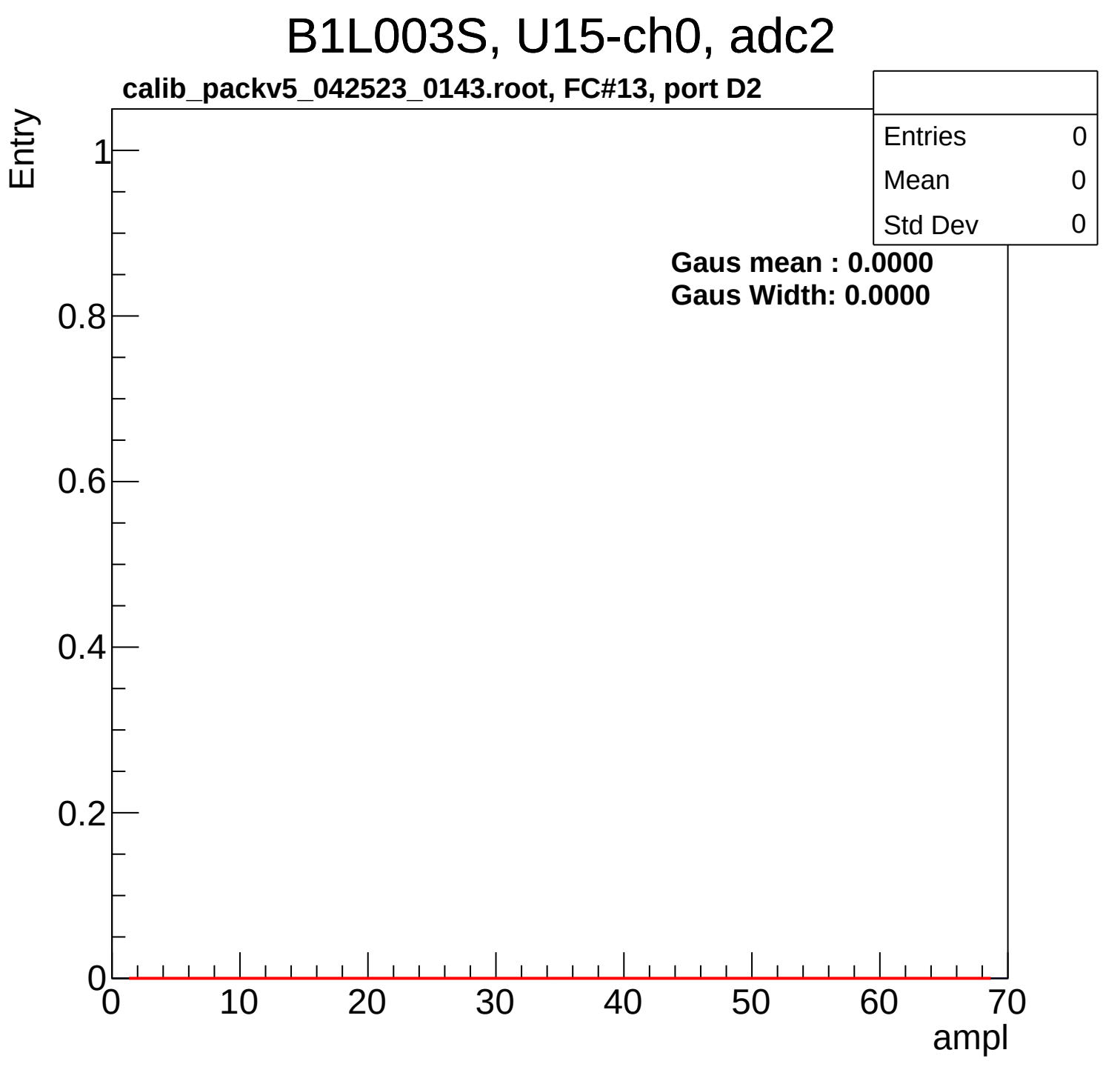
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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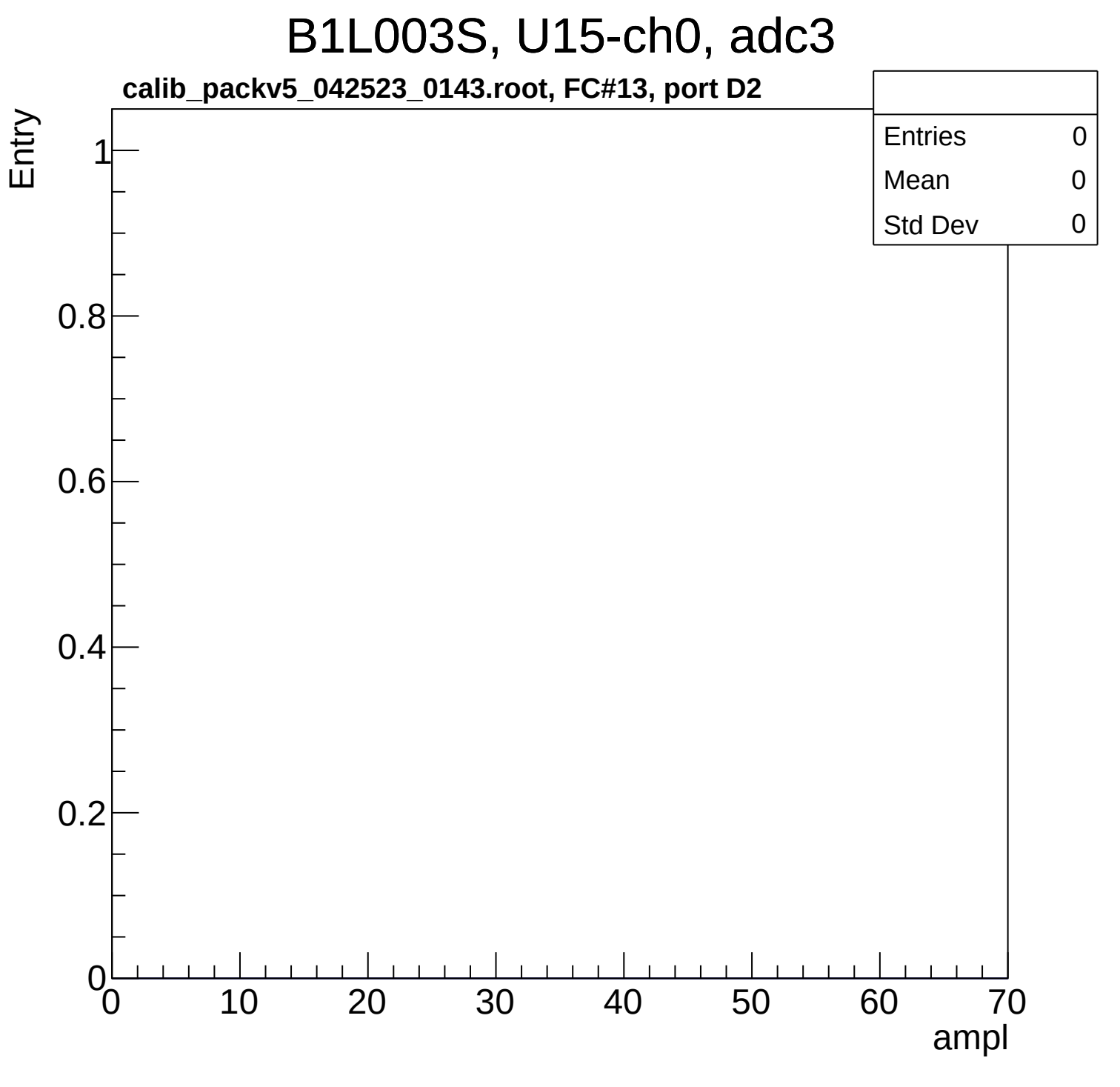
ampl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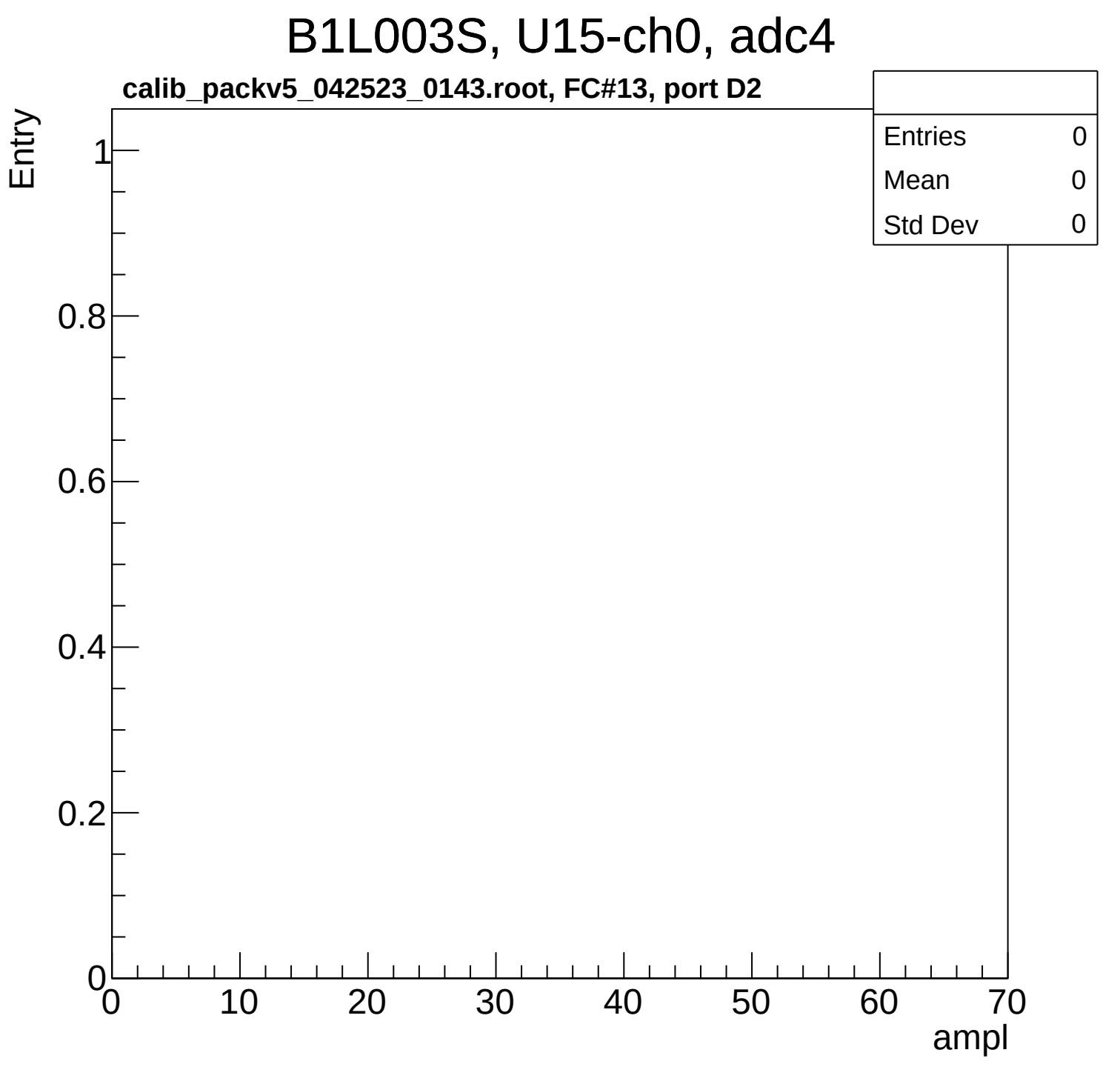
ampl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Std Dev 0
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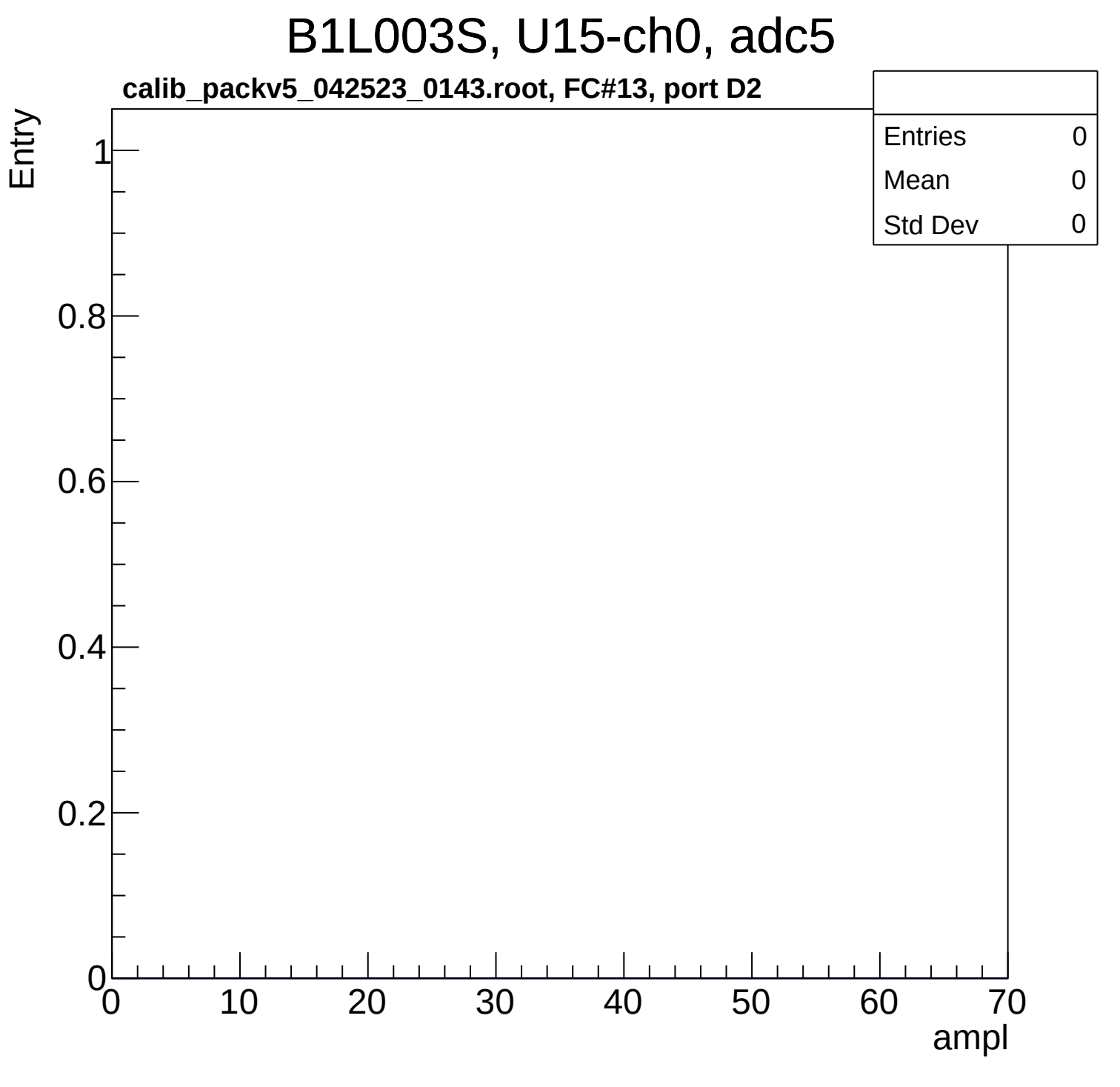
ampl


B1L003S, U15-ch0, adc5
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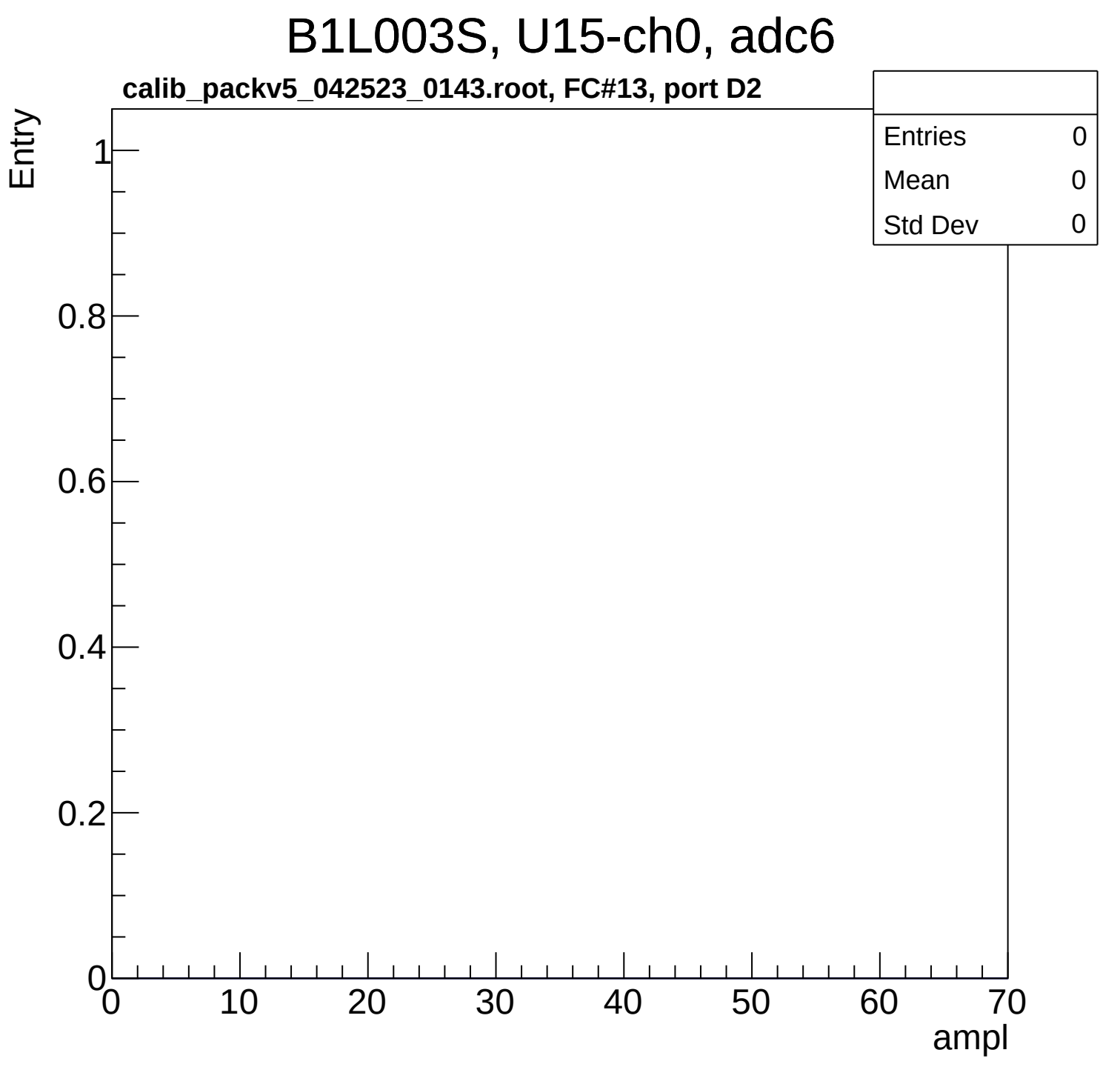
ampl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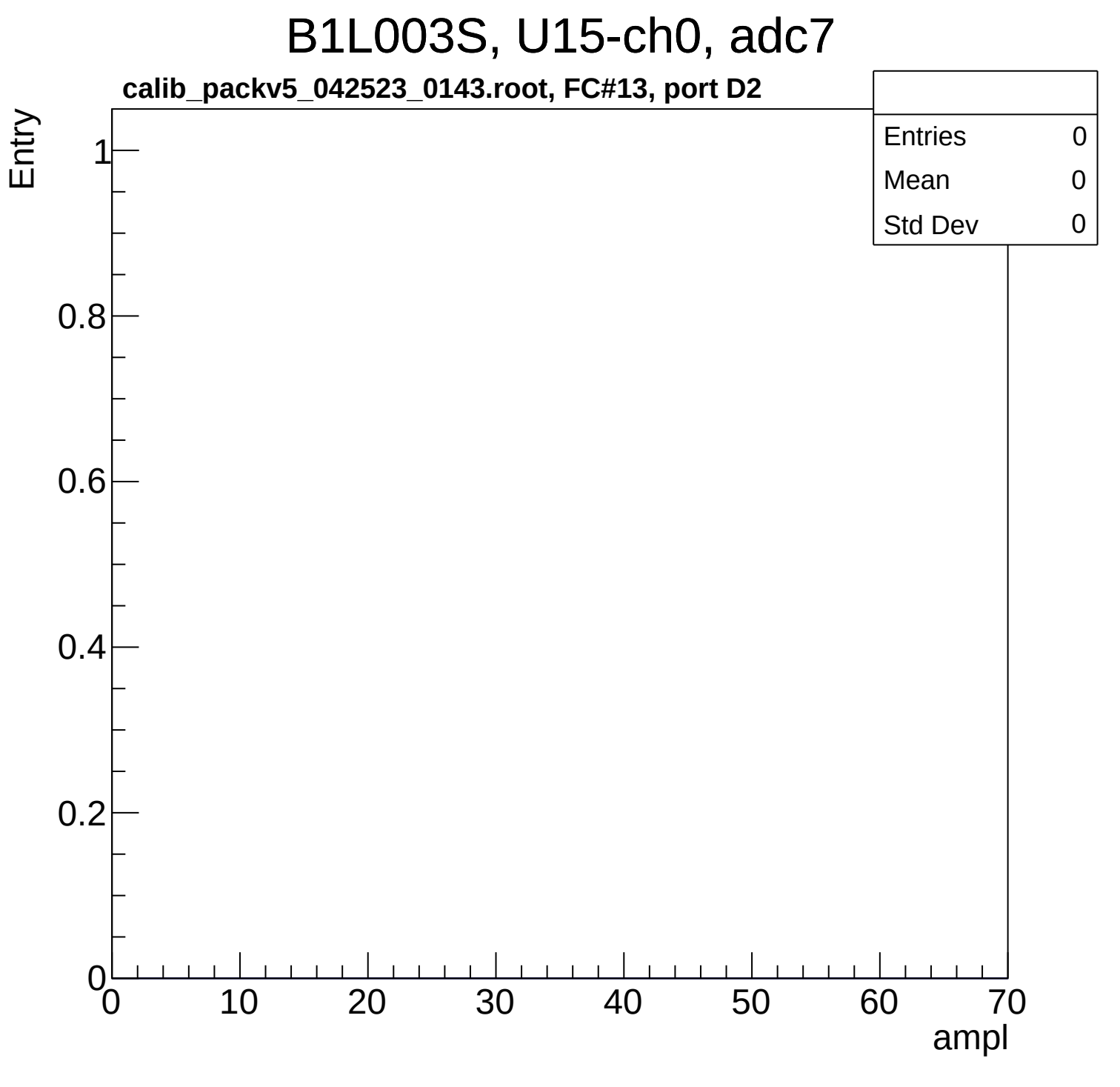
ampl


B1L003S, U15-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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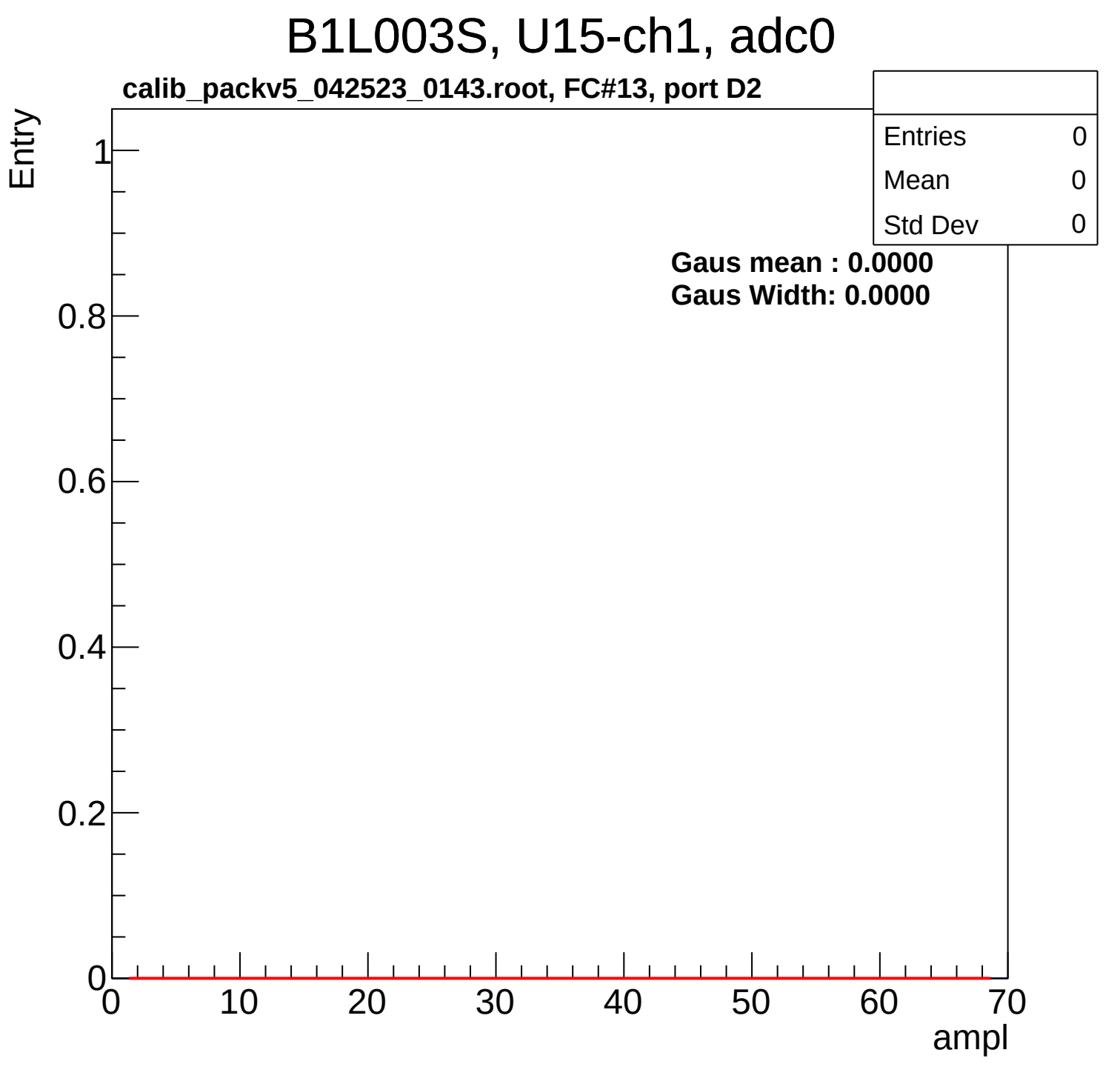
ampl


B1L003S, U15-ch1, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.6
0.4
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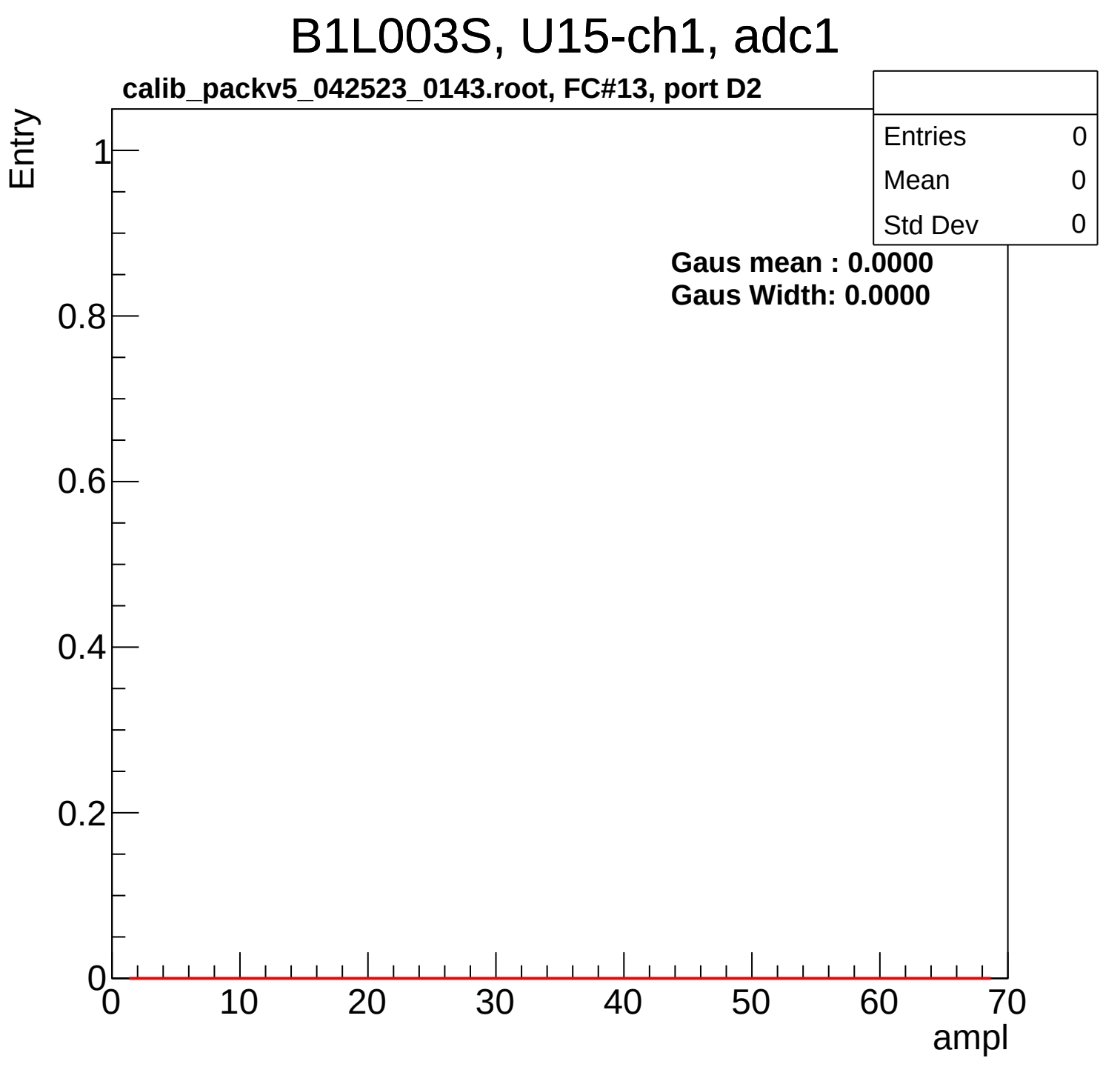
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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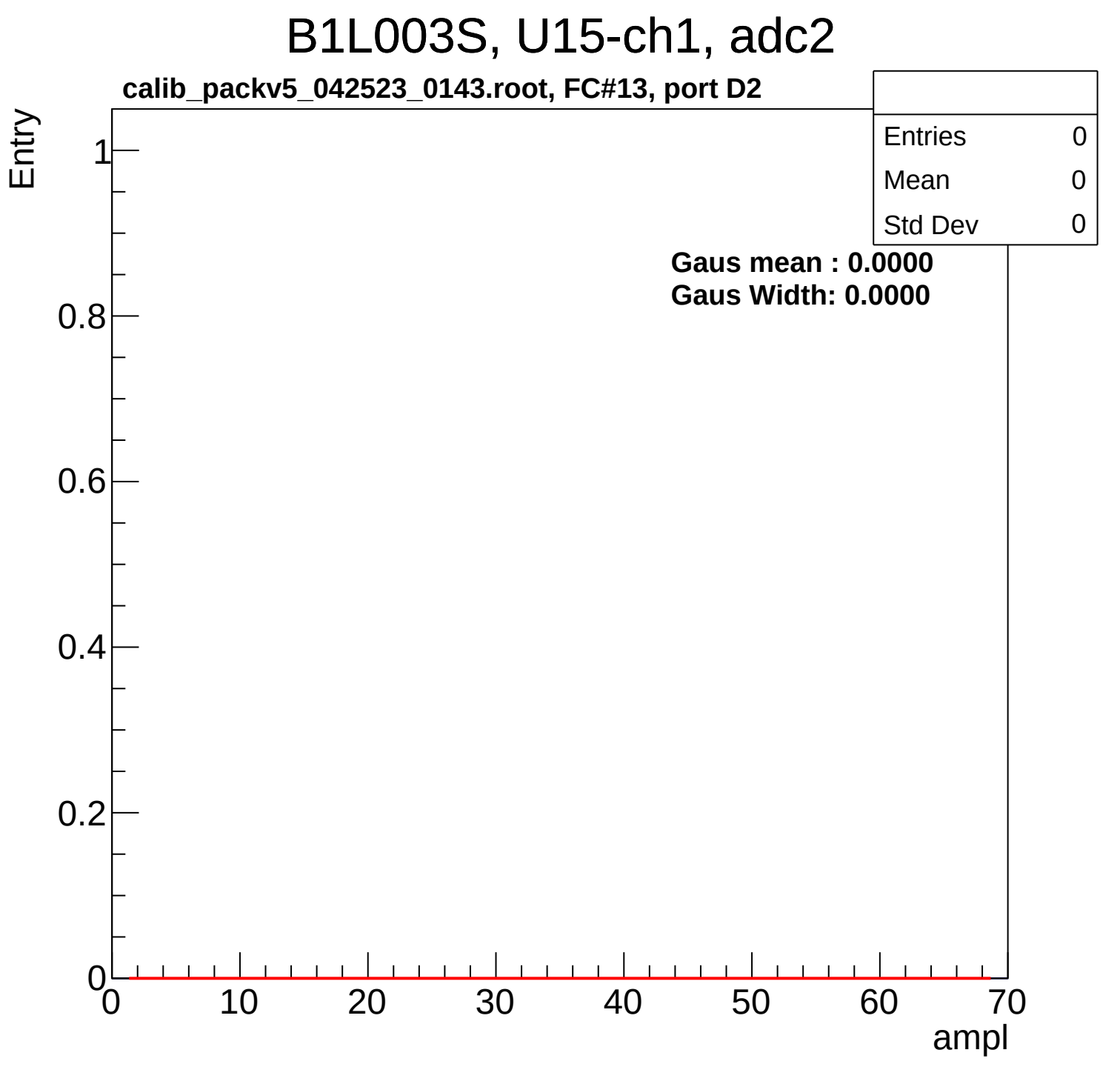
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Std Dev 0
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Gaus Width: 0.0000
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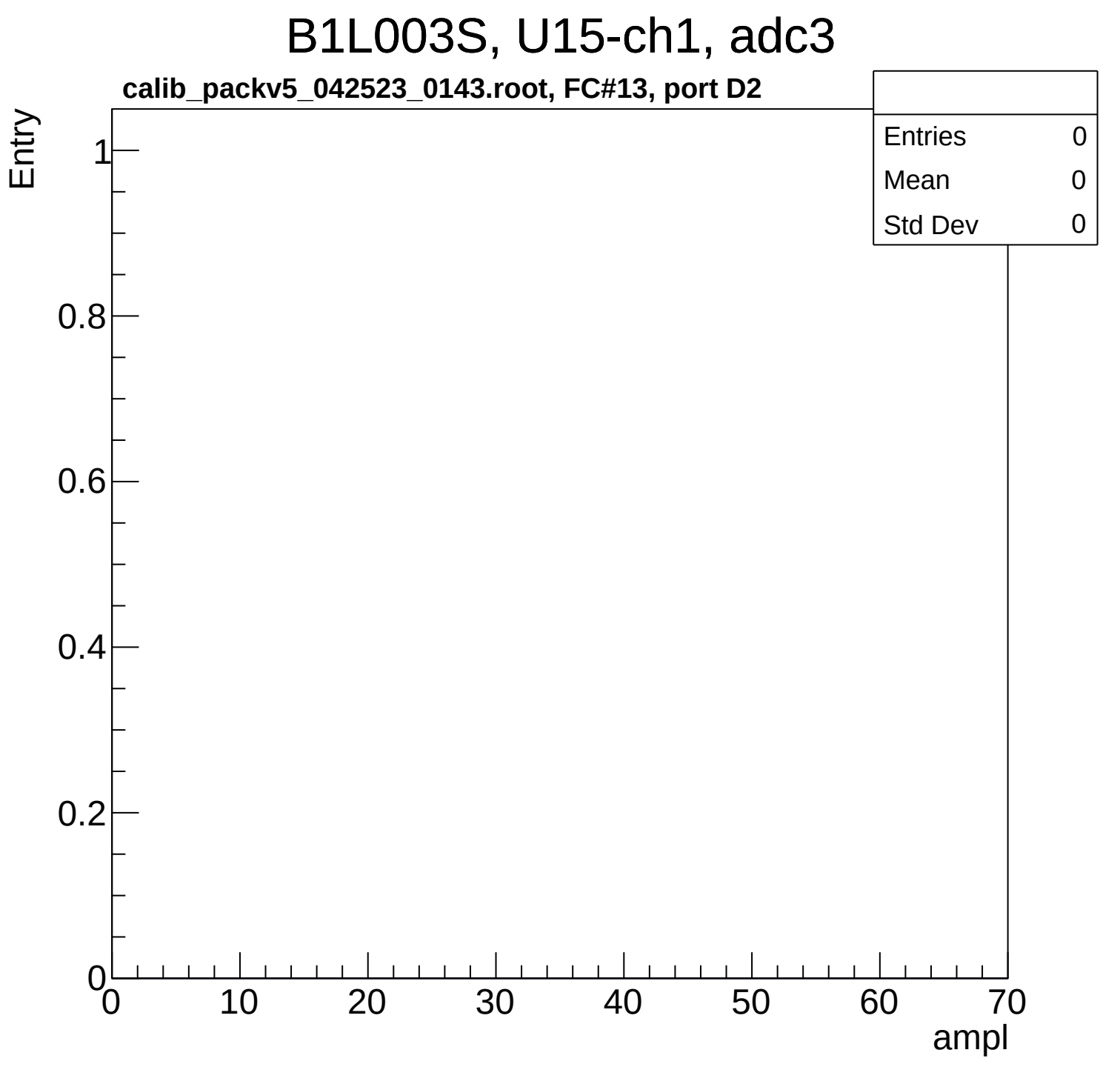
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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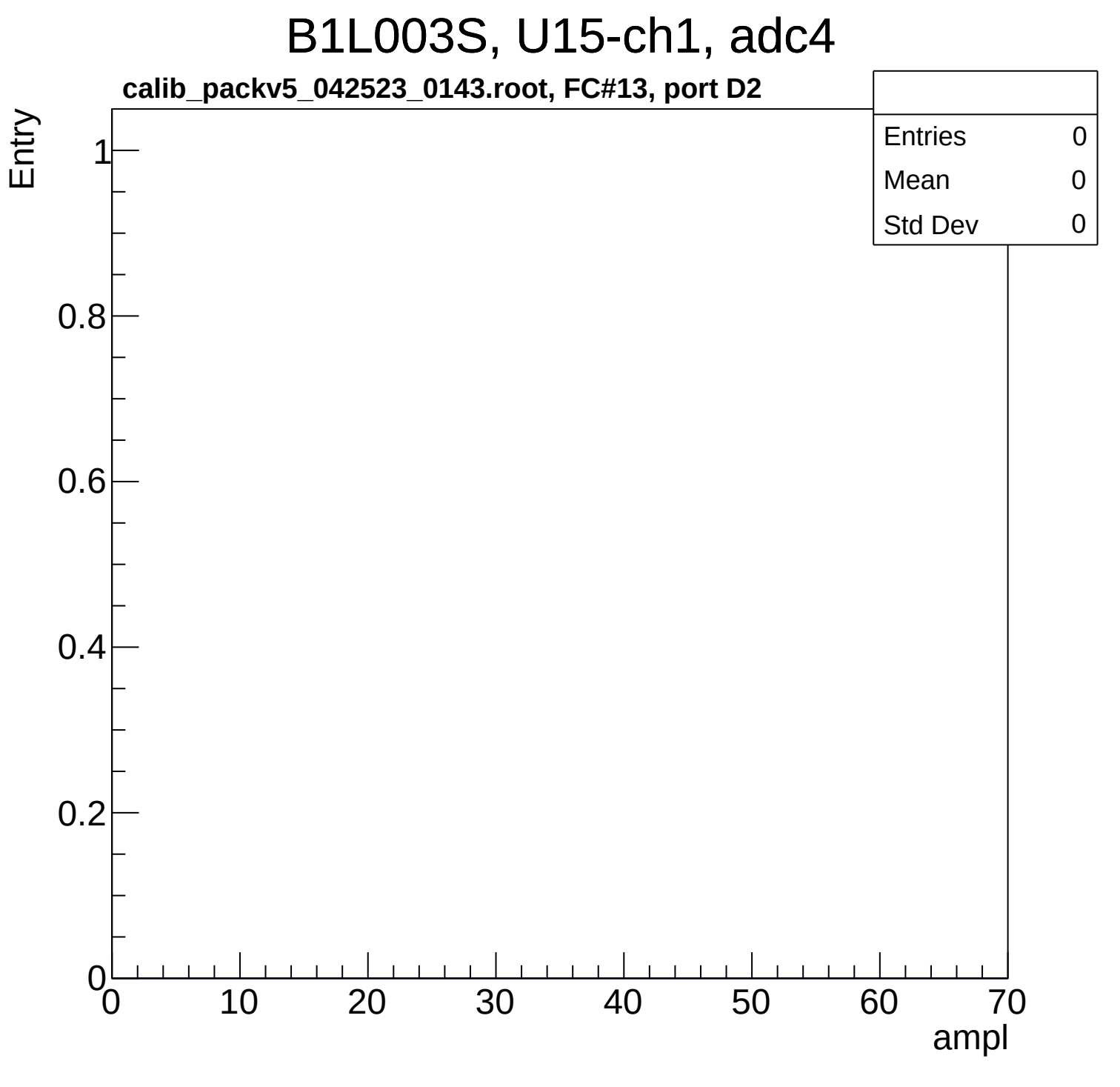
ampl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Std Dev 0
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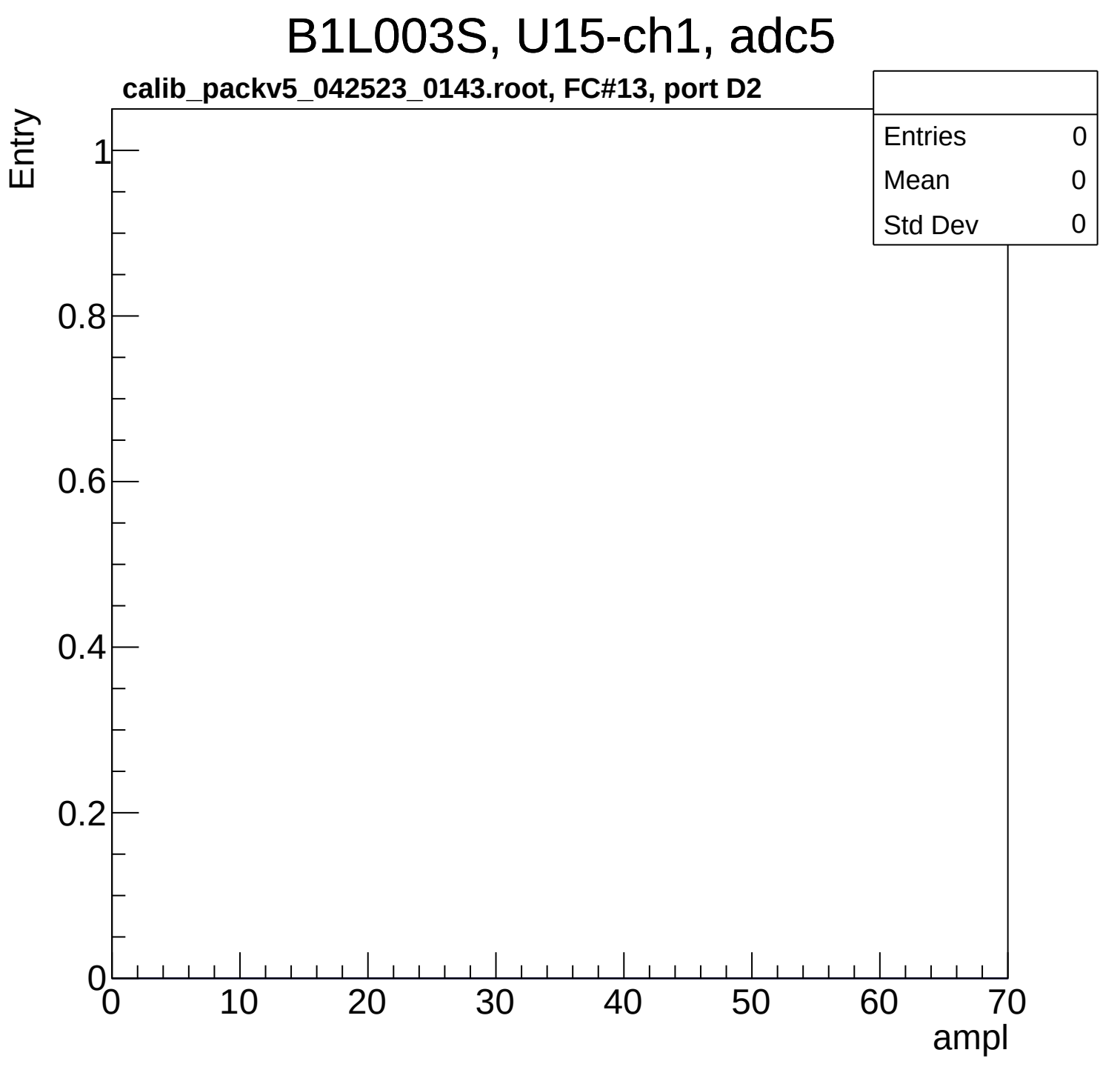
ampl


B1L003S, U15-ch1, adc5
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Std Dev 0
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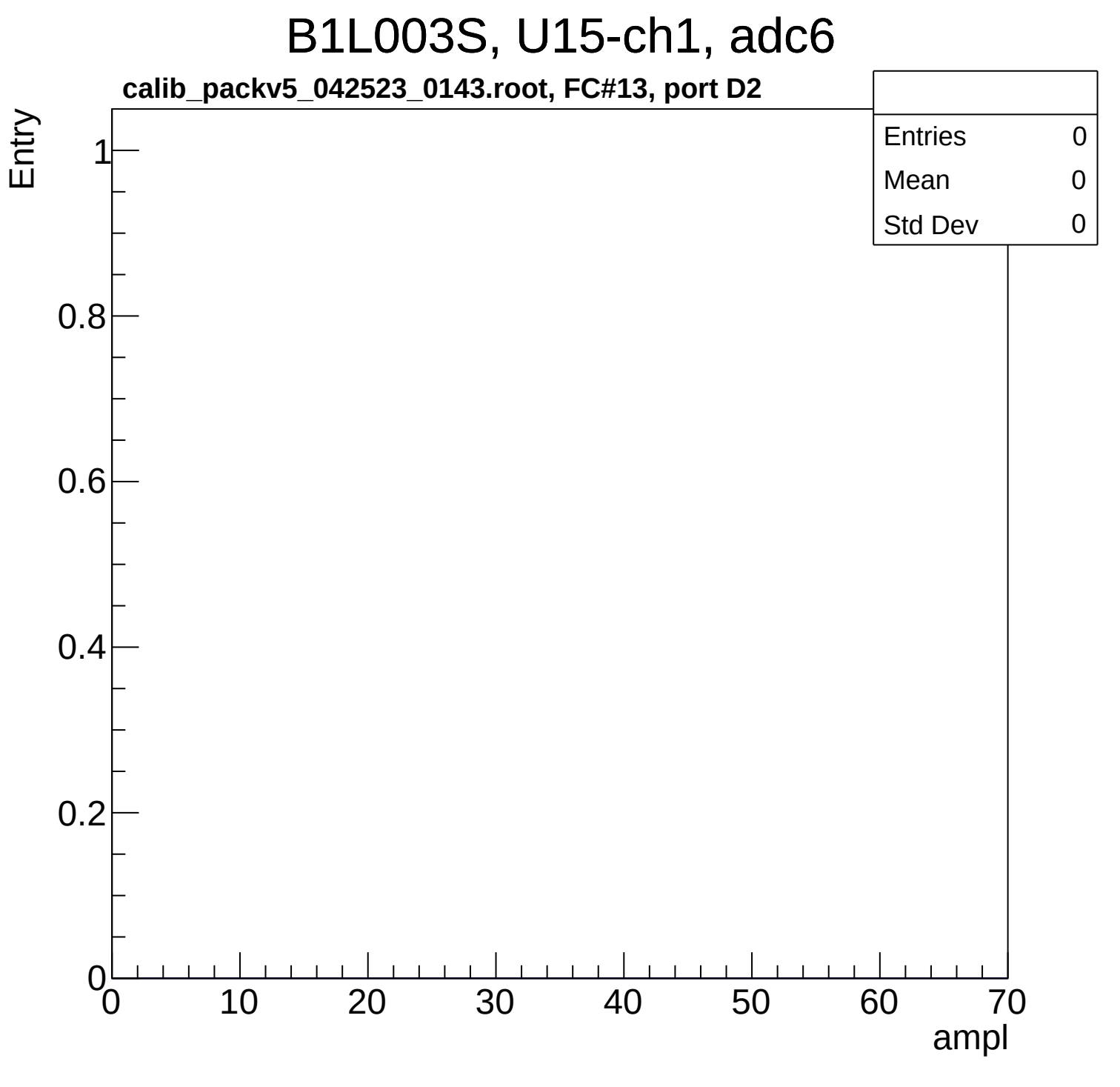
ampl


B1L003S, U15-ch1, adc6
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Entries 0
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Std Dev 0
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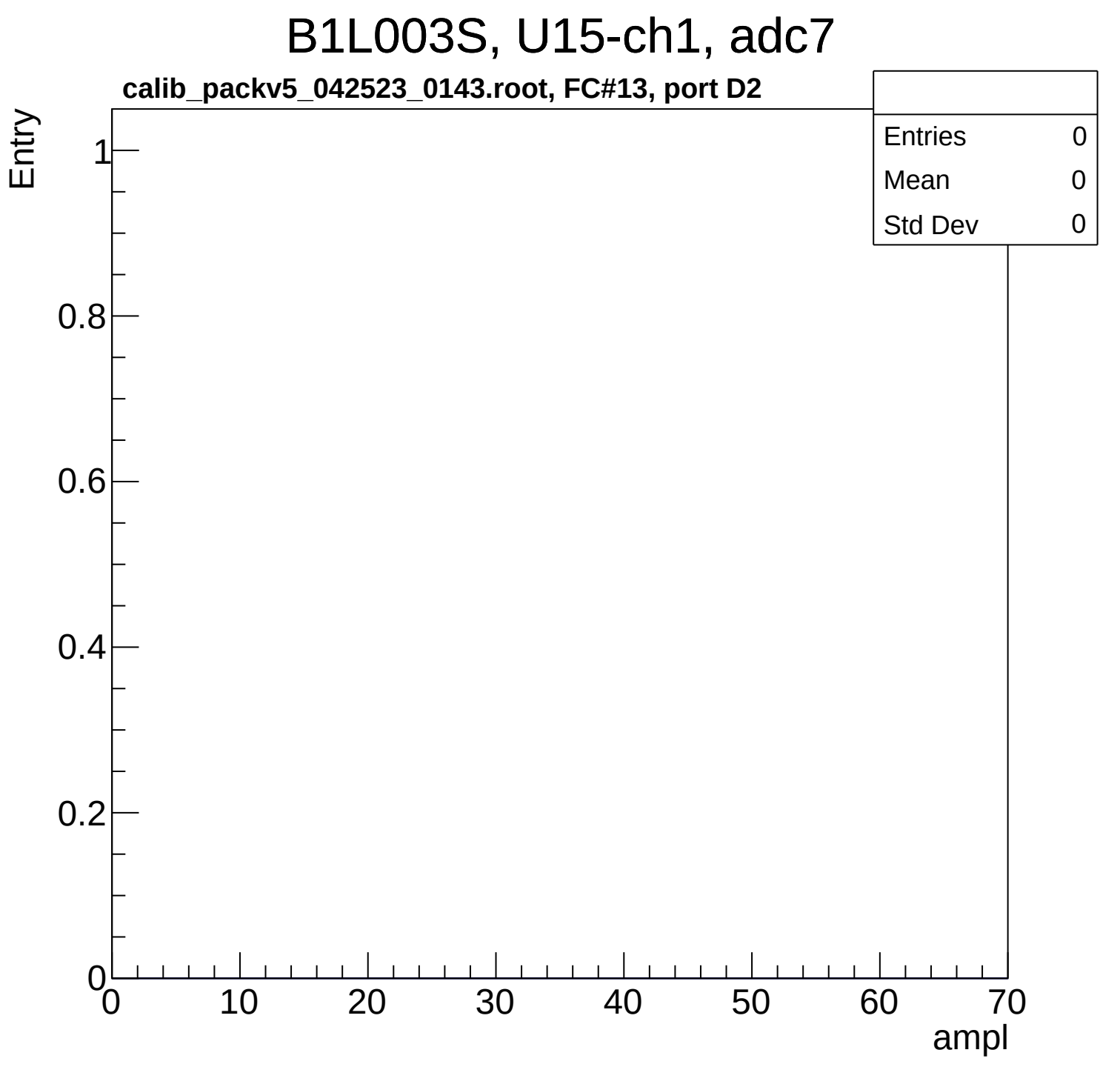
ampl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Mean 0
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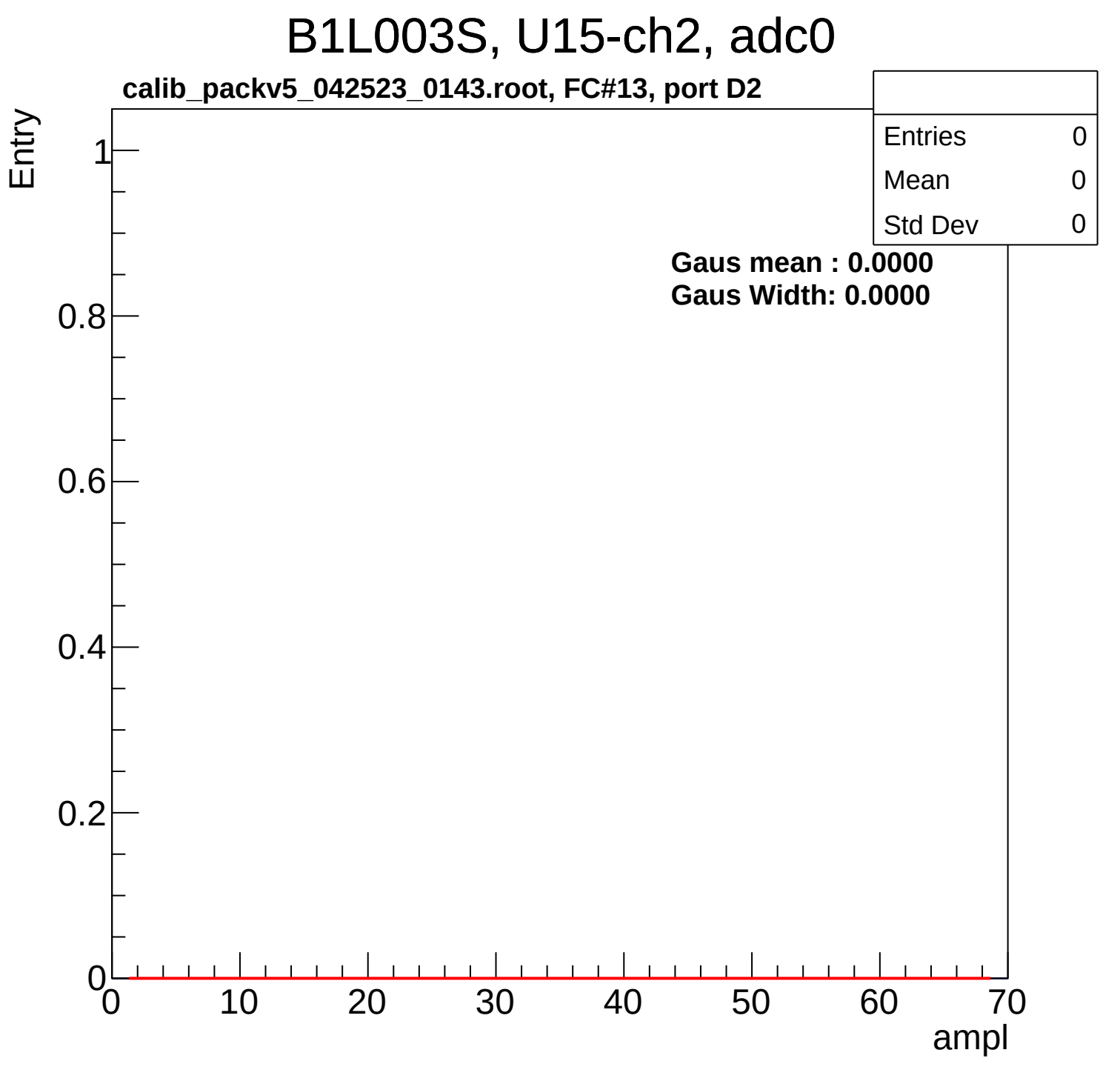
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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.4
0.2
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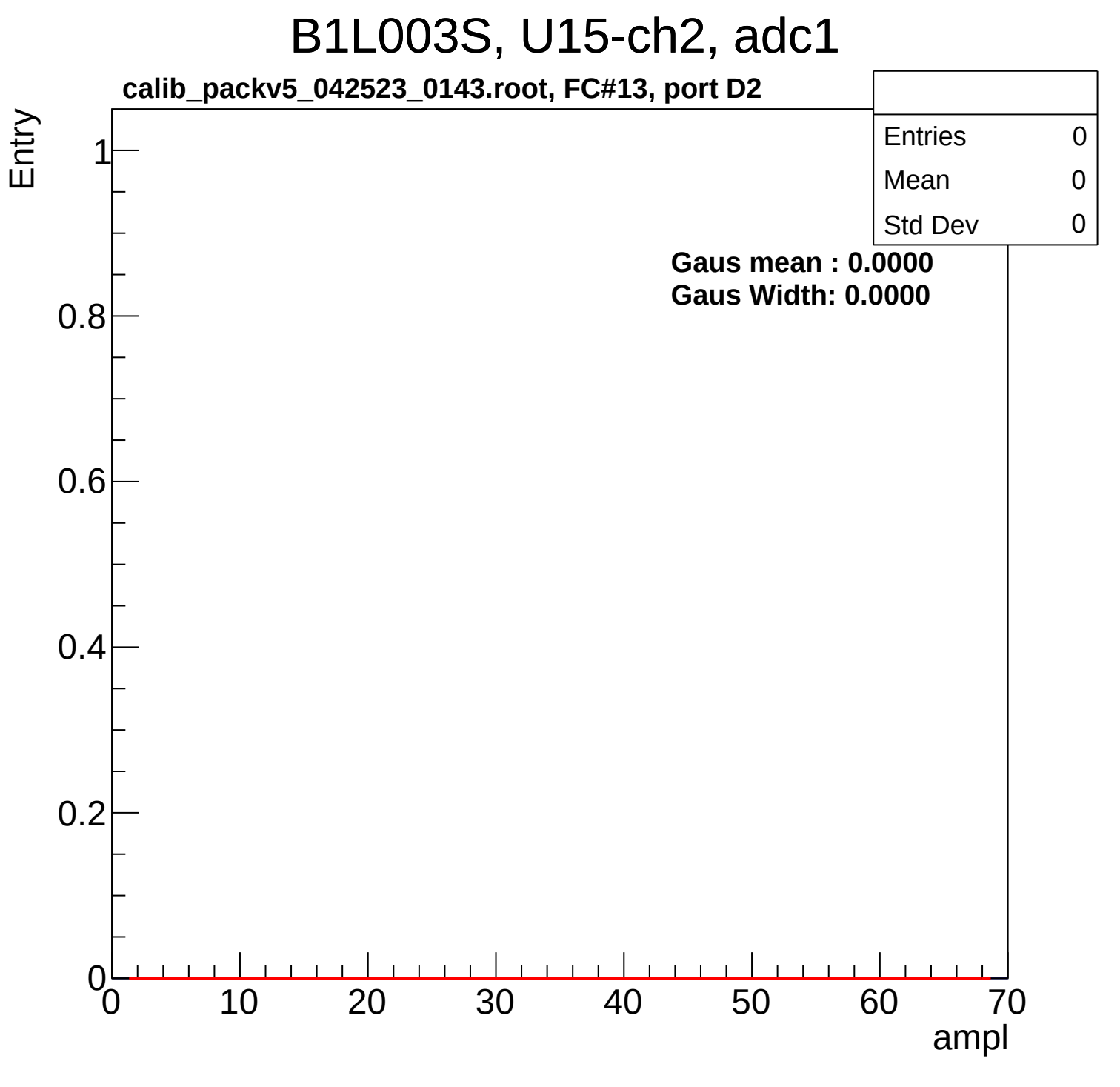
ampl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Entries 0
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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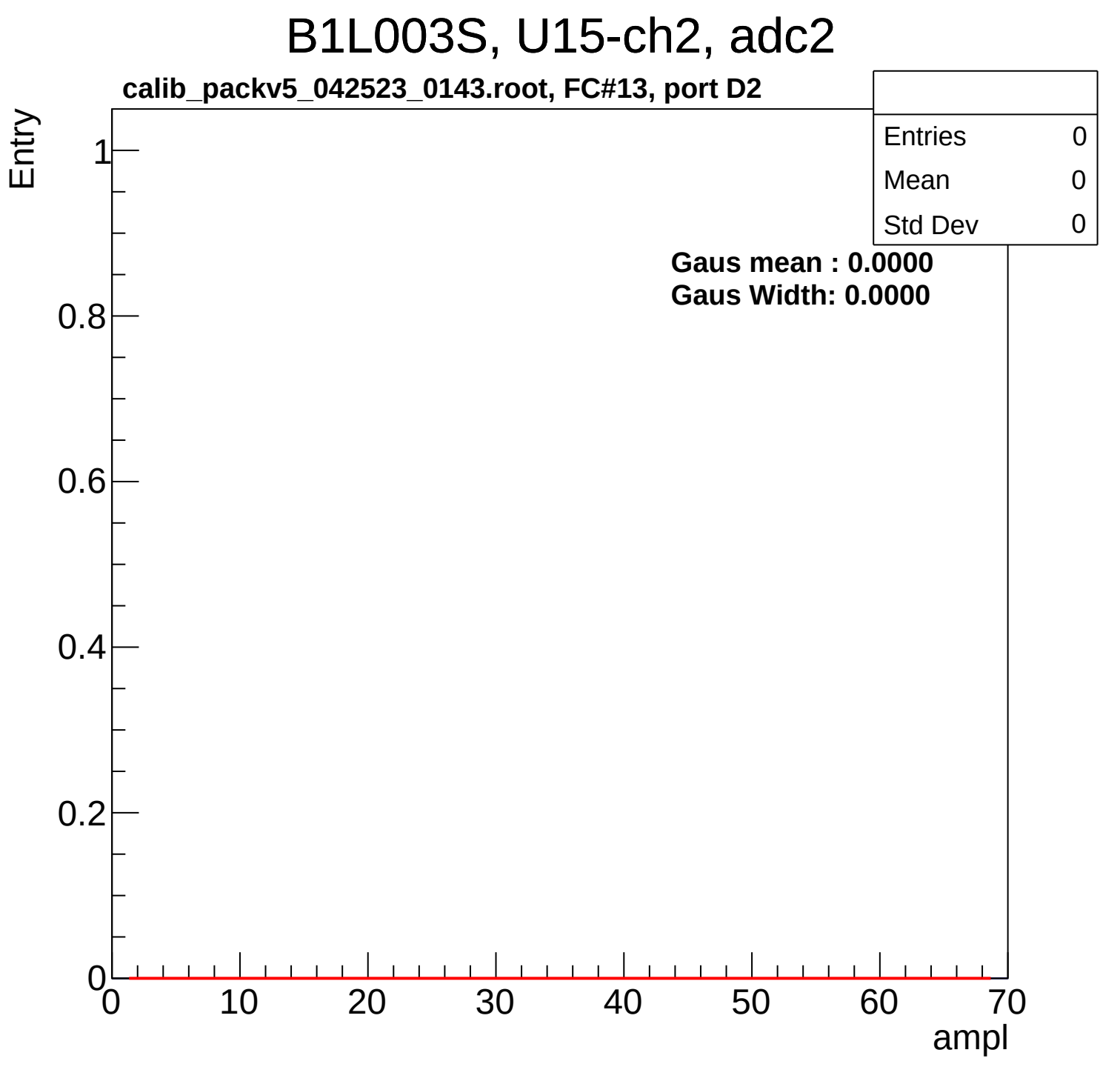
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Gaus Width: 0.0000
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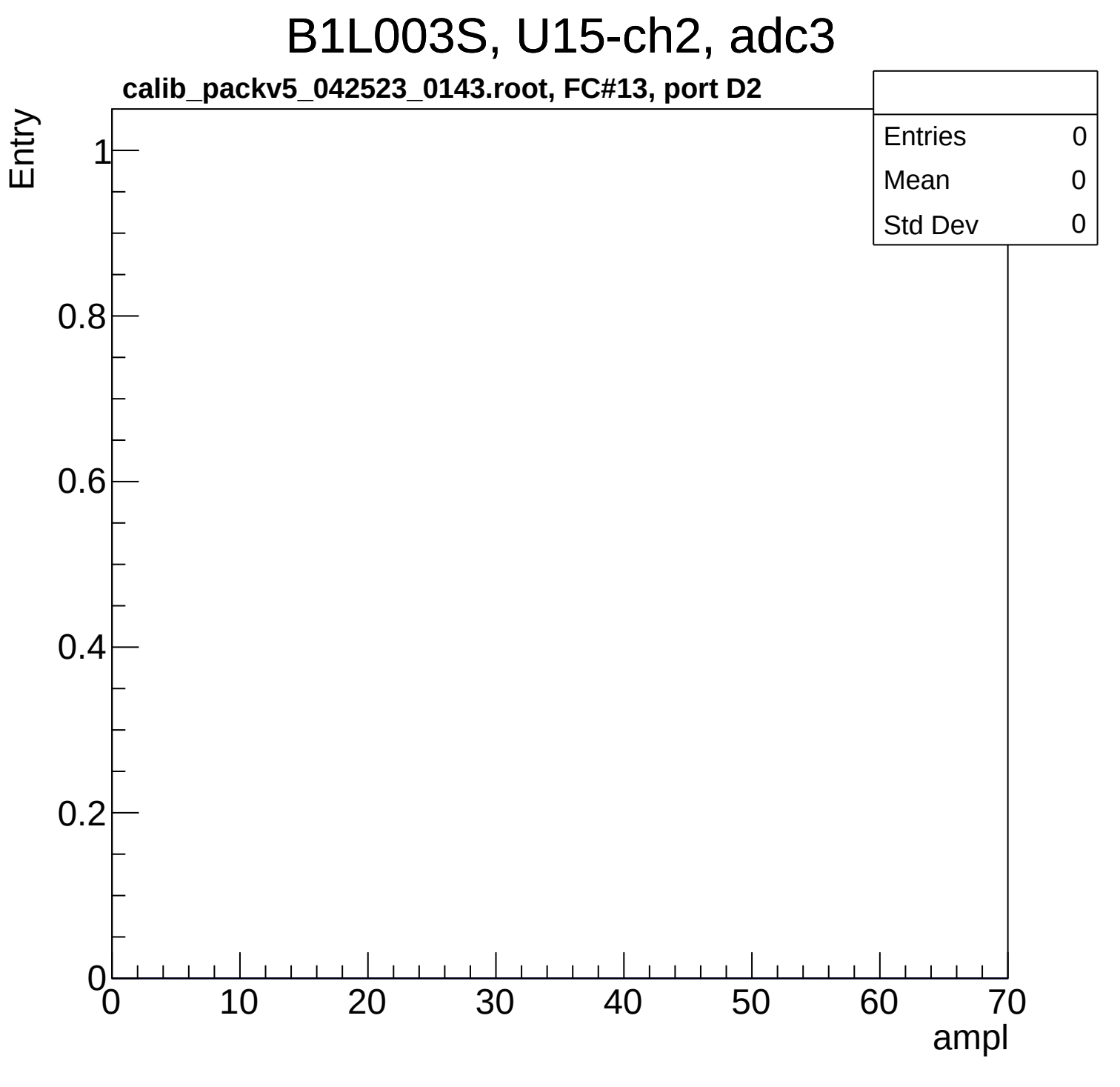
B1L003S, U15-ch2, adc3
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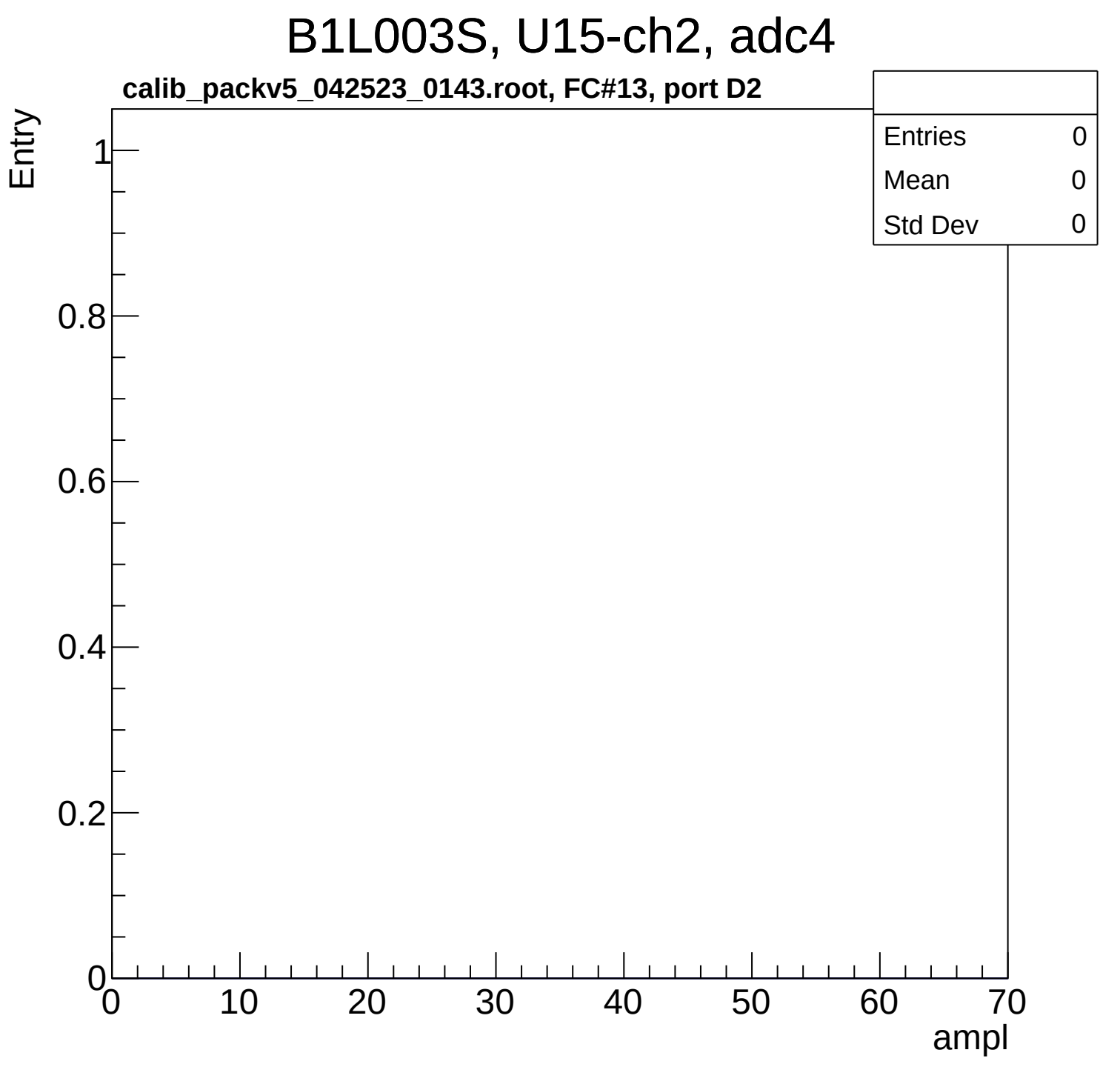
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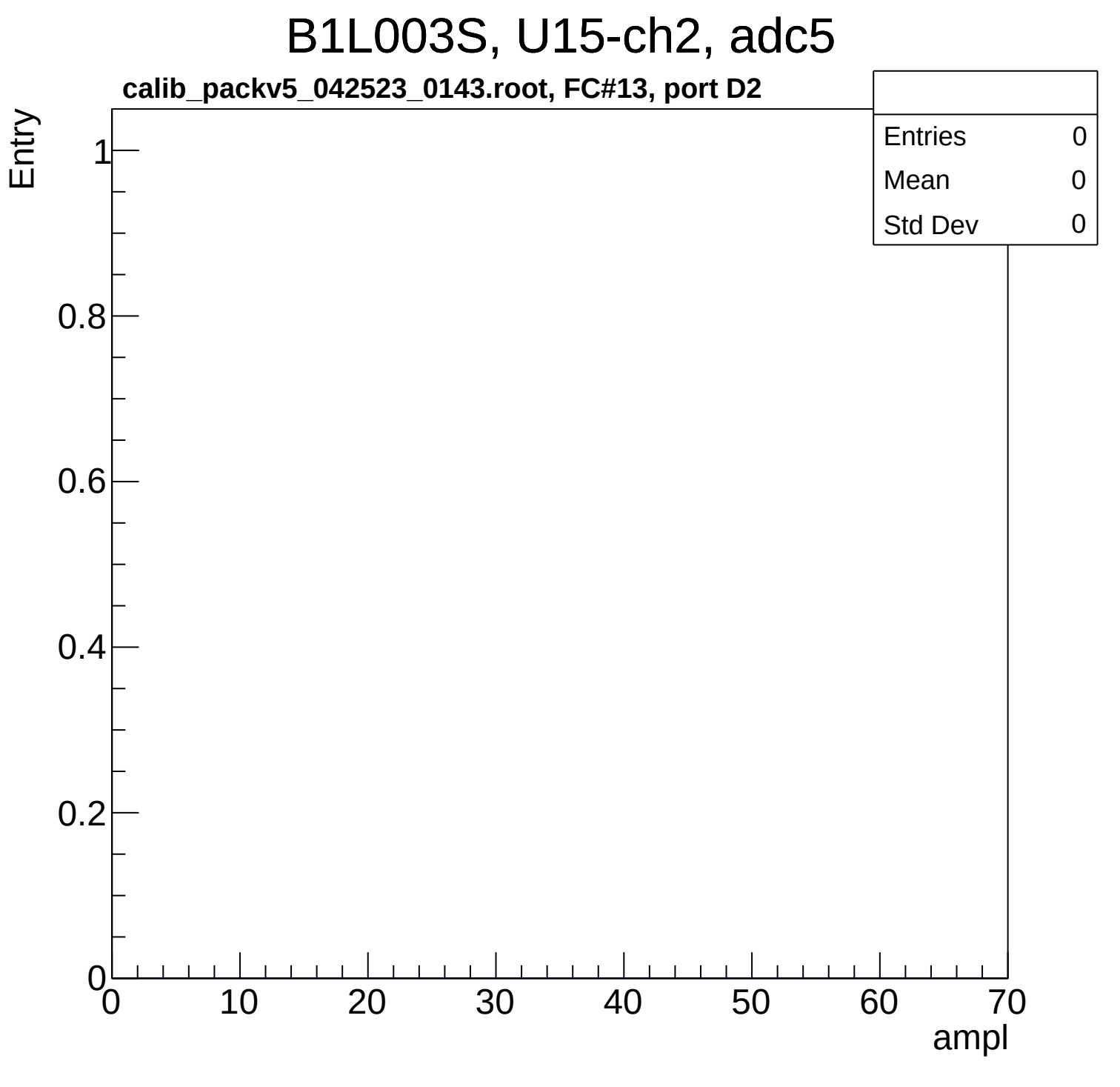
B1L003S, U15-ch2, adc5
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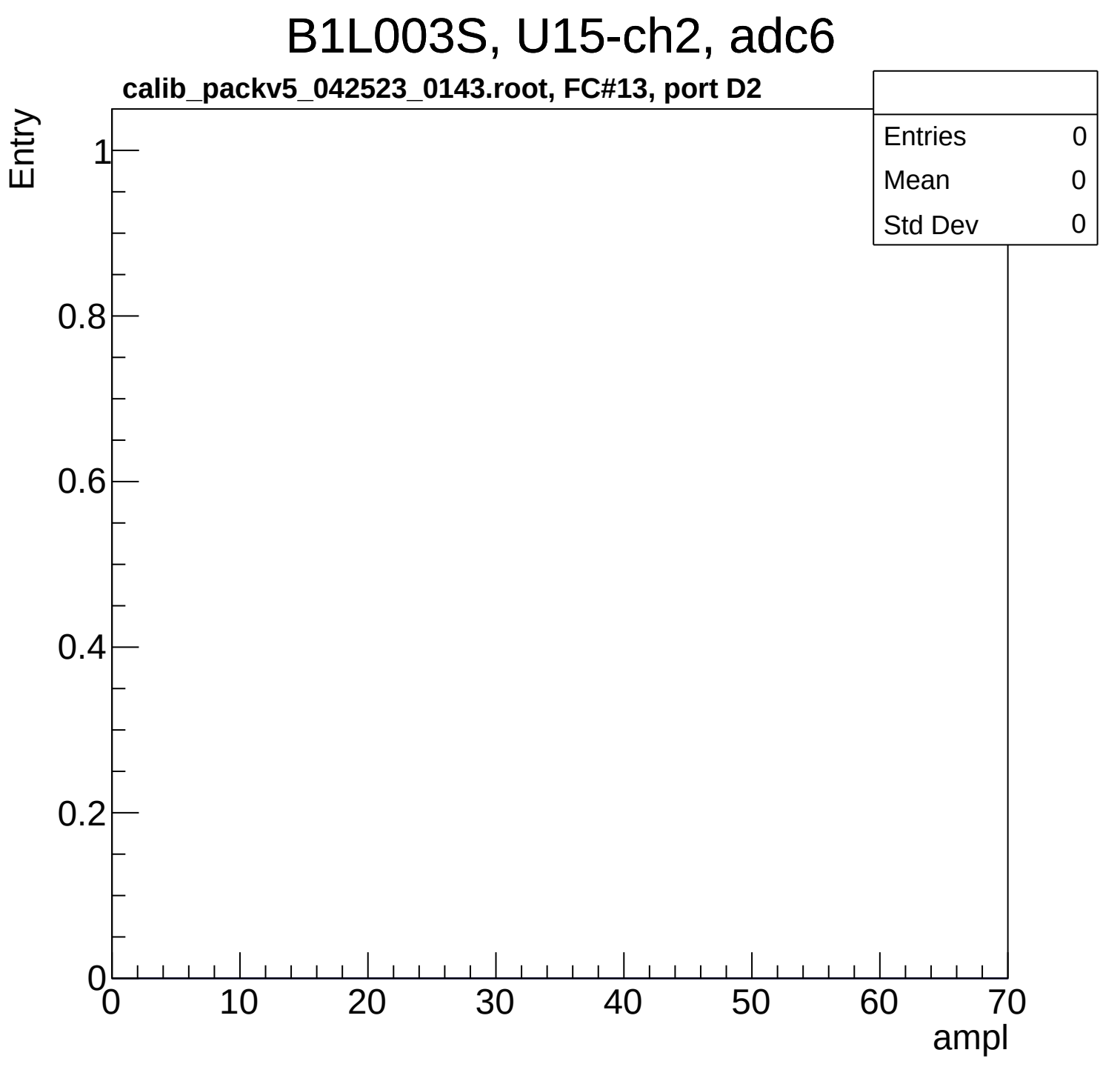
B1L003S, U15-ch2, adc6
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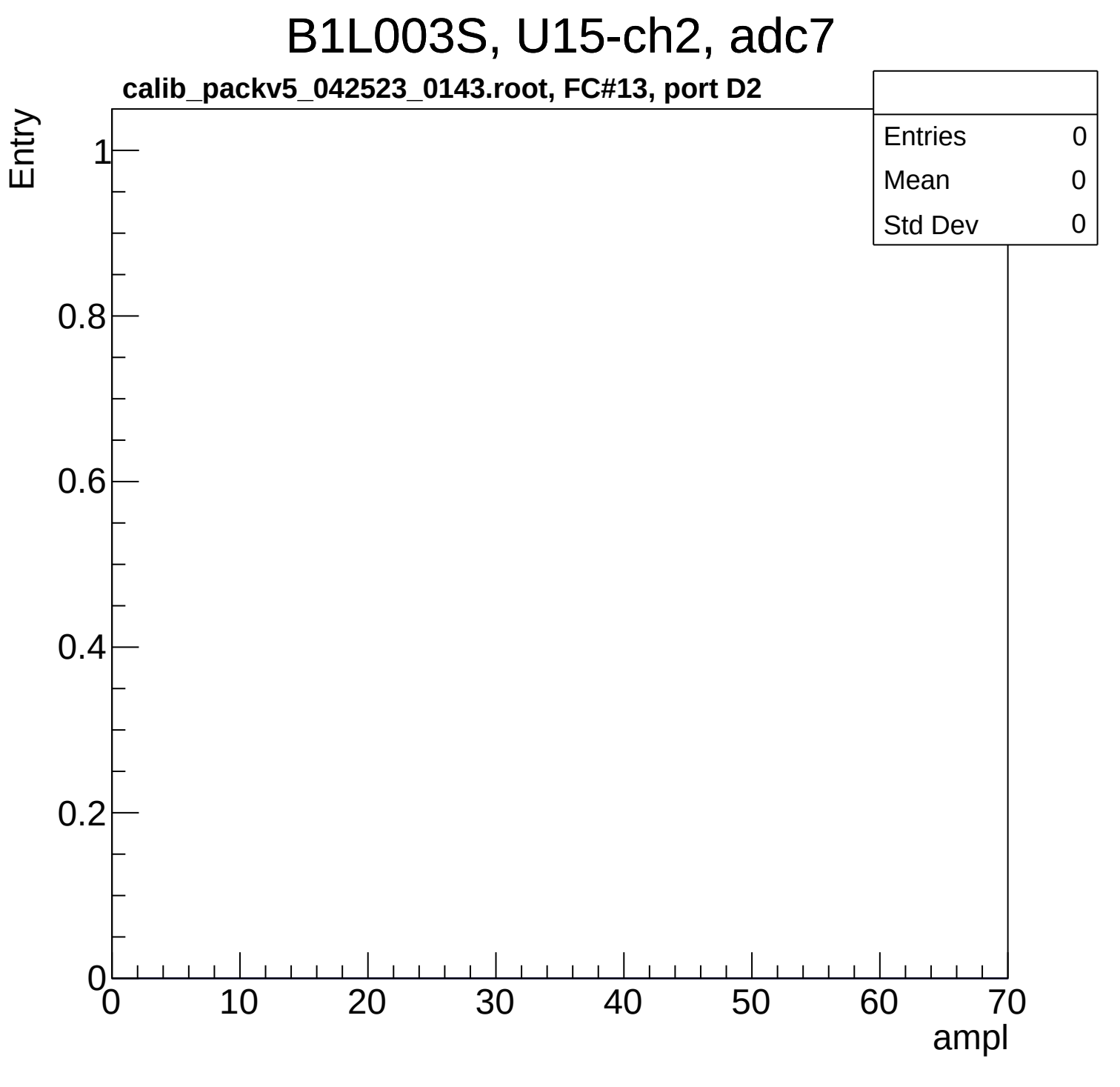
ampl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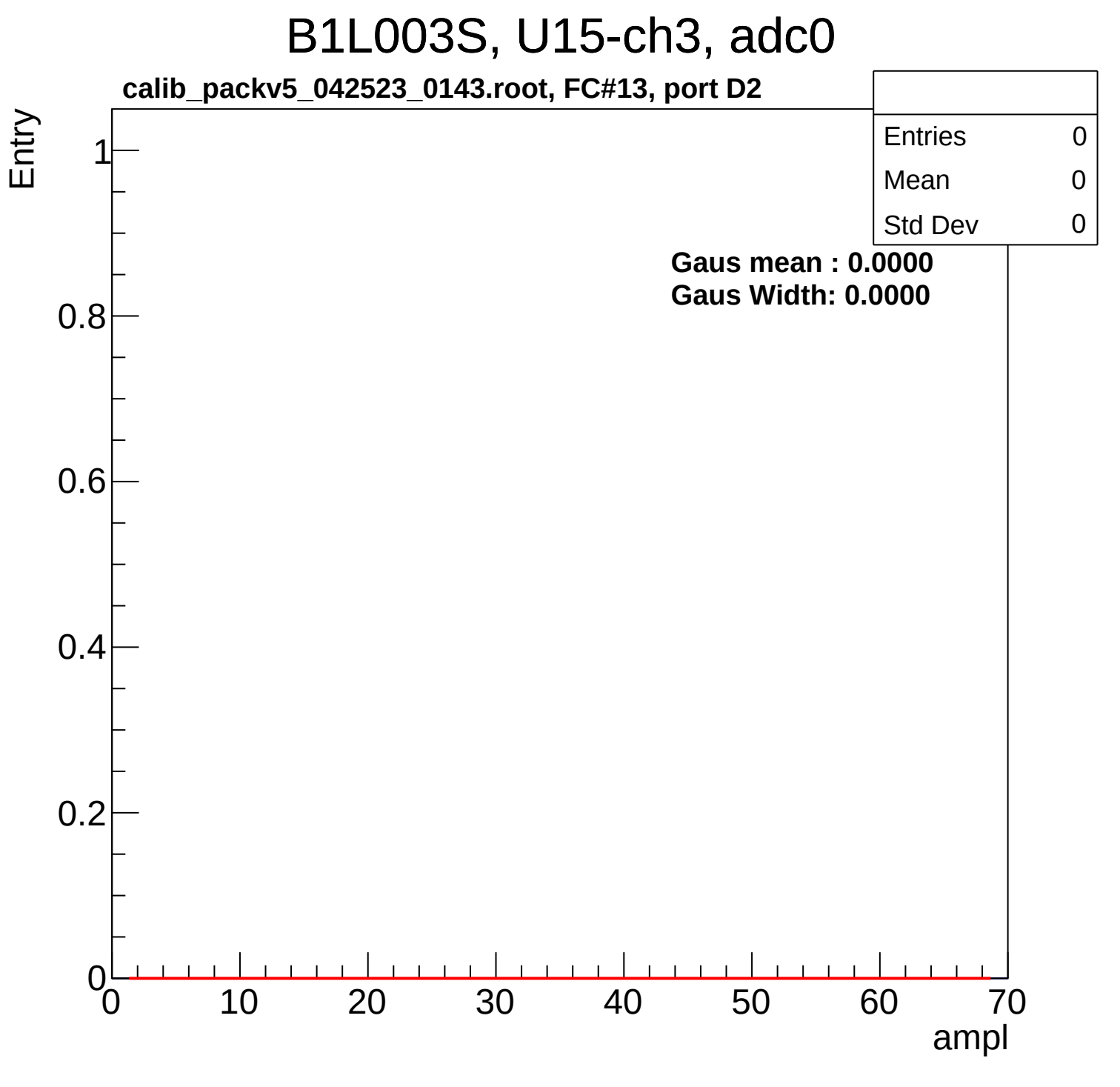
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Gaus Width: 0.0000
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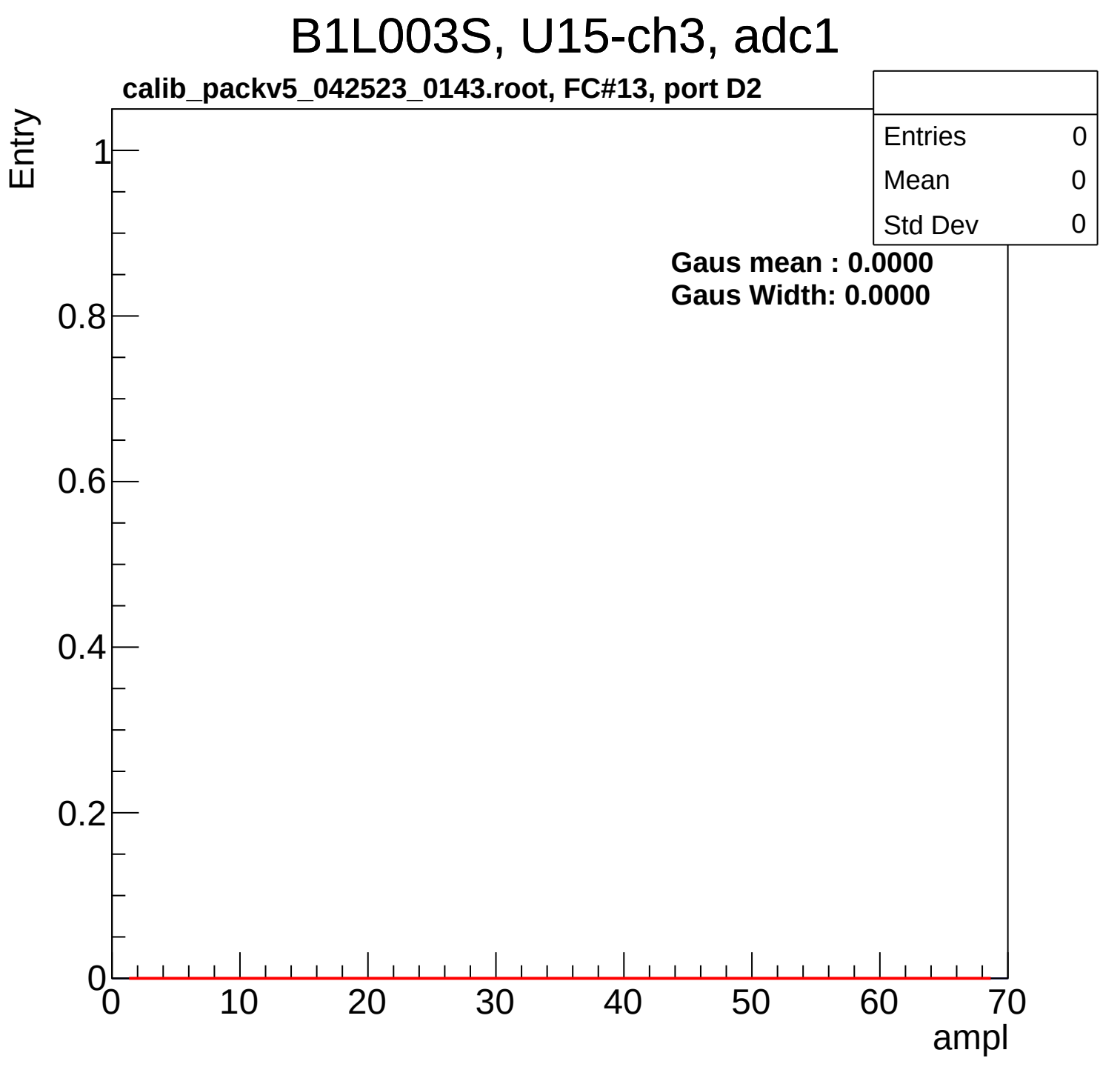
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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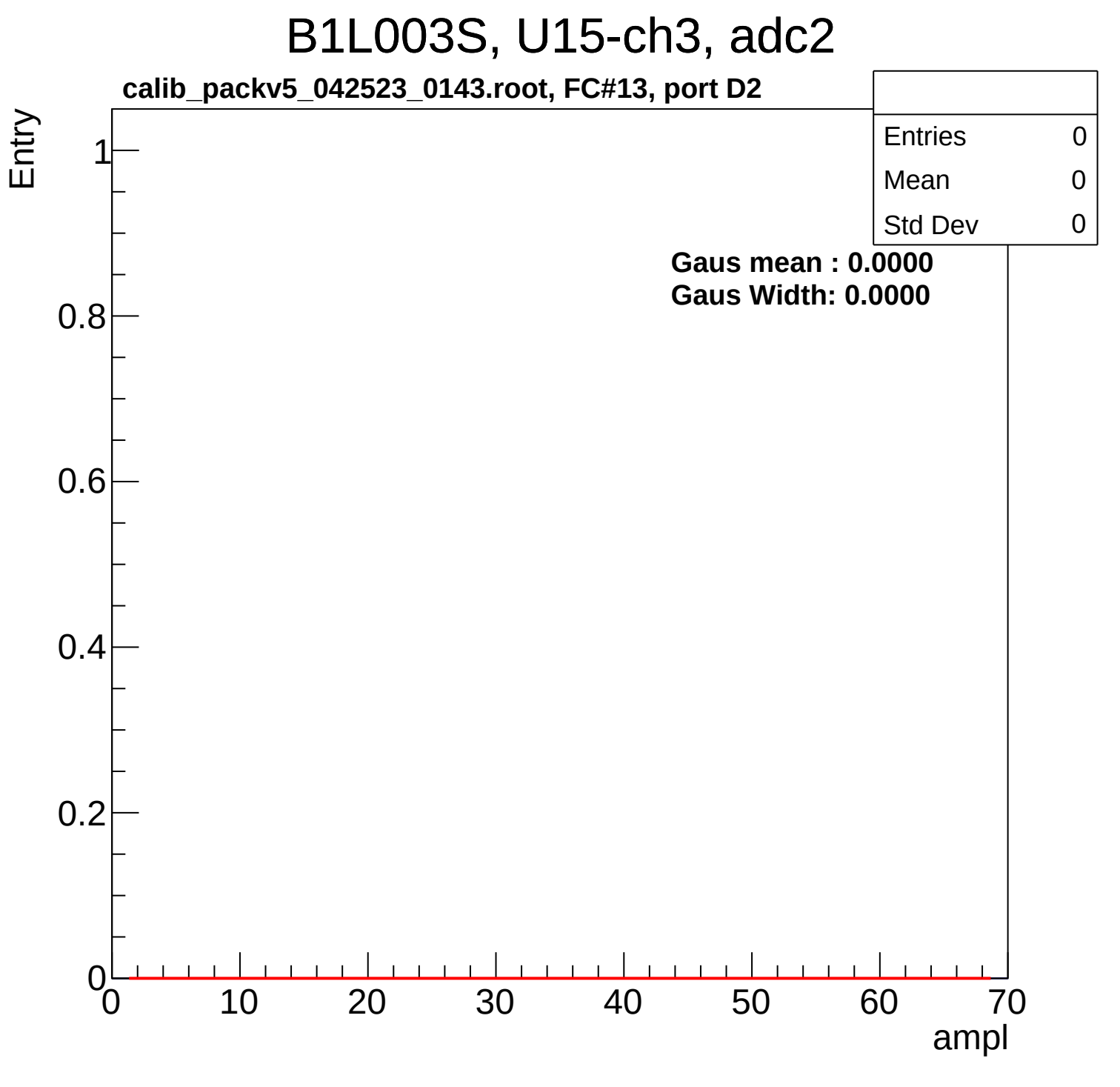
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Gaus mean : 0.0000
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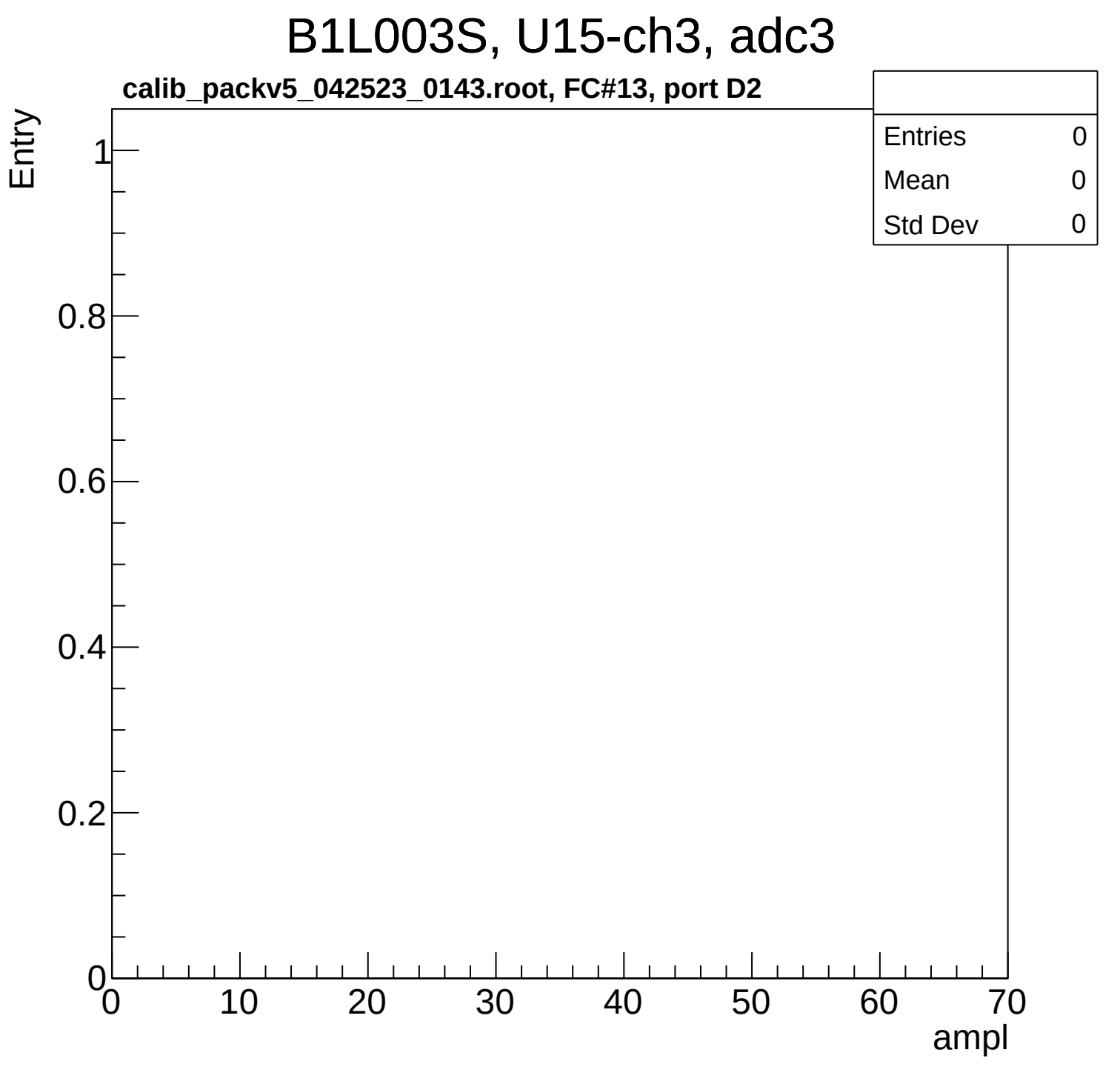
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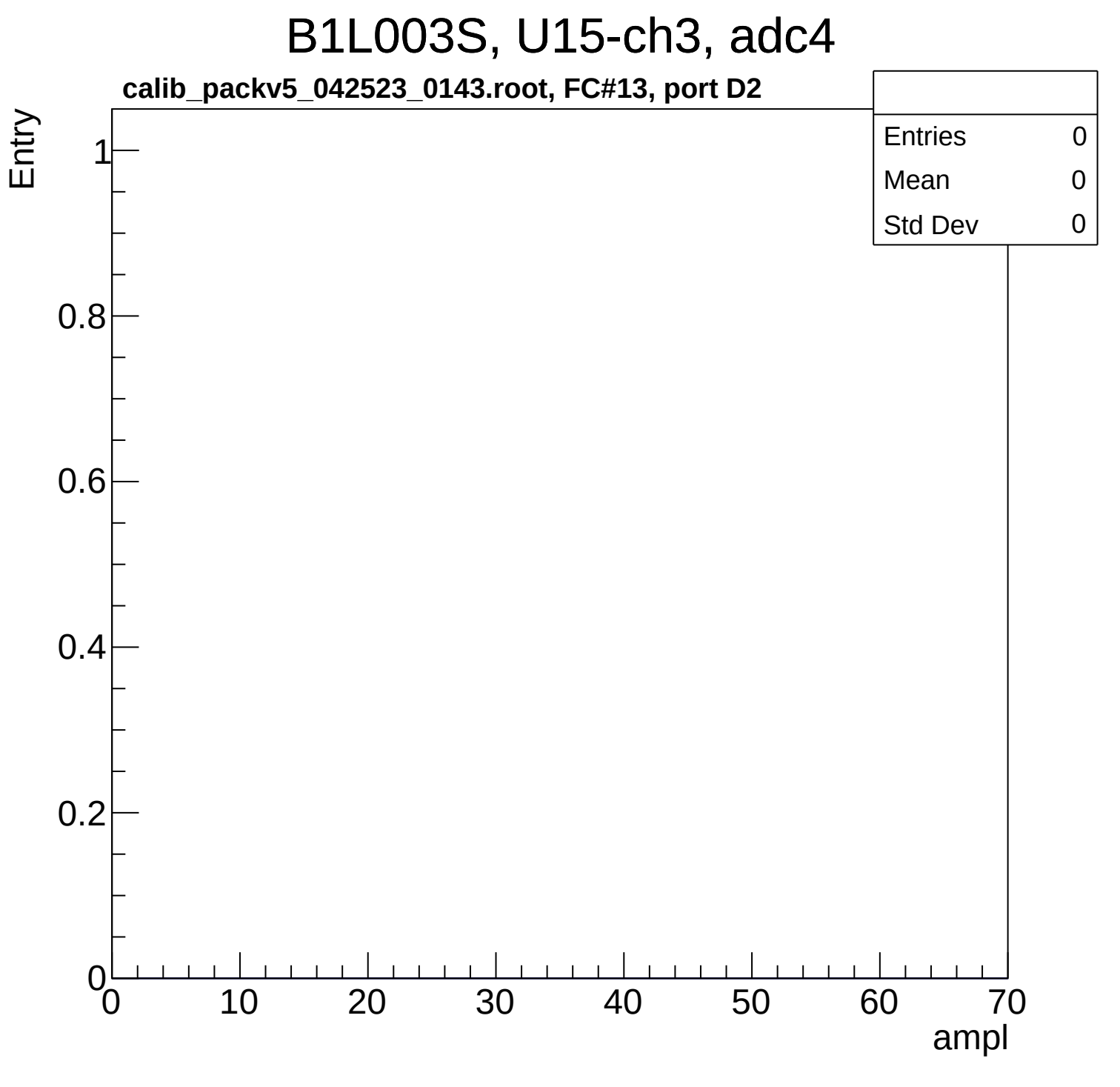
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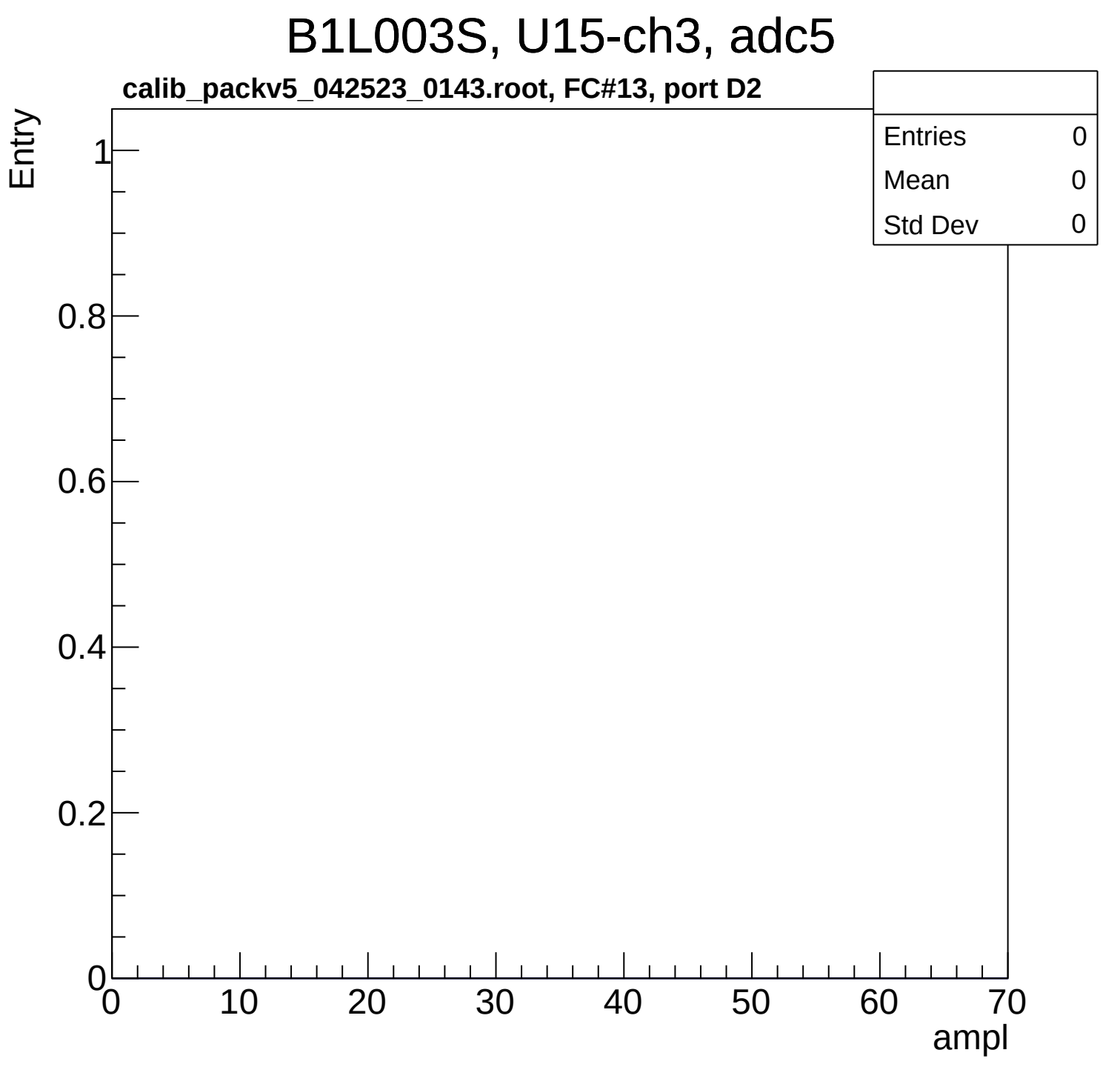
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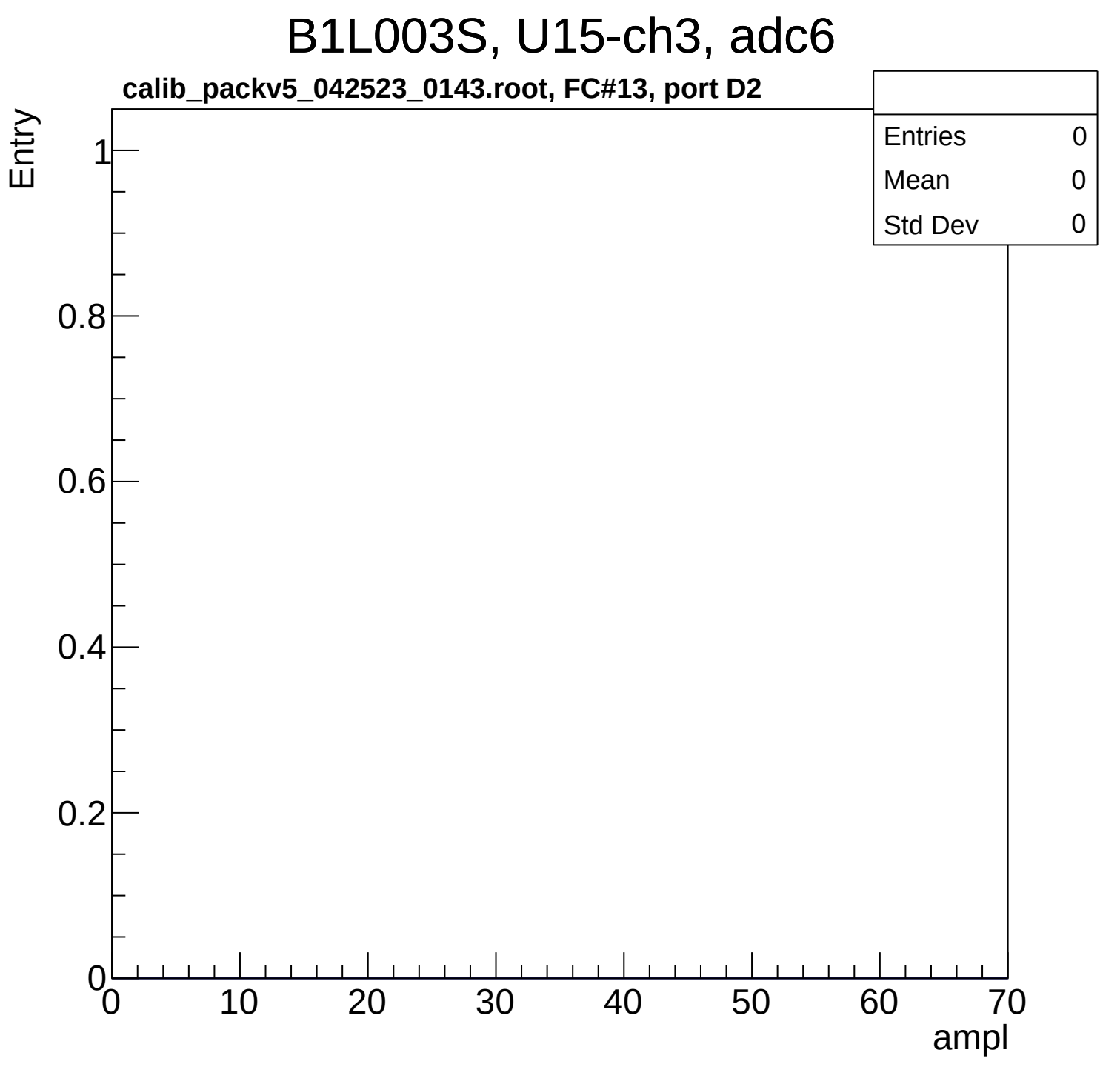
B1L003S, U15-ch3, adc6
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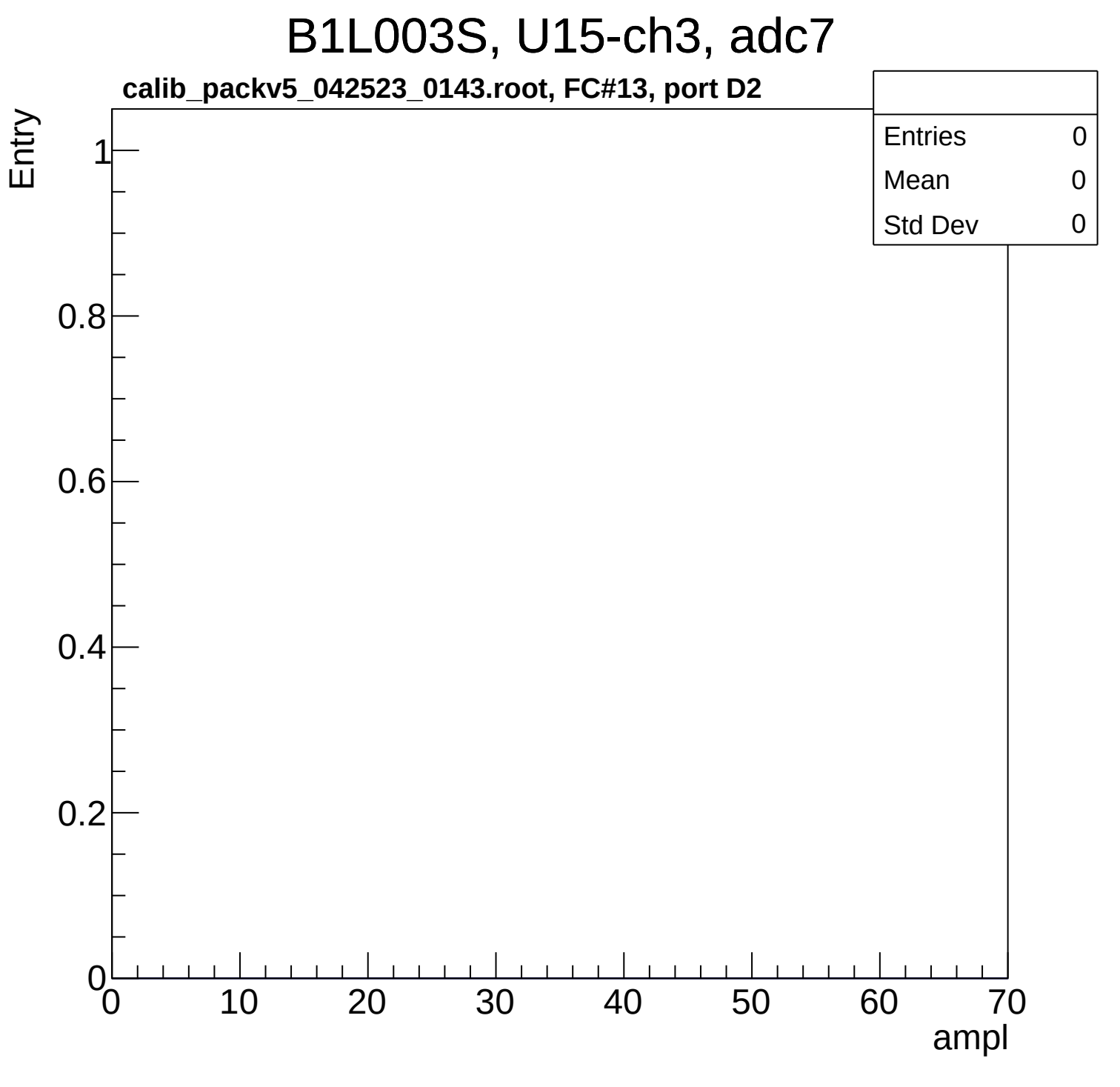
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Std Dev 0
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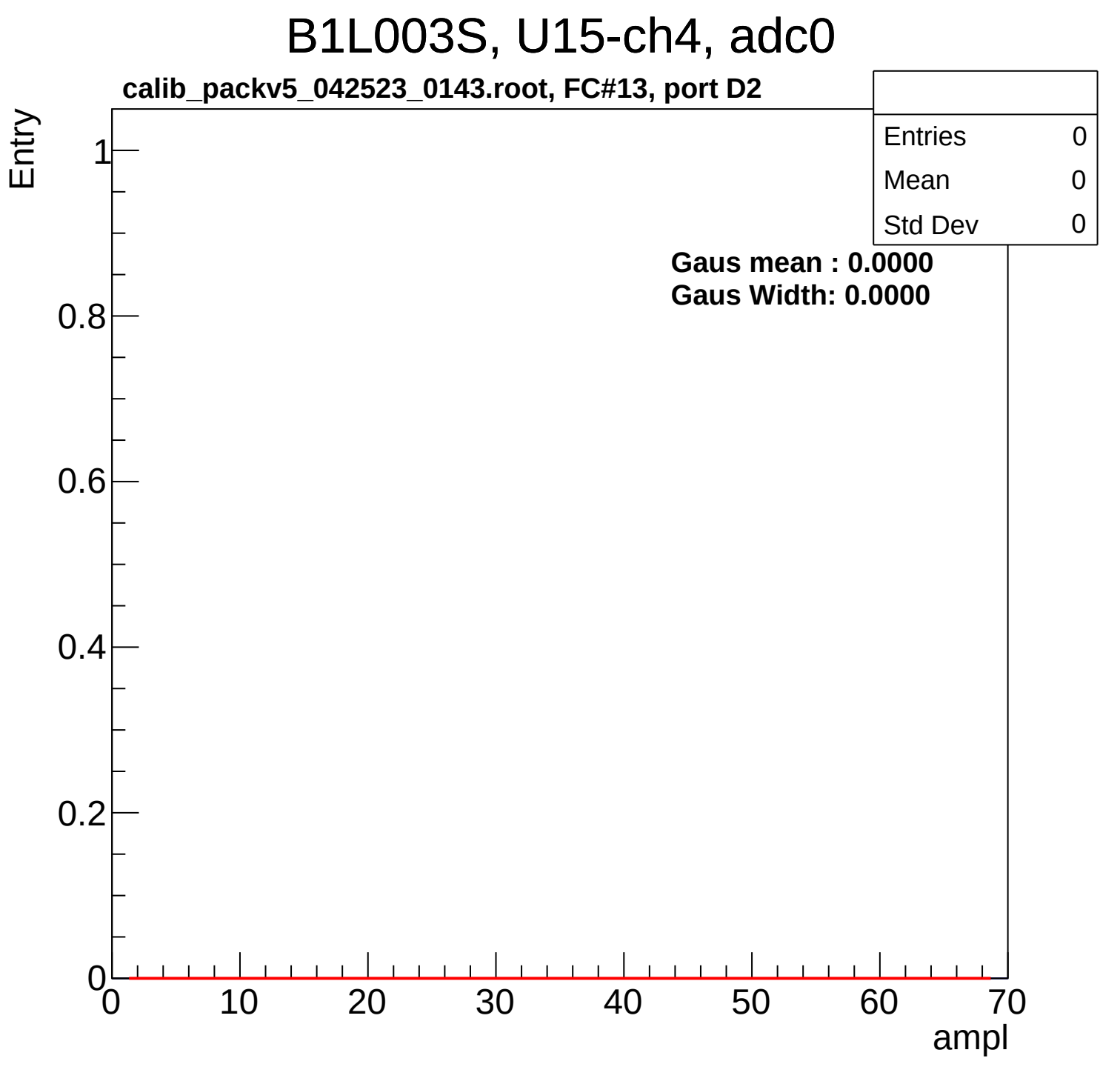
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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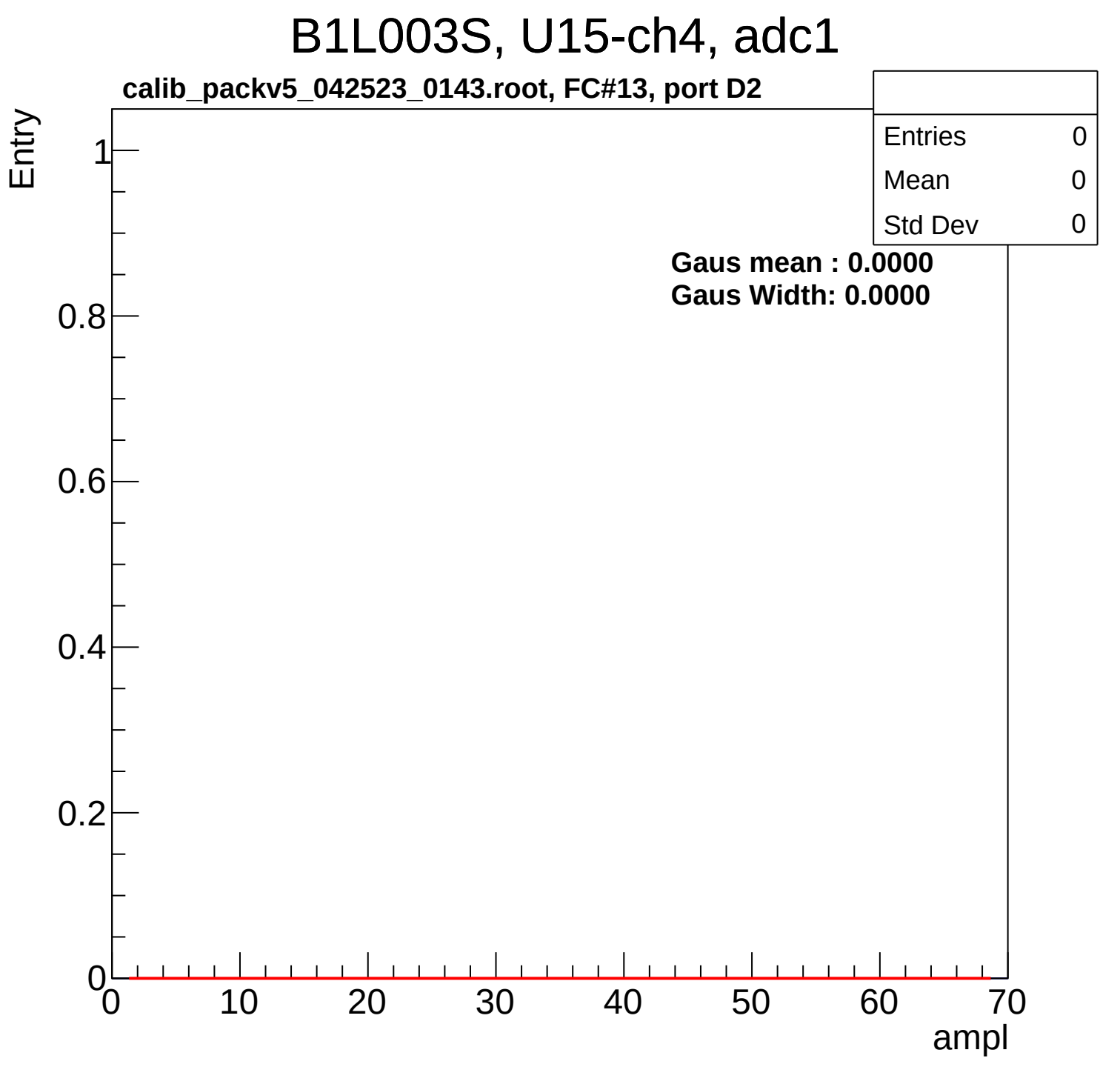
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Gaus mean : 0.0000
Gaus Width: 0.0000
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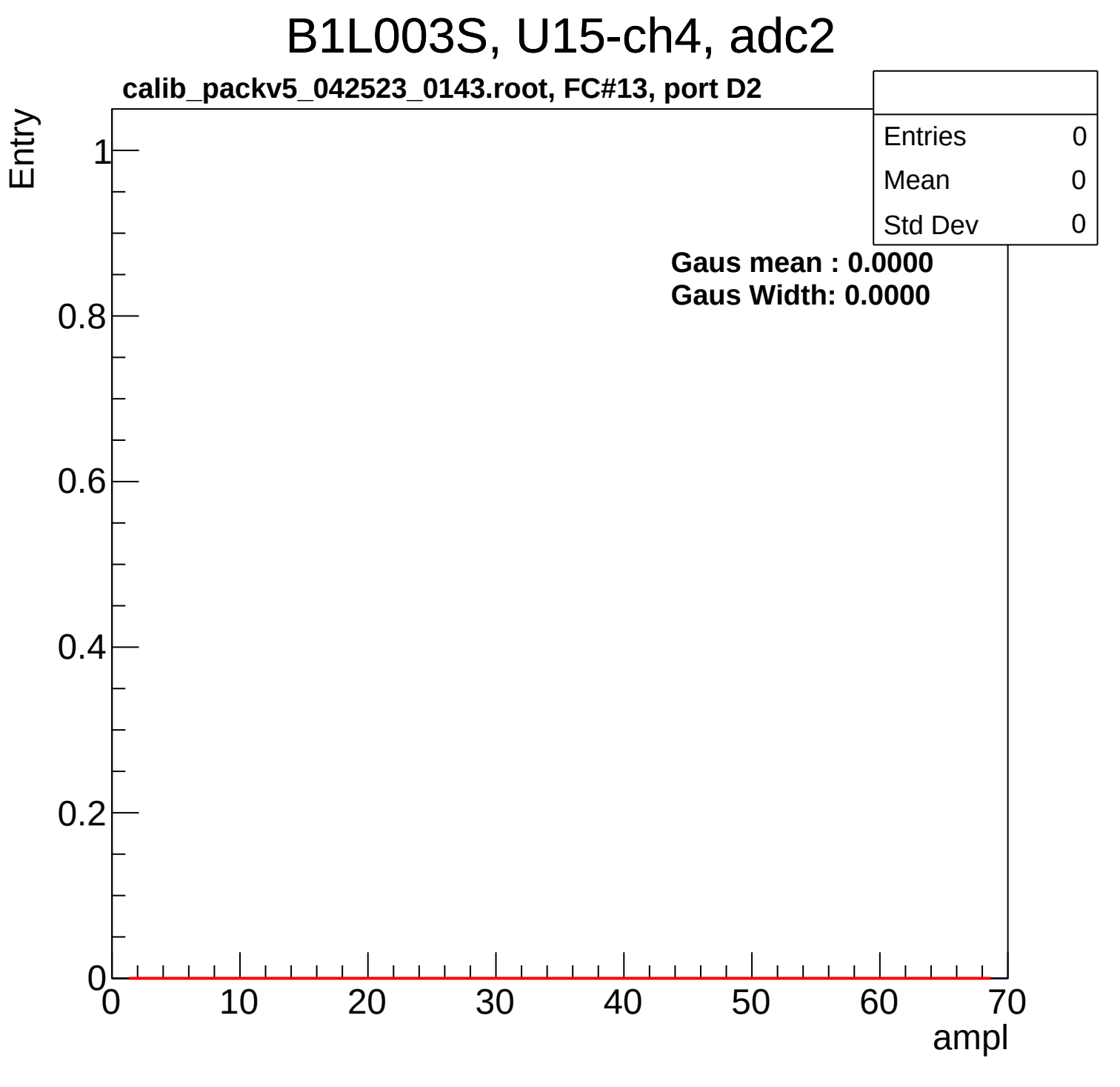
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Gaus Width: 0.0000
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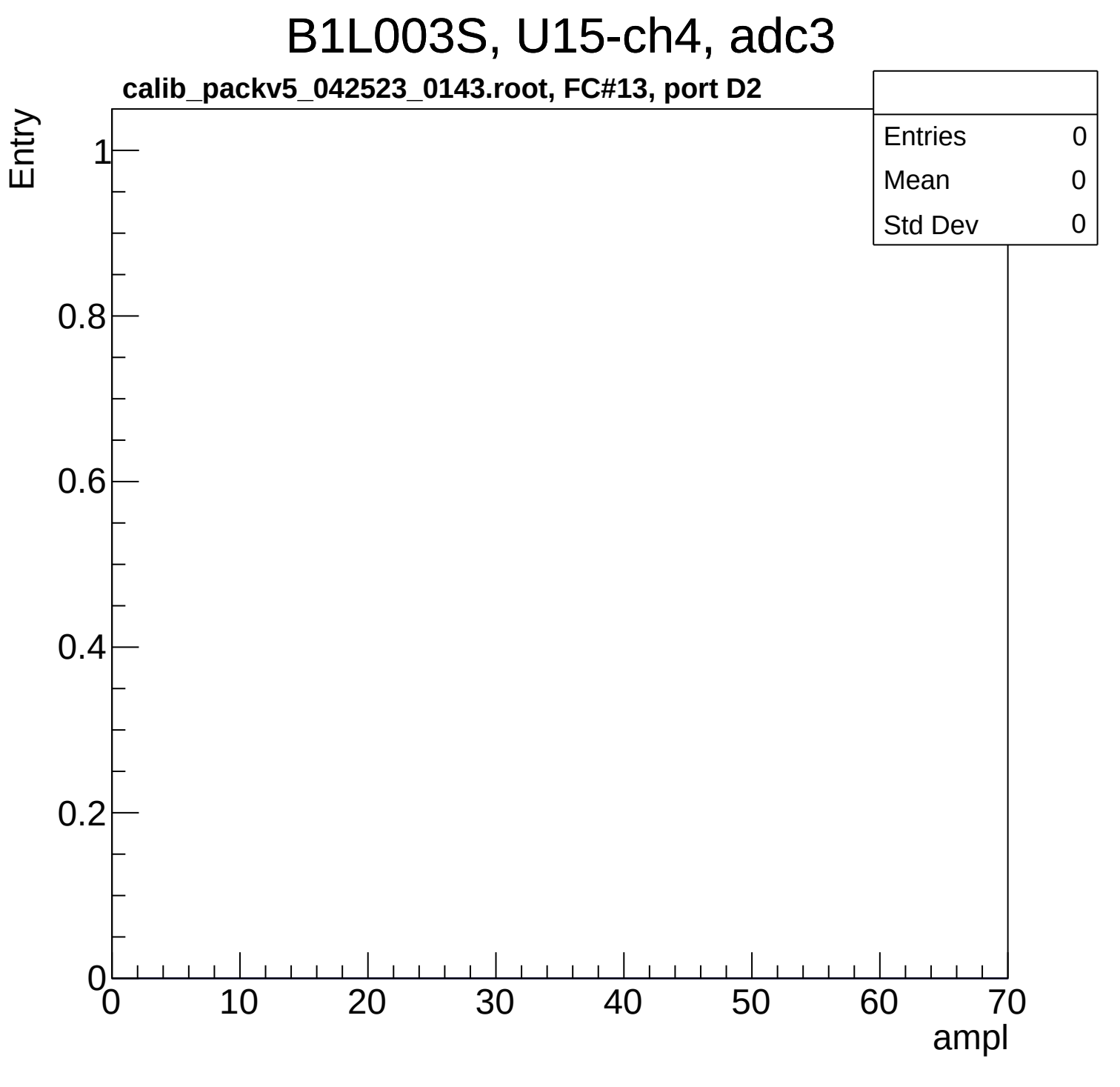
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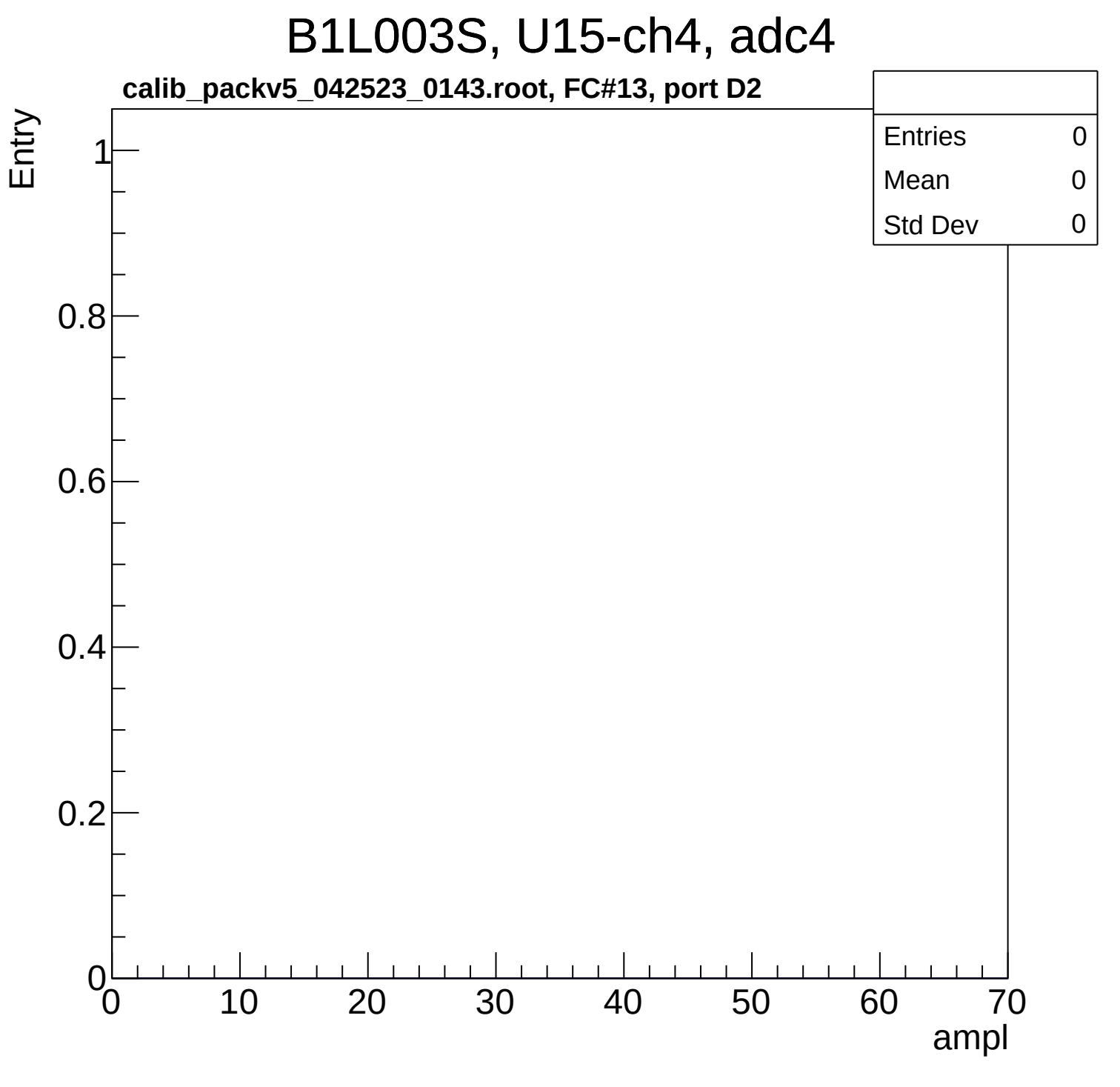
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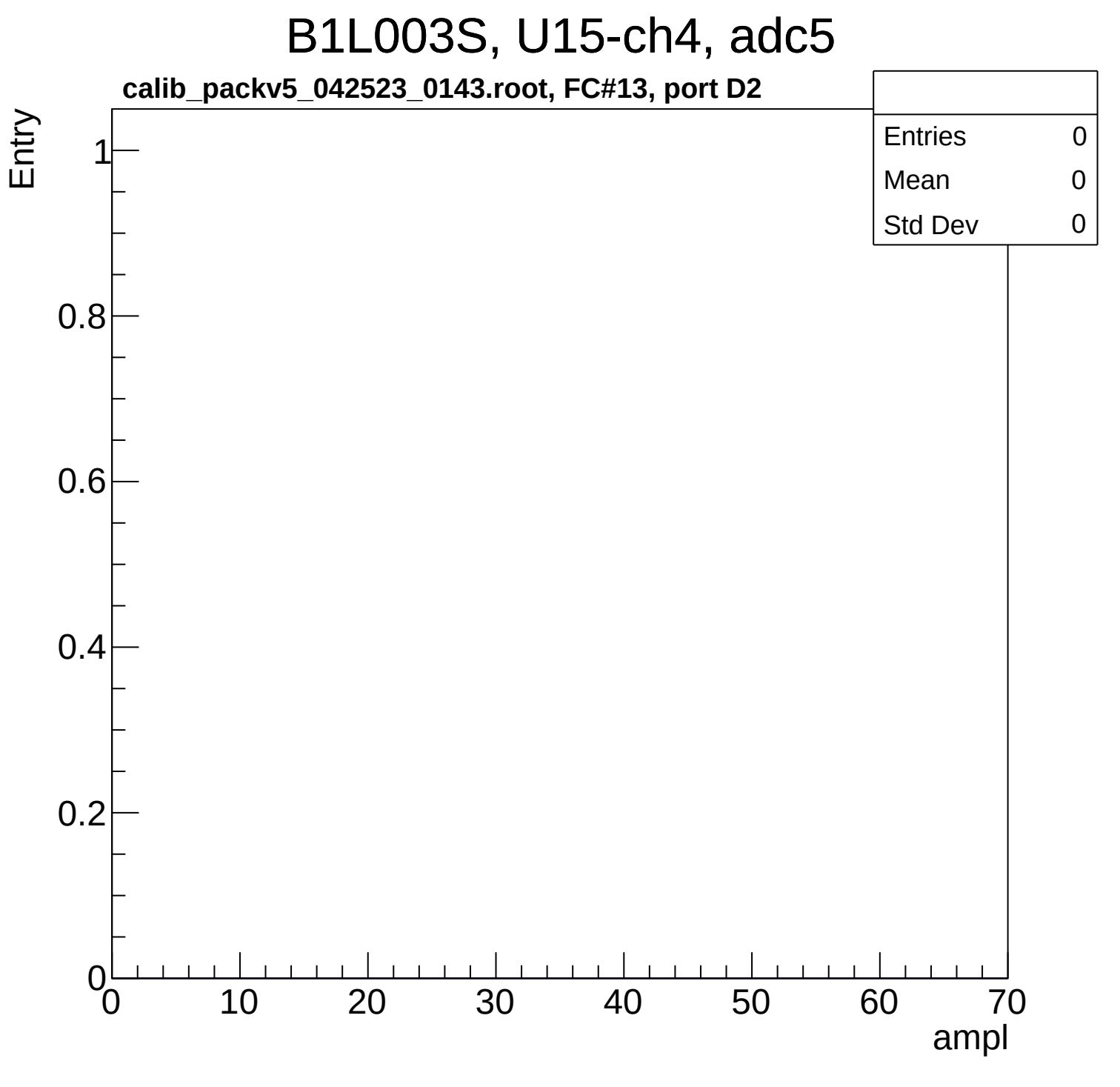
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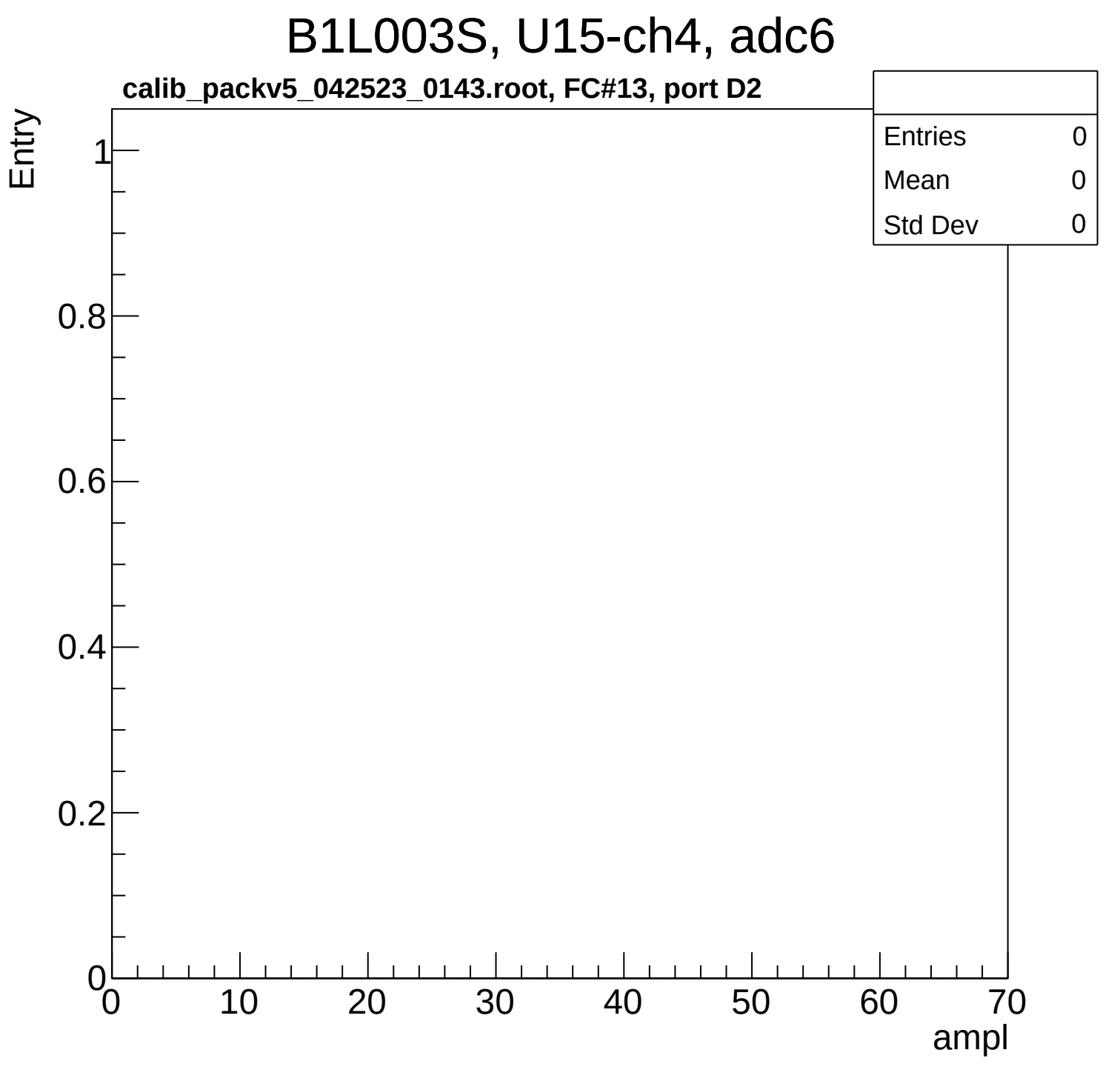
ampl


B1L003S, U15-ch4, adc6
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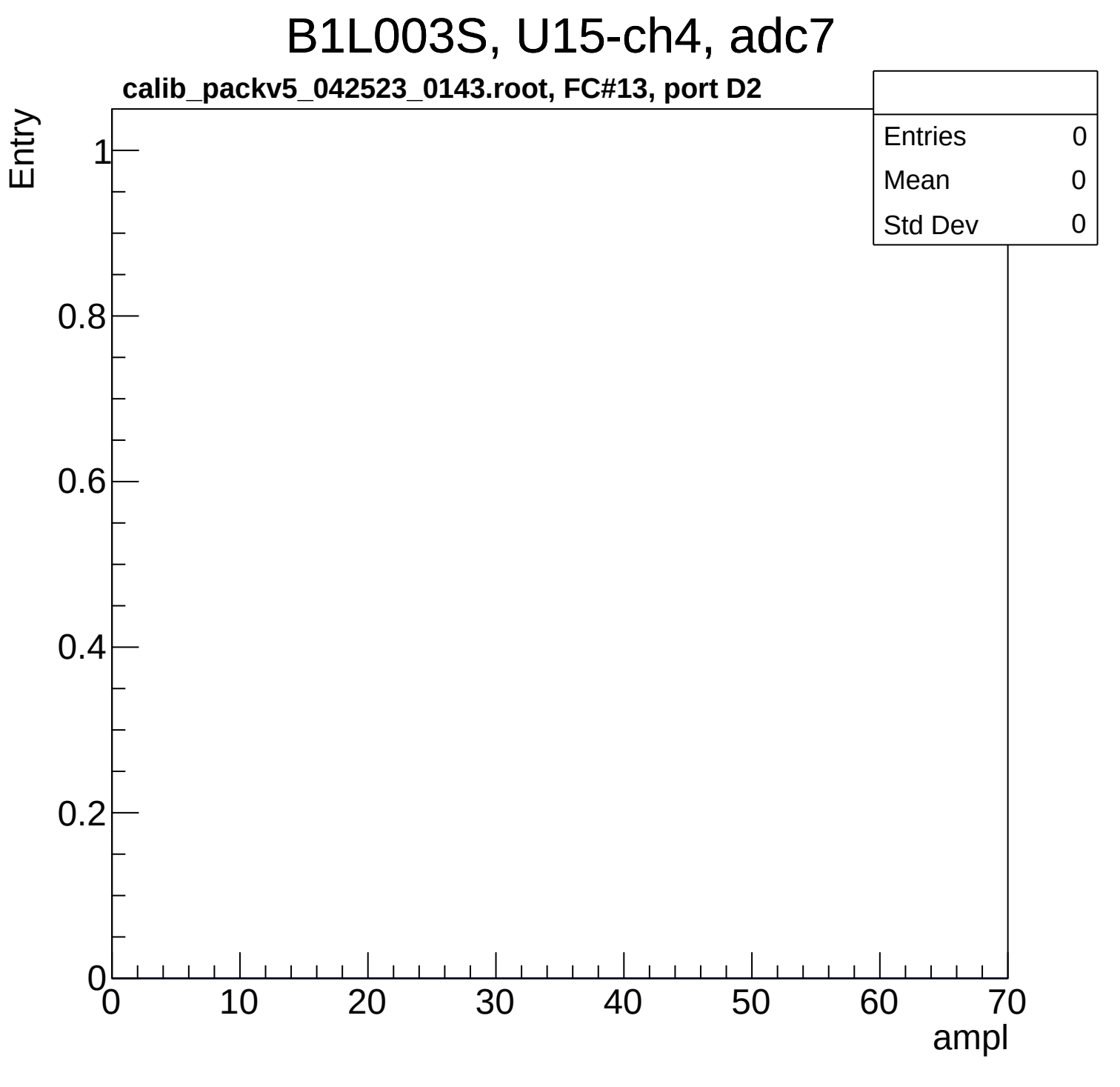
ampl


B1L003S, U15-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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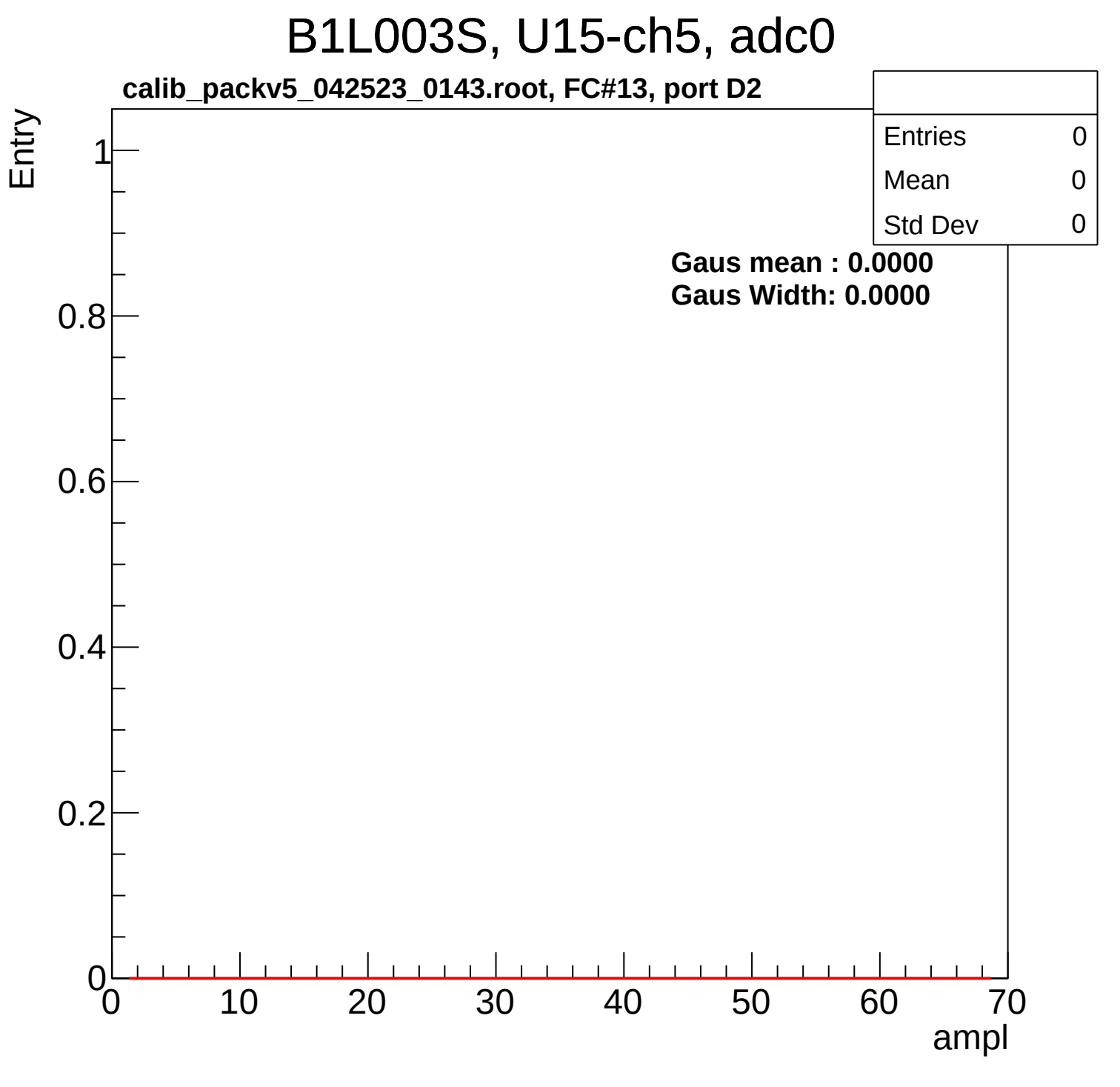
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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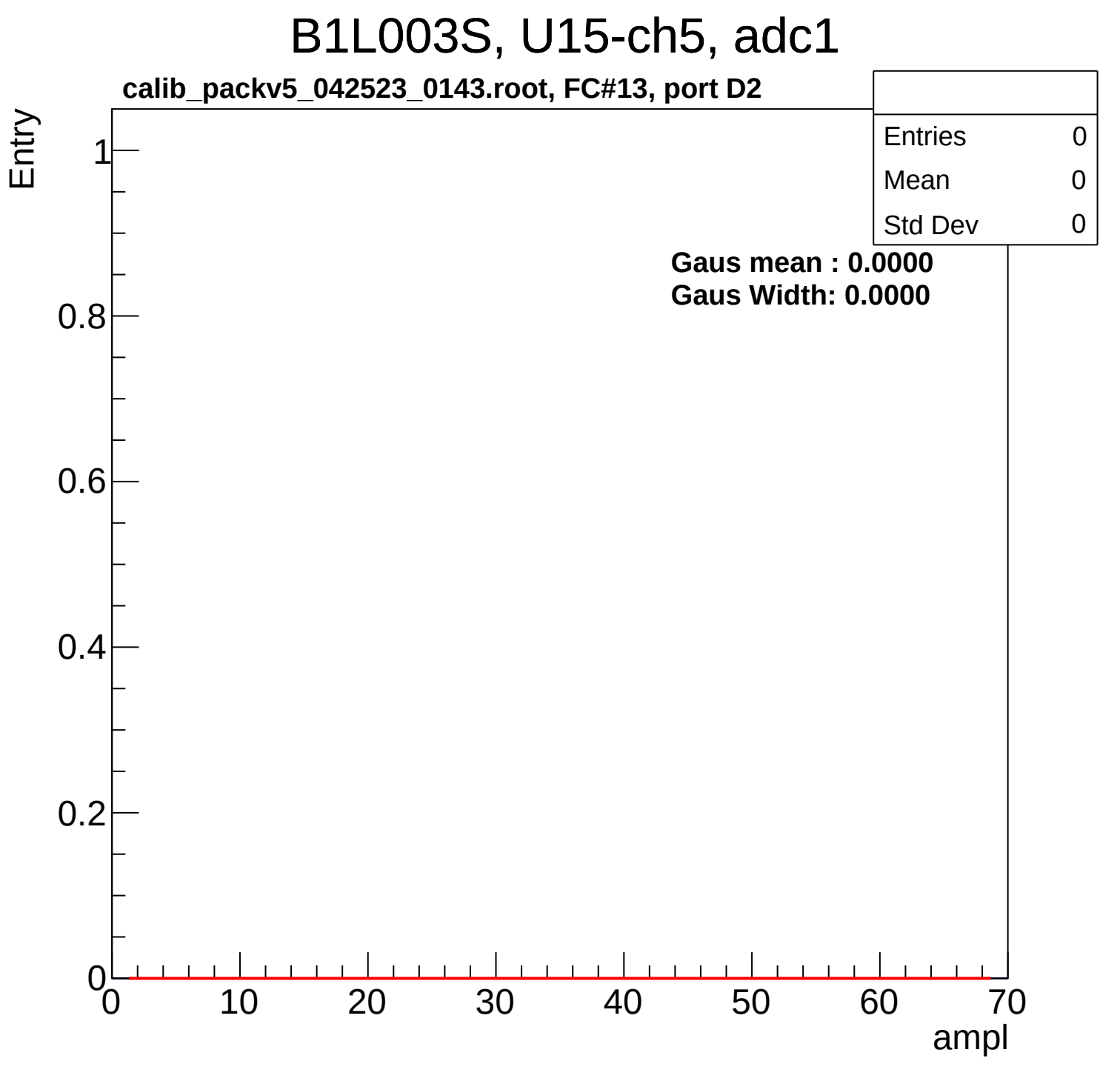
ampl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calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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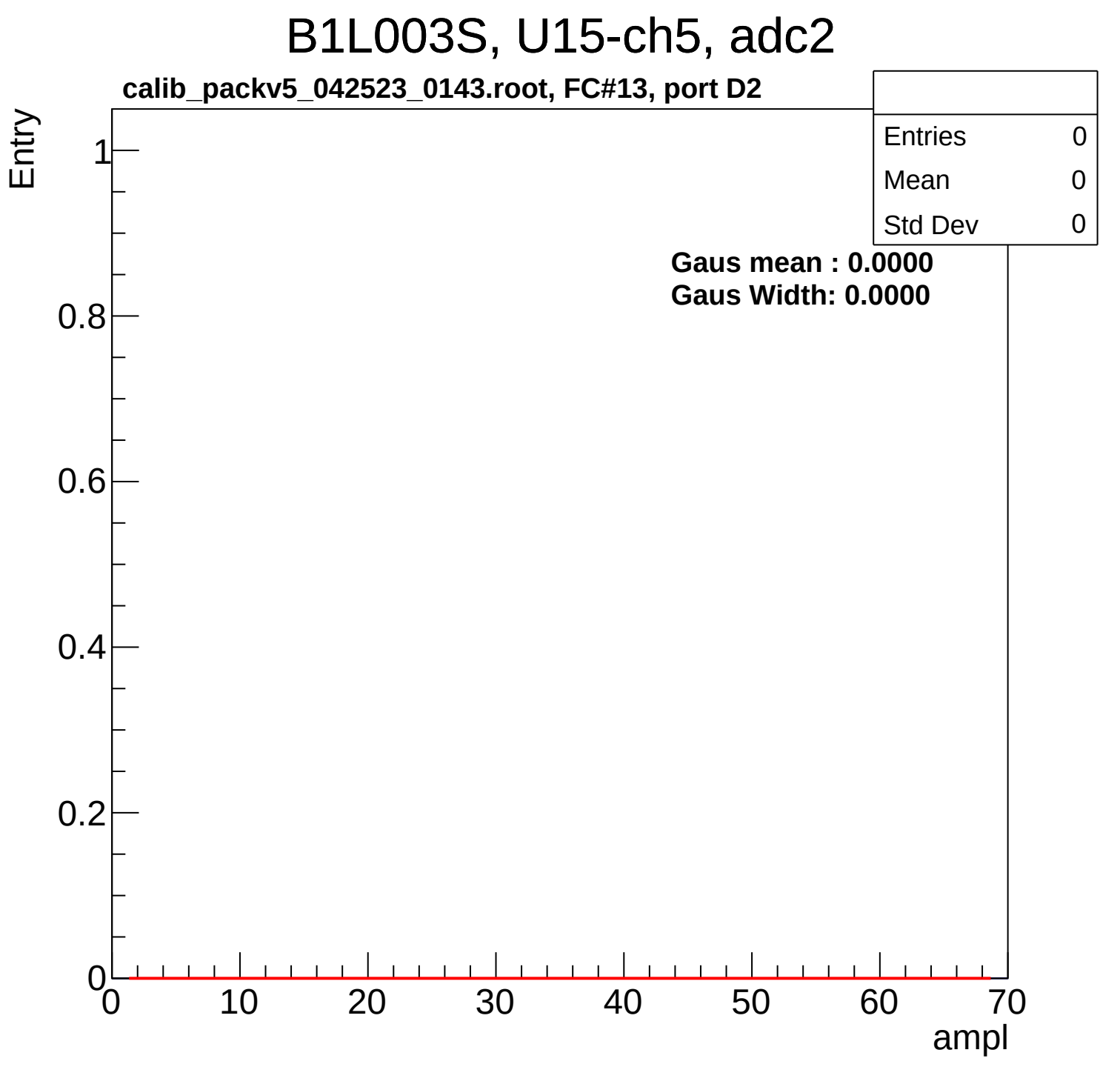
ampl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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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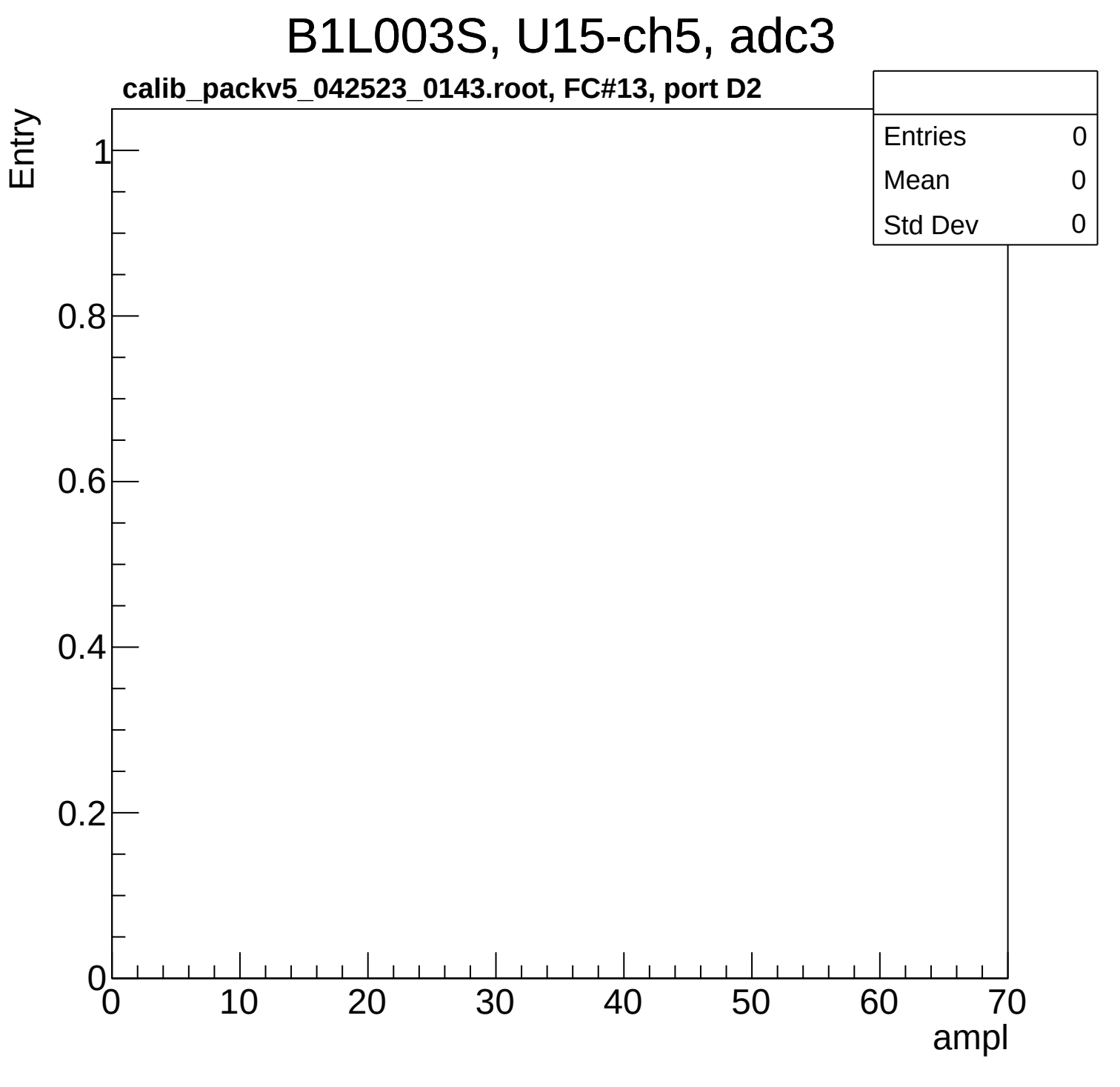
ampl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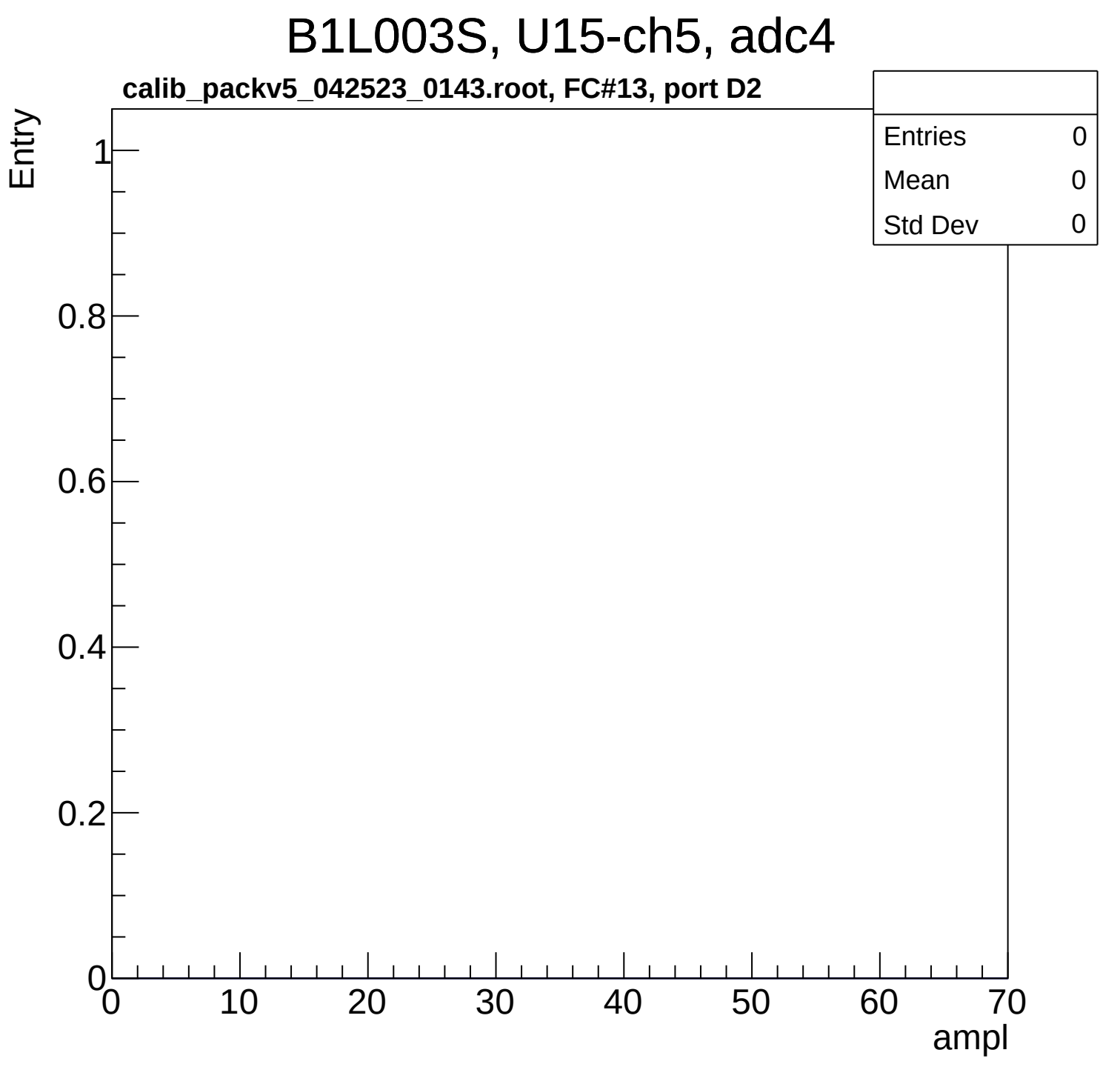
ampl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Mean 0
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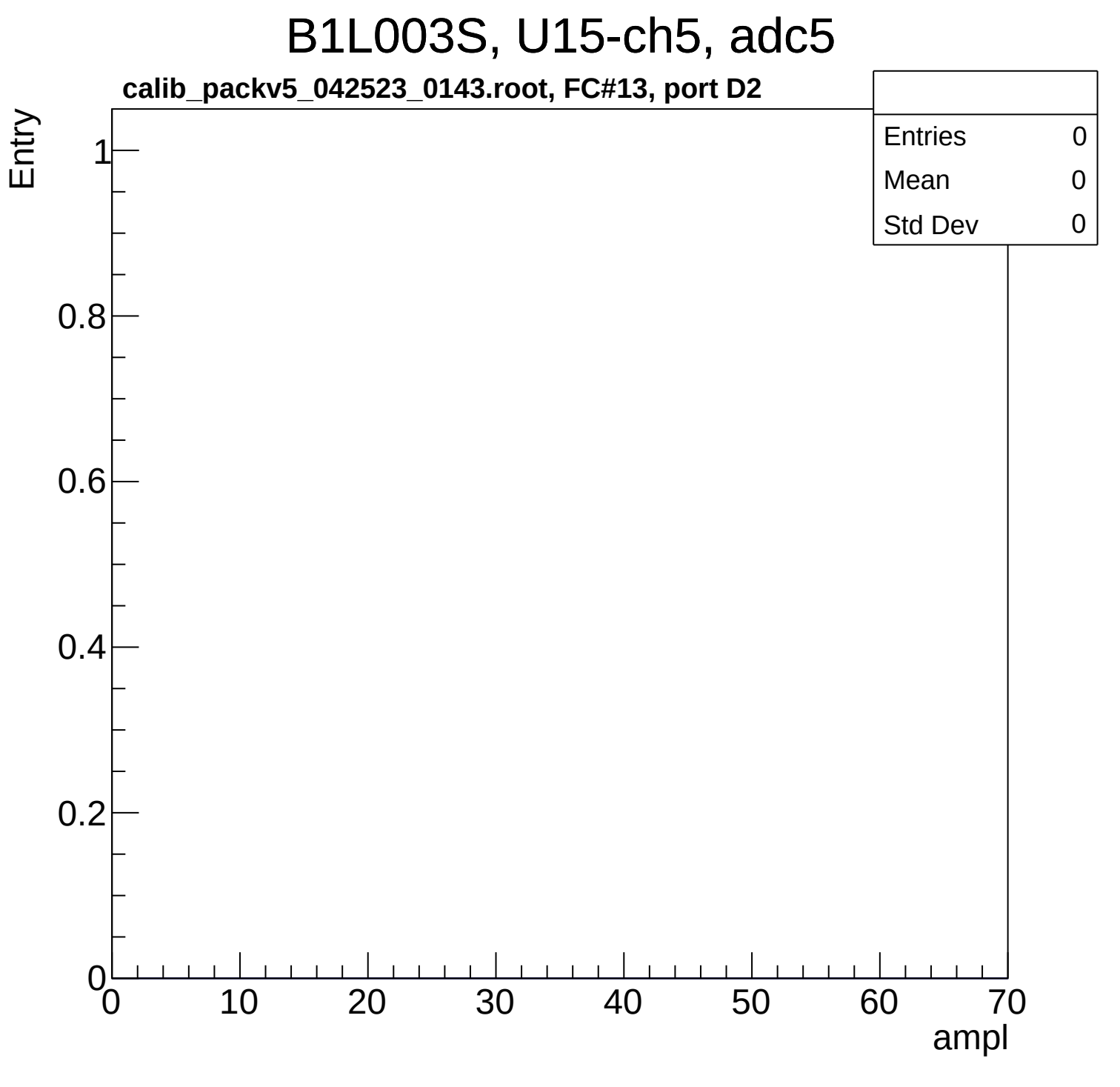
ampl


B1L003S, U15-ch5, adc5
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Entries 0
Mean 0
Std Dev 0
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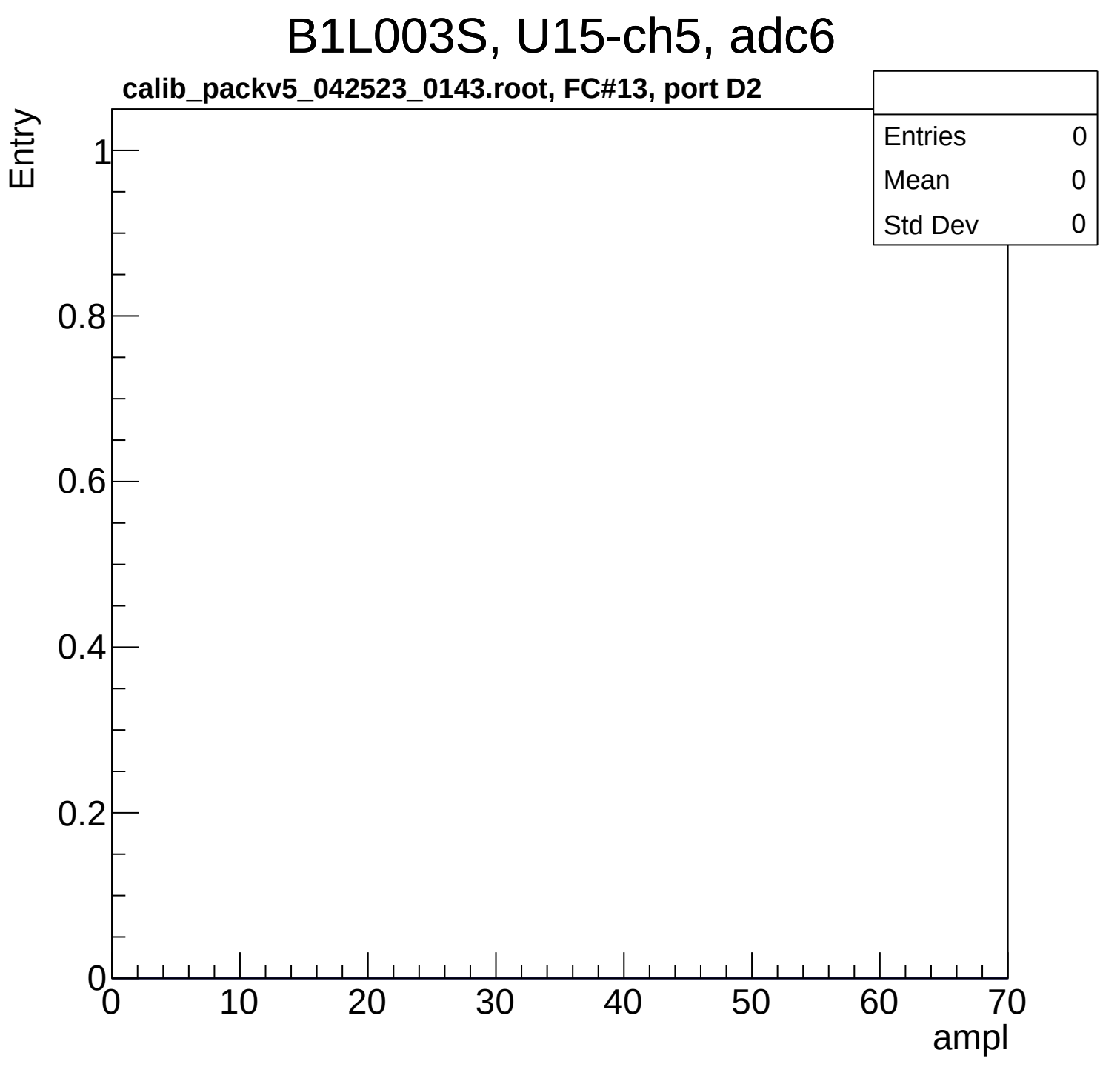
B1L003S, U15-ch5, adc6
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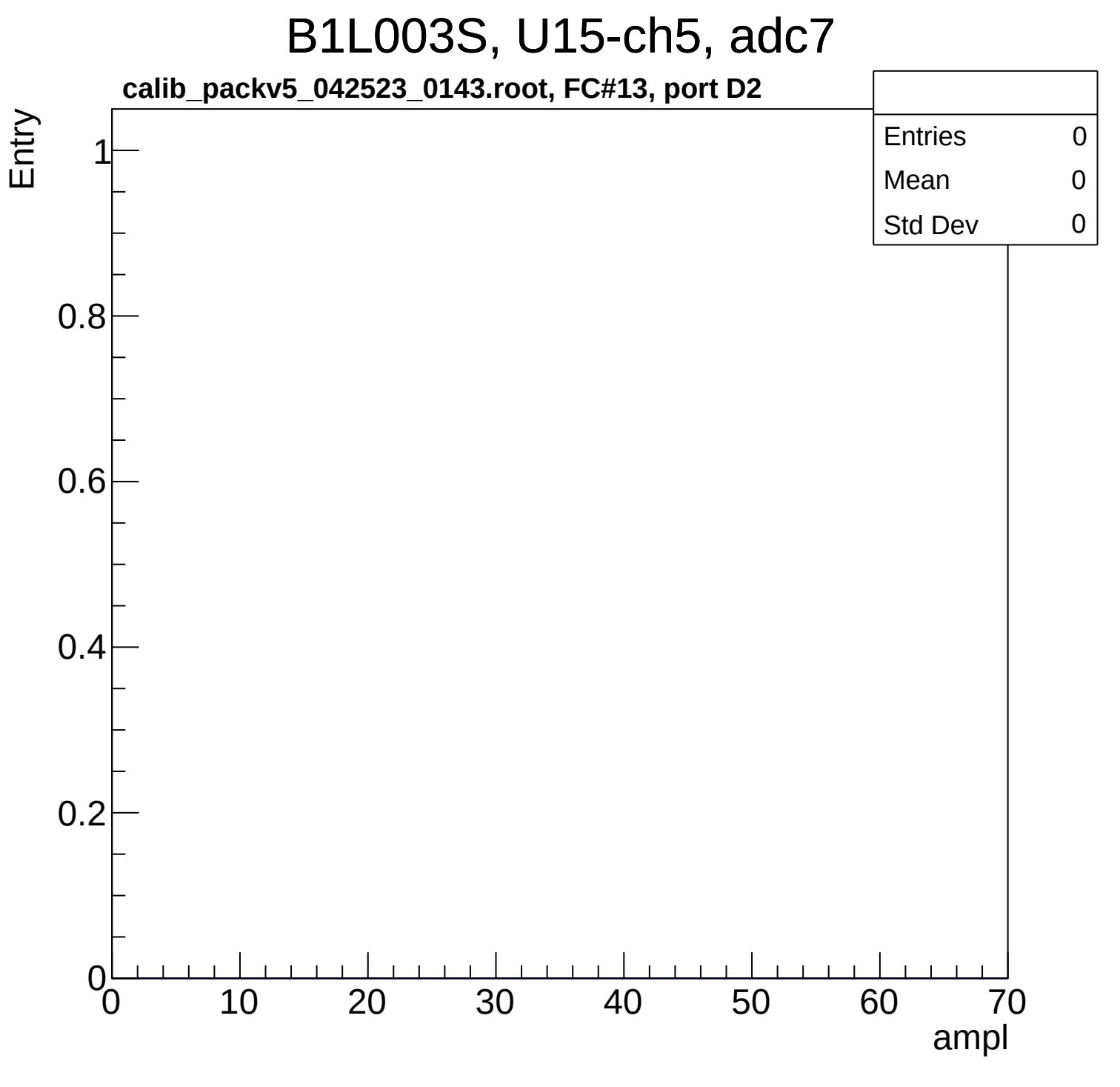
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calib_packv5_042523_0143.root, FC#13, port D2
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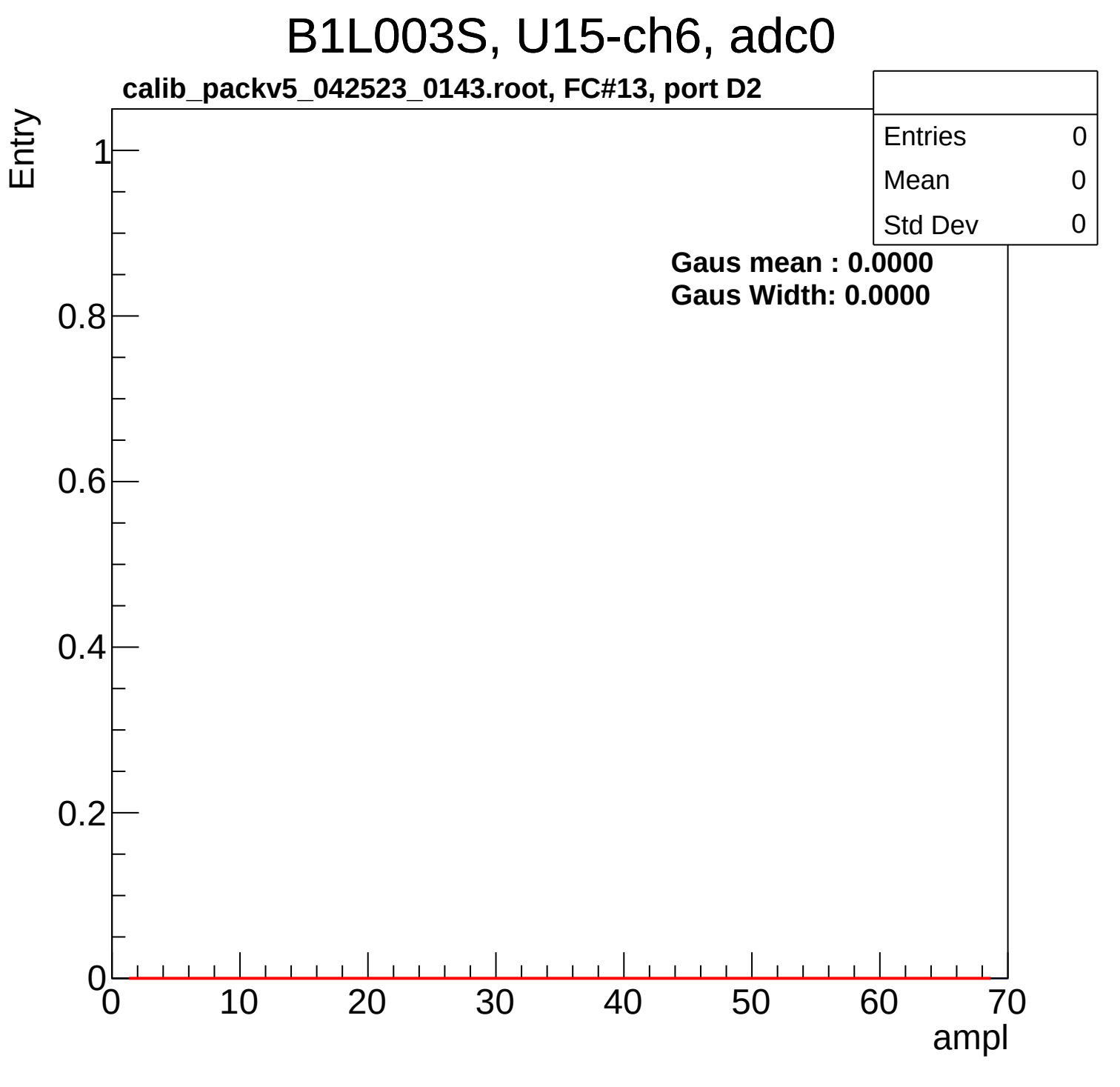
ampl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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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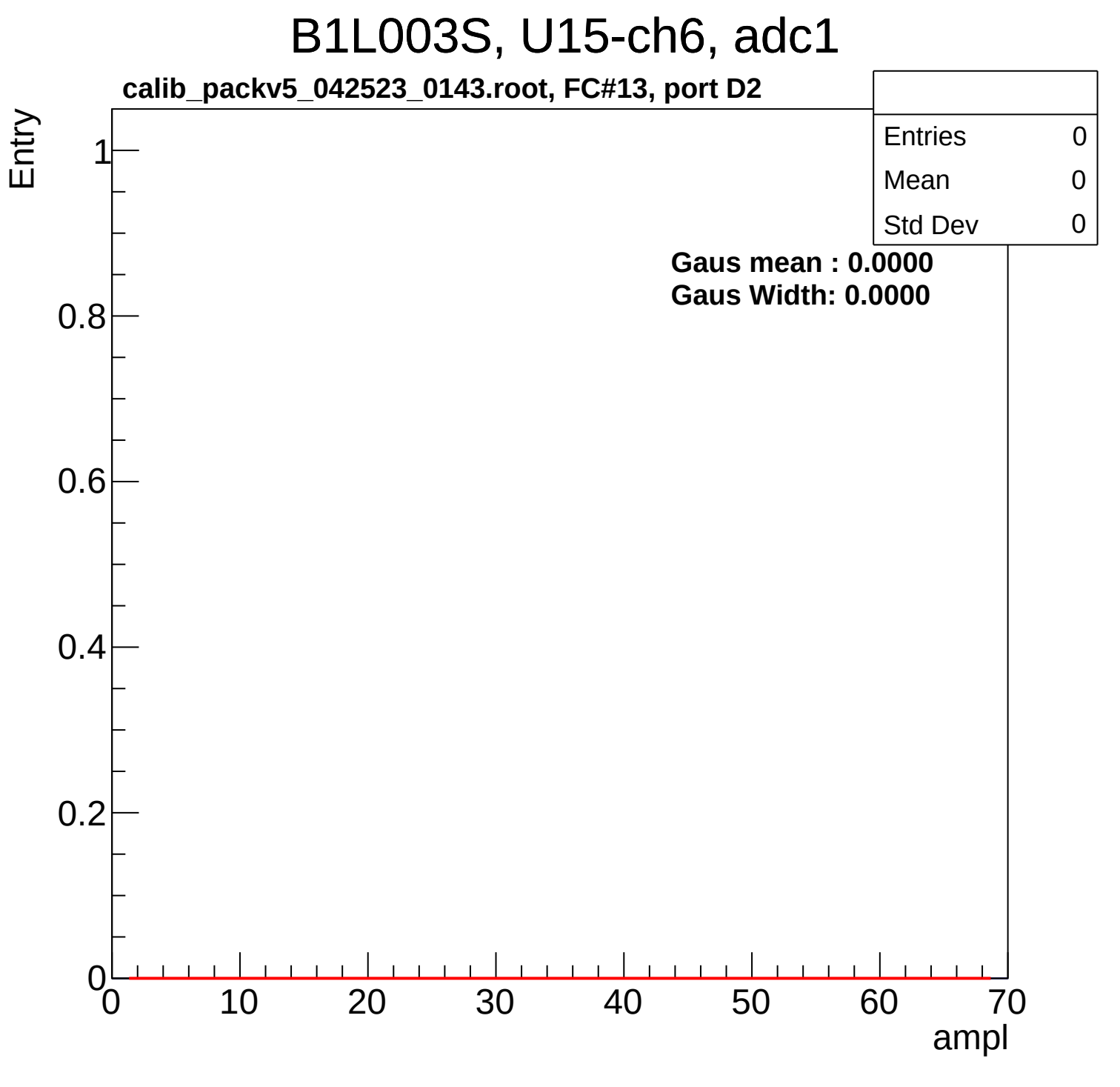
B1L003S, U15-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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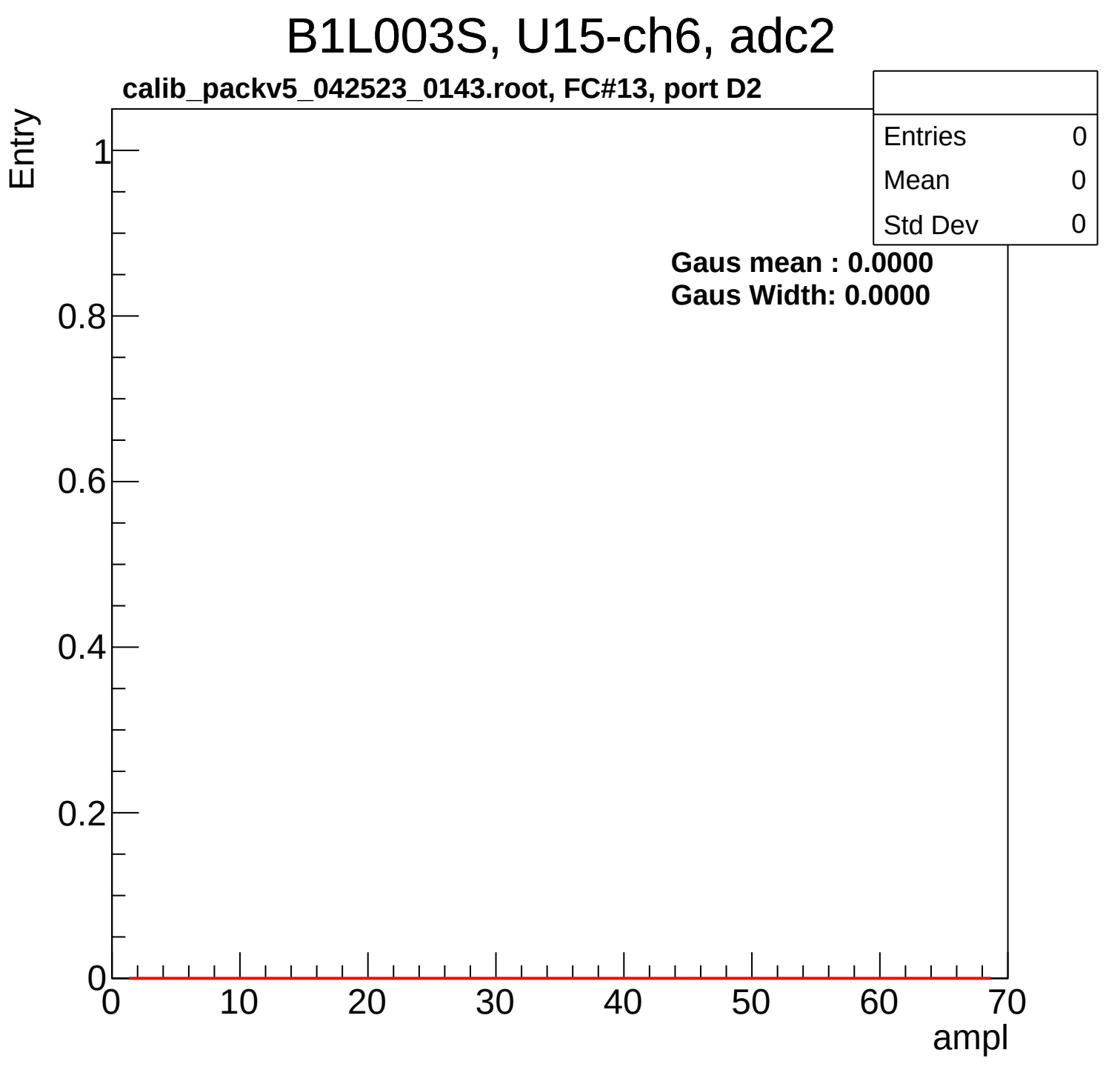
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Std Dev 0
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Gaus Width: 0.0000
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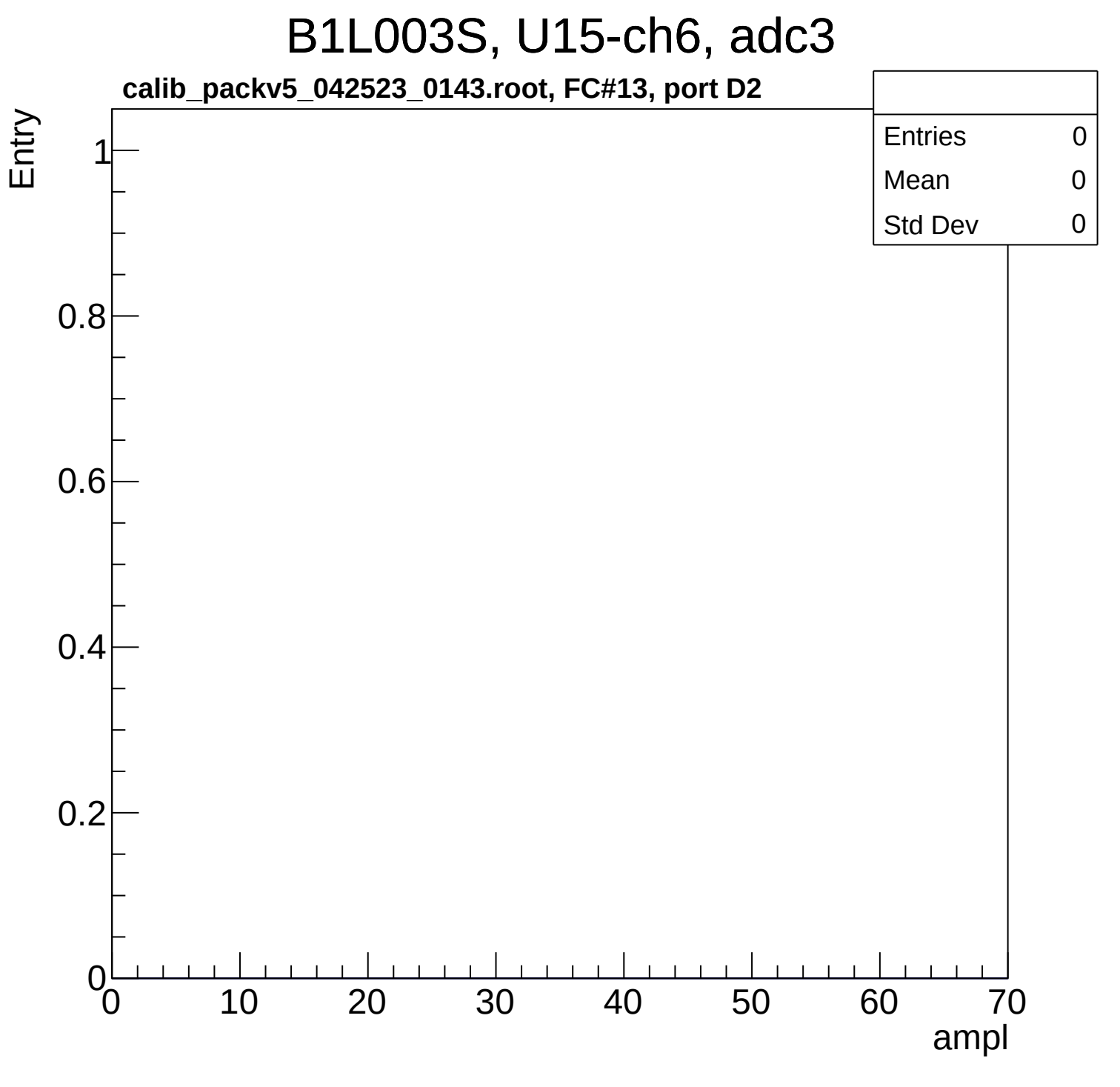
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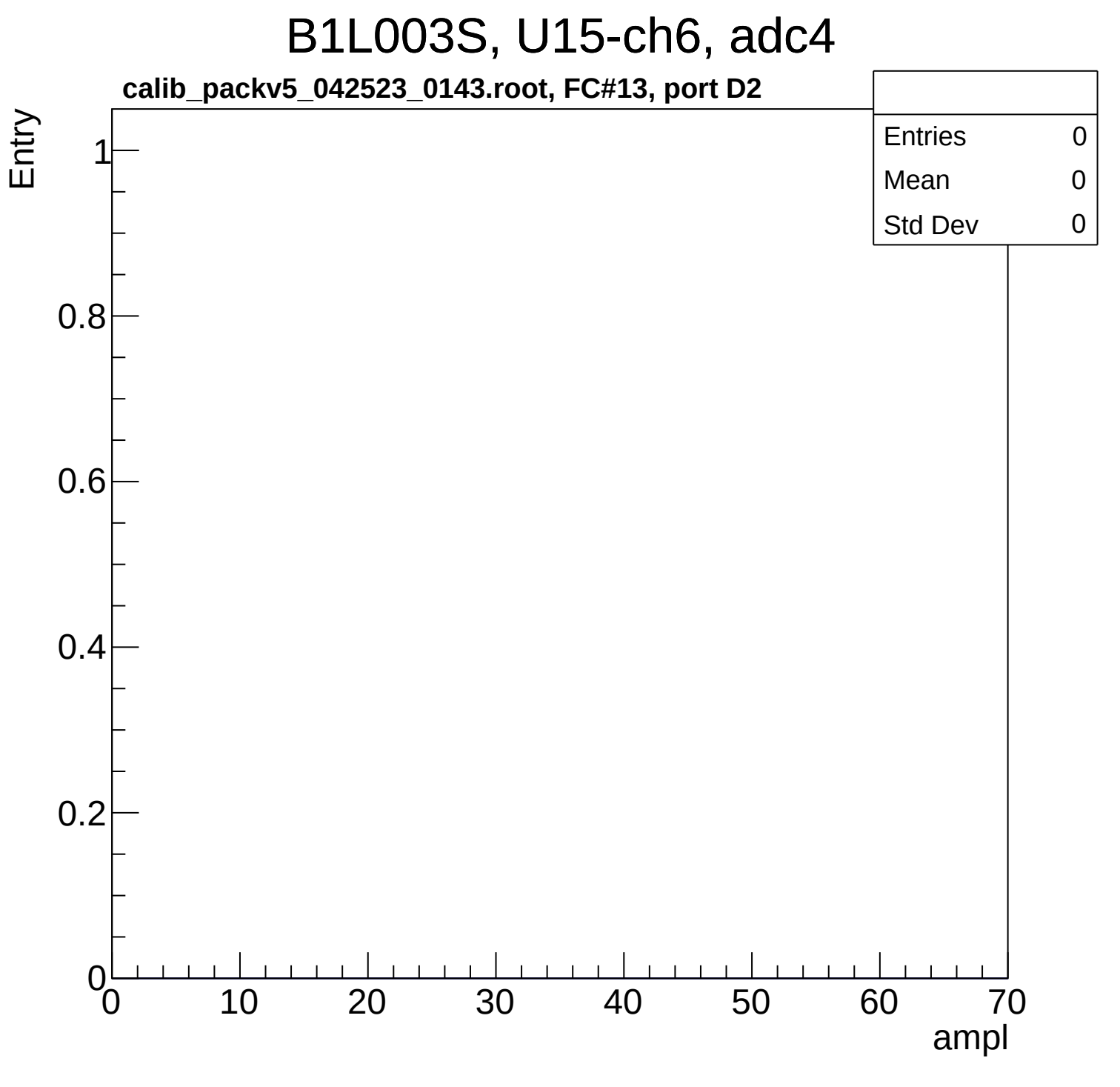
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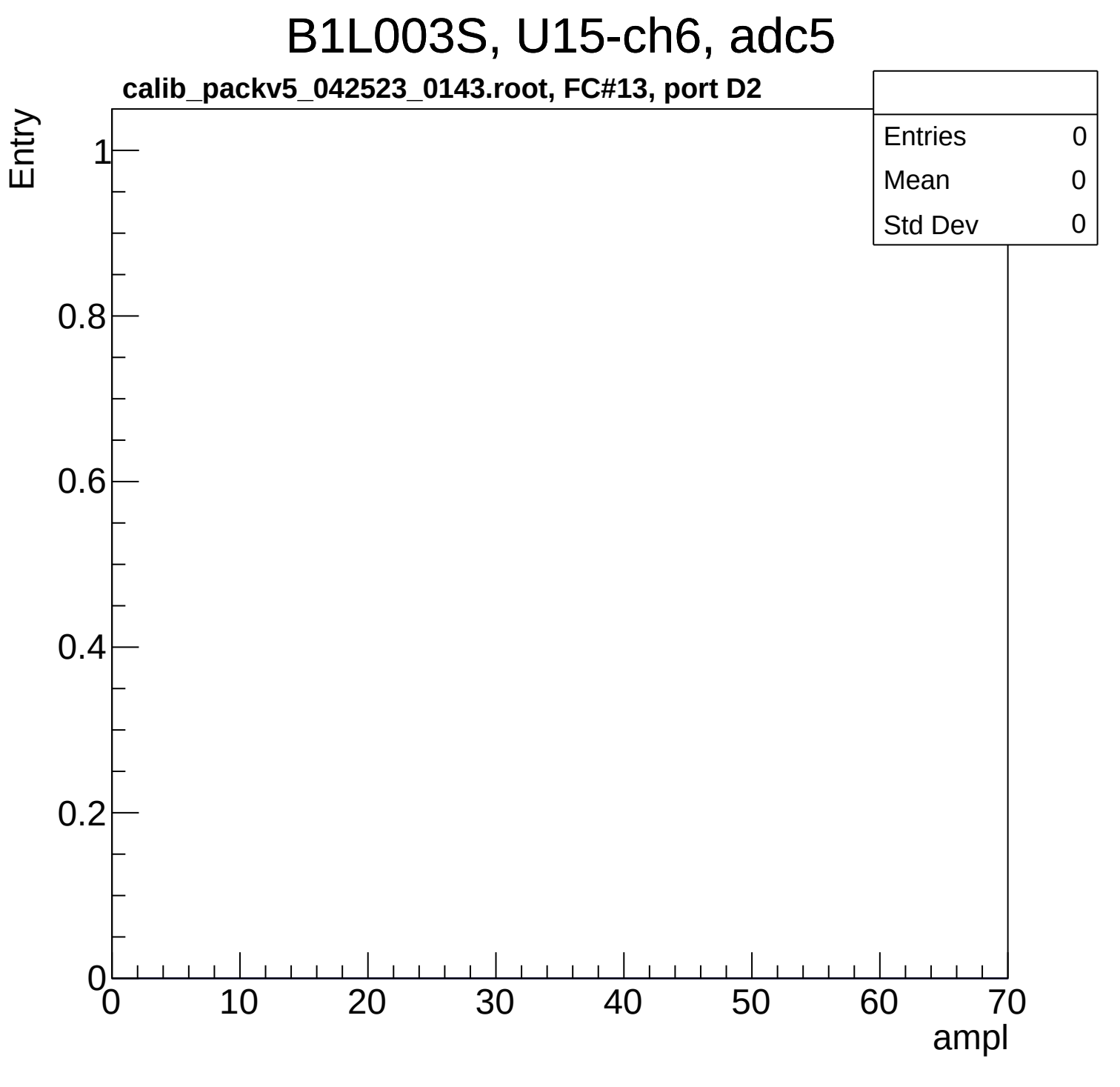
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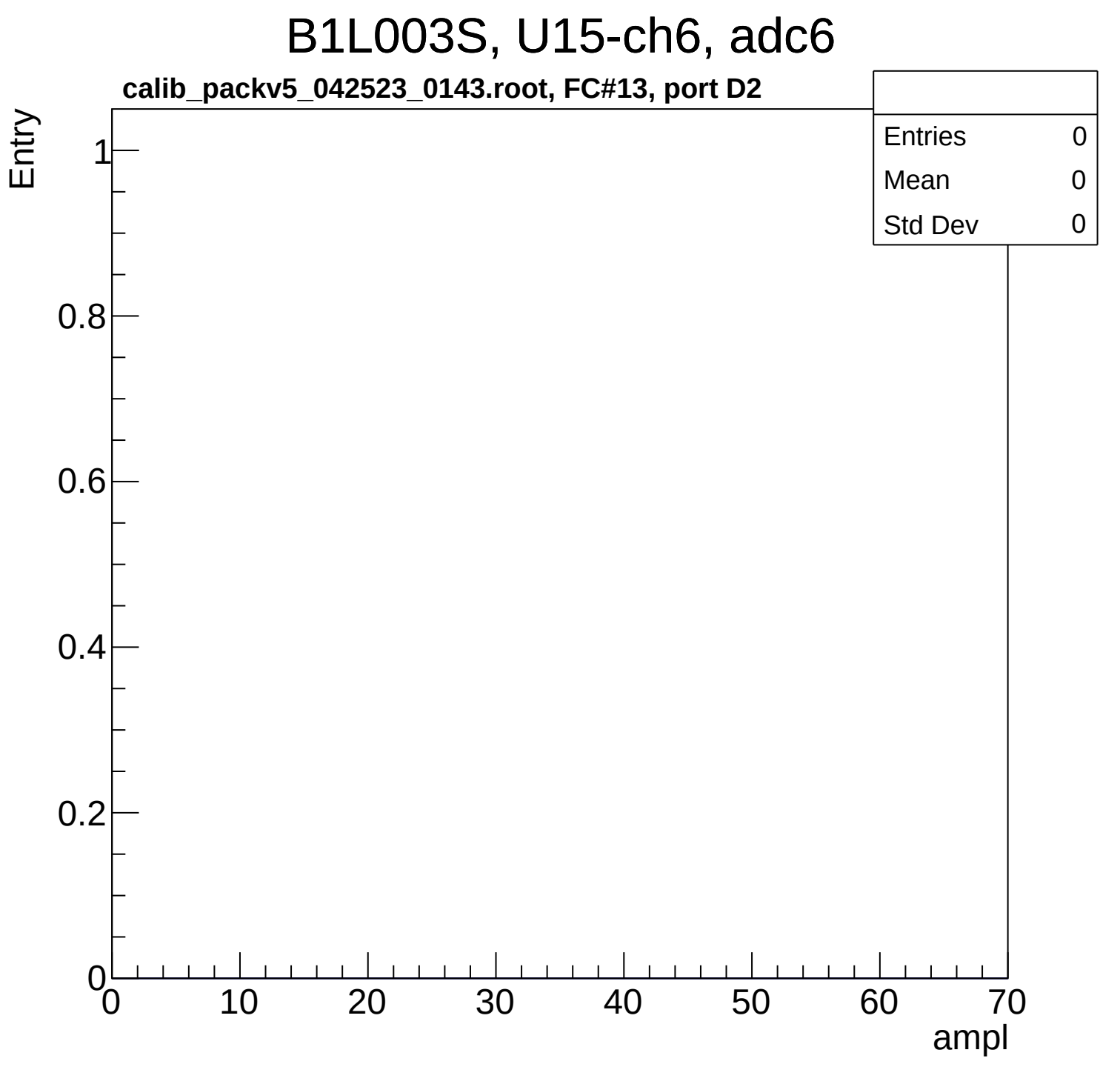
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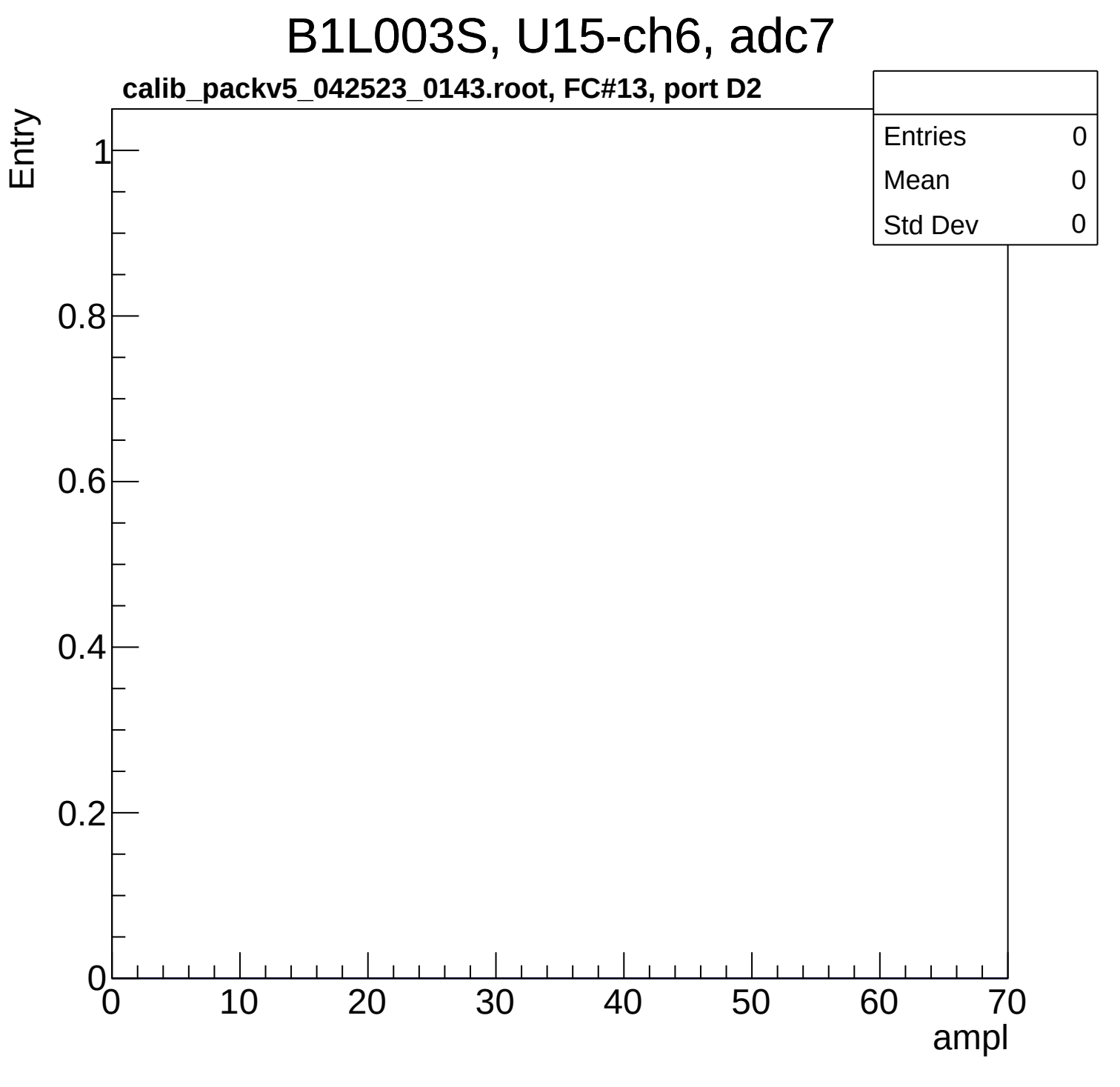
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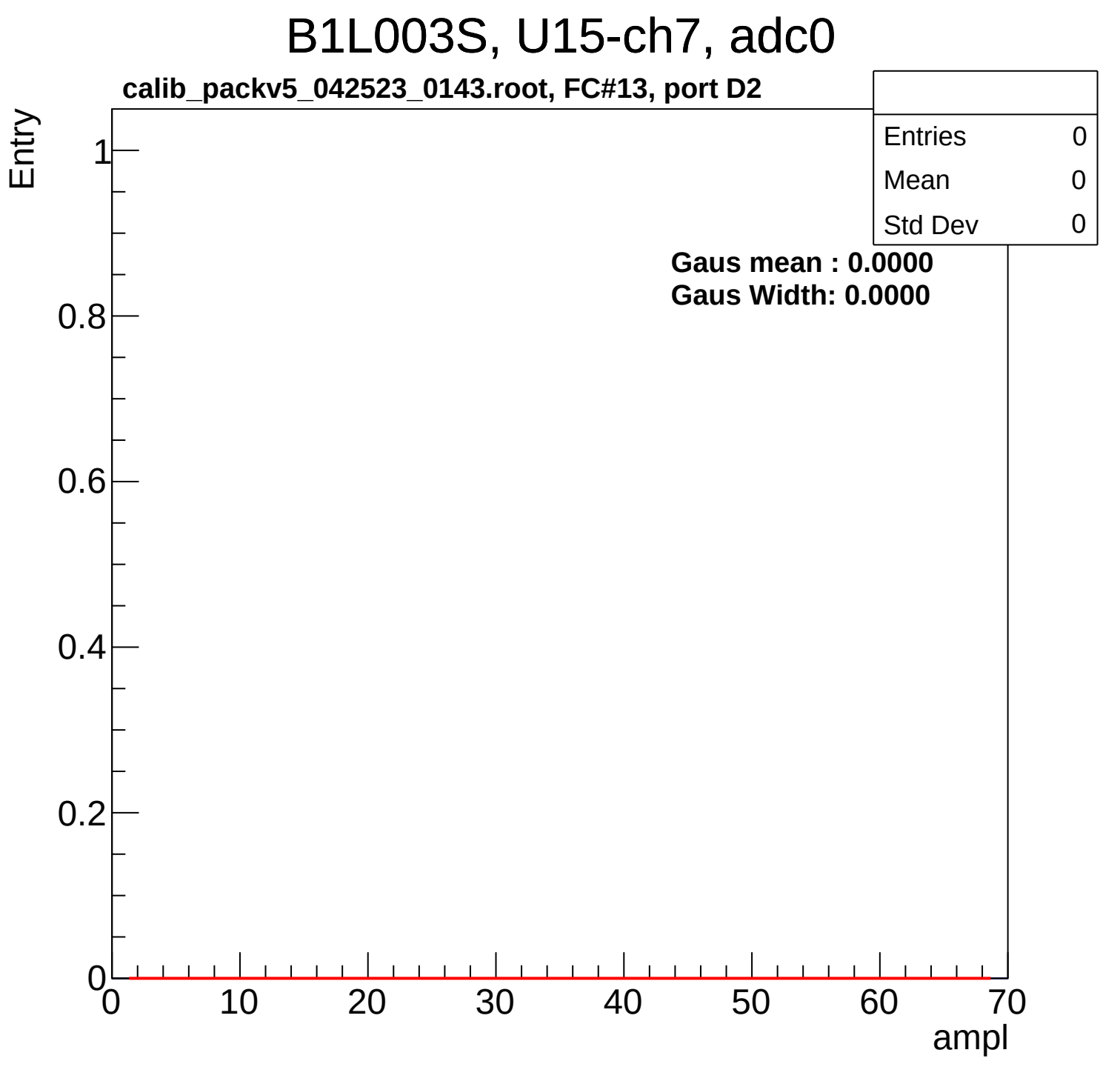
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Gaus Width: 0.0000
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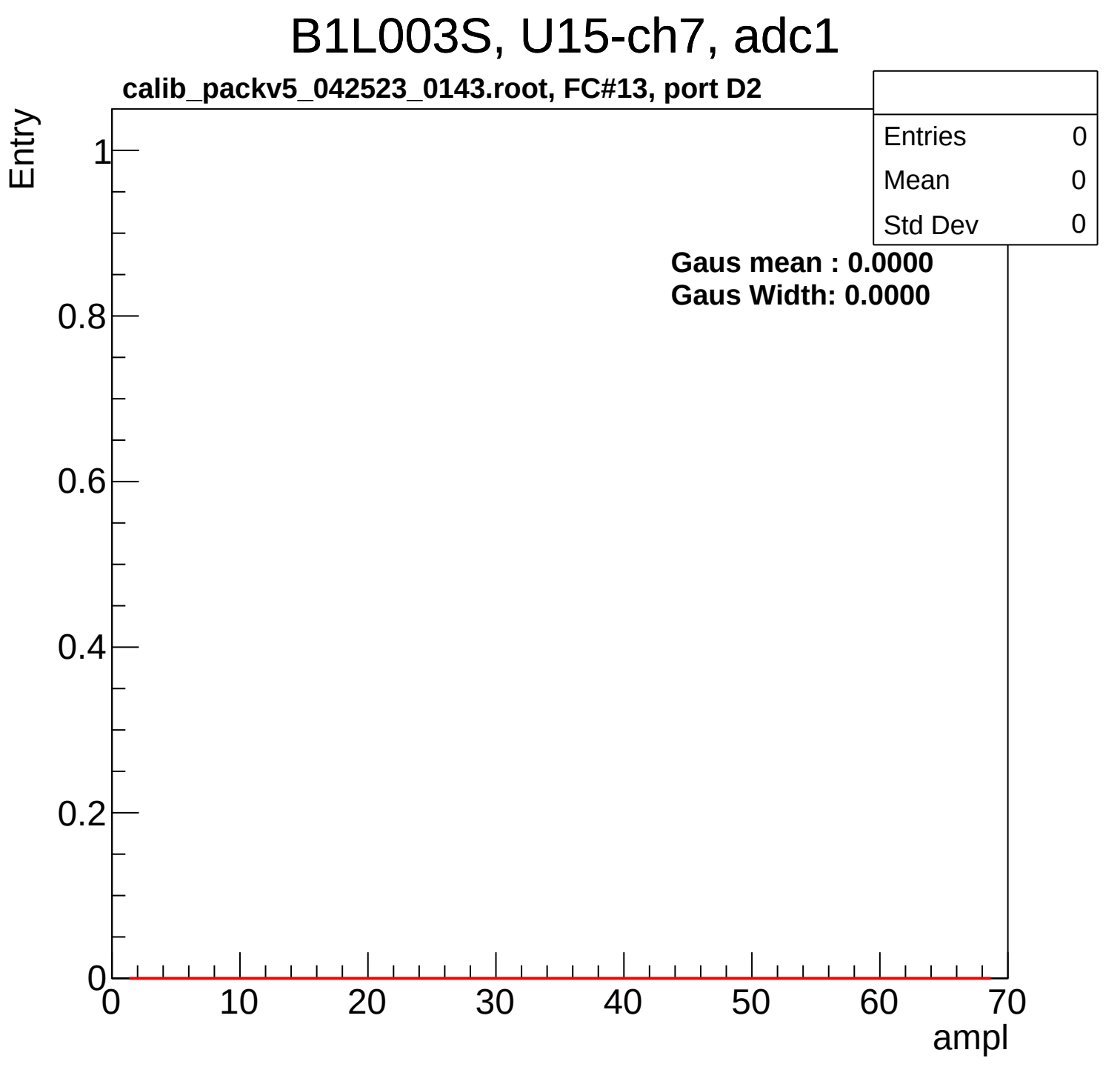
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Gaus Width: 0.0000
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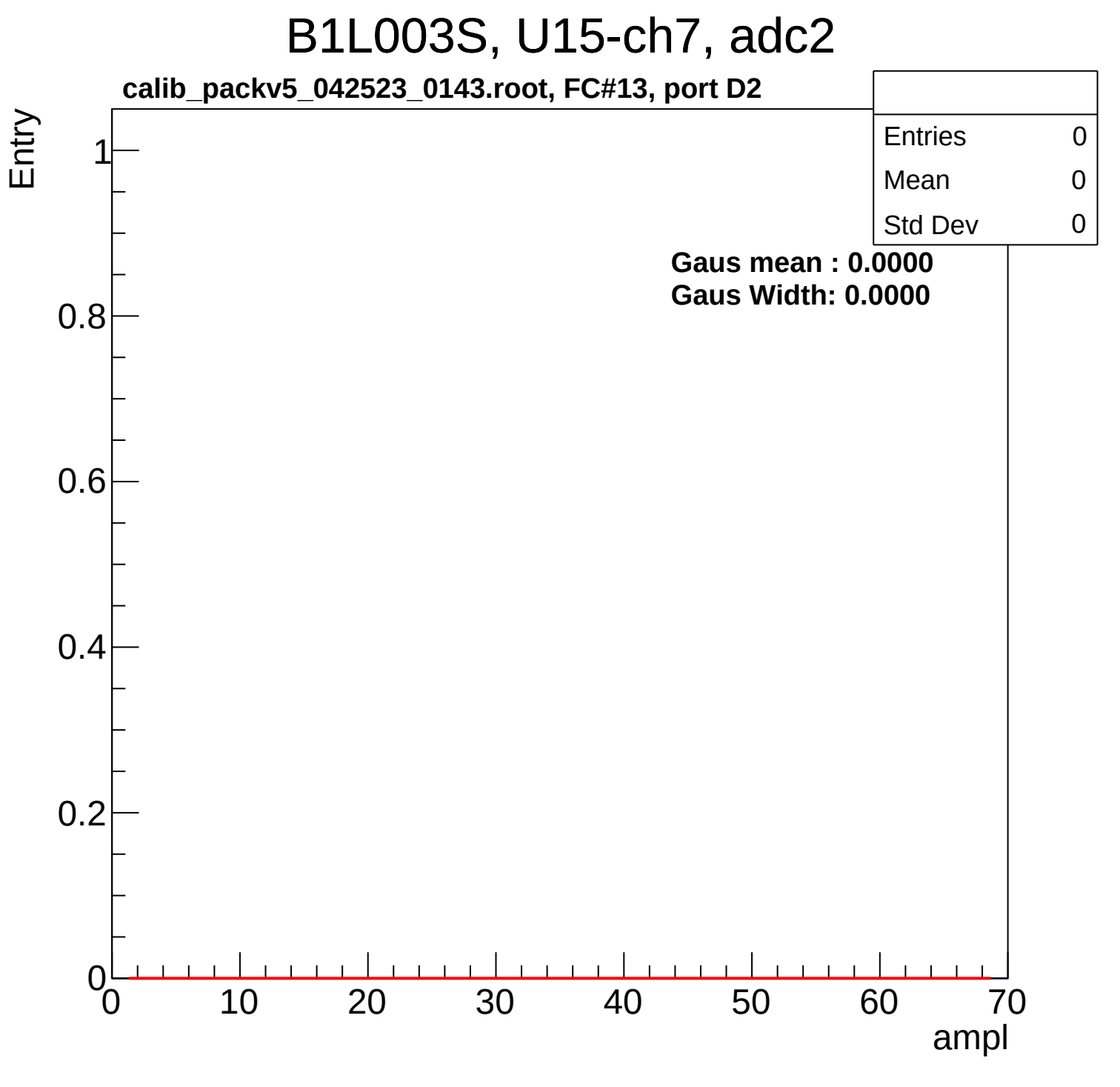
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Std Dev 0
Gaus mean : 0.0000
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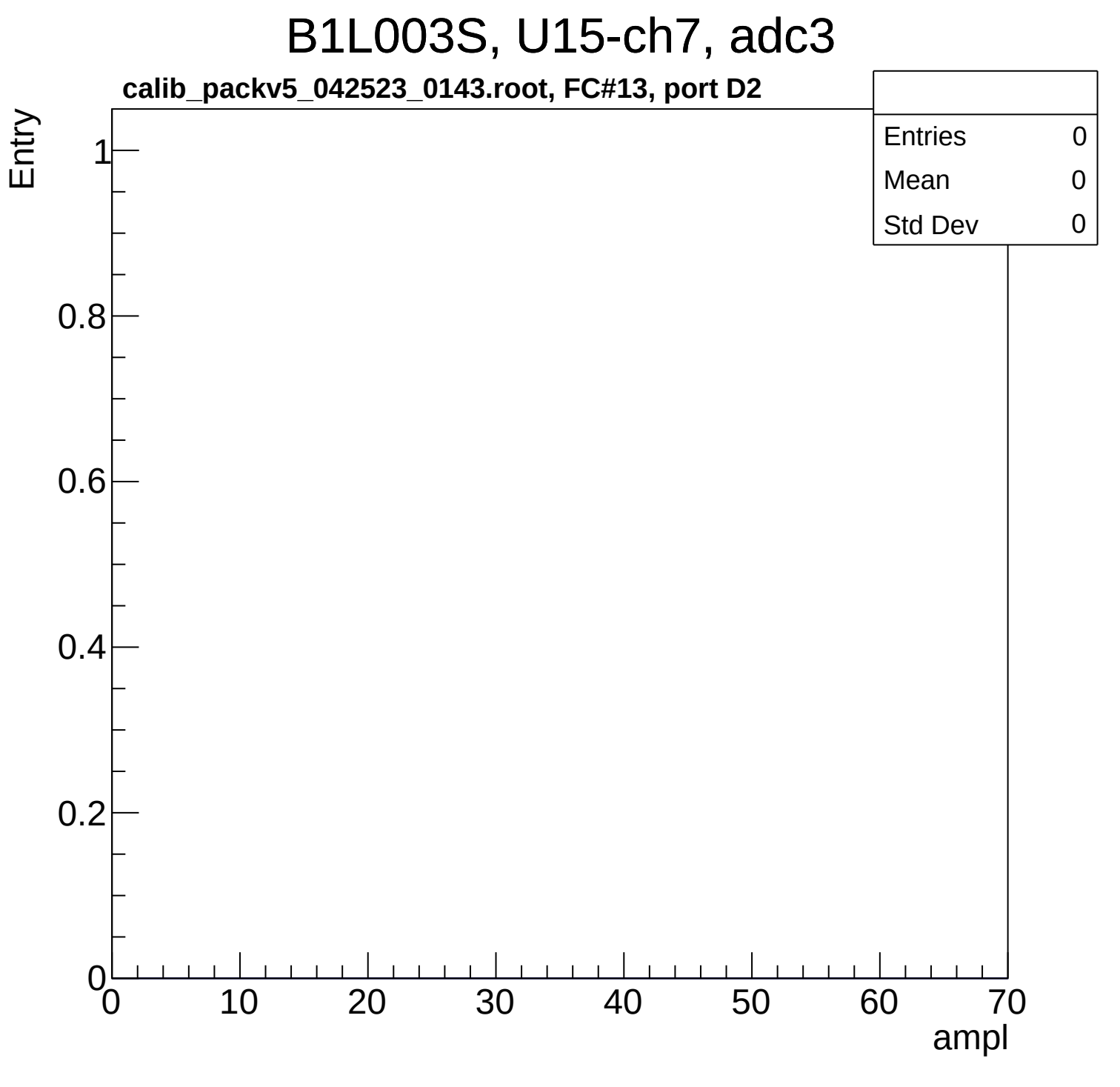
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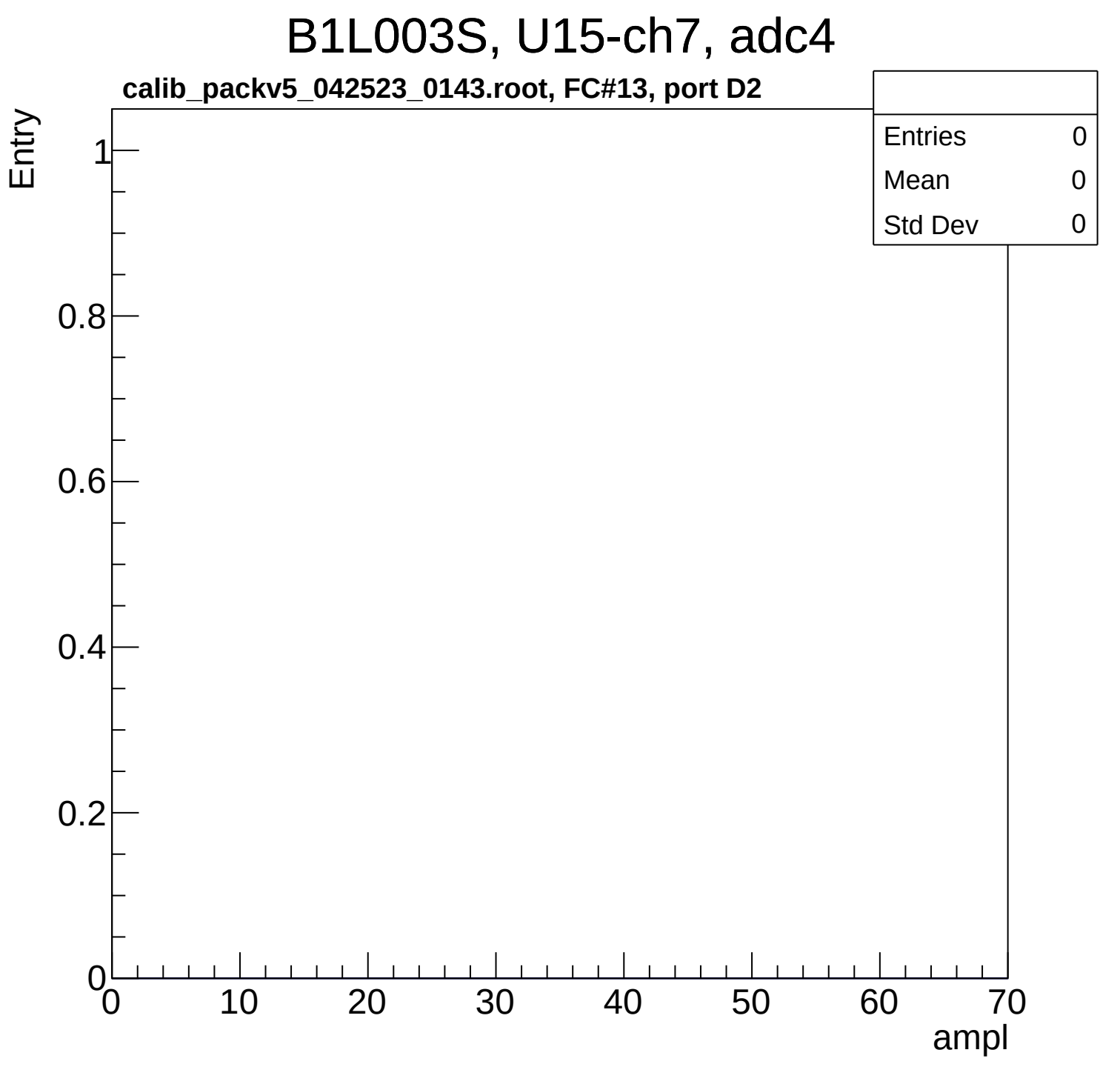
ampl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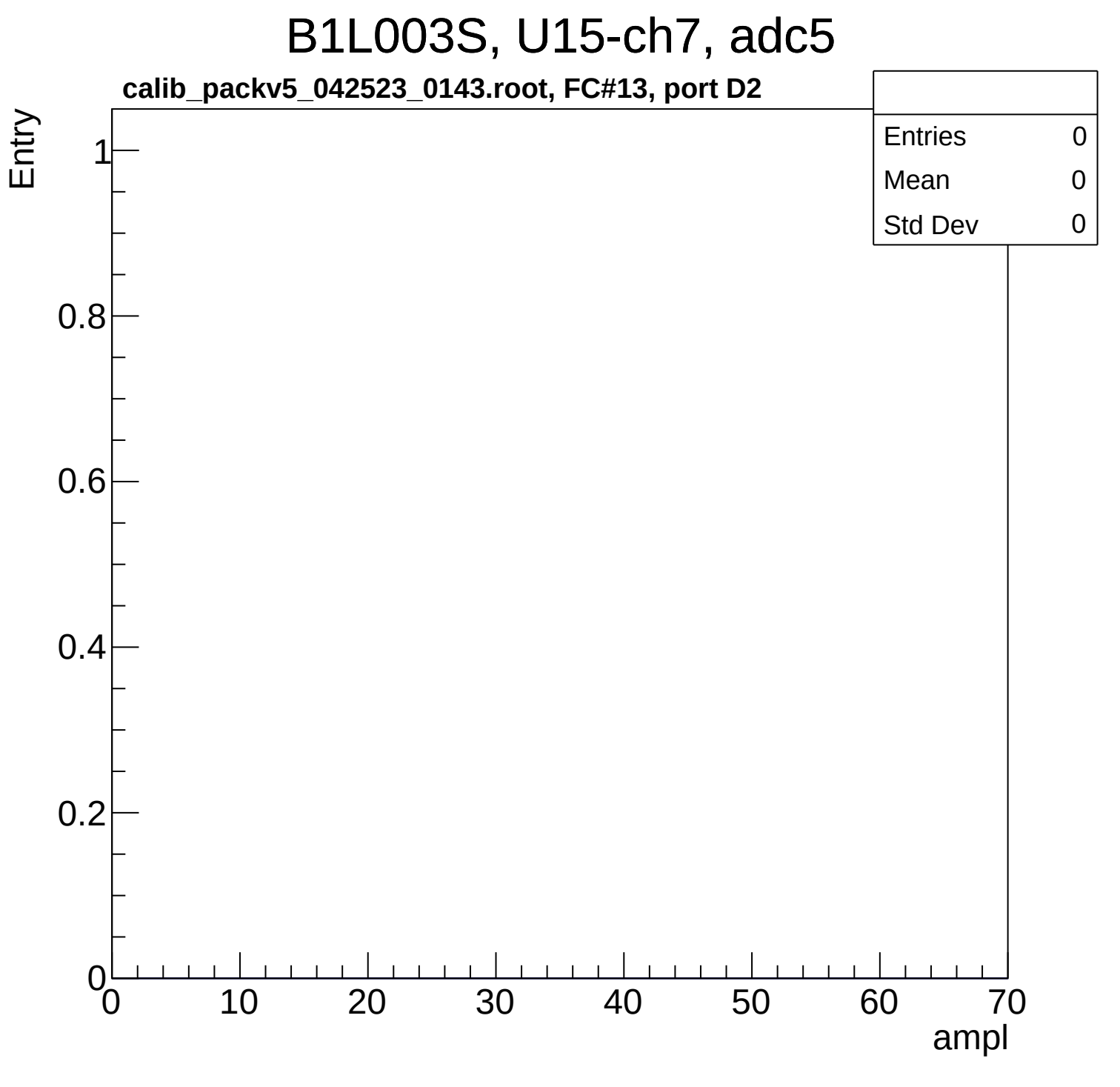
ampl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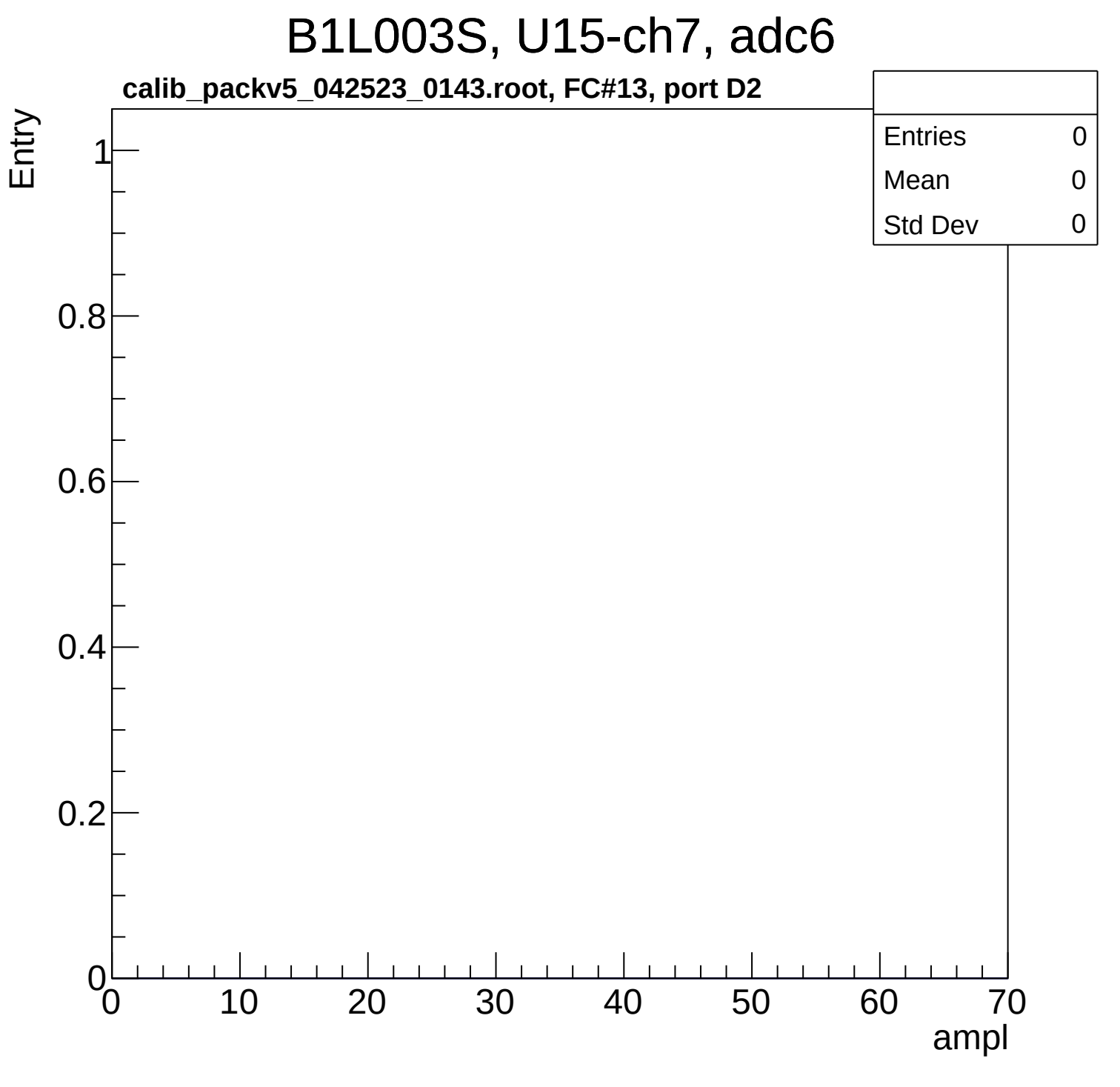
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Std Dev 0
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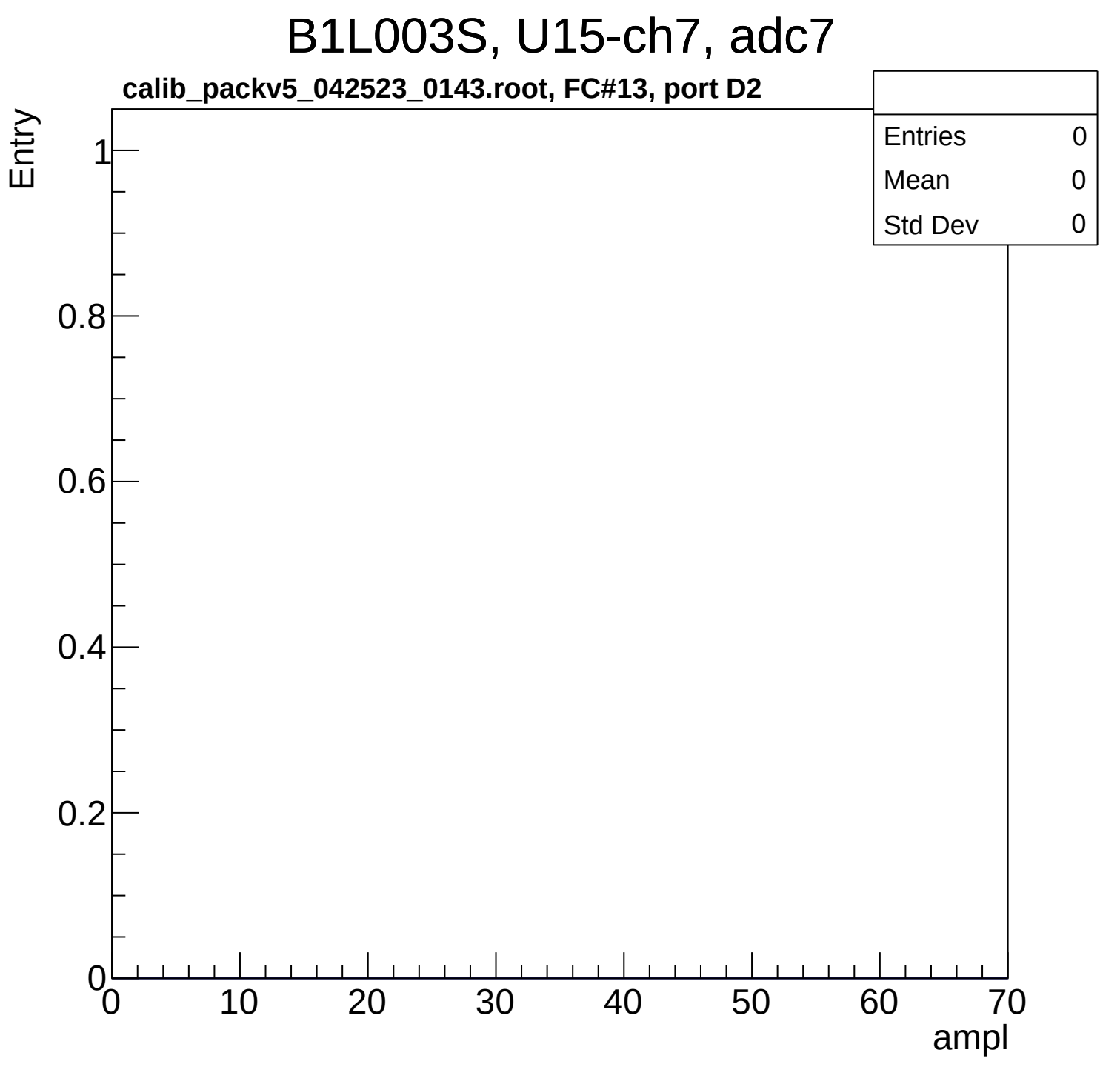
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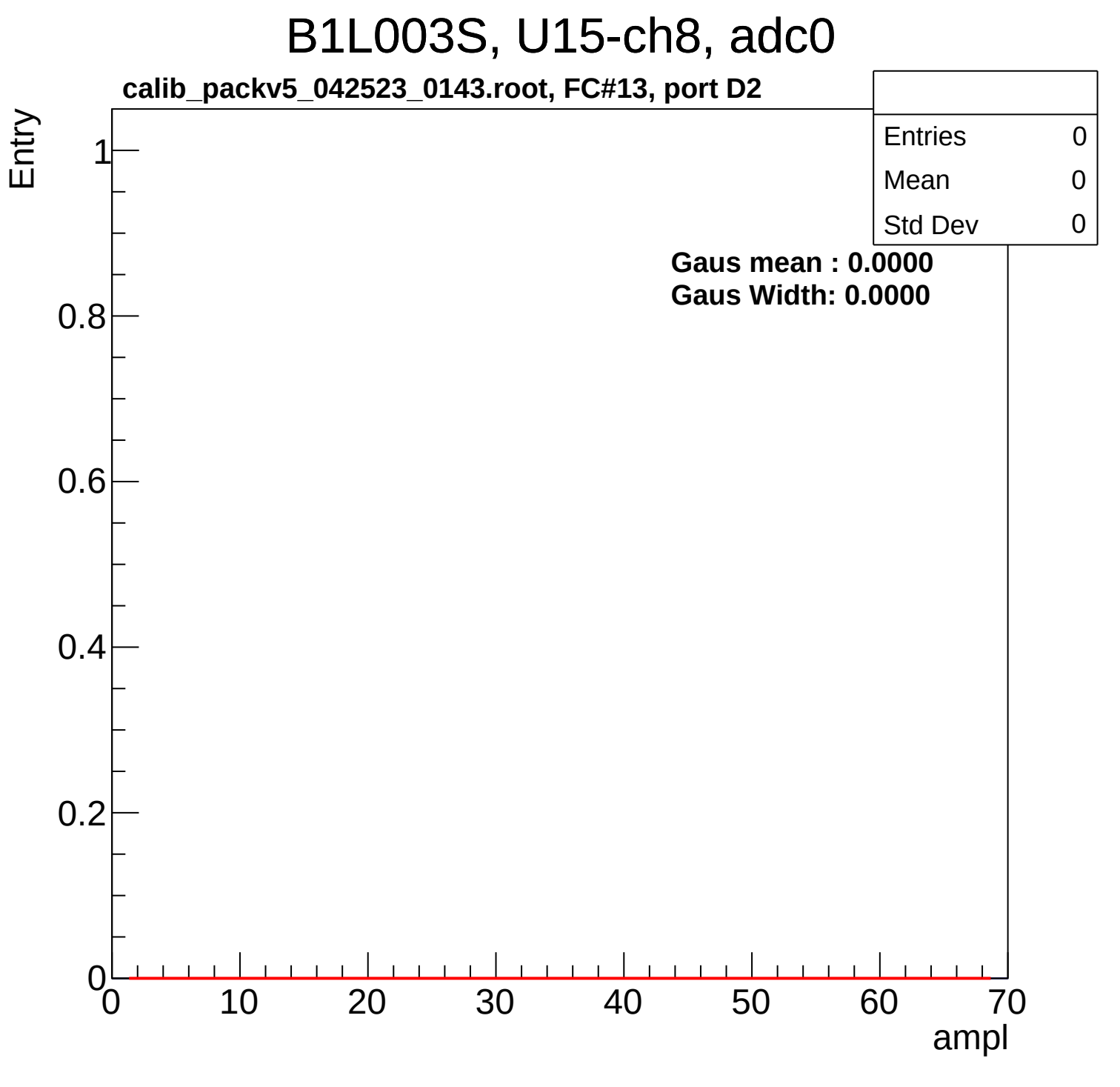
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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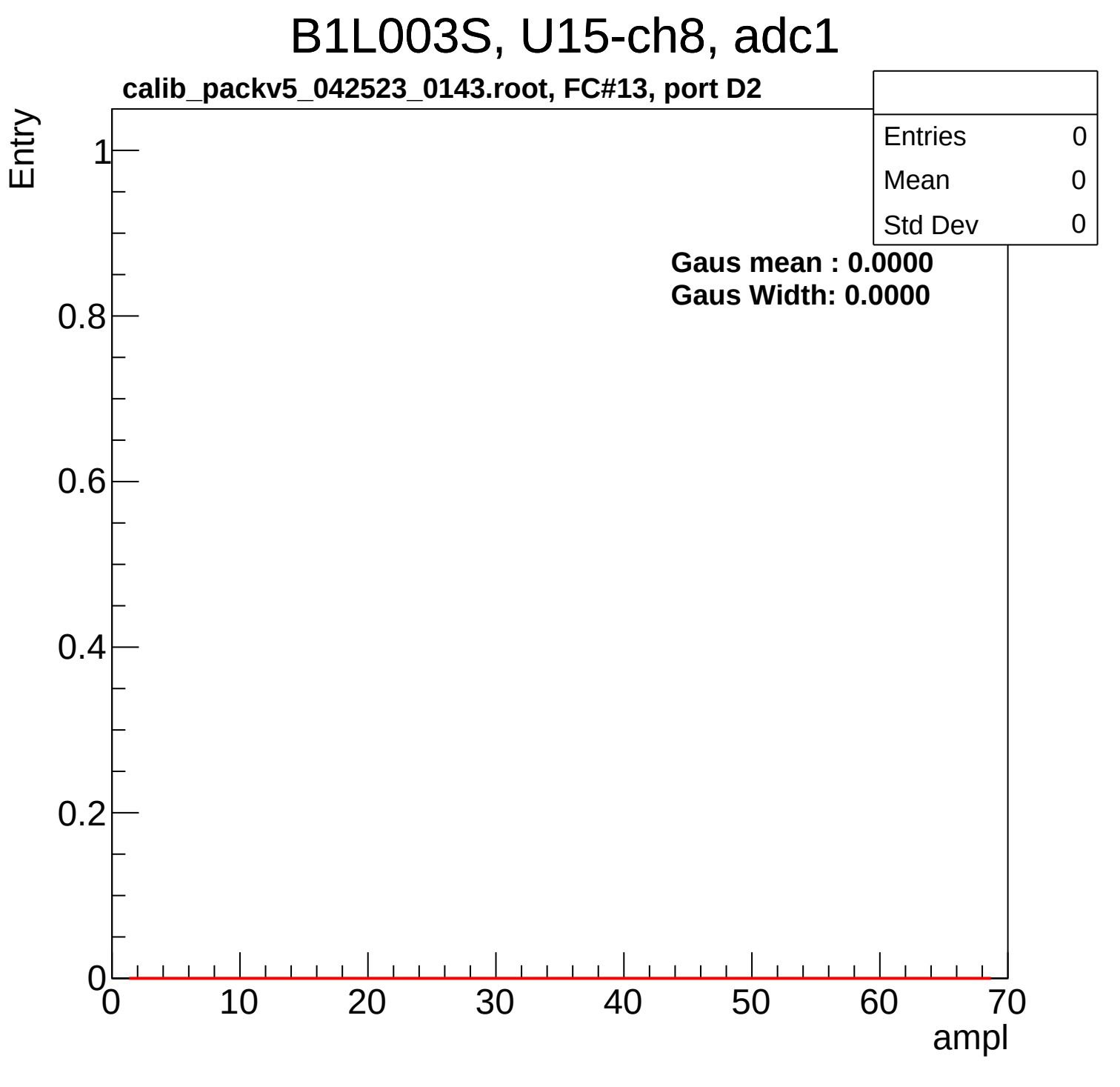
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Gaus mean : 0.0000
Gaus Width: 0.0000
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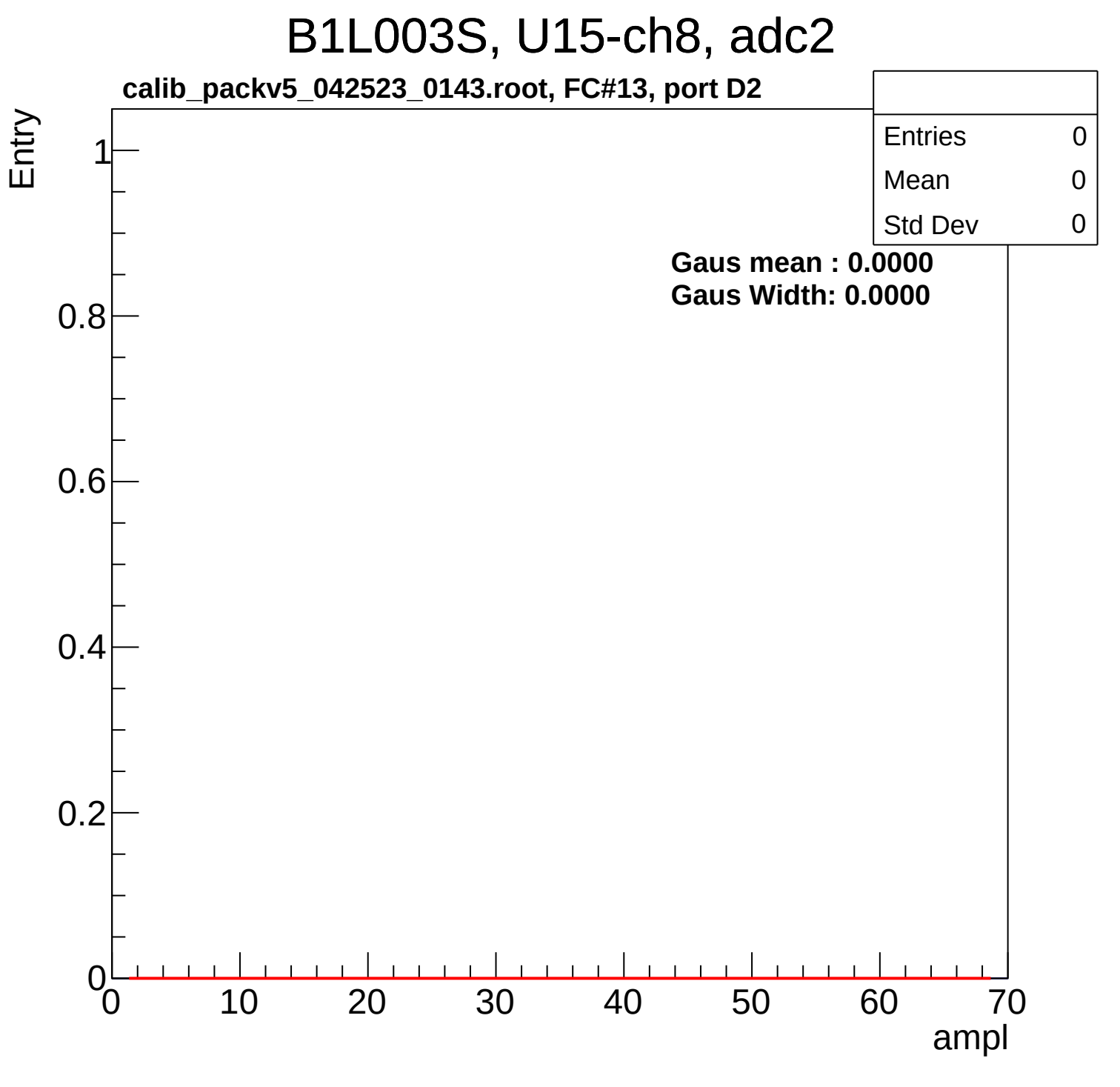
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Std Dev 0
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Gaus Width: 0.0000
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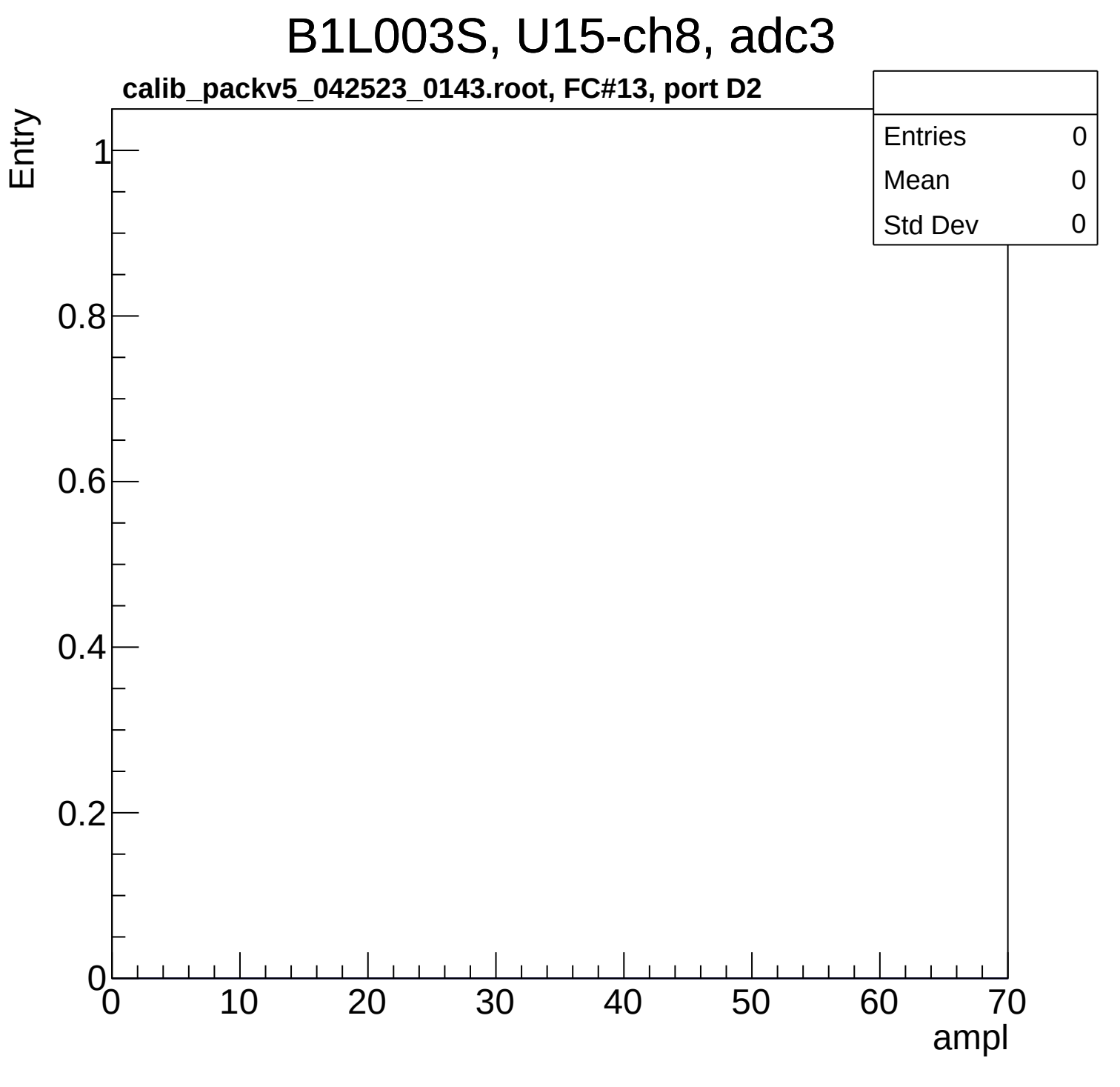
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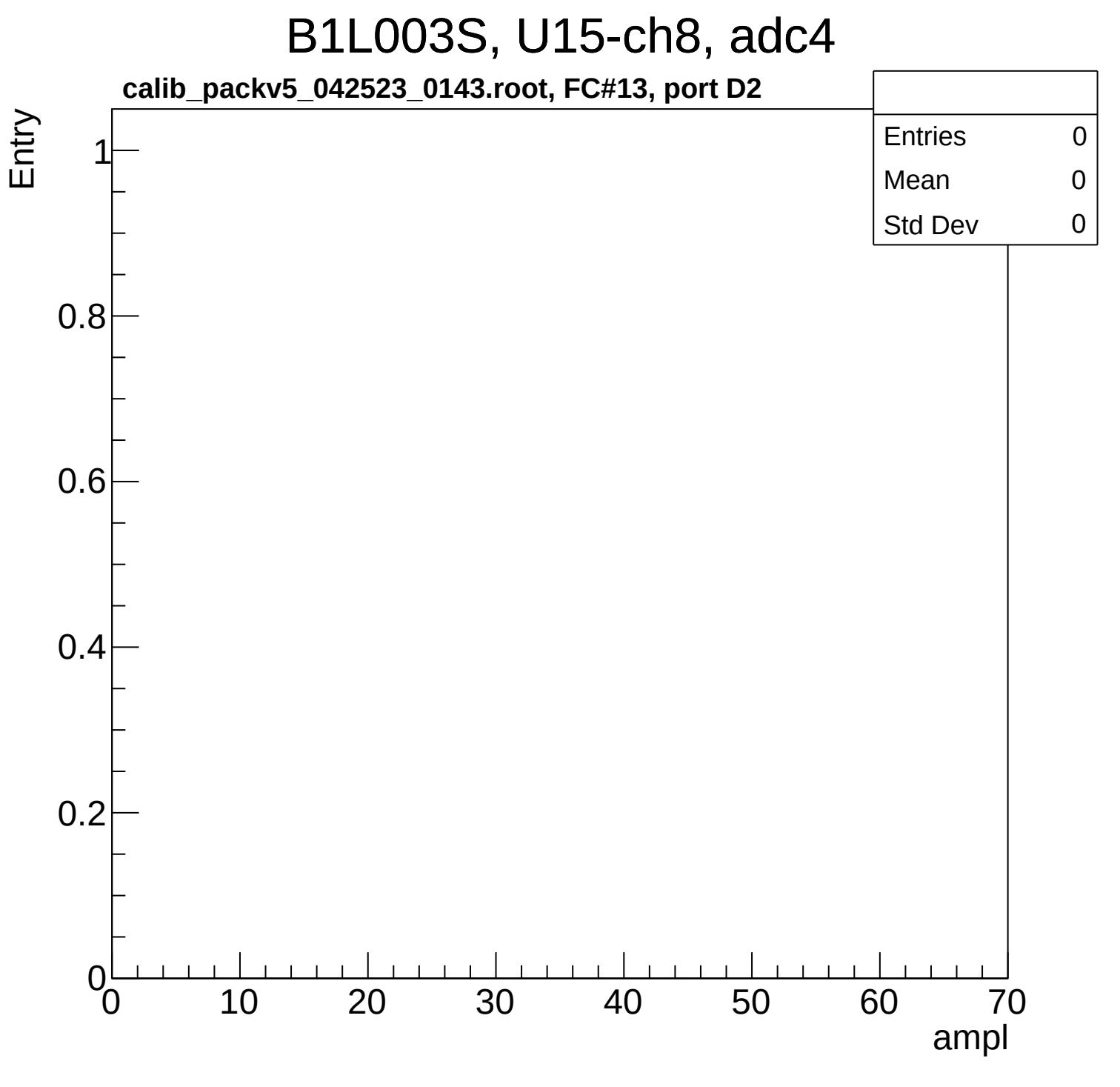
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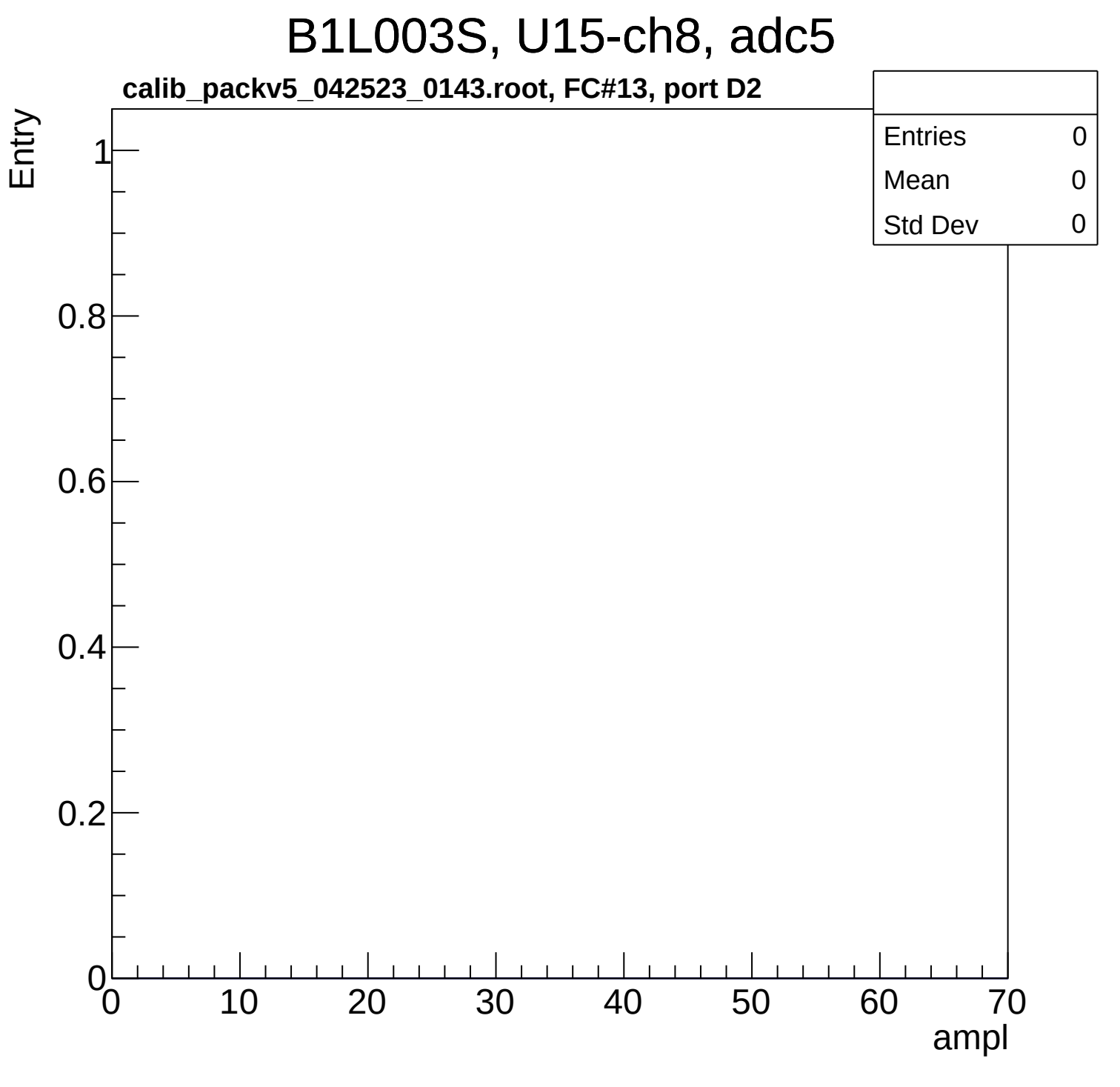
ampl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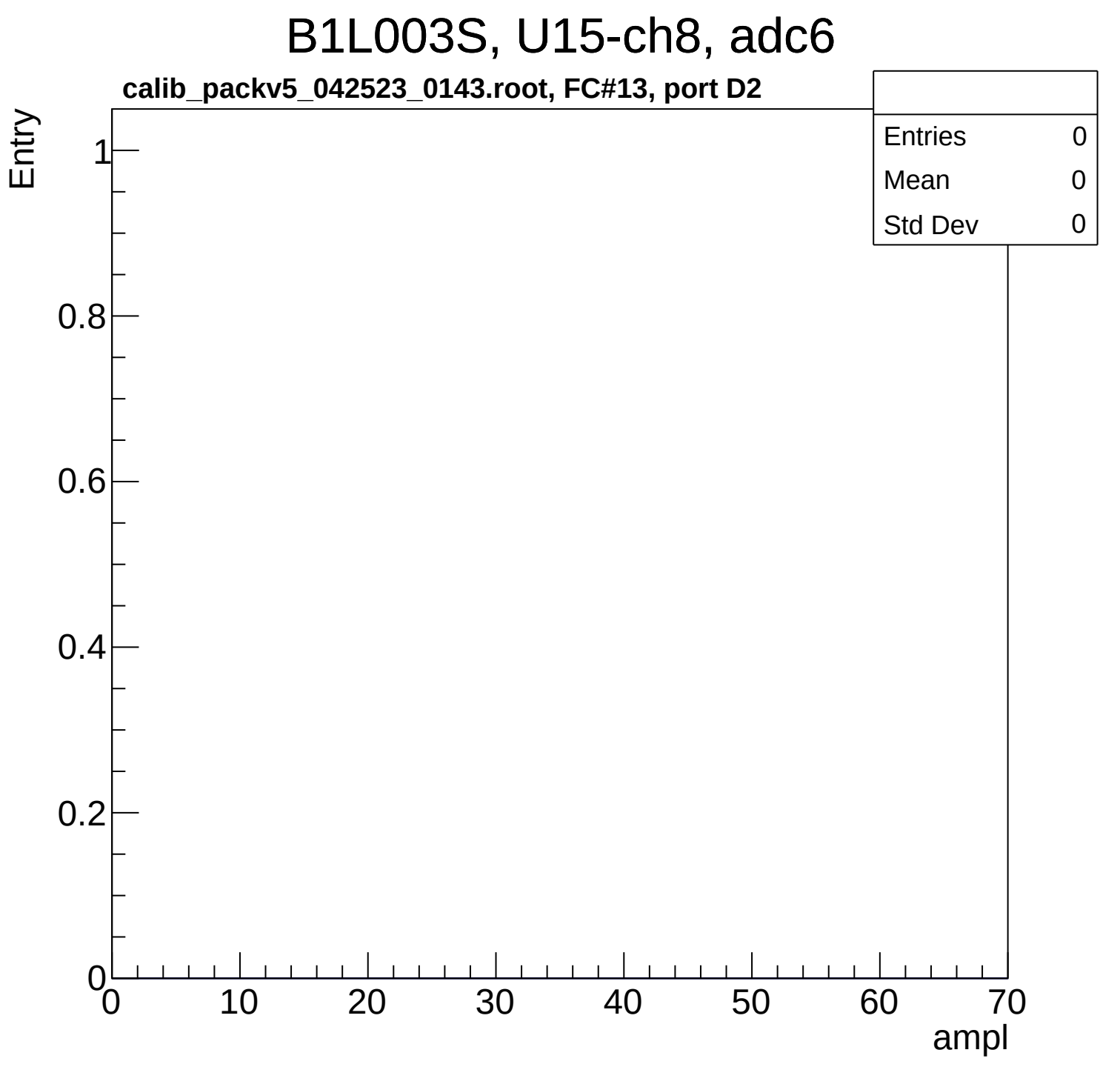
B1L003S, U15-ch8, adc6
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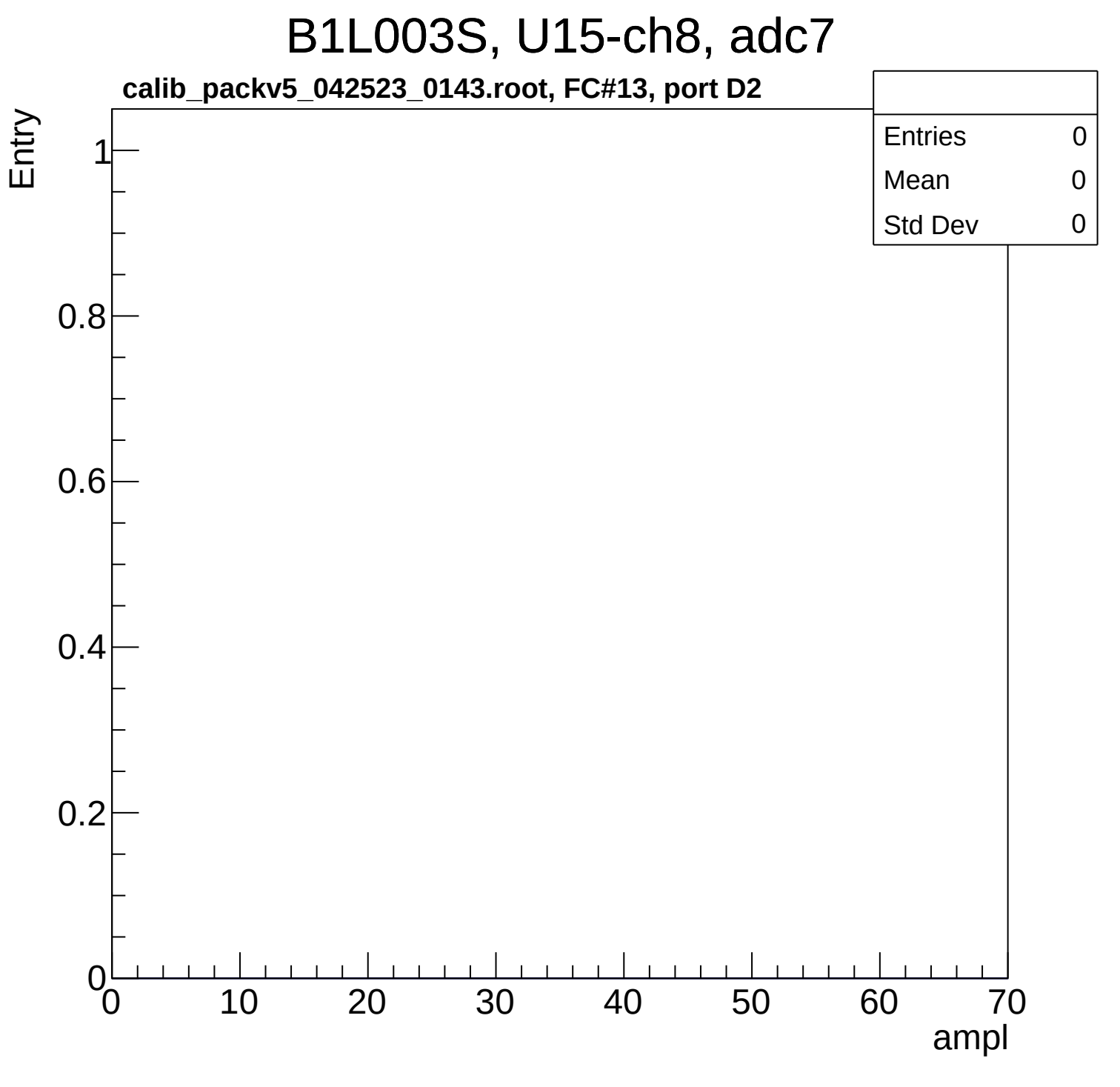
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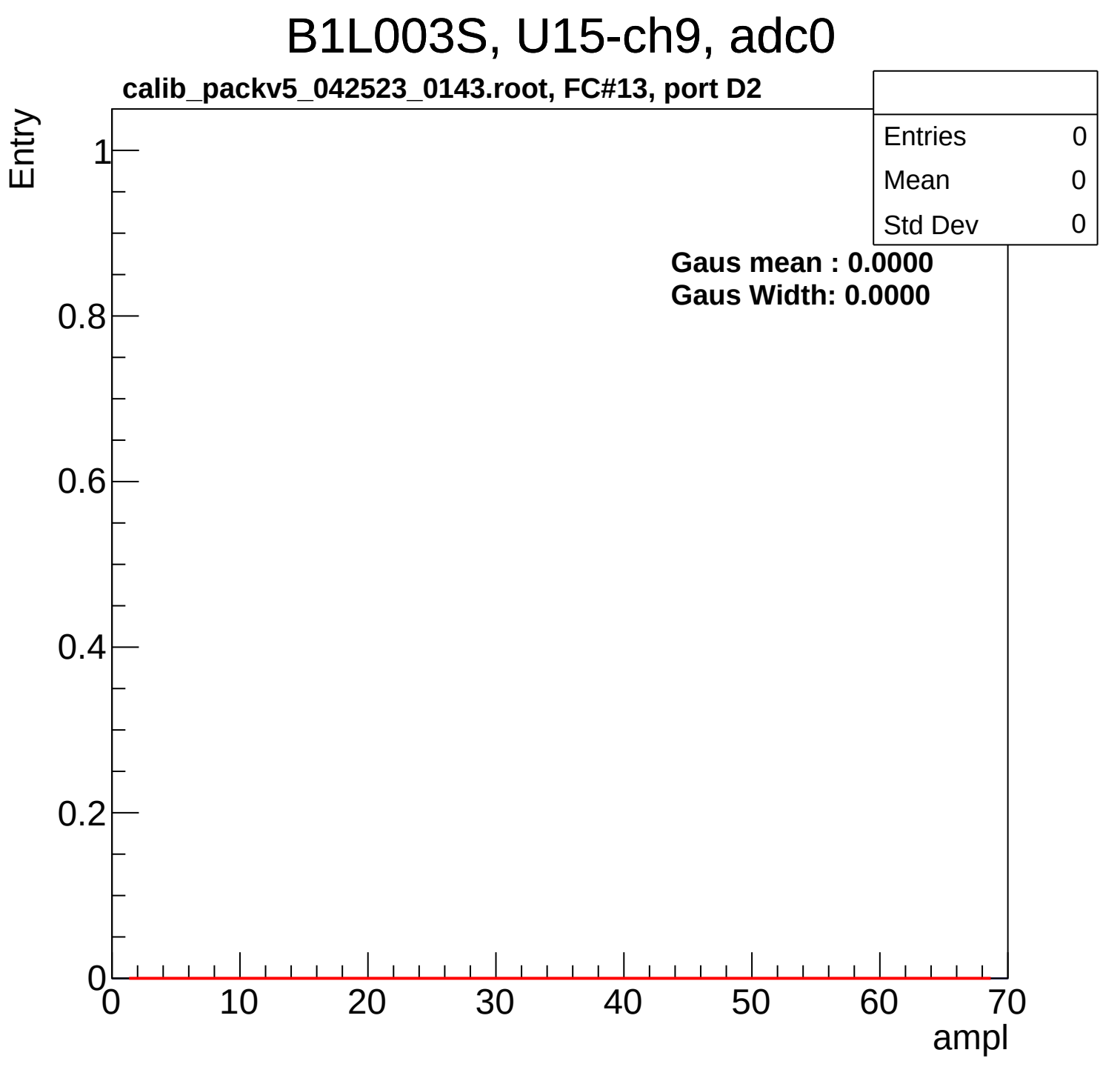
ampl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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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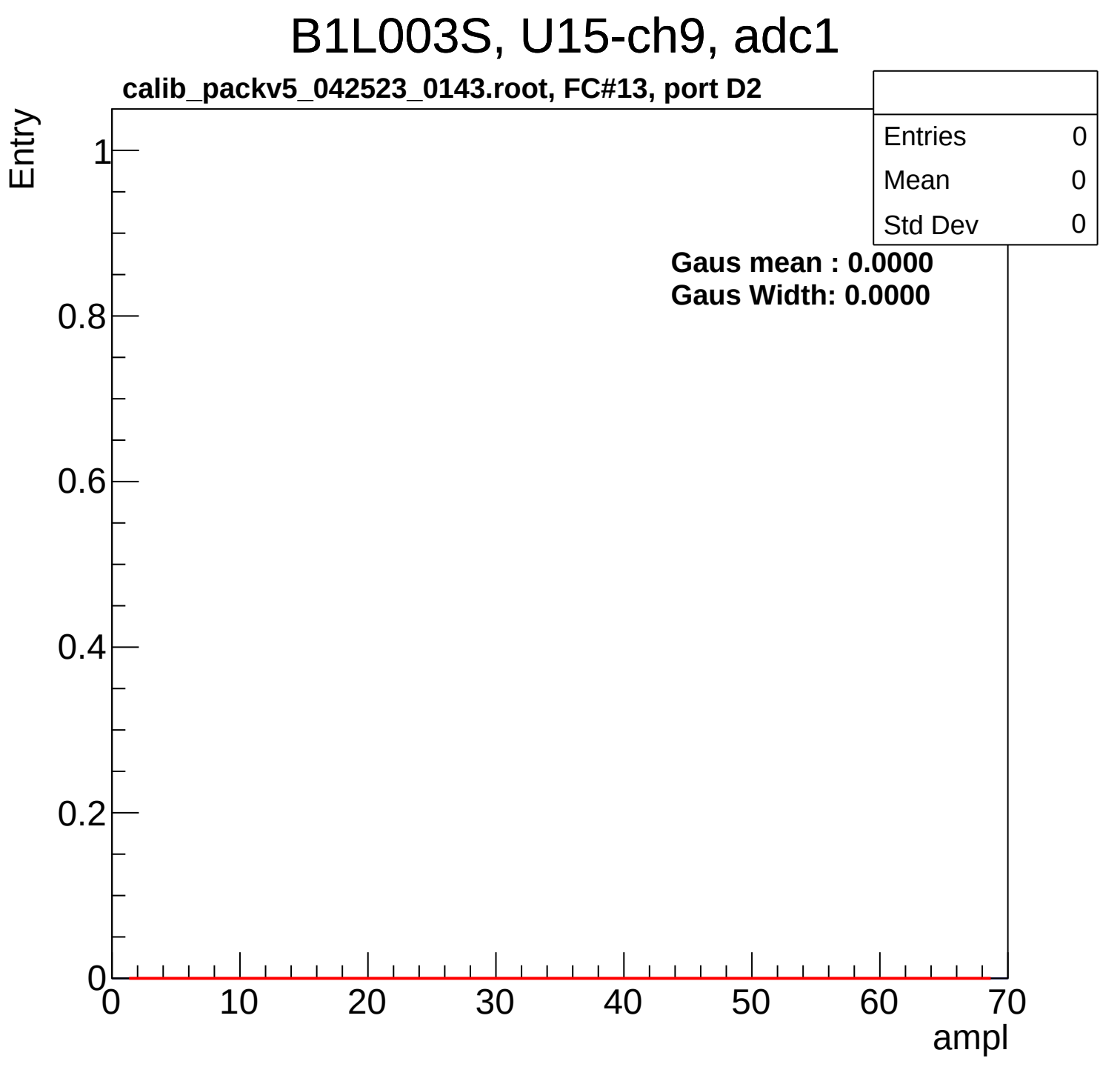
ampl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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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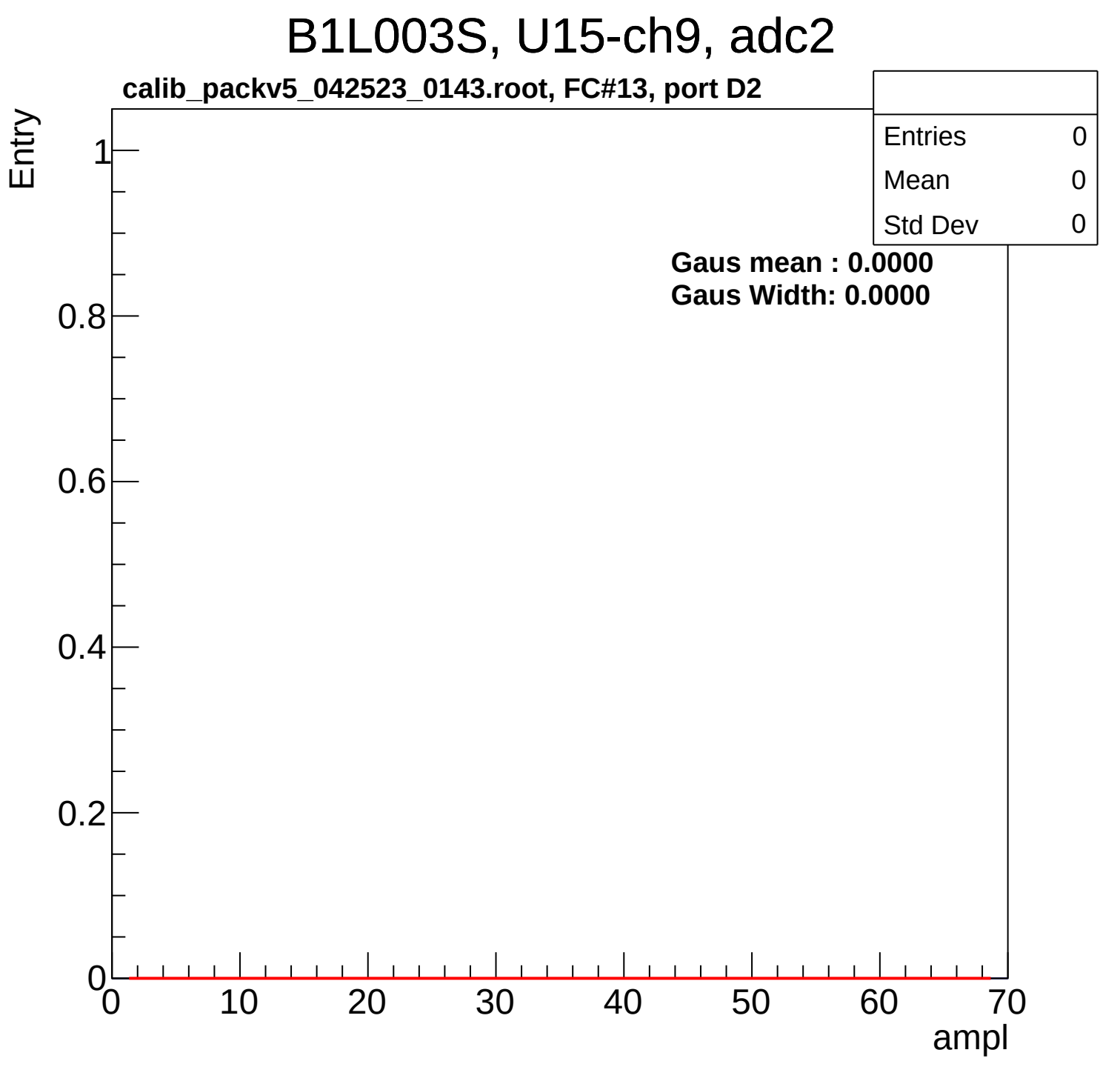
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Std Dev 0
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Gaus Width: 0.0000
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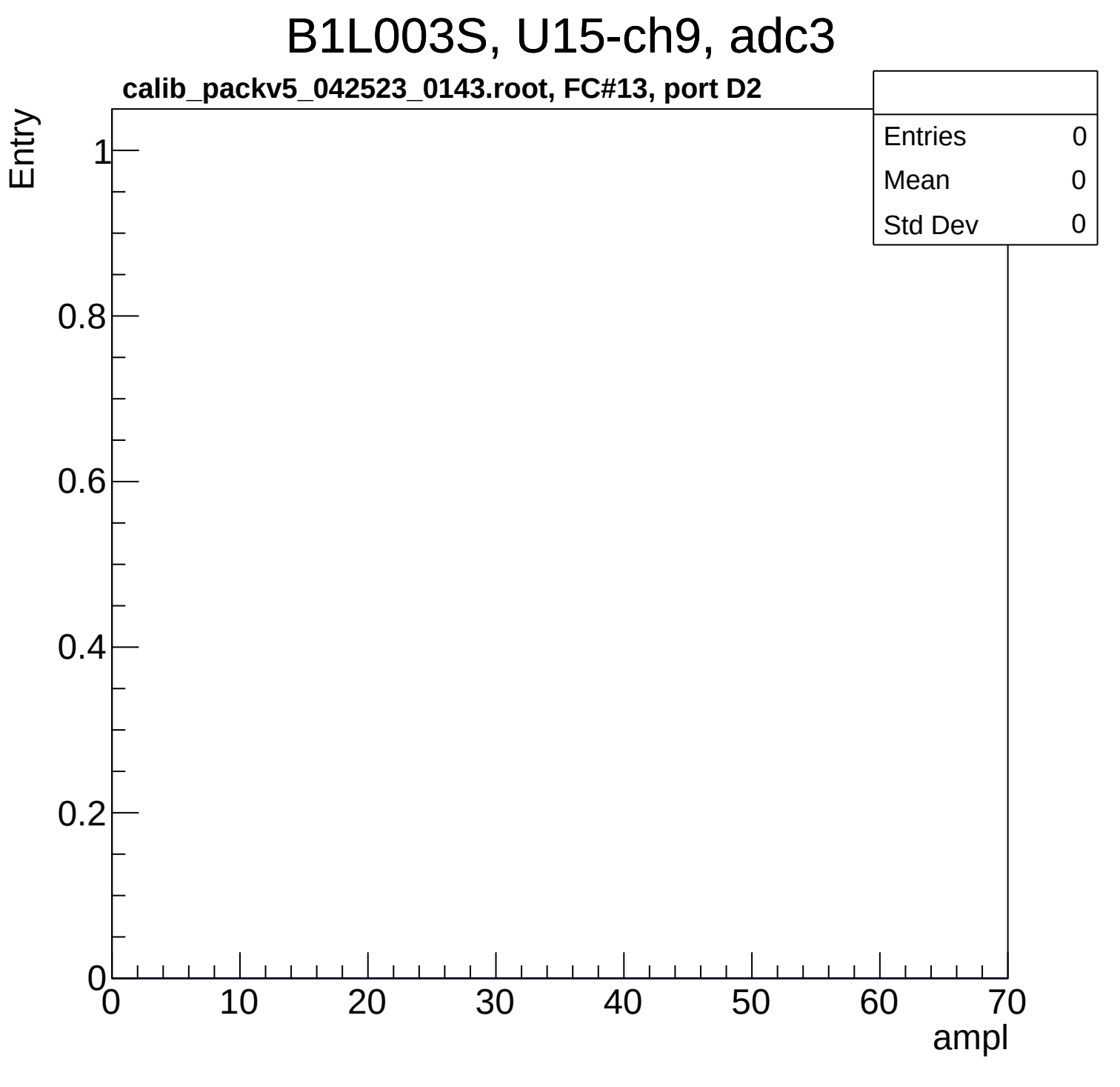
ampl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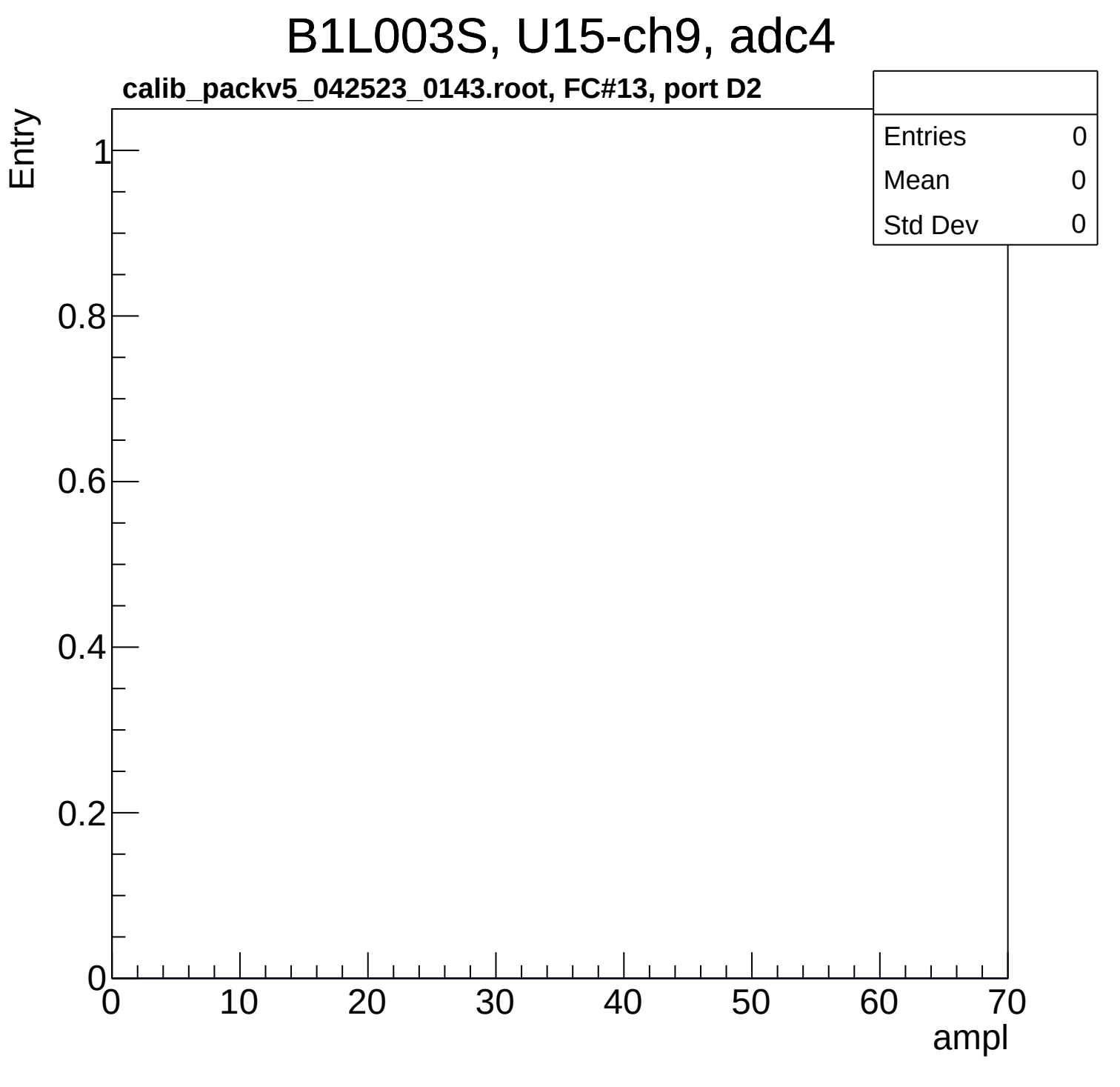
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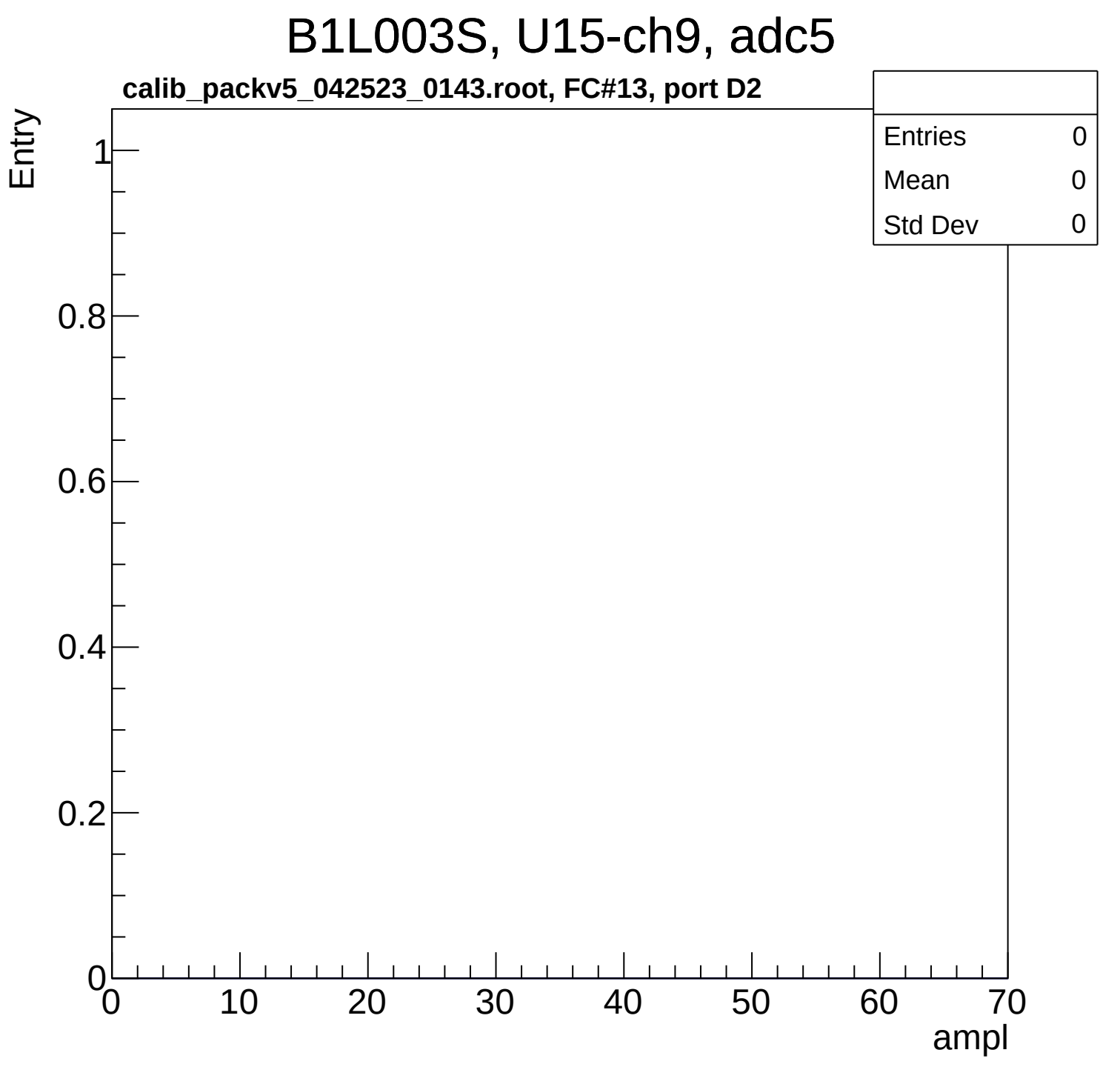
ampl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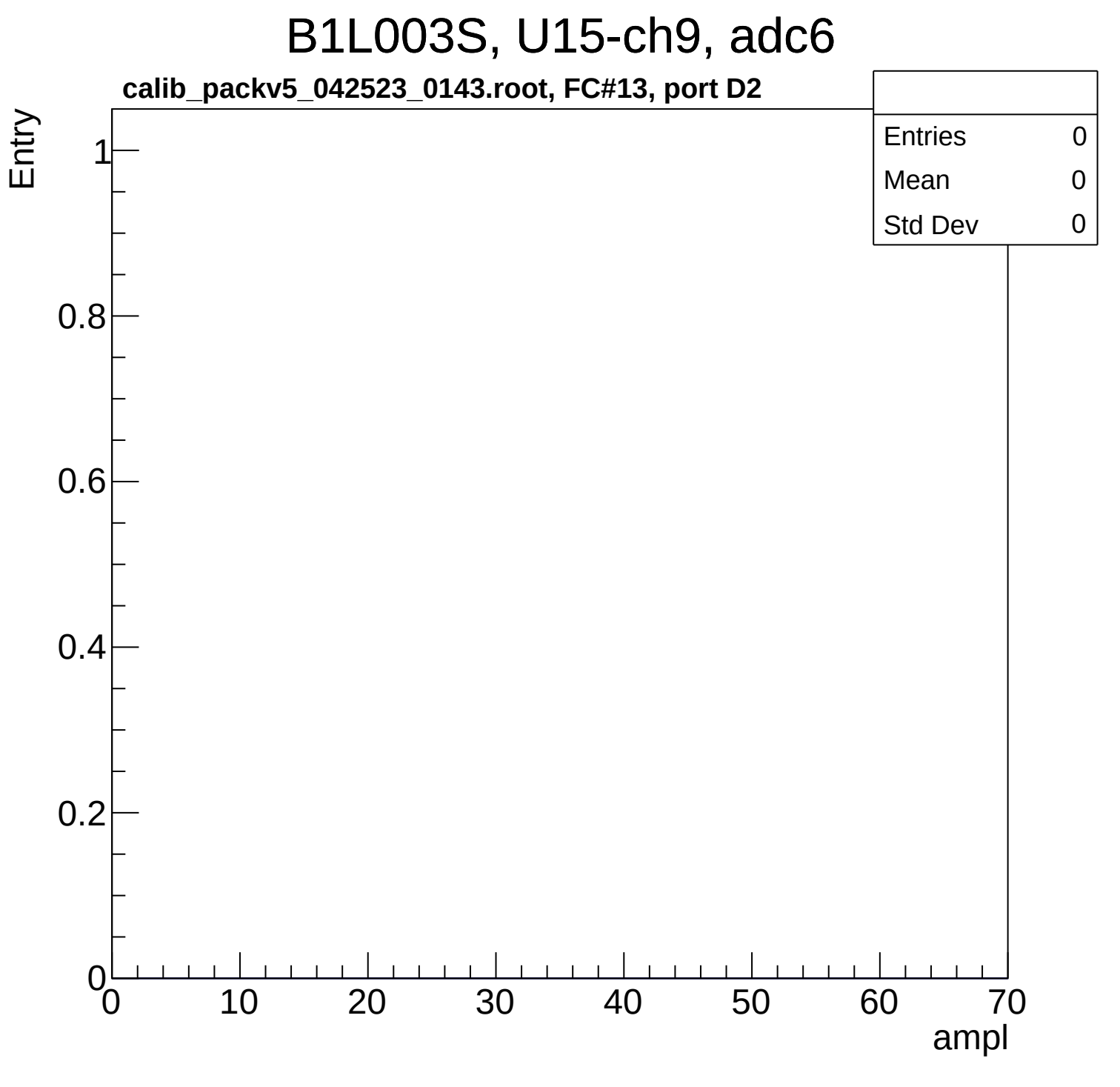
B1L003S, U15-ch9, adc6
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Std Dev 0
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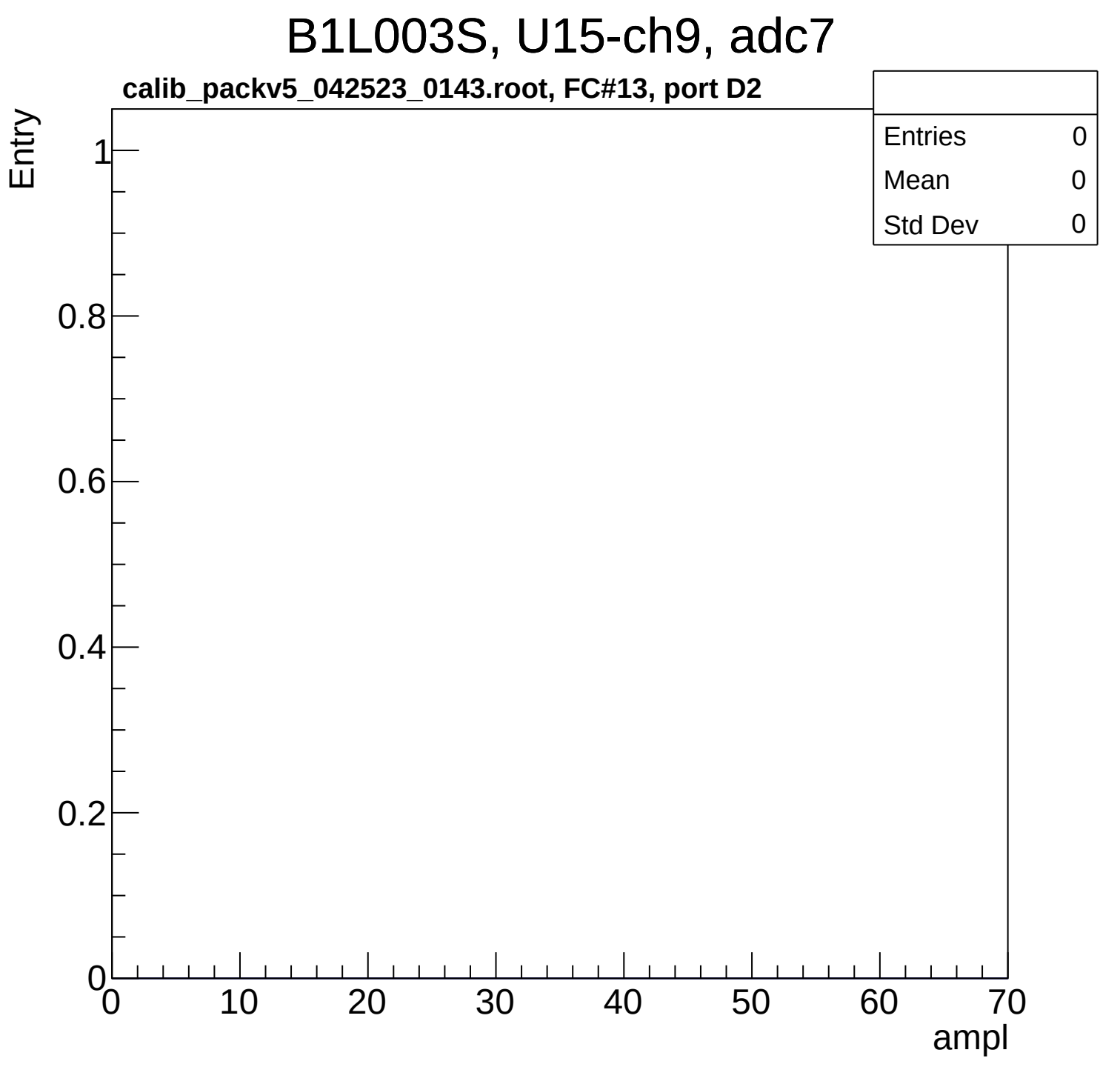
B1L003S, U15-ch9, adc7
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Entries 0
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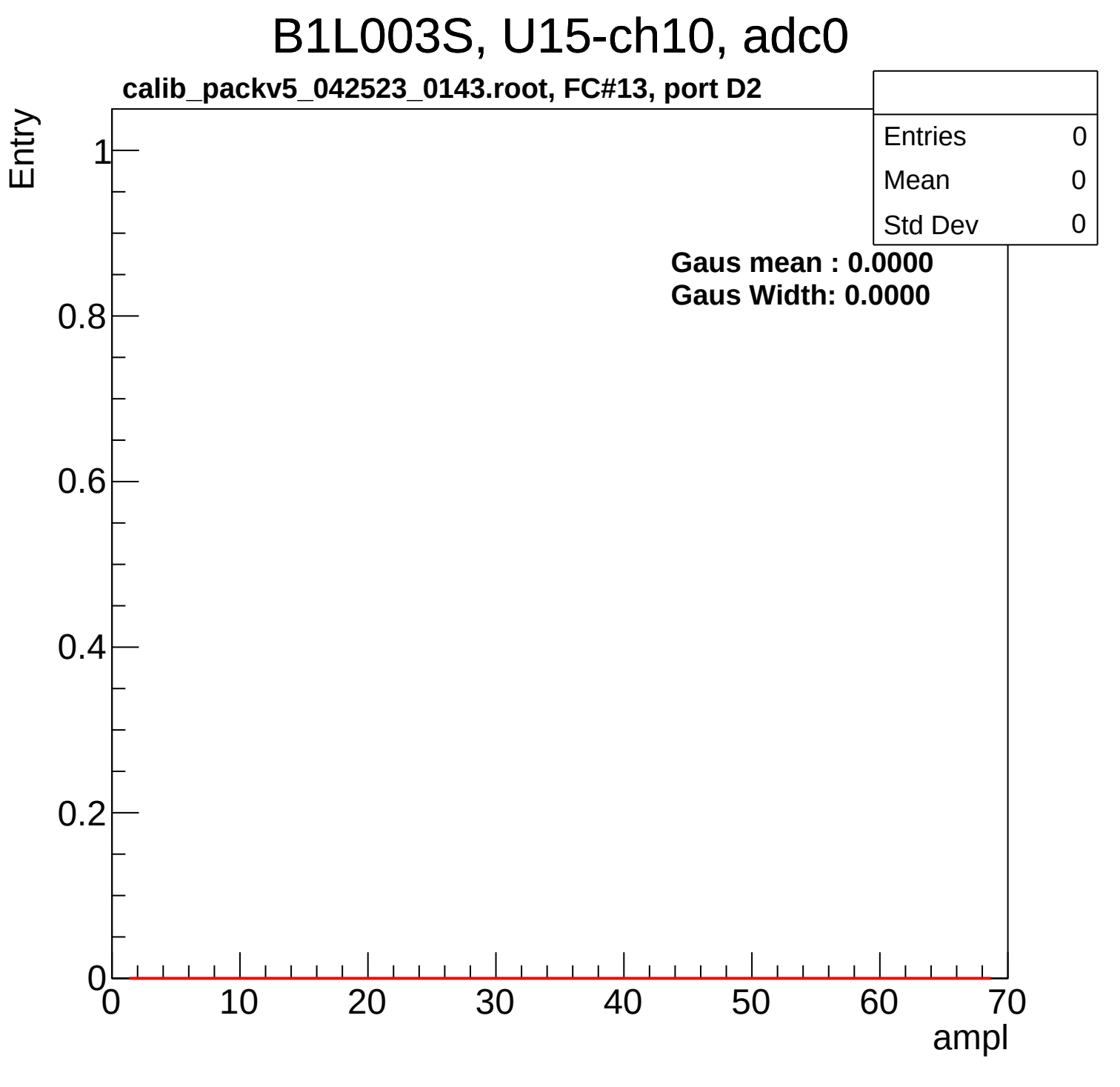
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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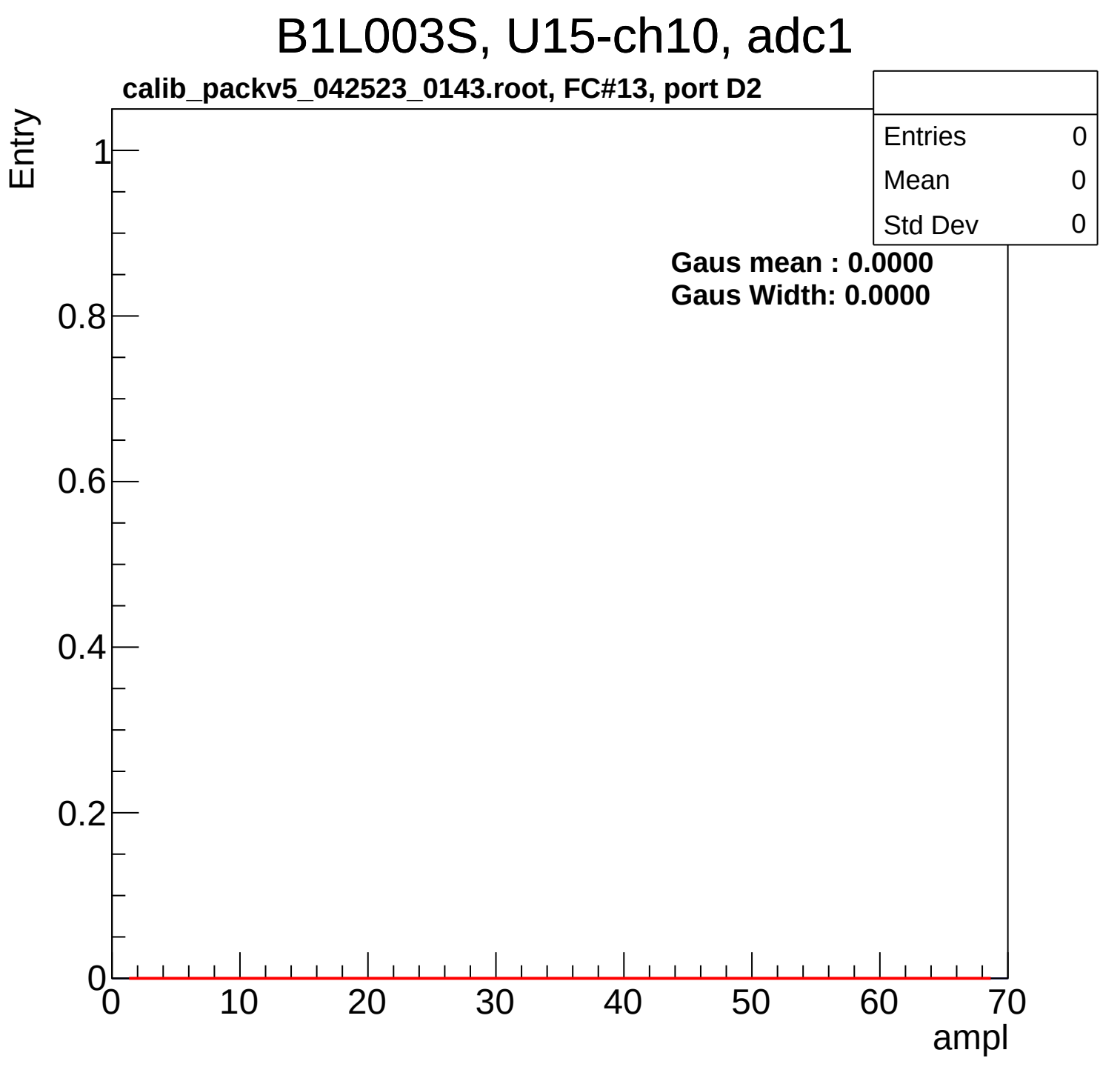
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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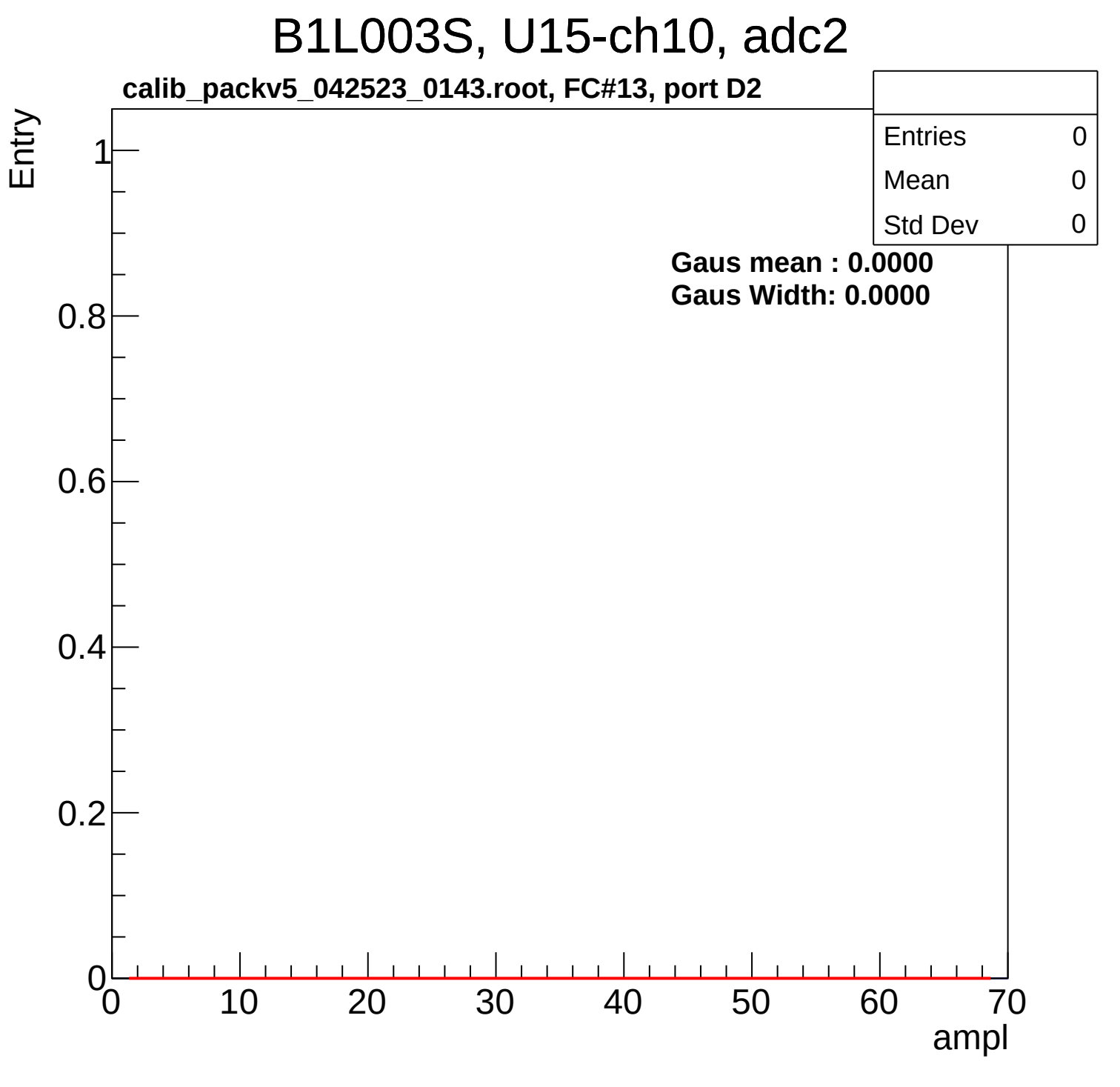
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Gaus Width: 0.0000
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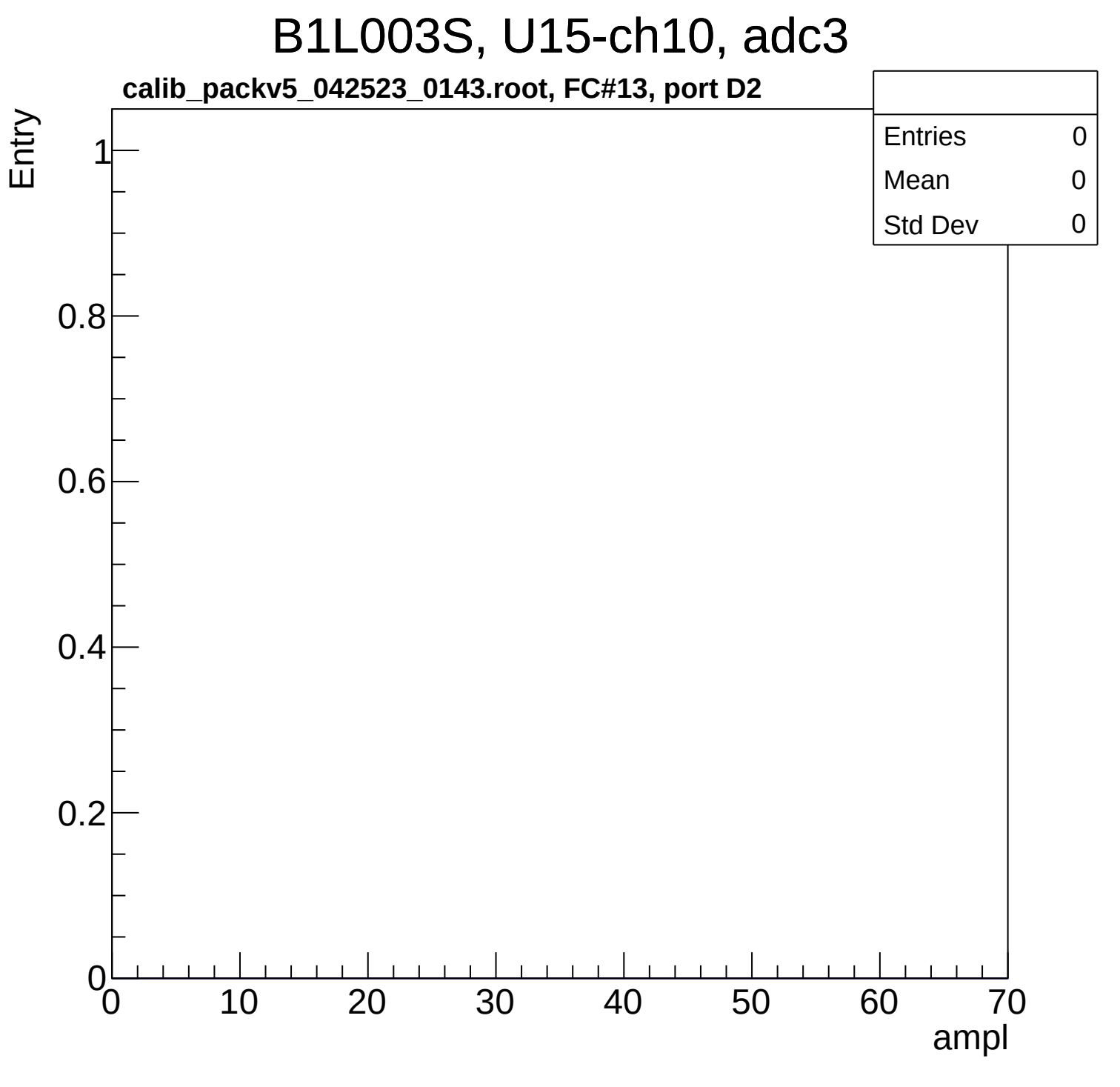
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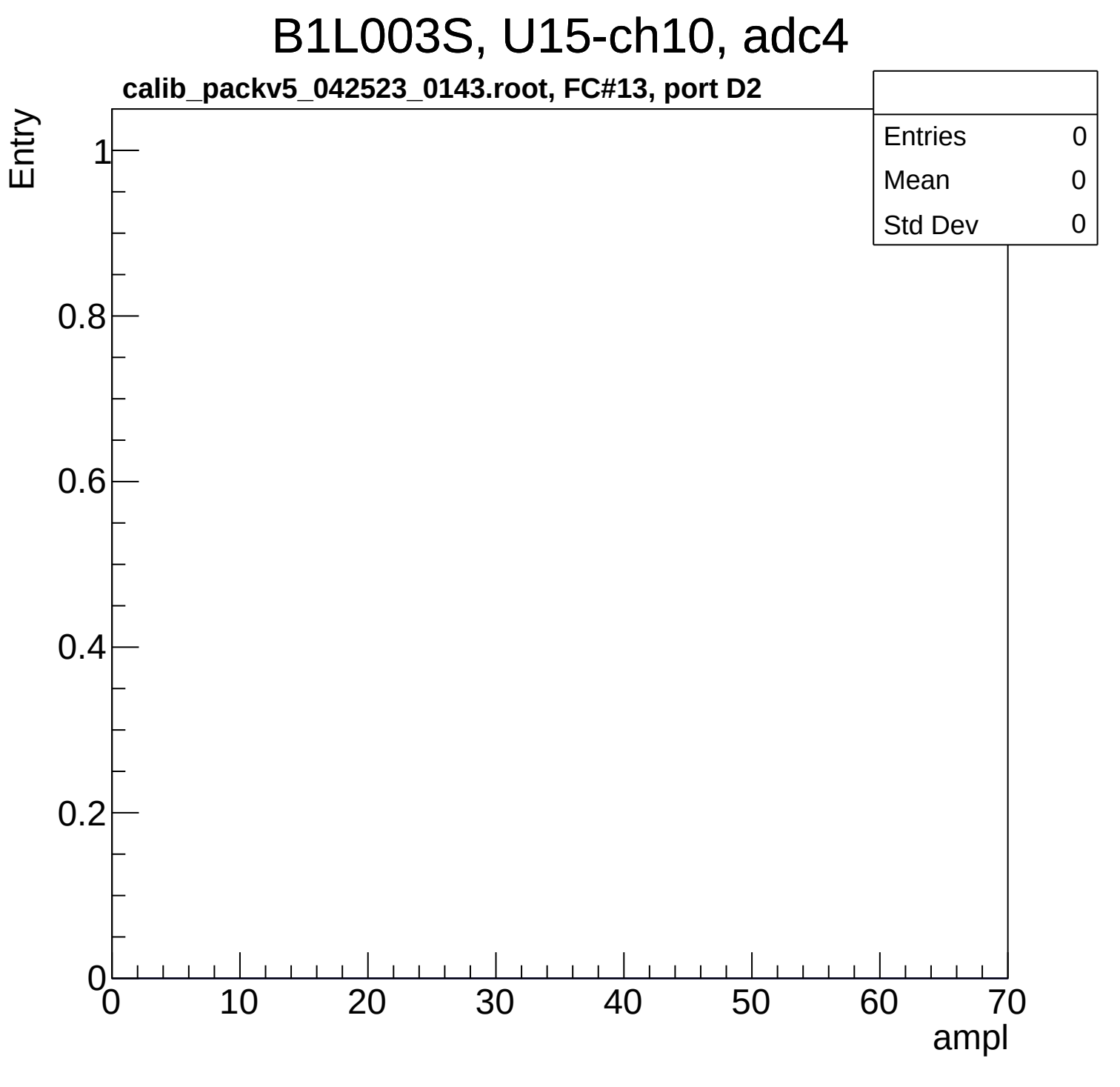
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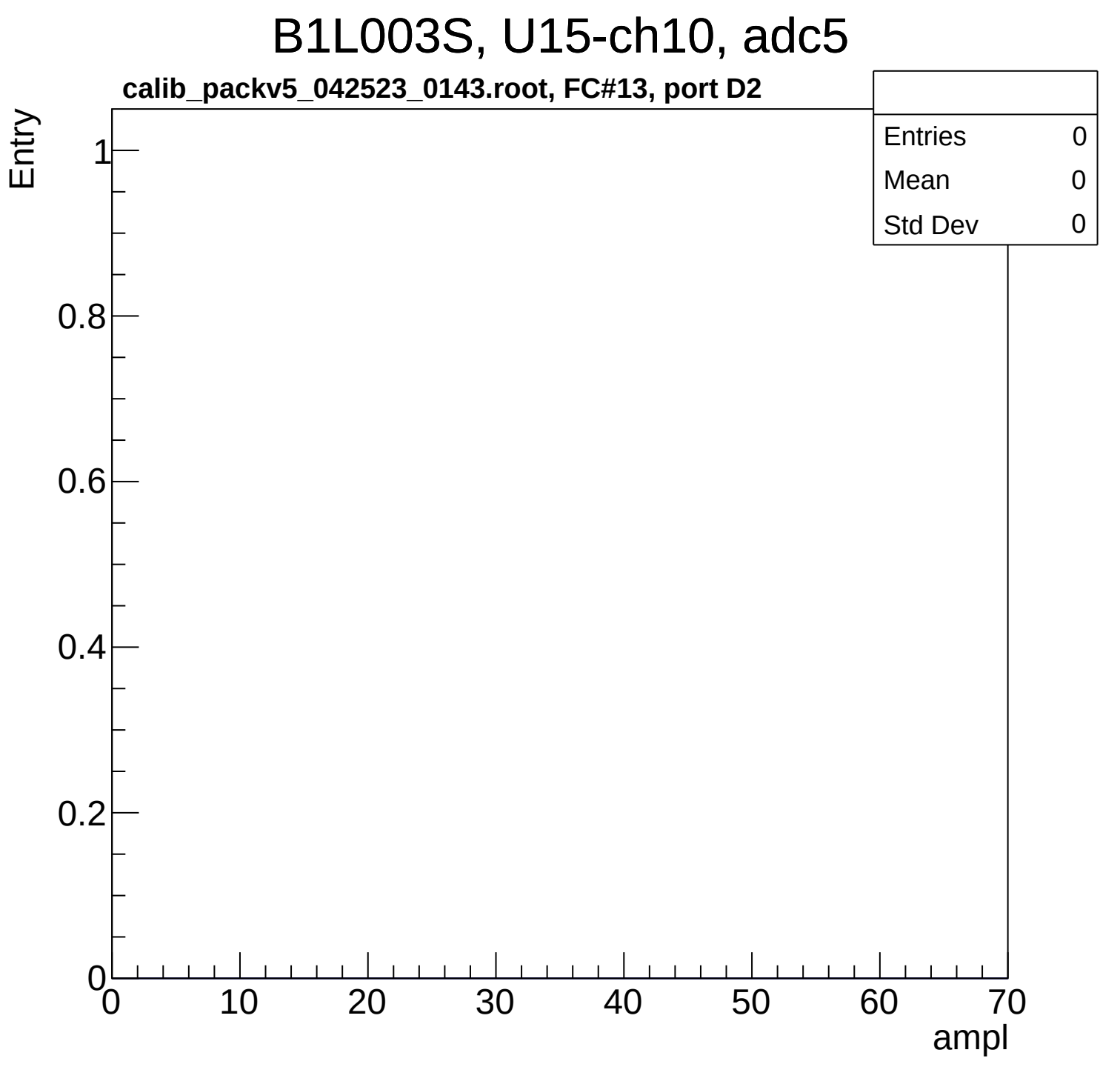
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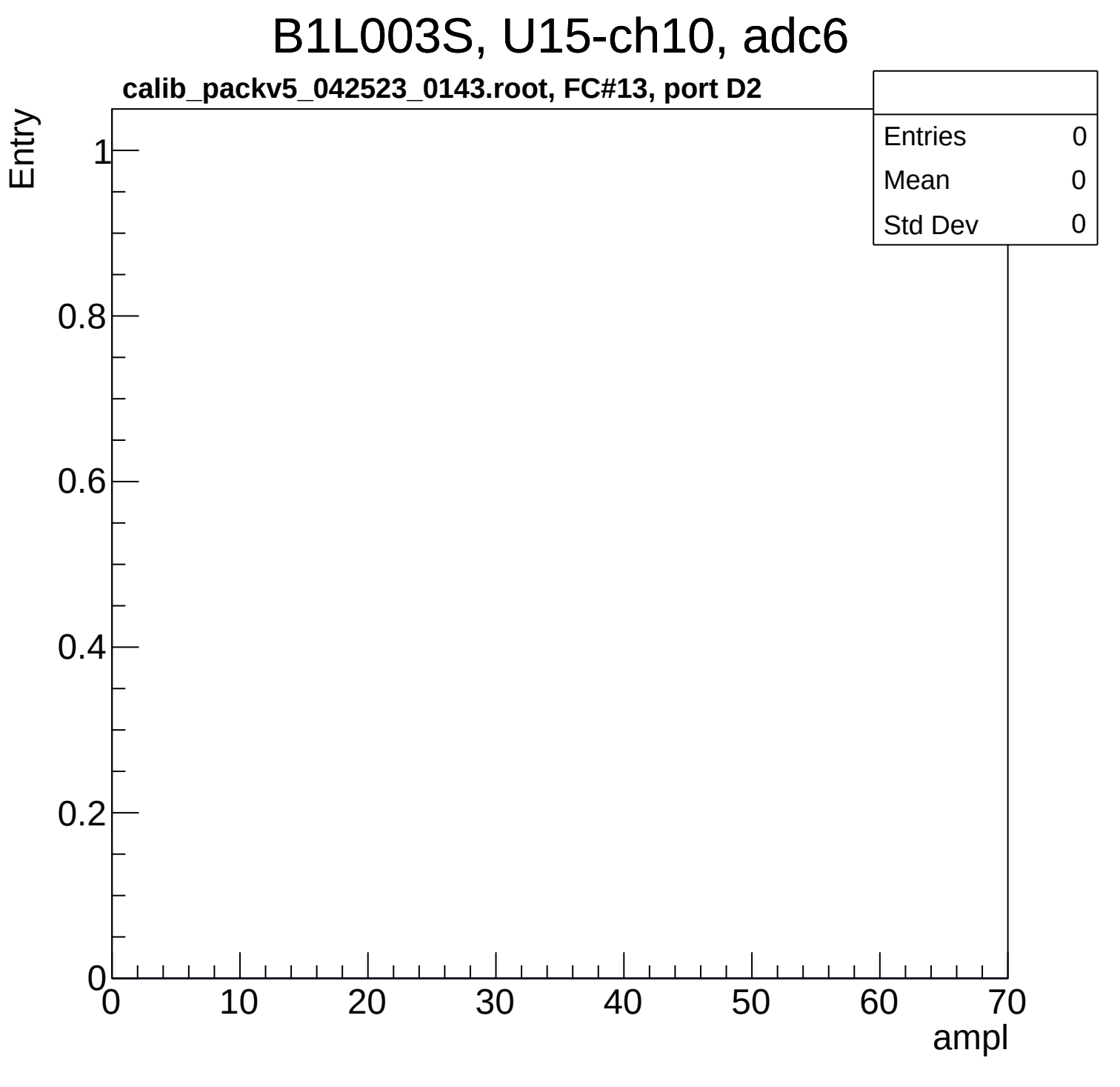
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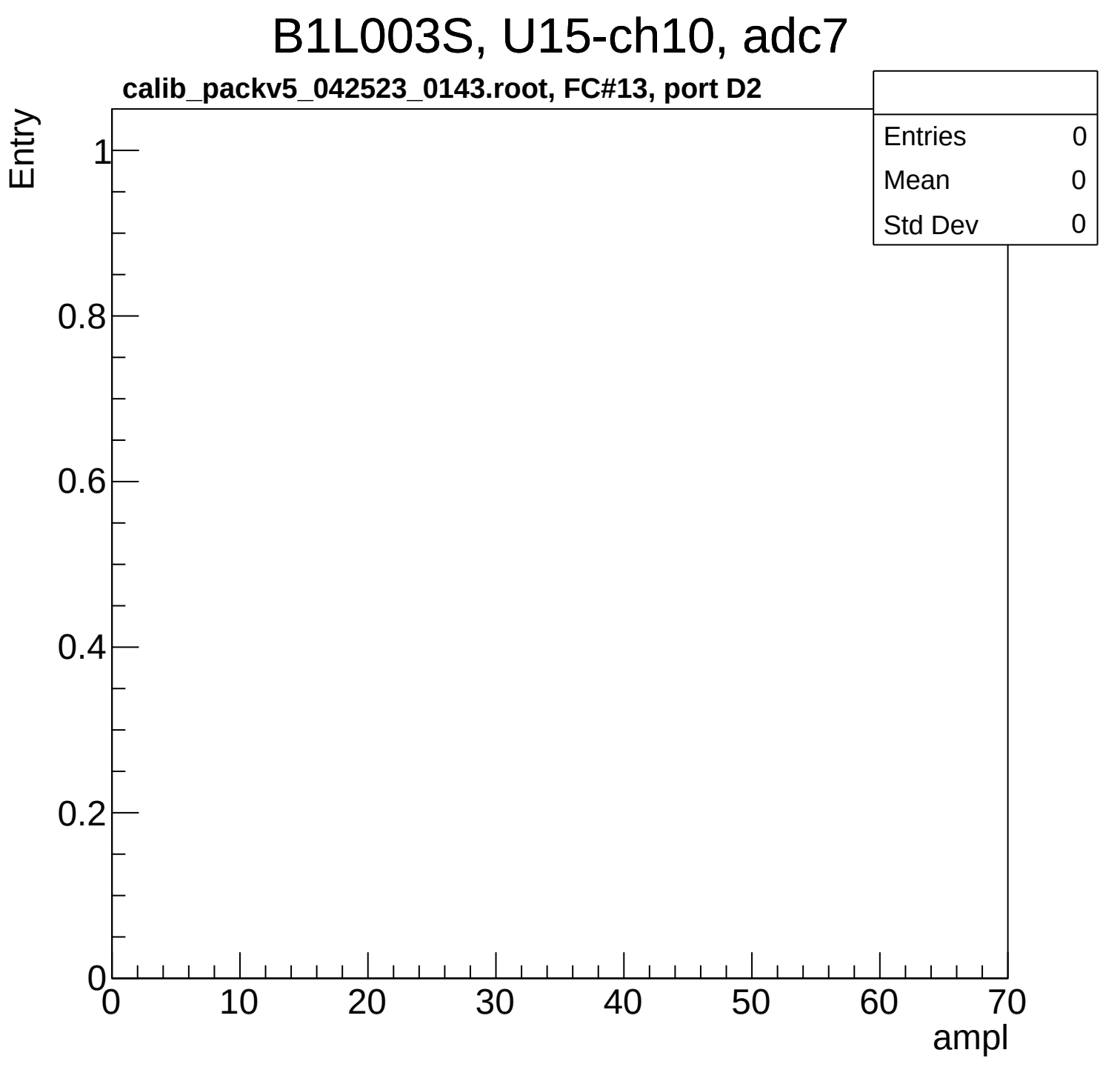
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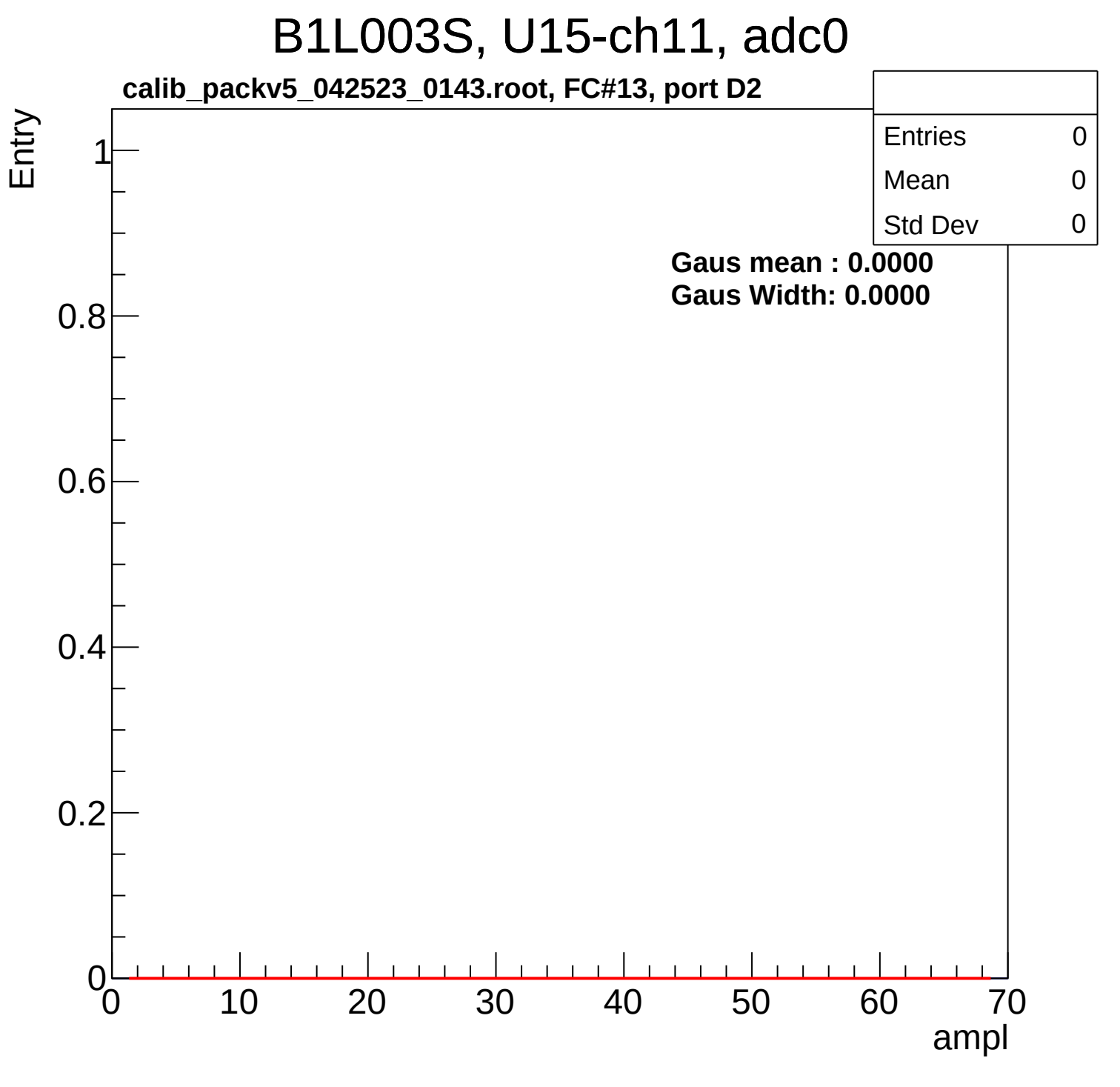
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Gaus Width: 0.0000
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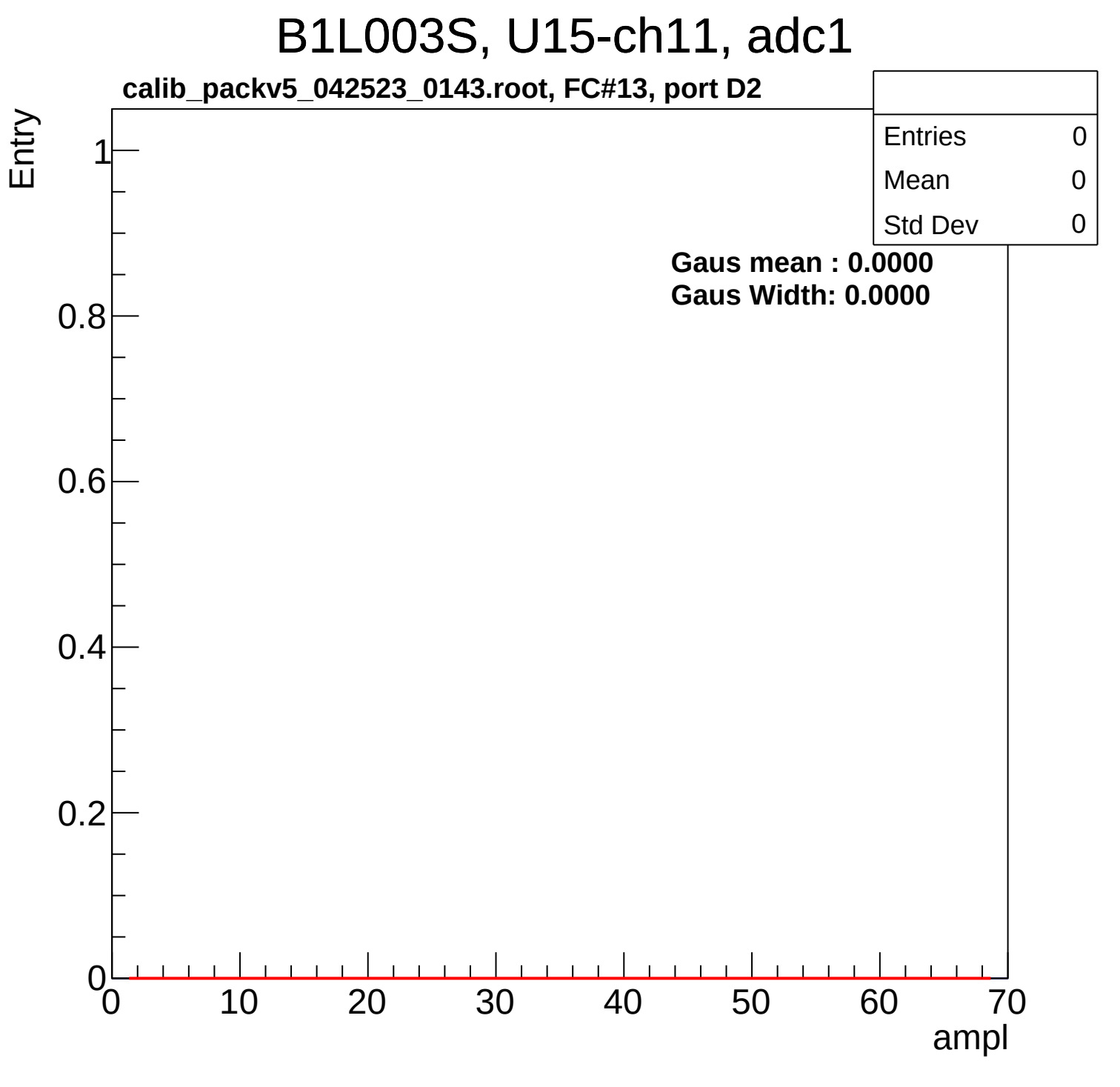
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Gaus Width: 0.0000
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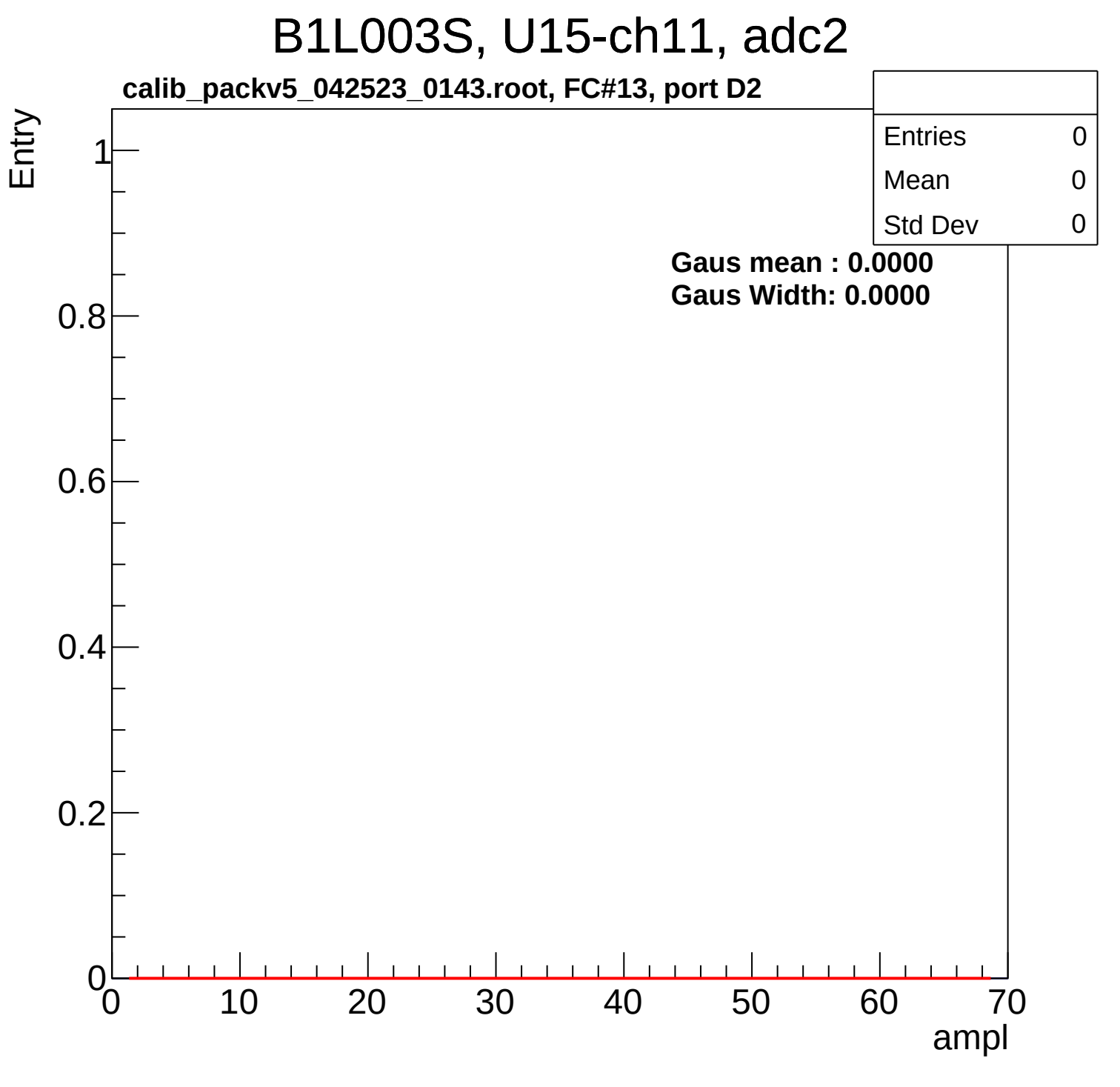
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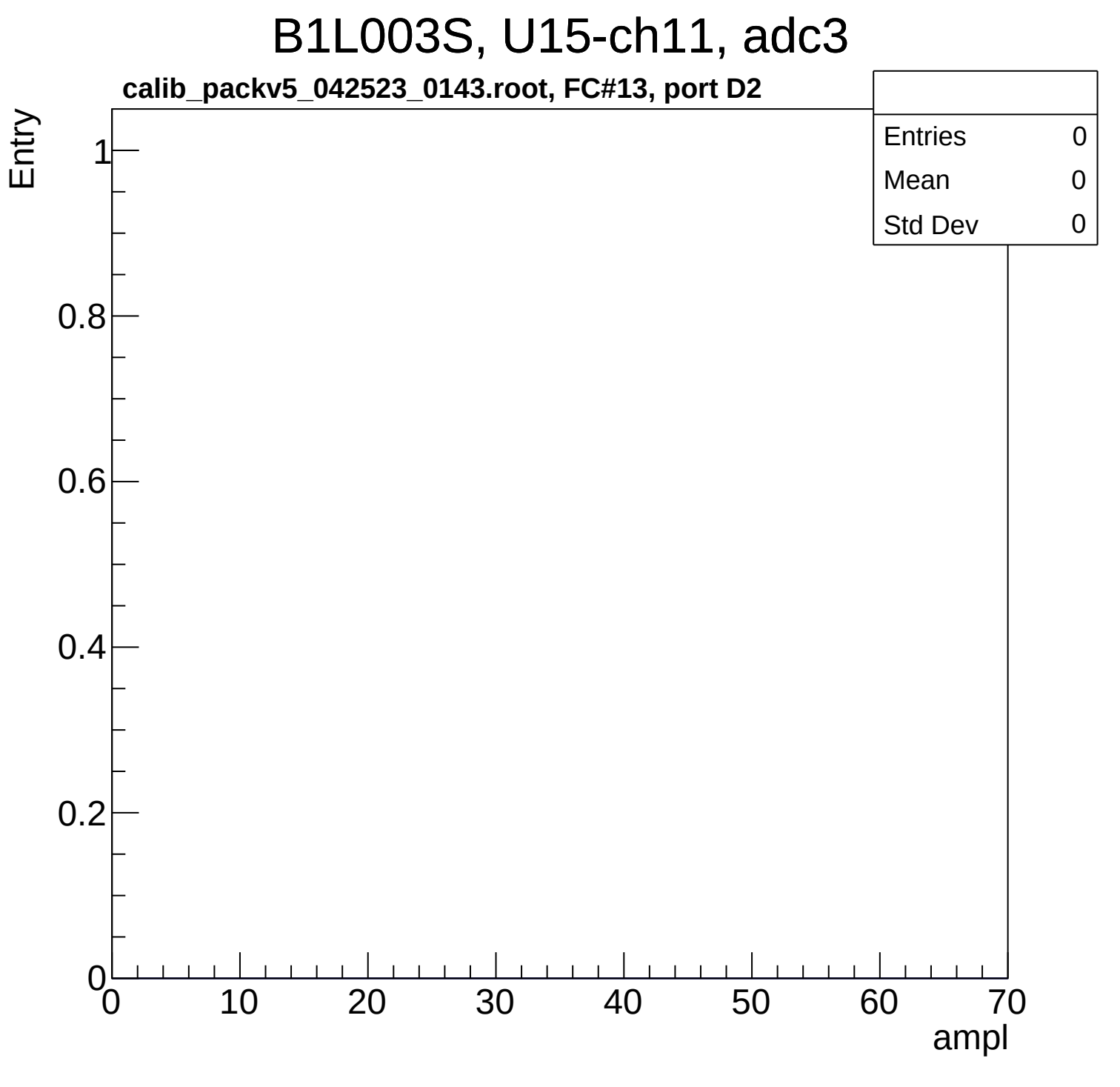
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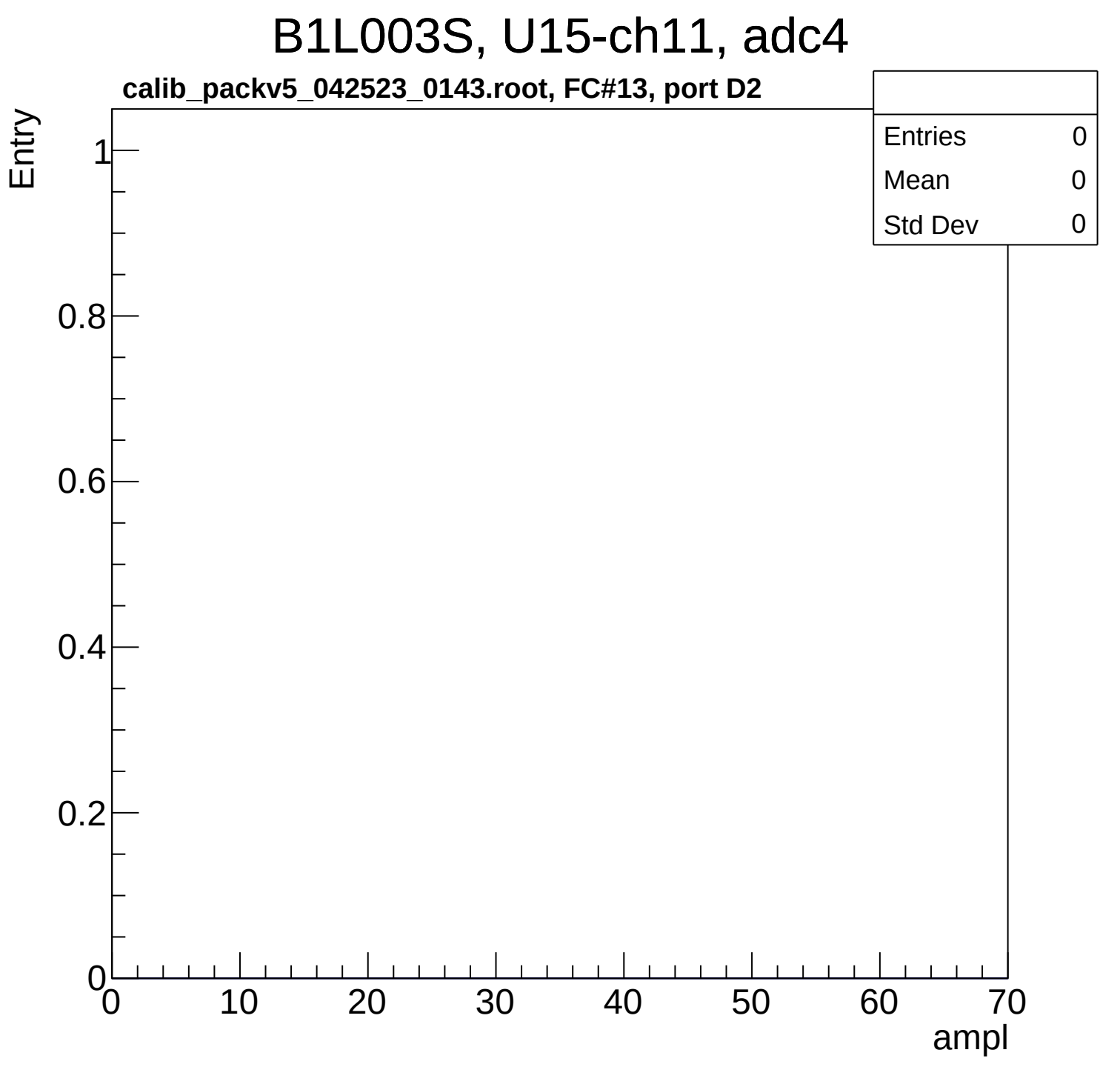
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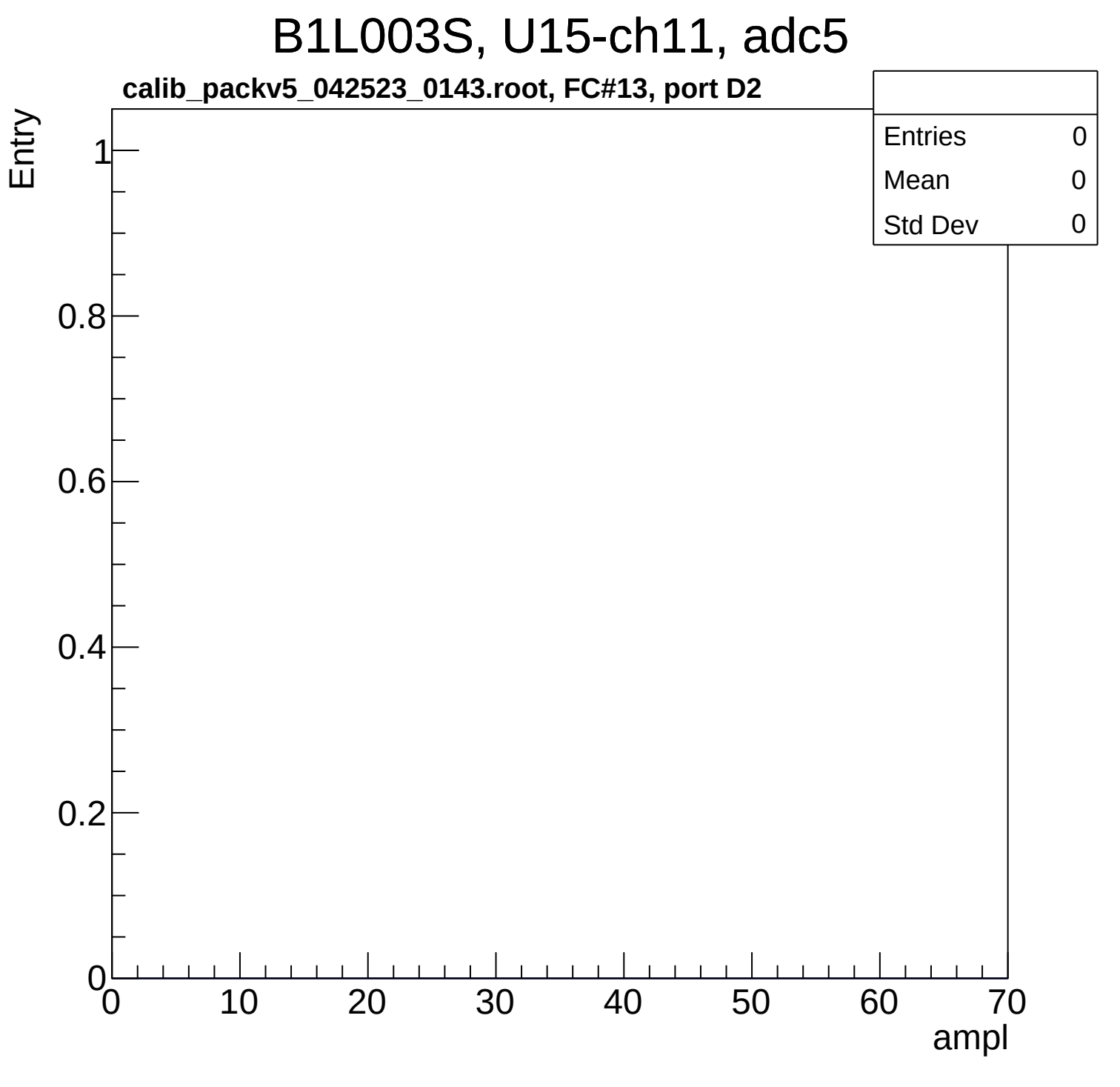
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Mean 0
Std Dev 0
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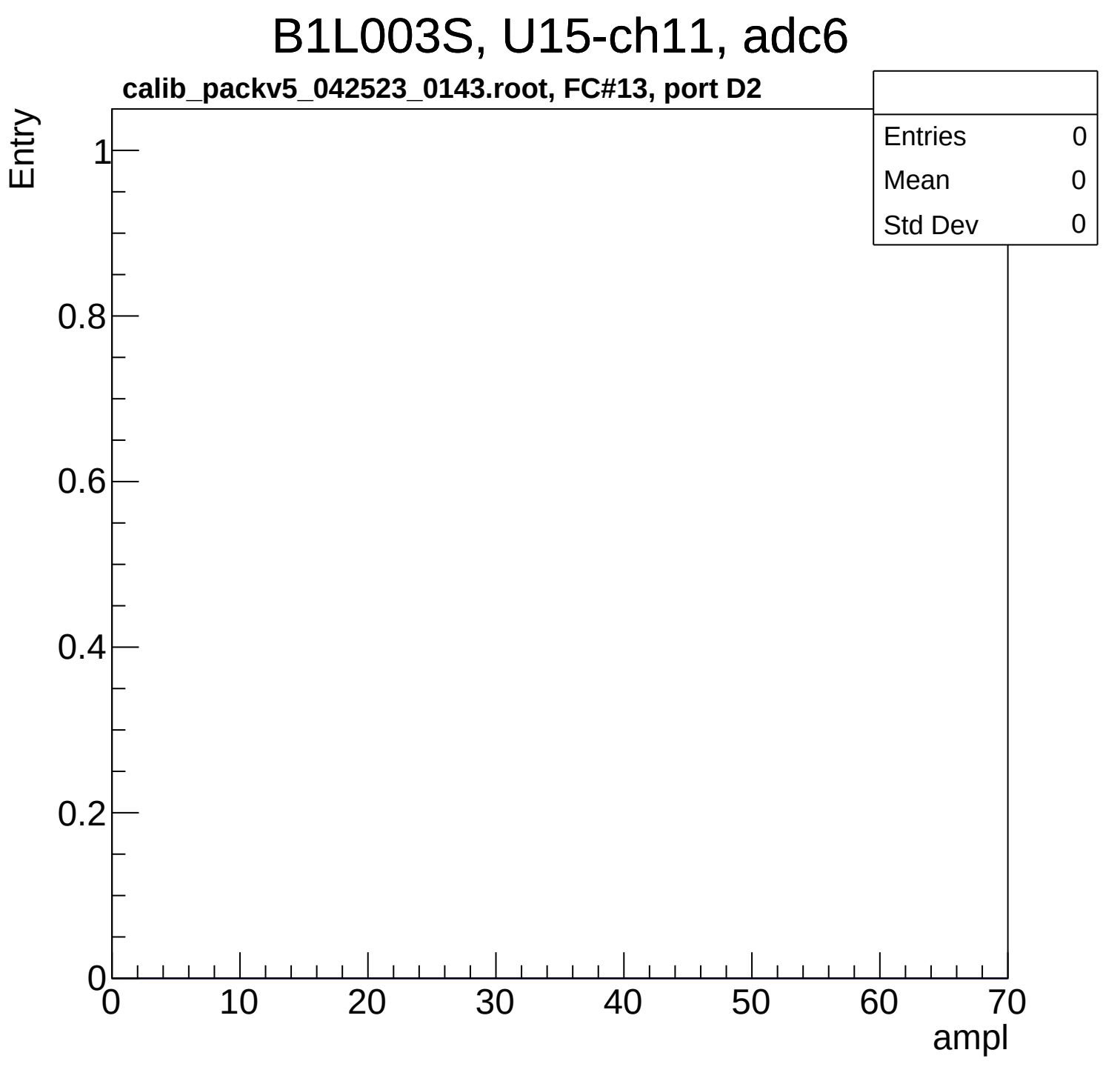
B1L003S, U15-ch11, adc6
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Mean 0
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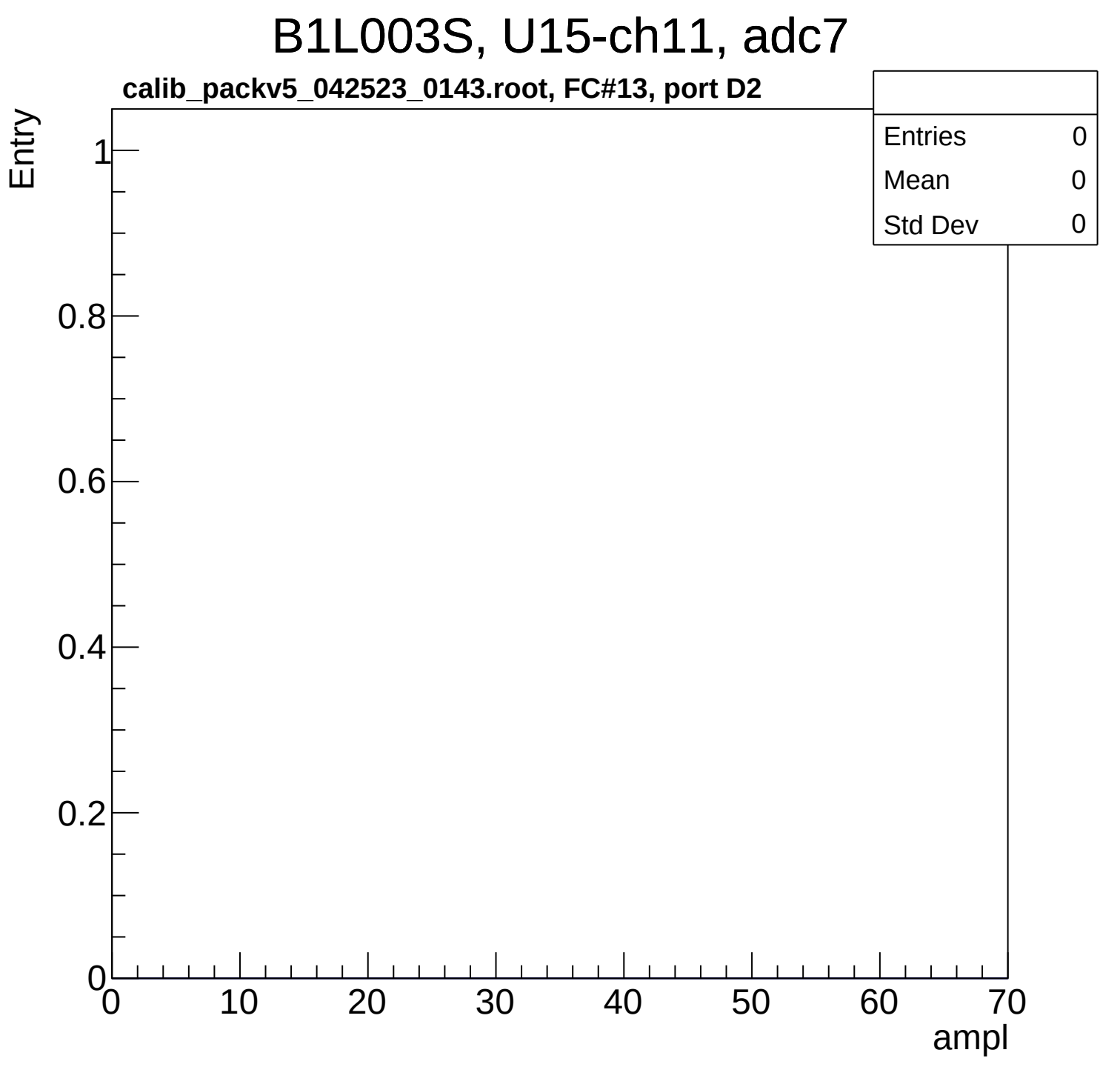
B1L003S, U15-ch11, adc7
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Mean 0
Std Dev 0
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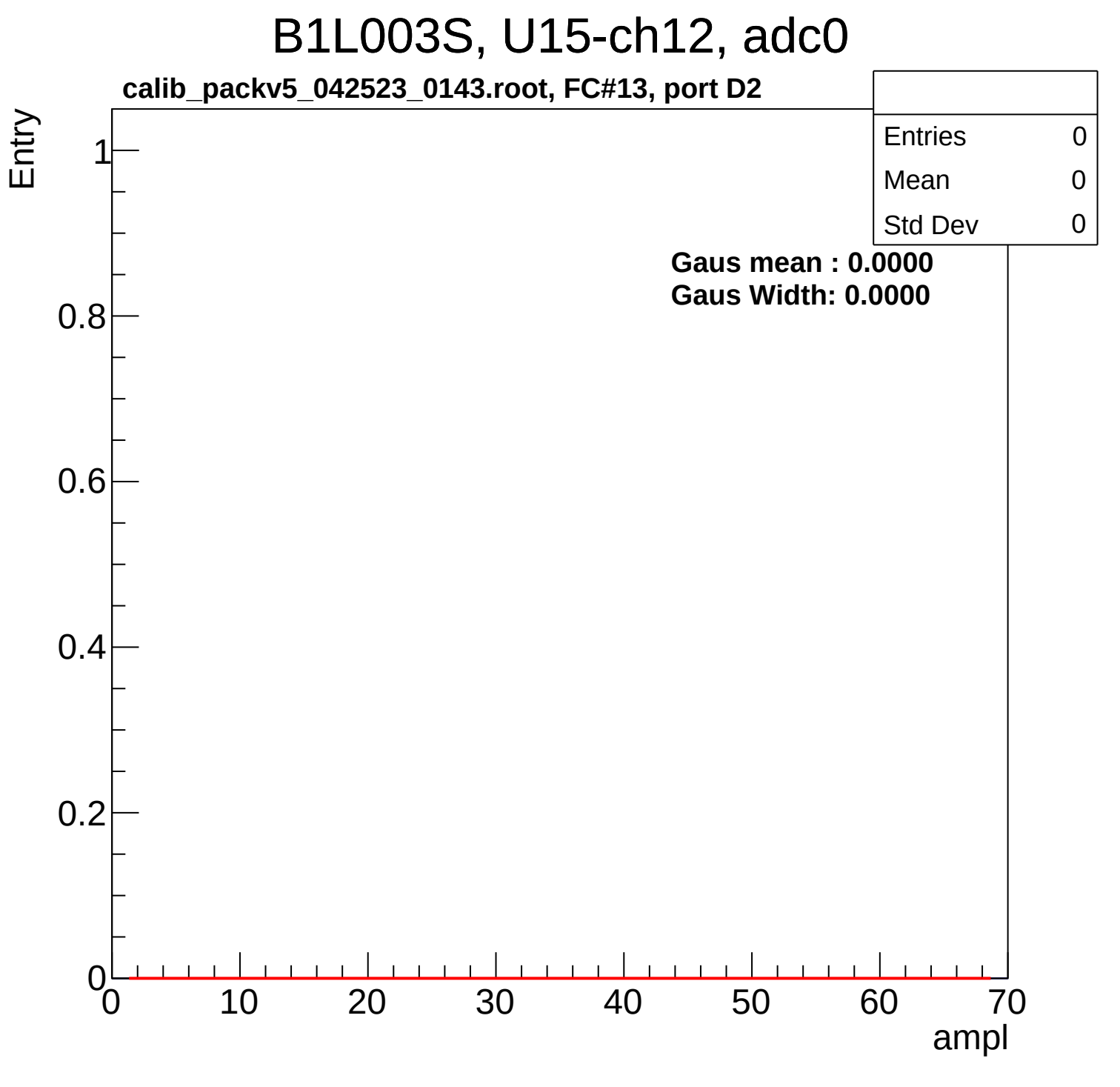
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Entries 0
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Std Dev 0
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Gaus Width: 0.0000
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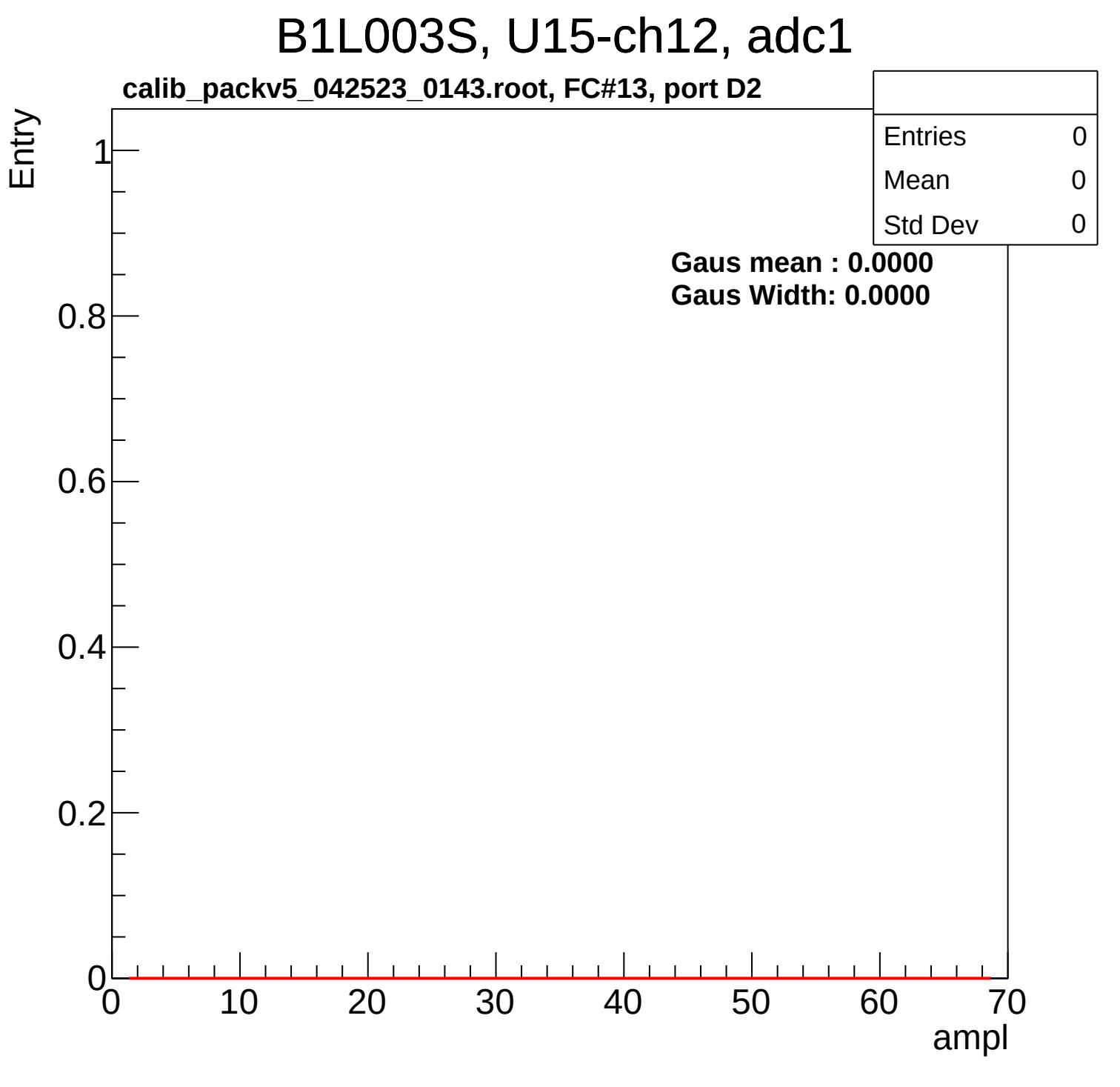
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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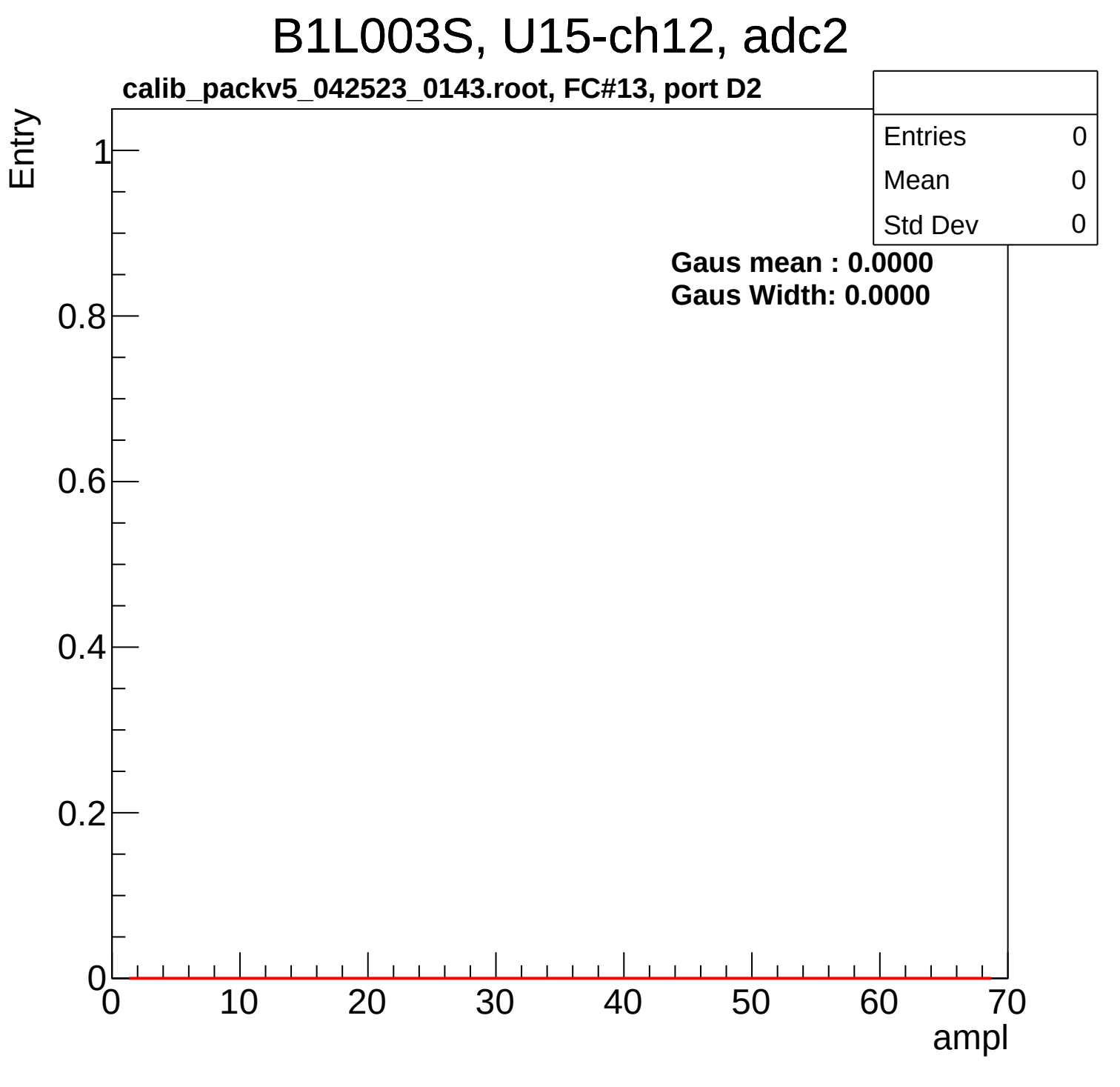
ampl


B1L003S, U15-ch12, adc2
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Std Dev 0
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Gaus Width: 0.0000
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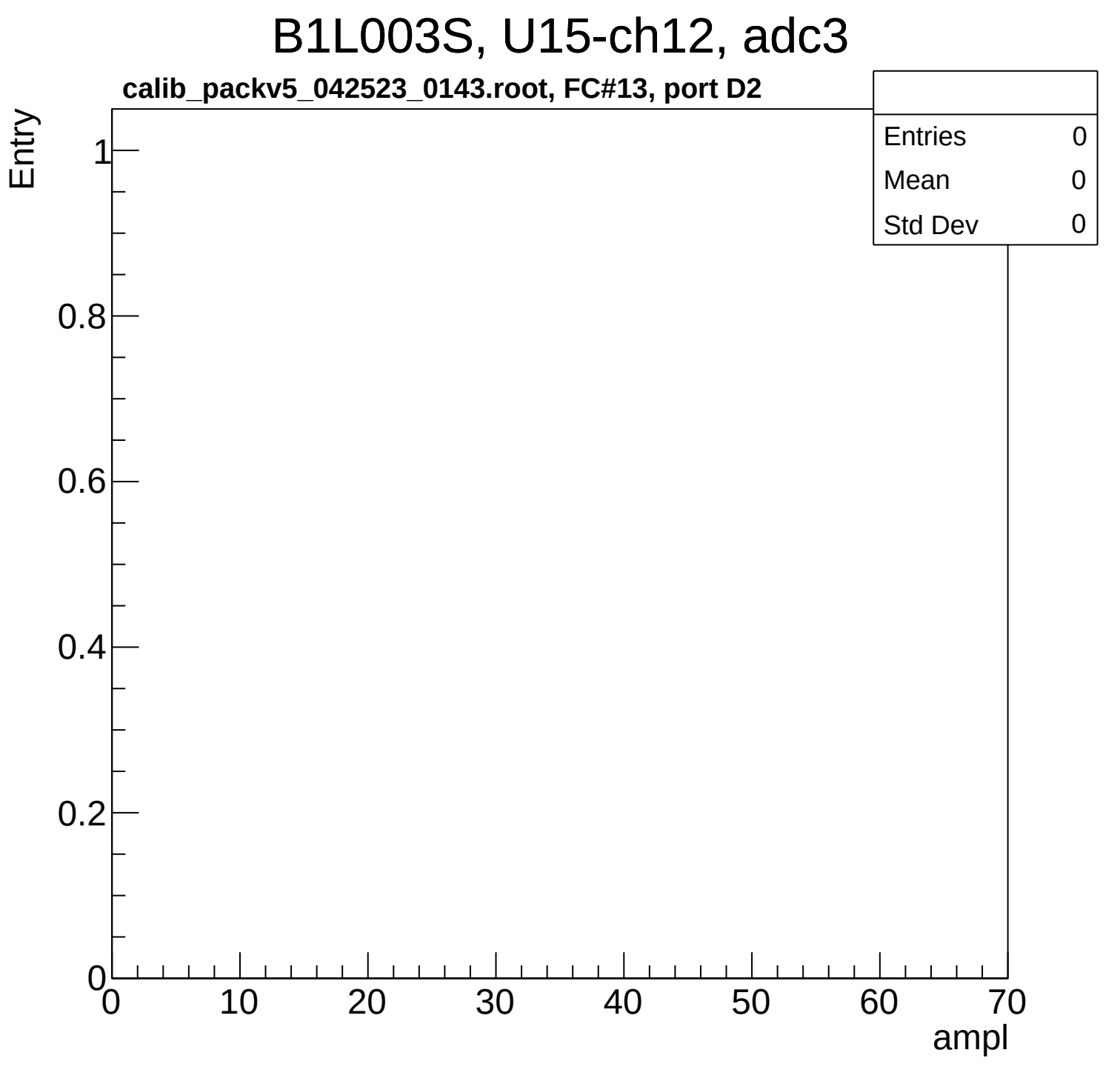
ampl


B1L003S, U15-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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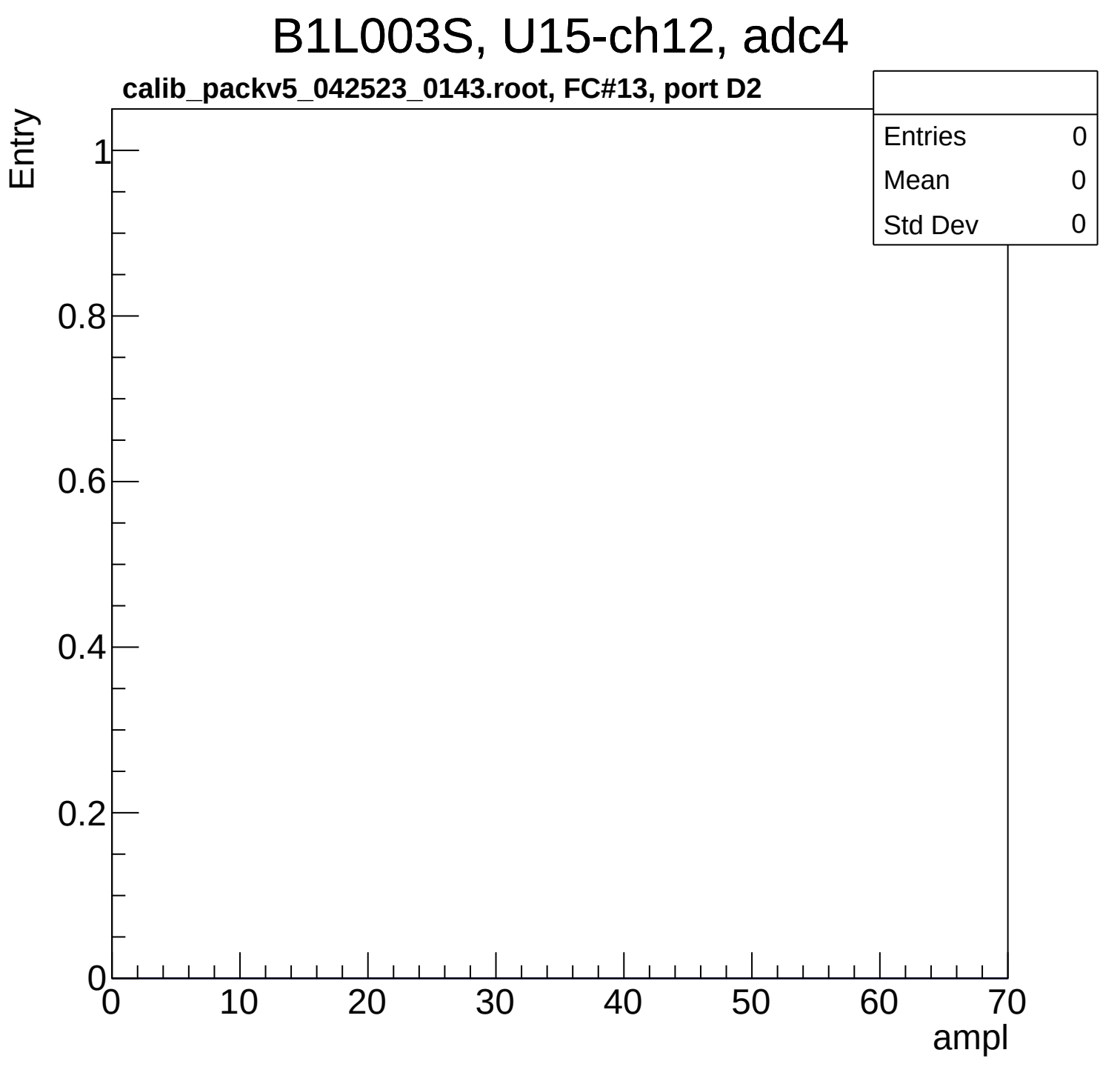
ampl


B1L003S, U15-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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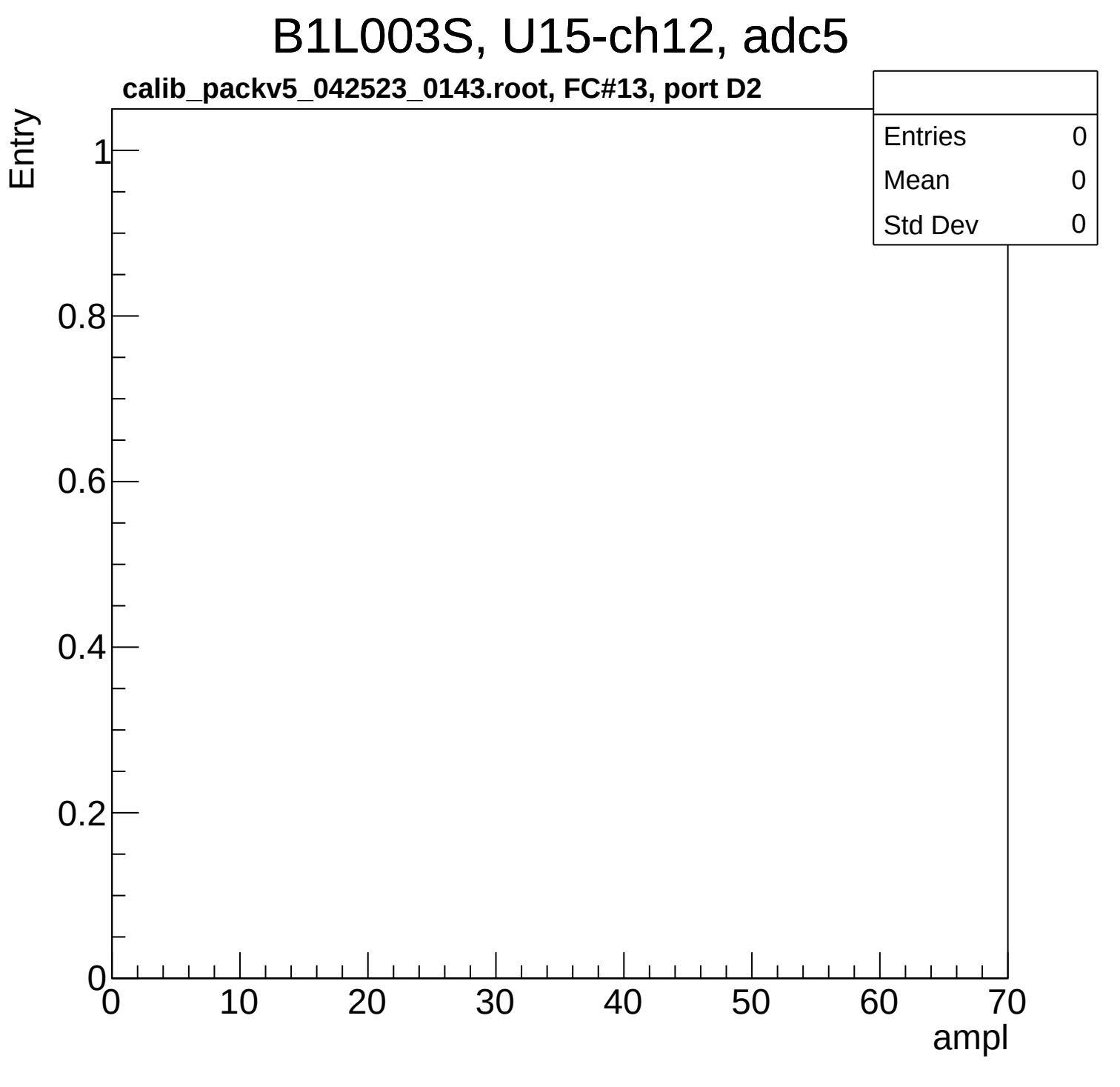
B1L003S, U15-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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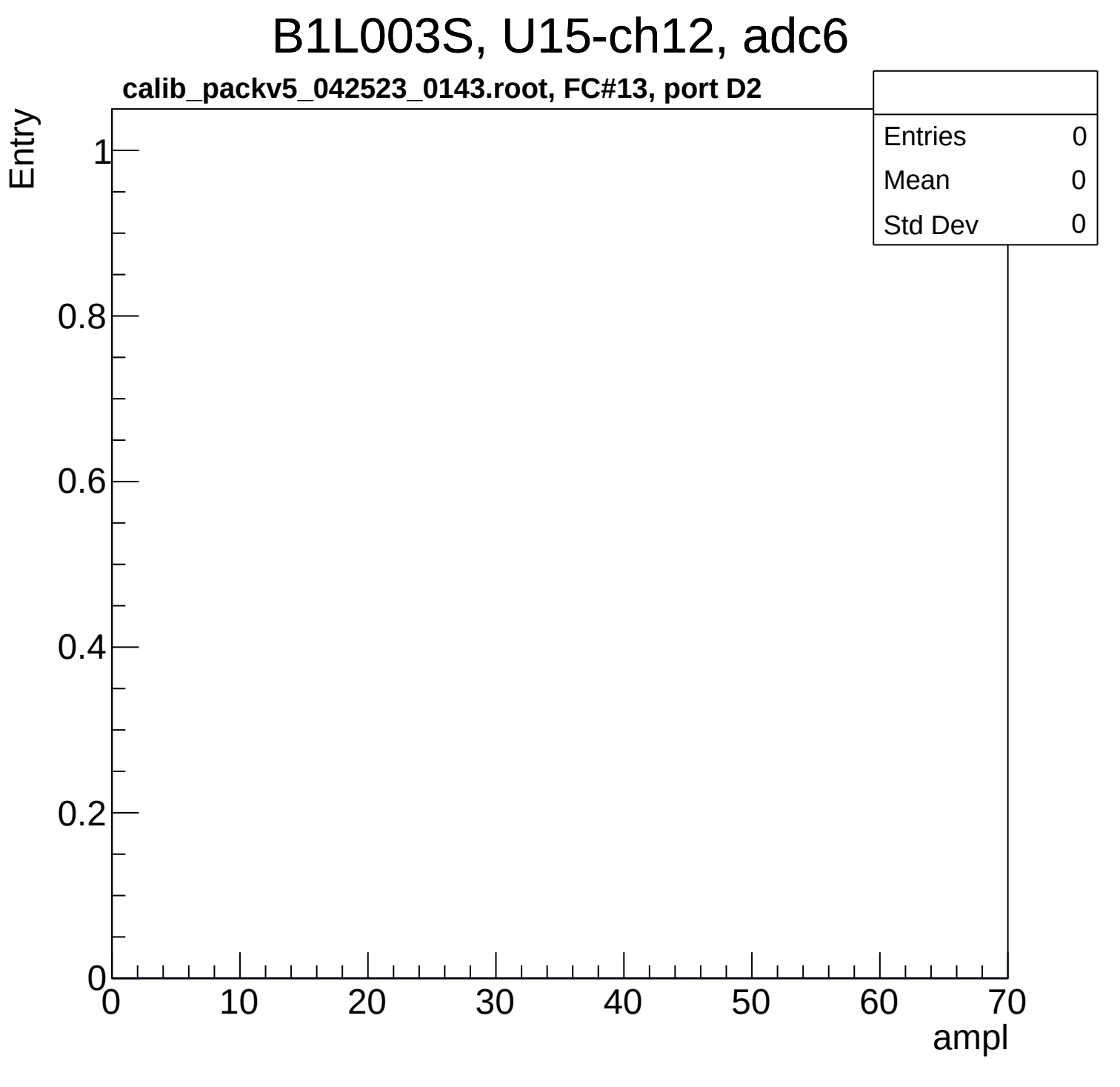
B1L003S, U15-ch12, adc6
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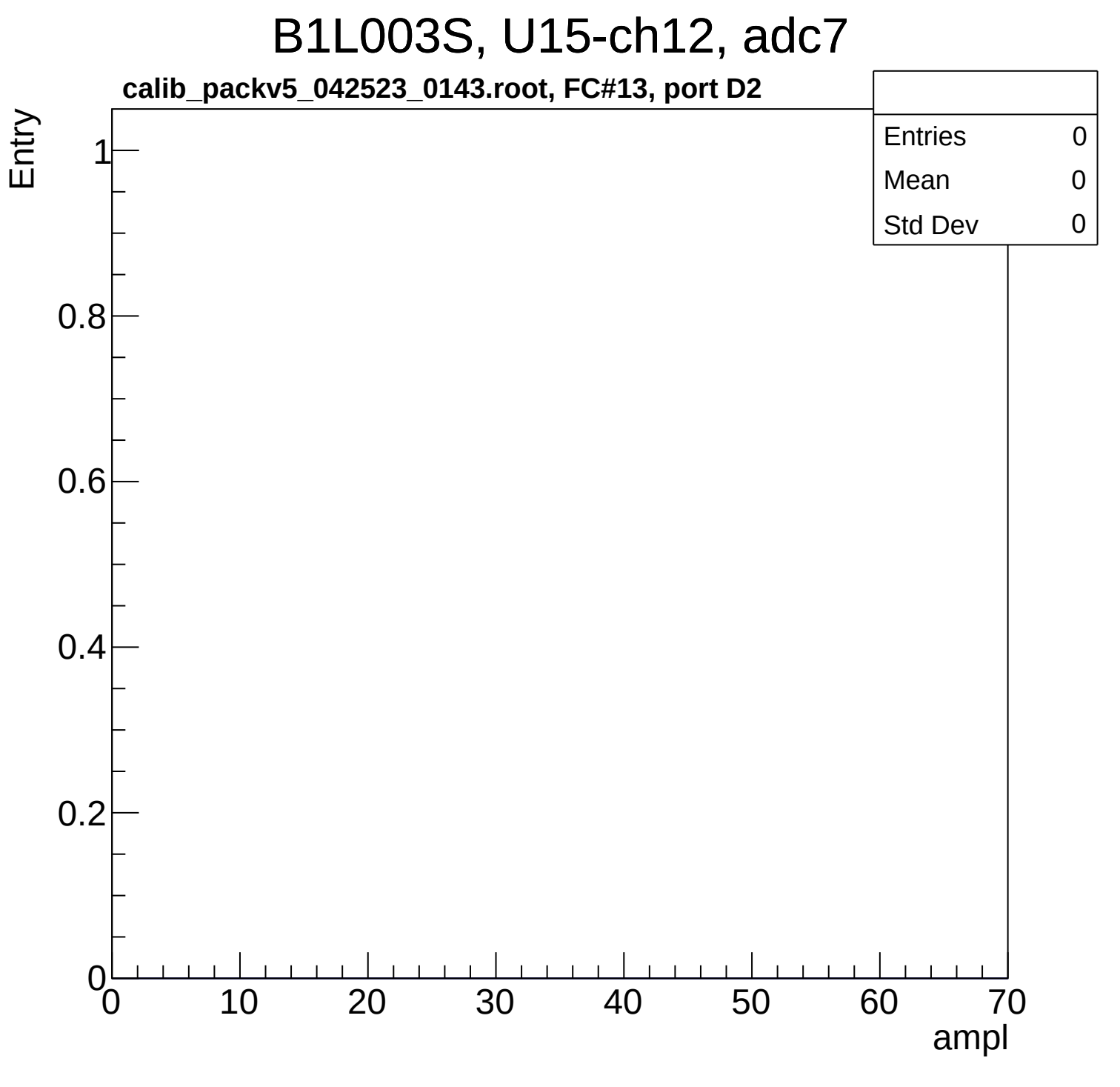
ampl


B1L003S, U15-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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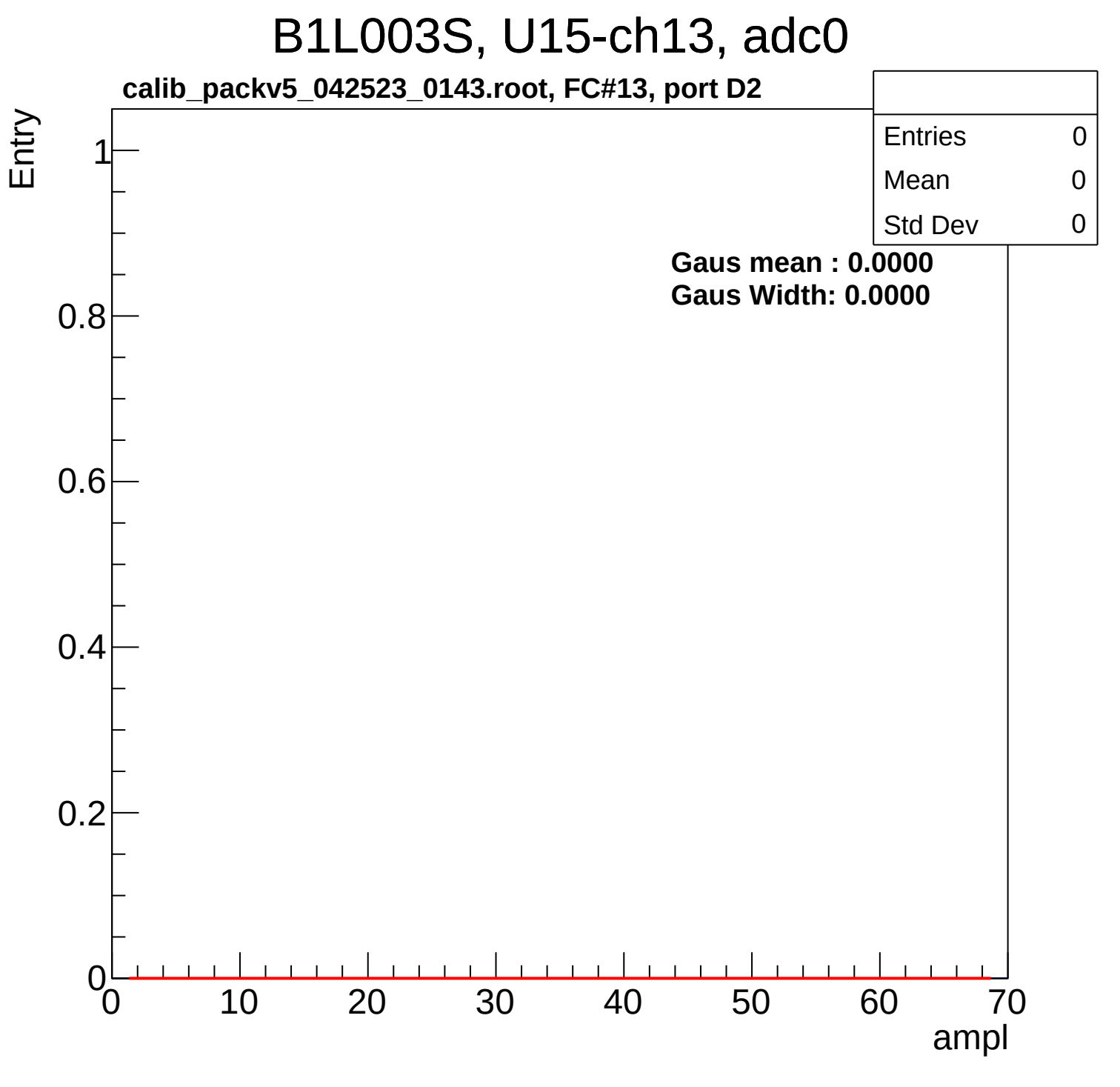
ampl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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.4
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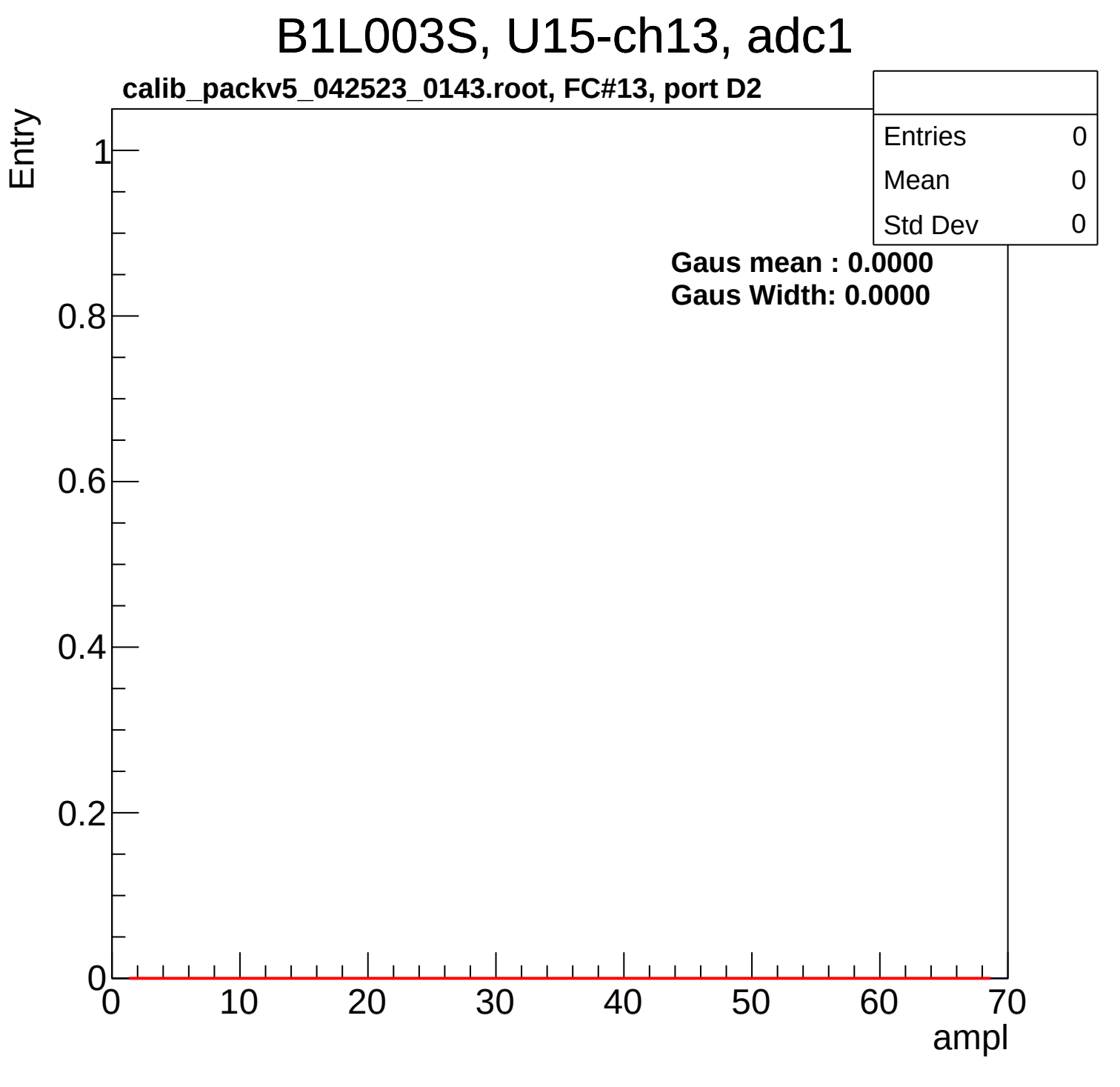
ampl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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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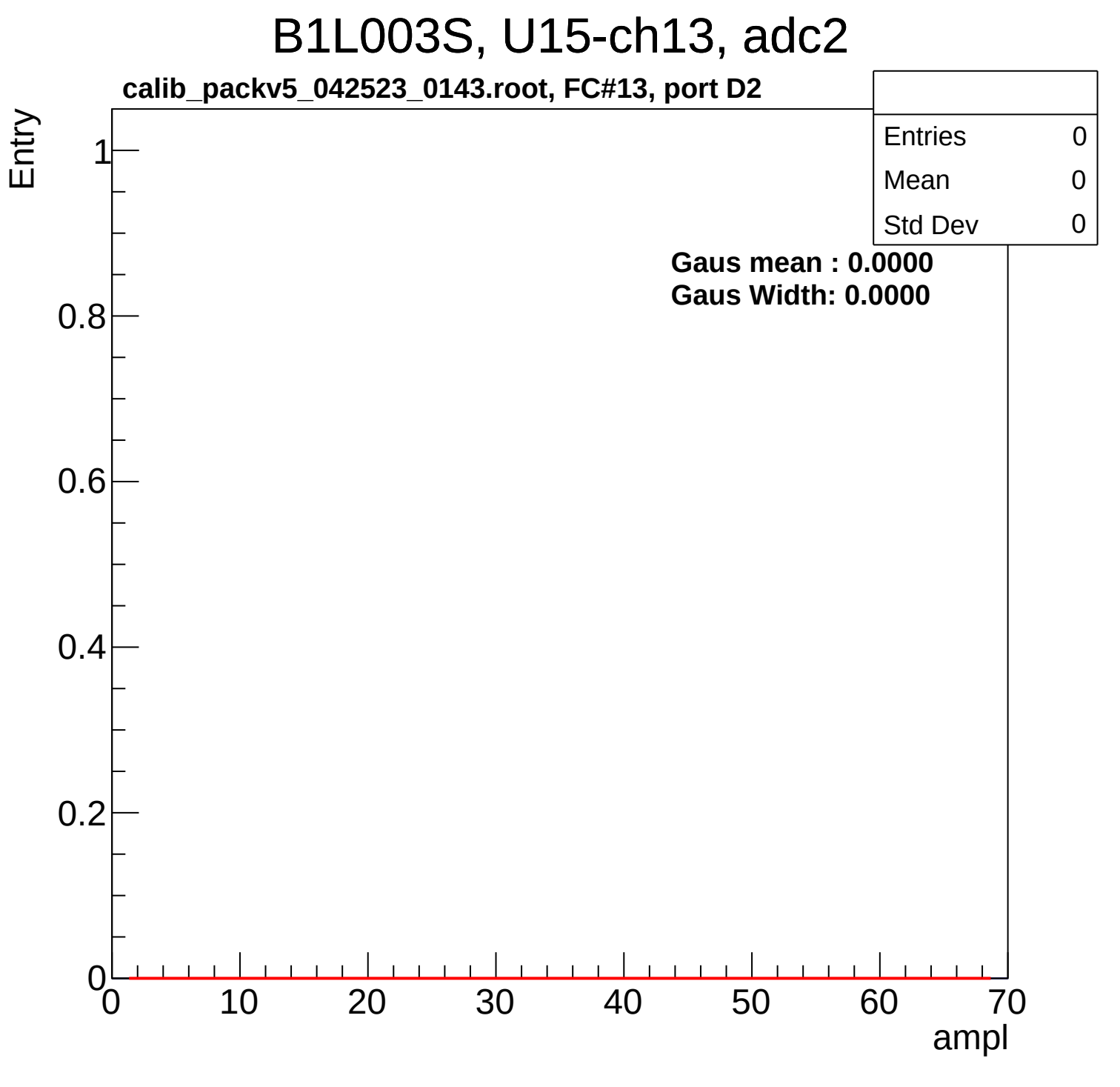
B1L003S, U15-ch13, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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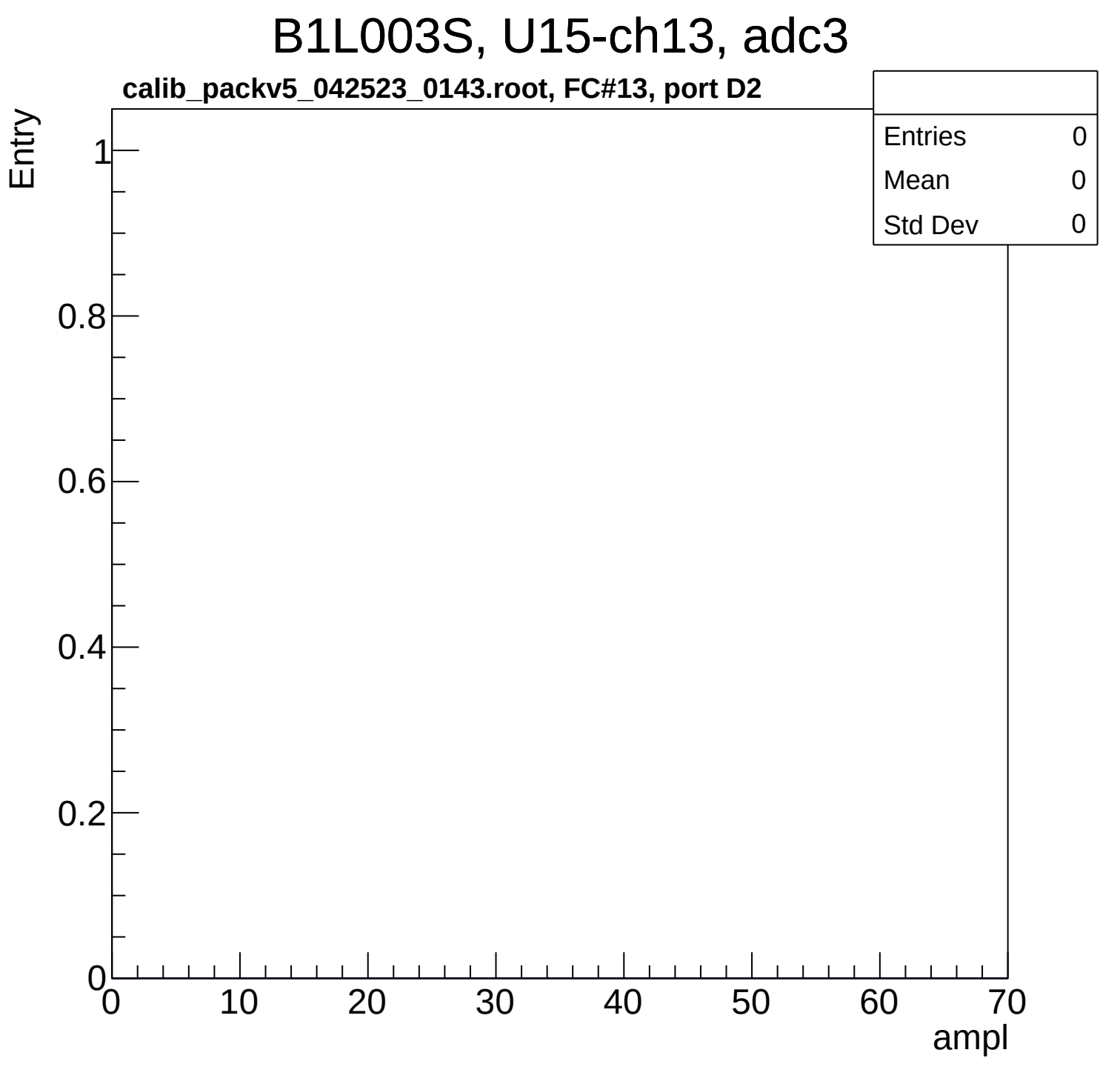
B1L003S, U15-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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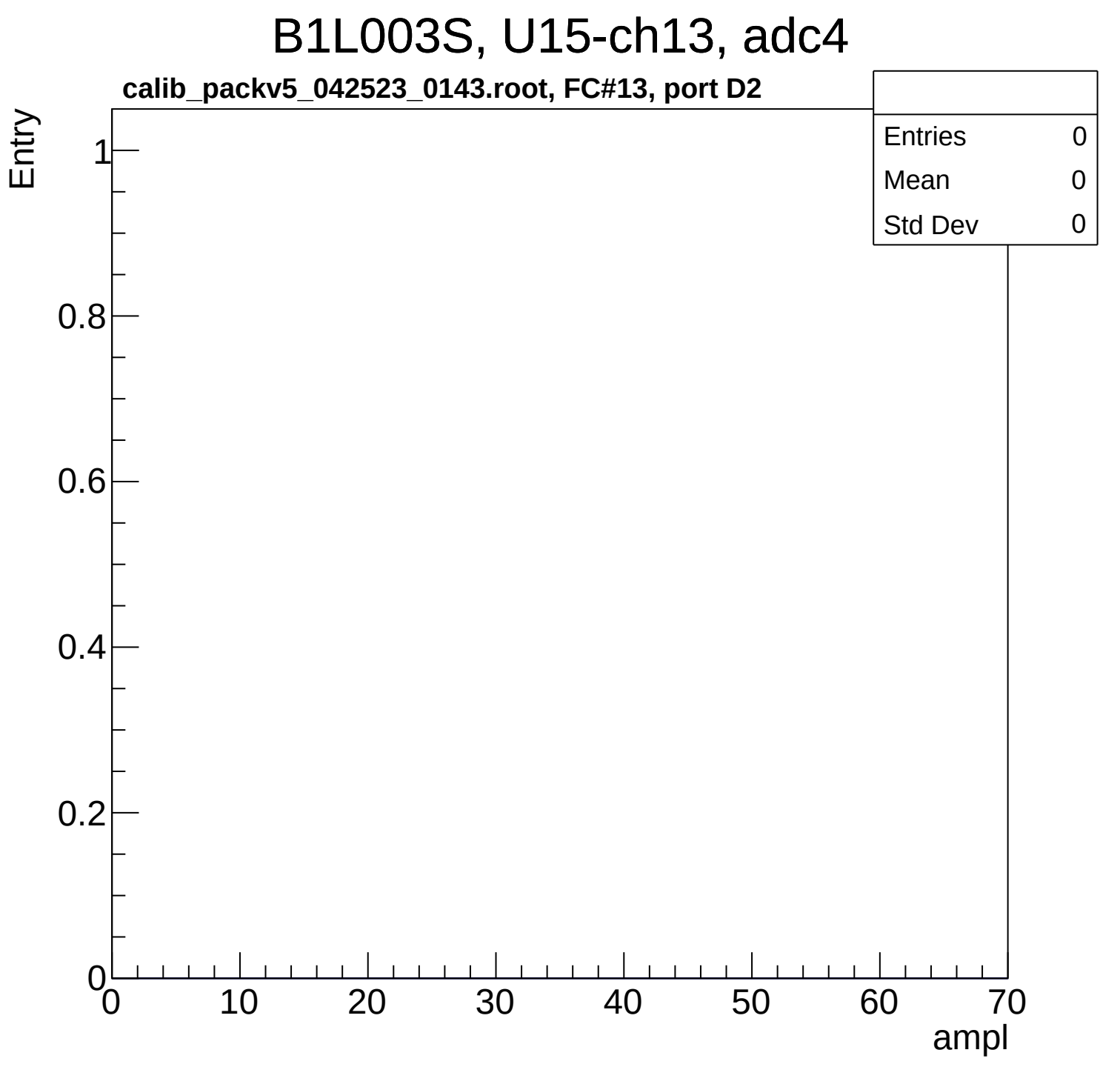
ampl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Entries 0
Mean 0
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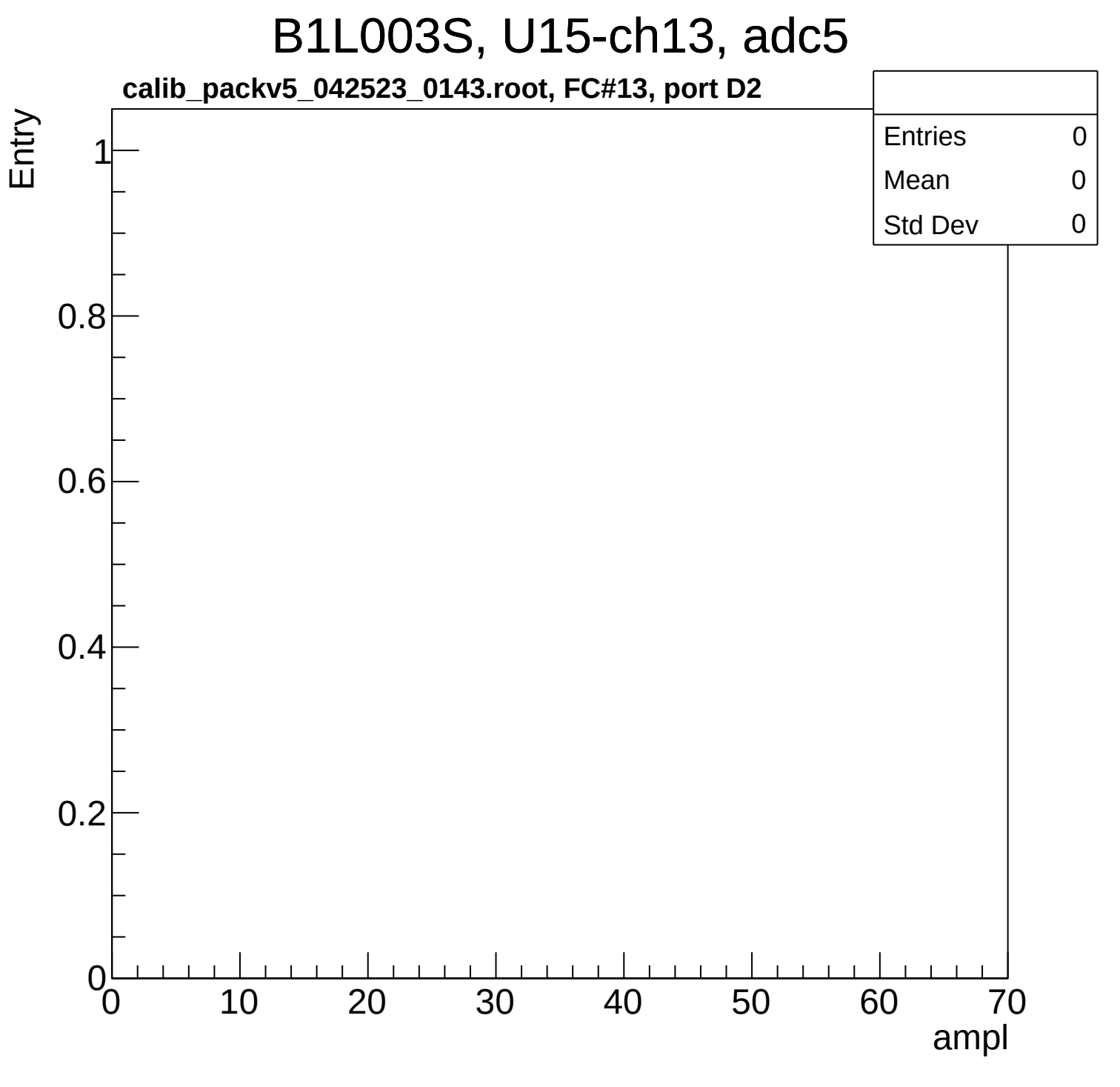
ampl


B1L003S, U15-ch13, adc5
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Std Dev 0
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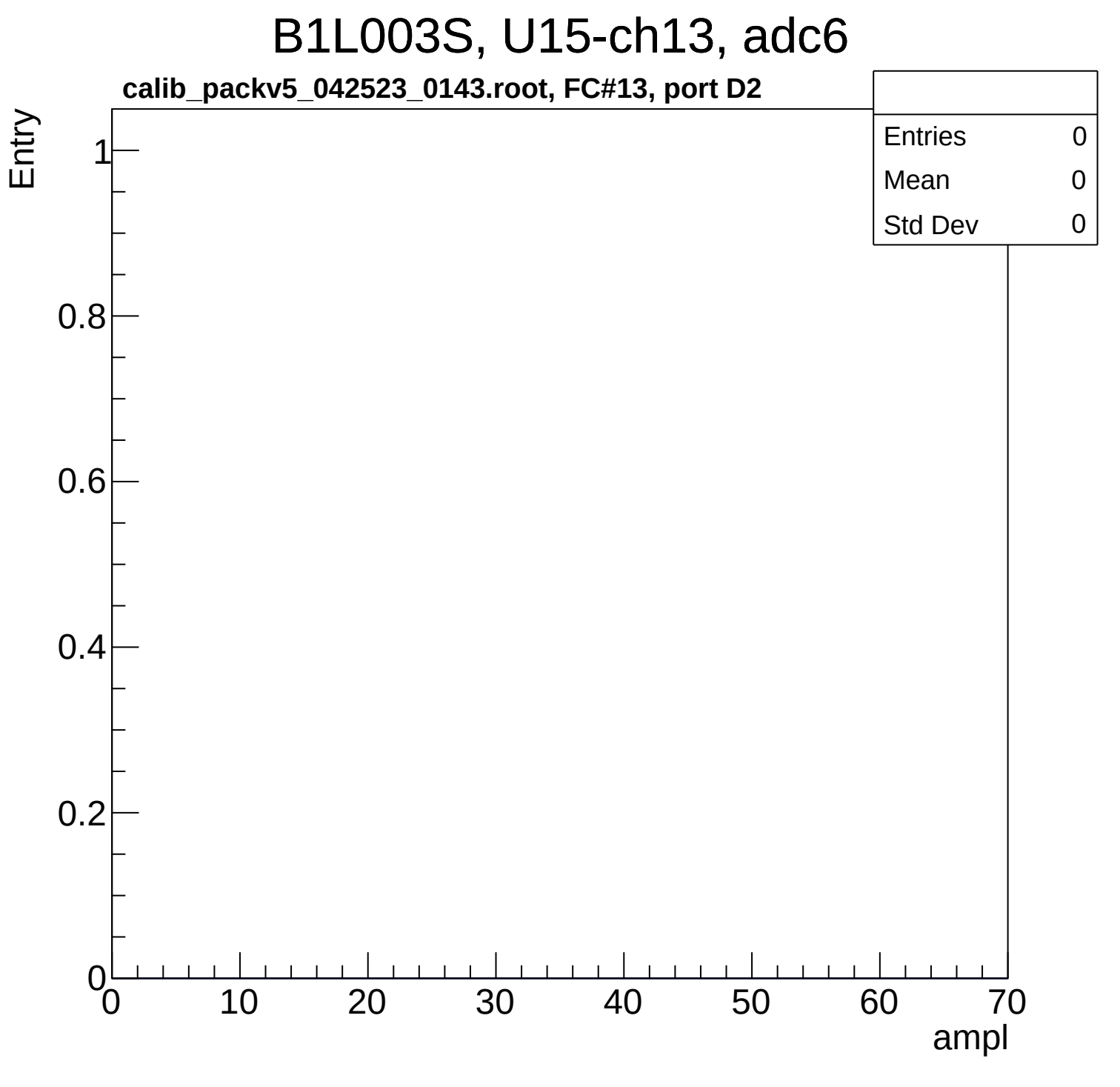
B1L003S, U15-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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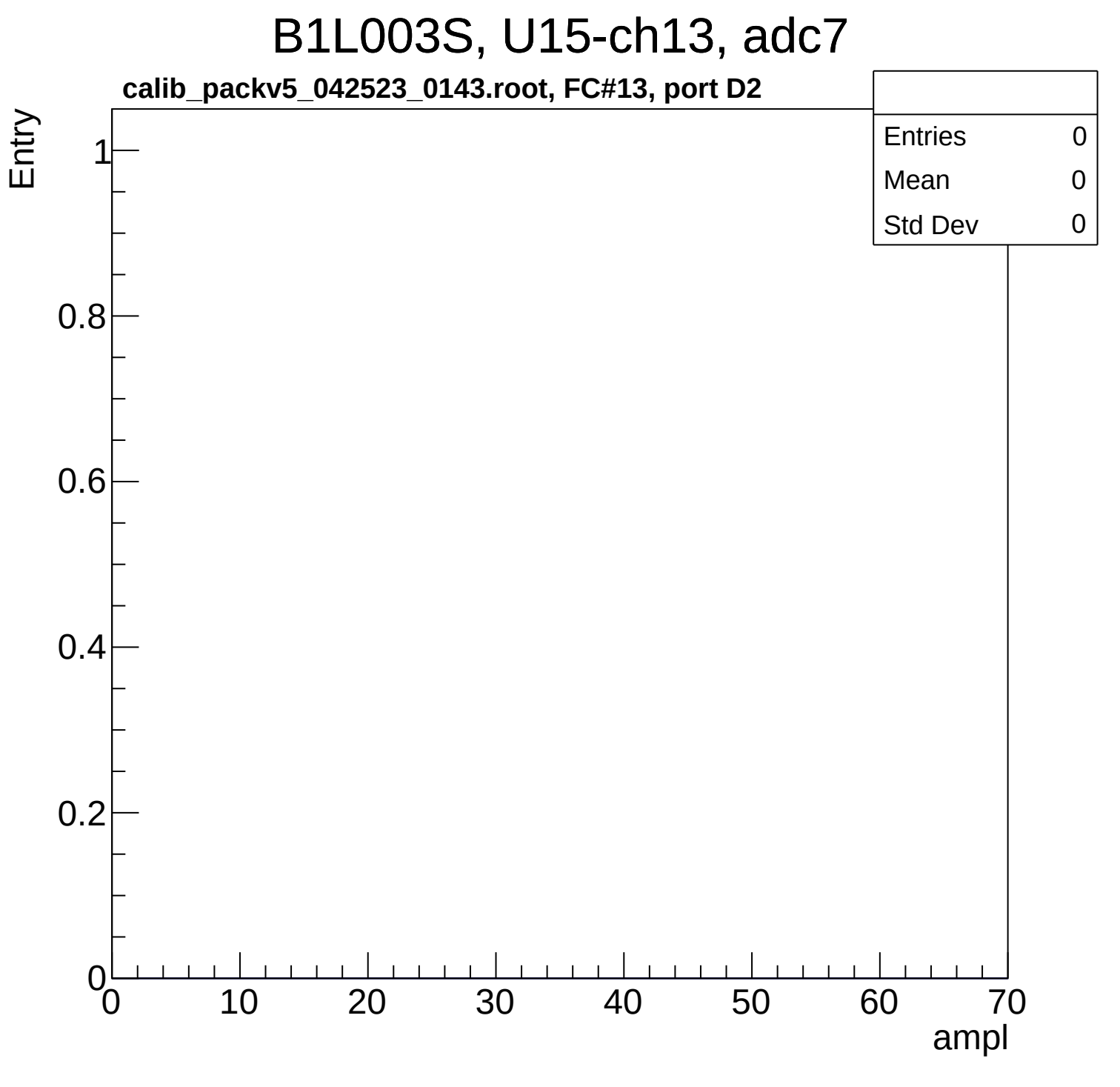
ampl


B1L003S, U15-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
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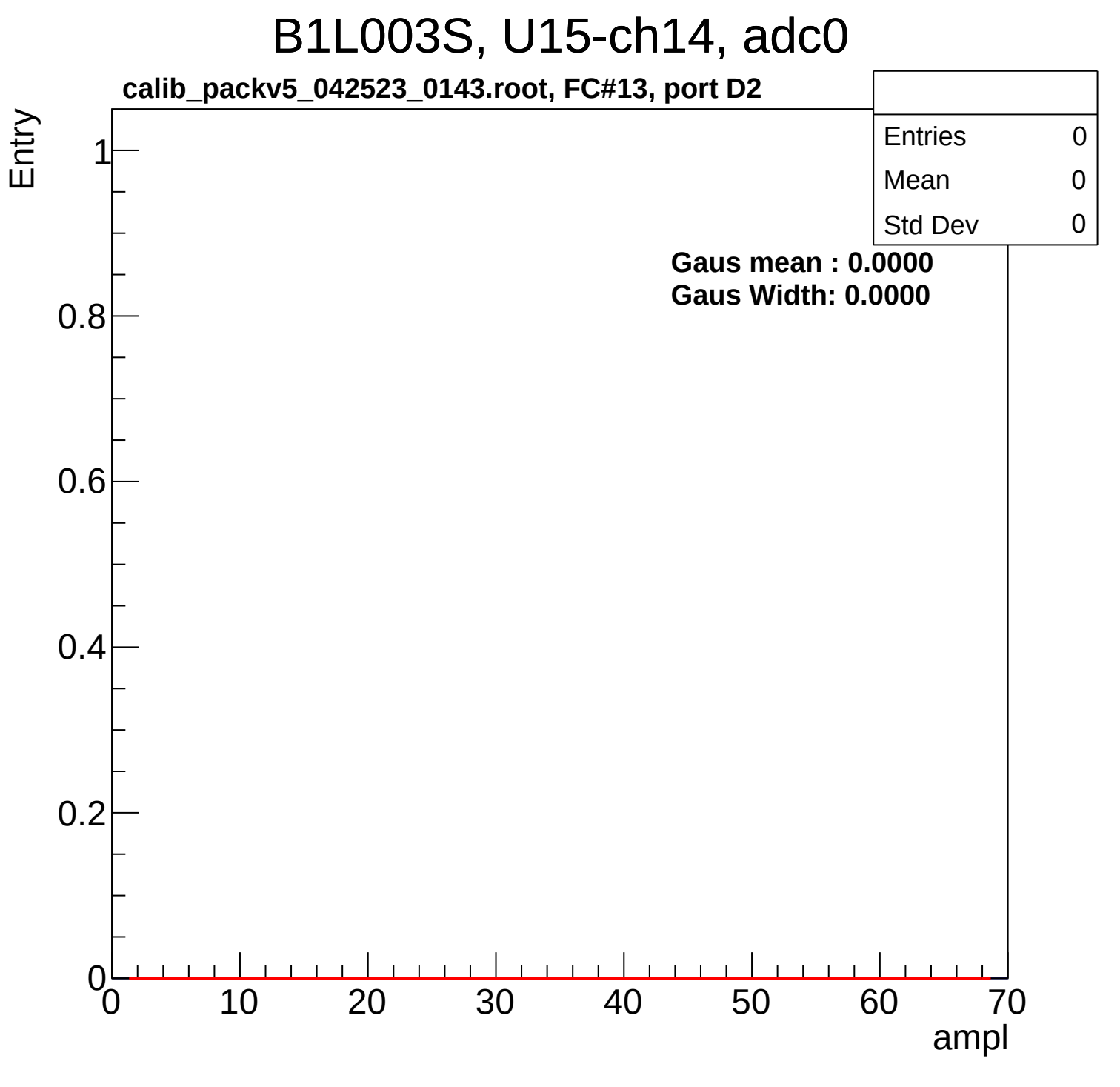
B1L003S, U15-ch14, adc0
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Std Dev 0
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Gaus Width: 0.0000
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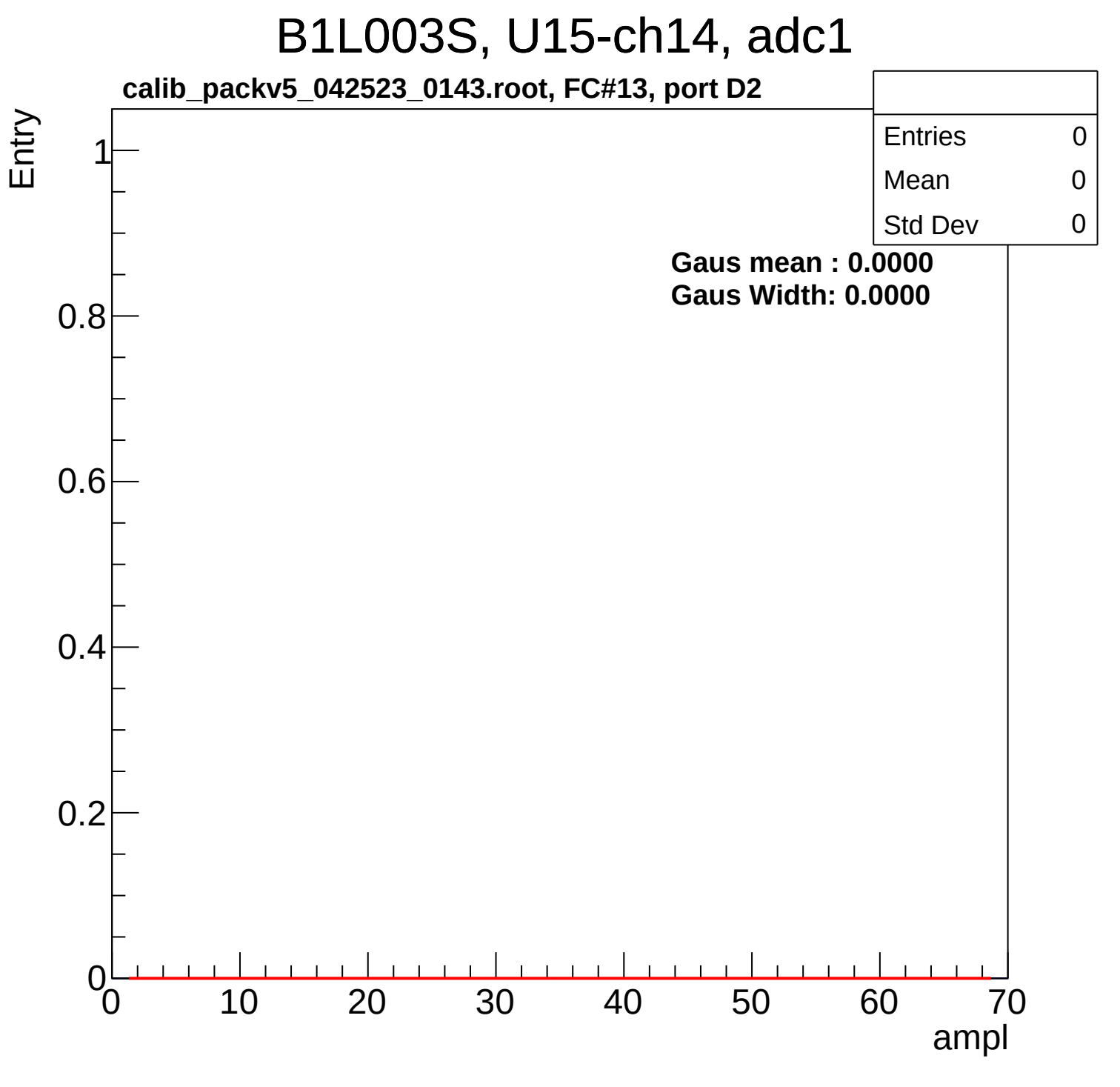
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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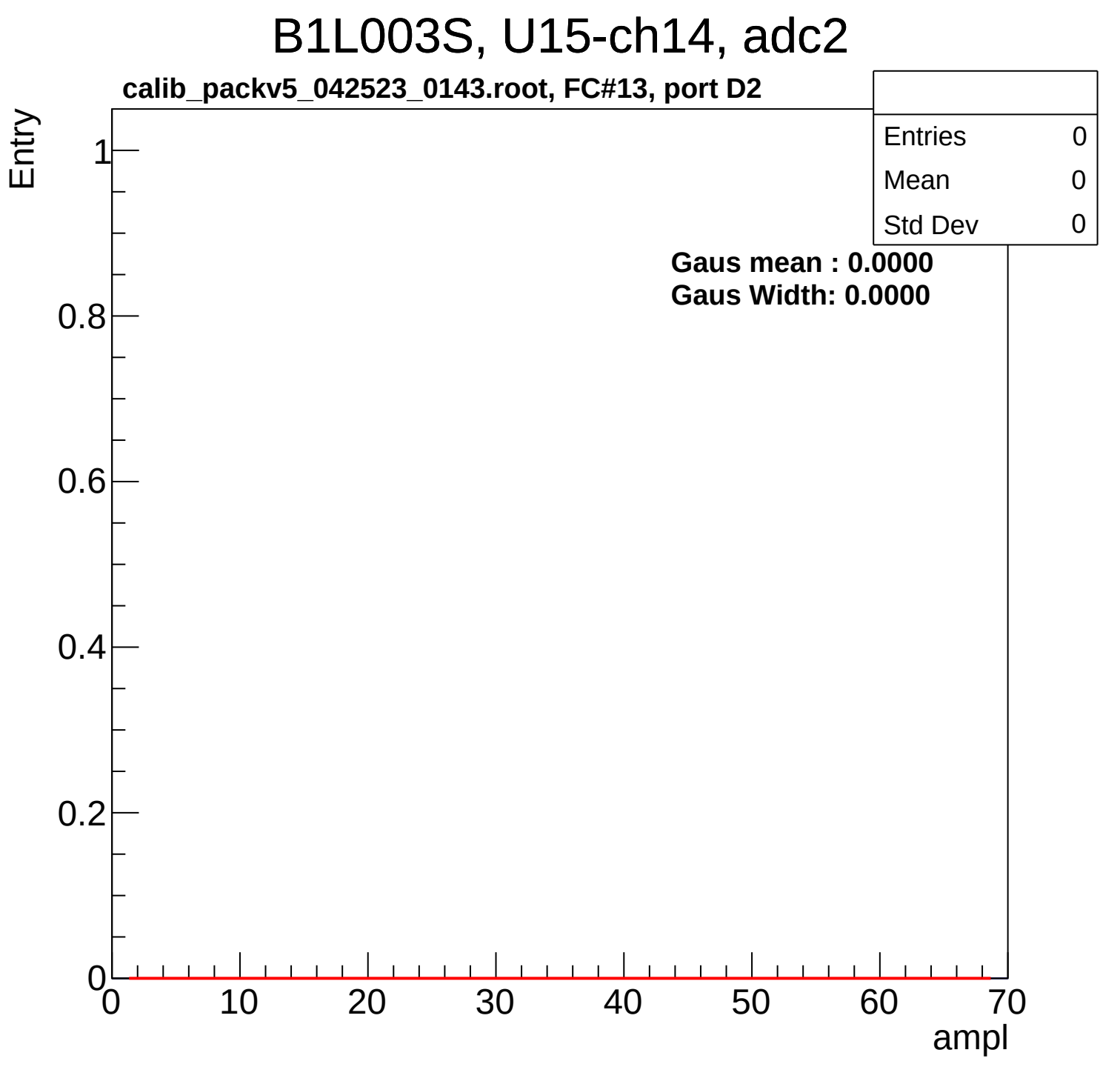
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Std Dev 0
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Gaus Width: 0.0000
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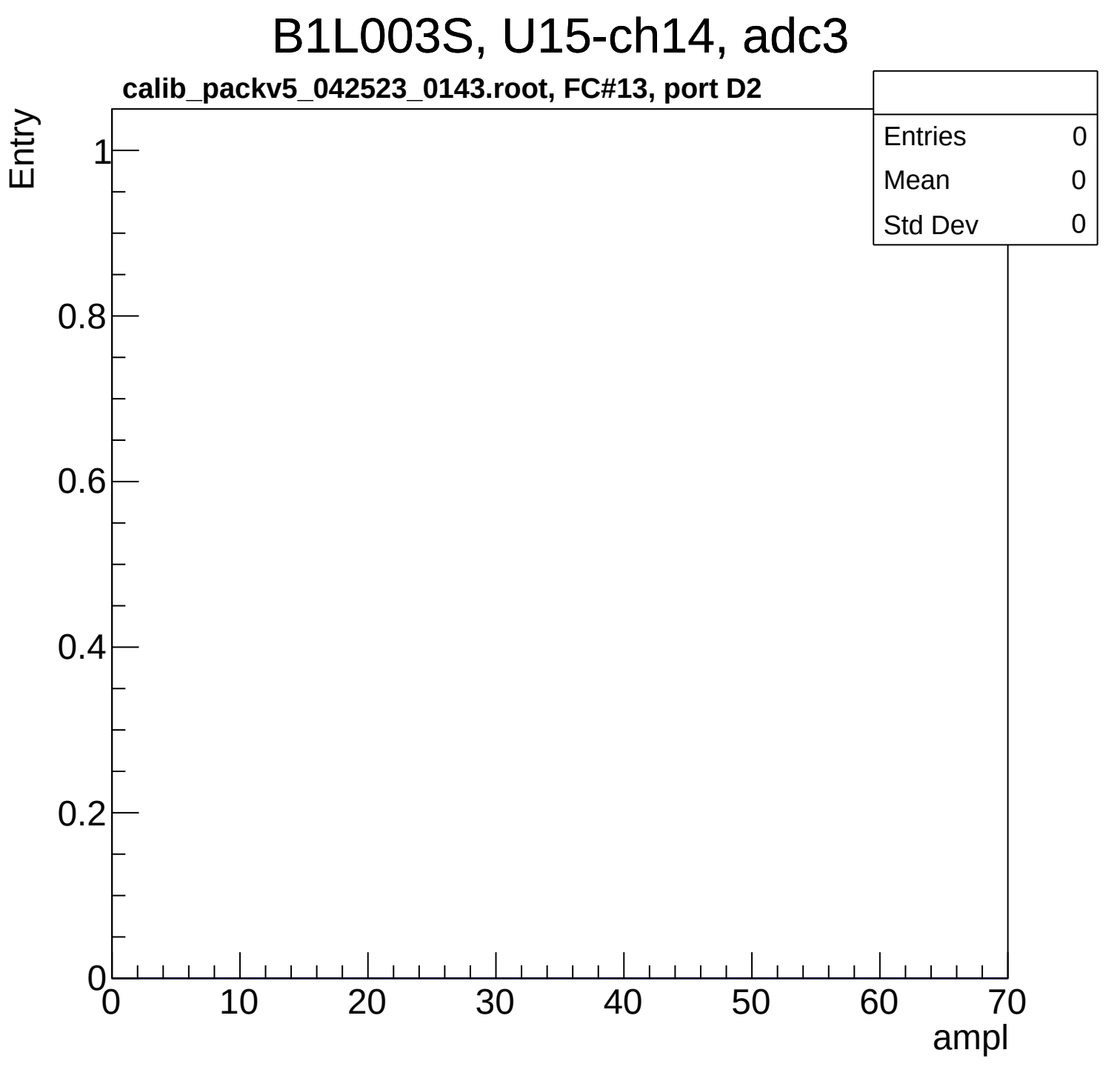
B1L003S, U15-ch14, adc3
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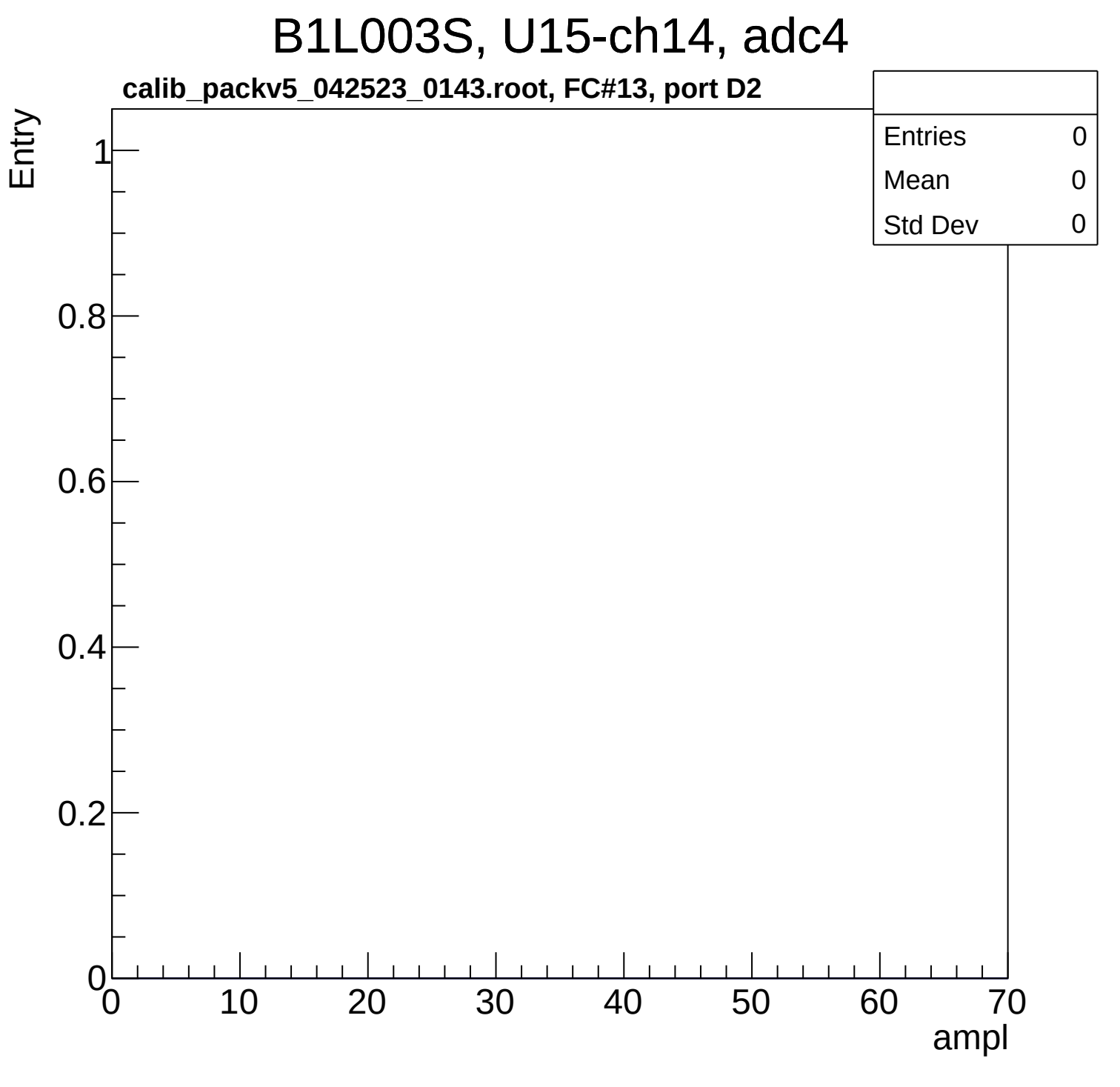
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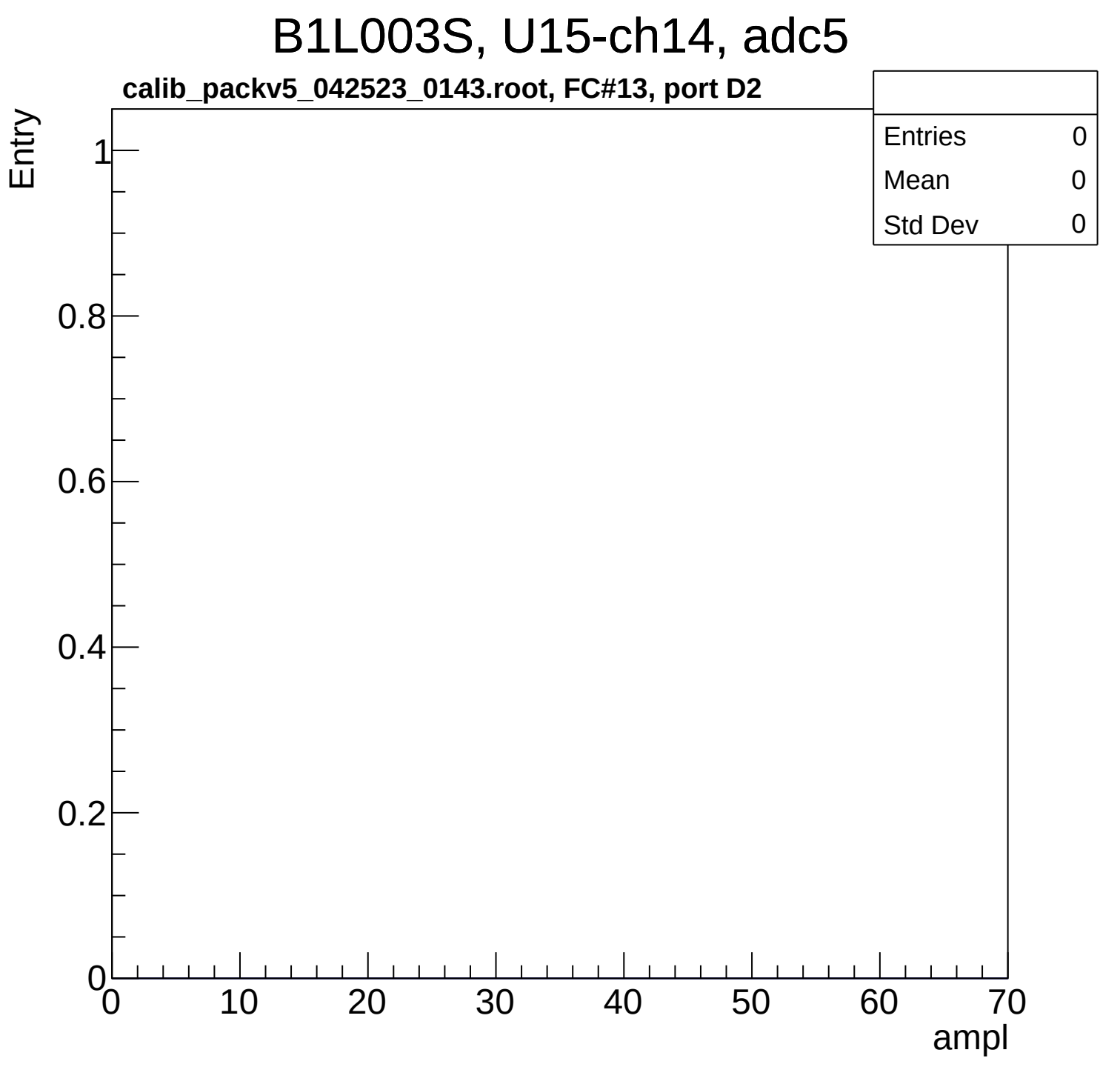
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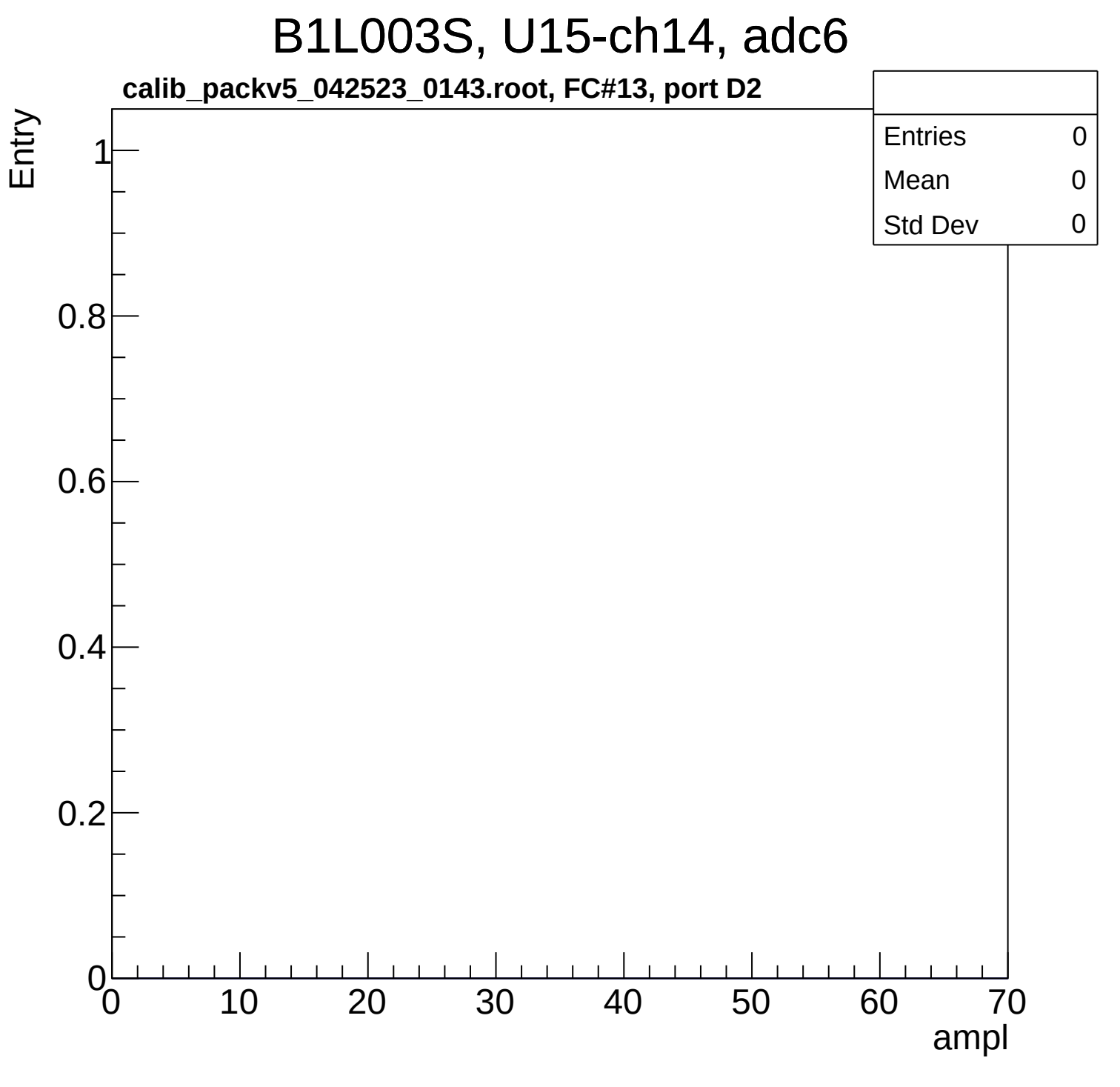
B1L003S, U15-ch14, adc6
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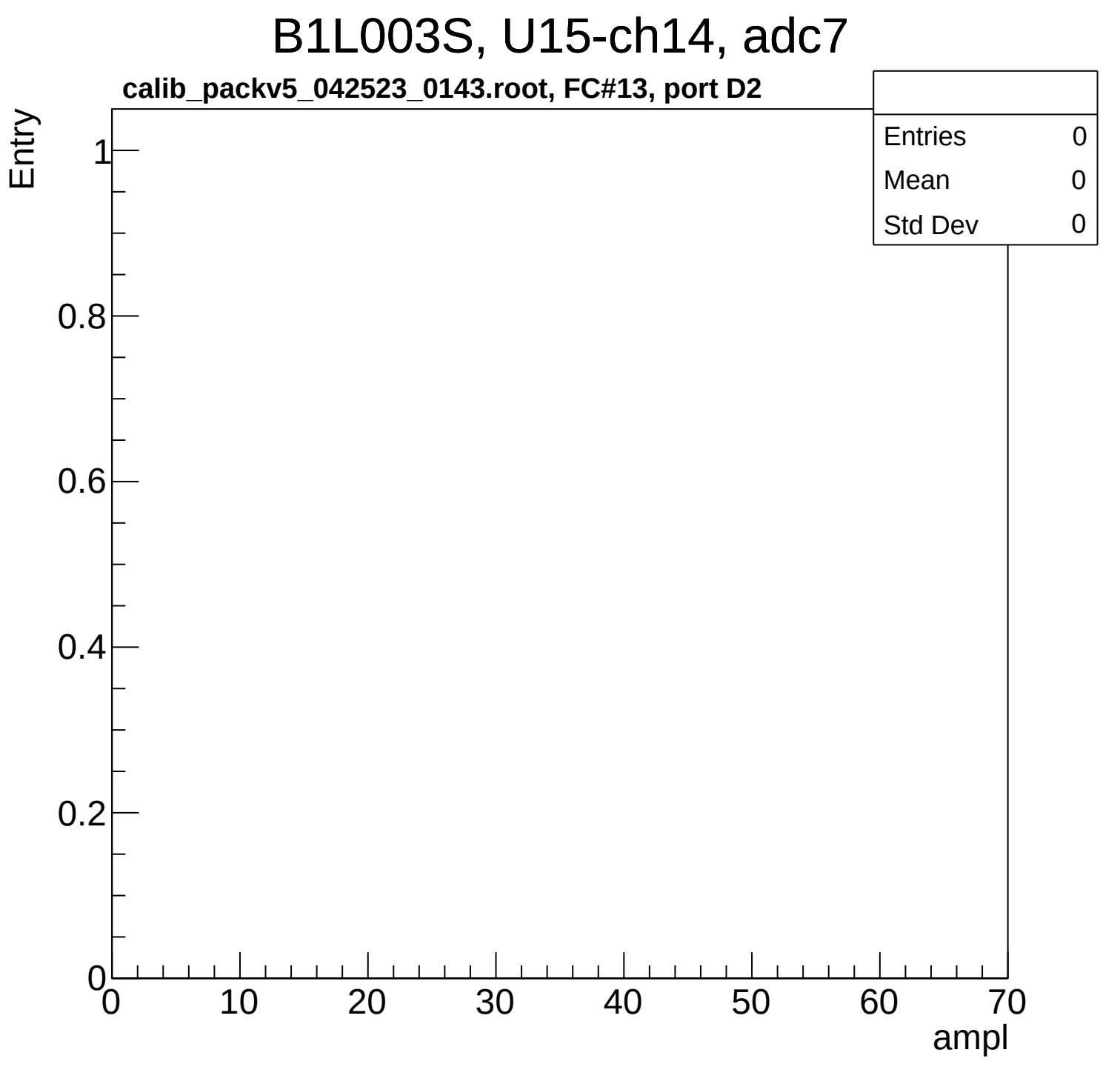
B1L003S, U15-ch14, adc7
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Entries 0
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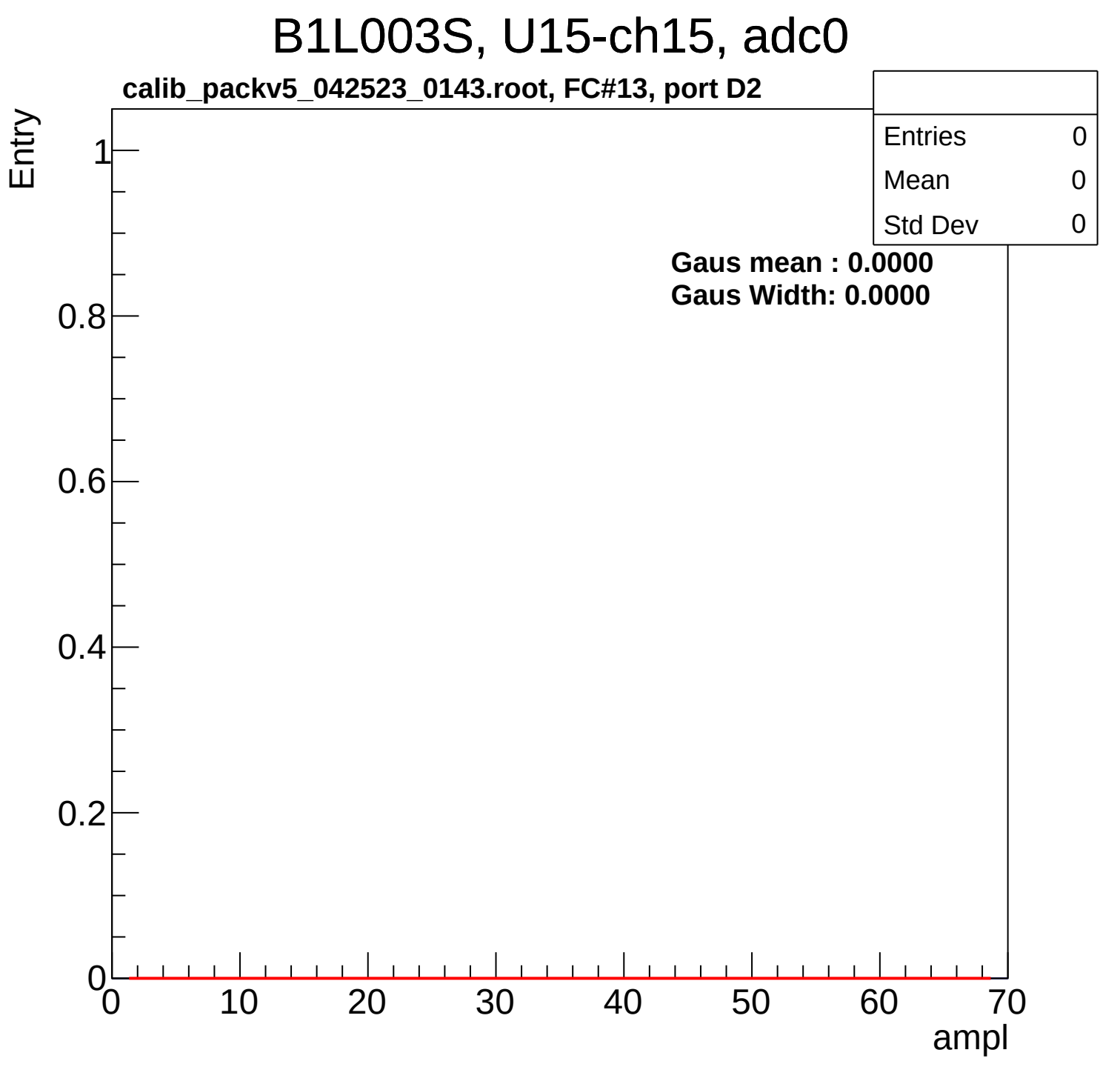
B1L003S, U15-ch15, adc0
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Gaus Width: 0.0000
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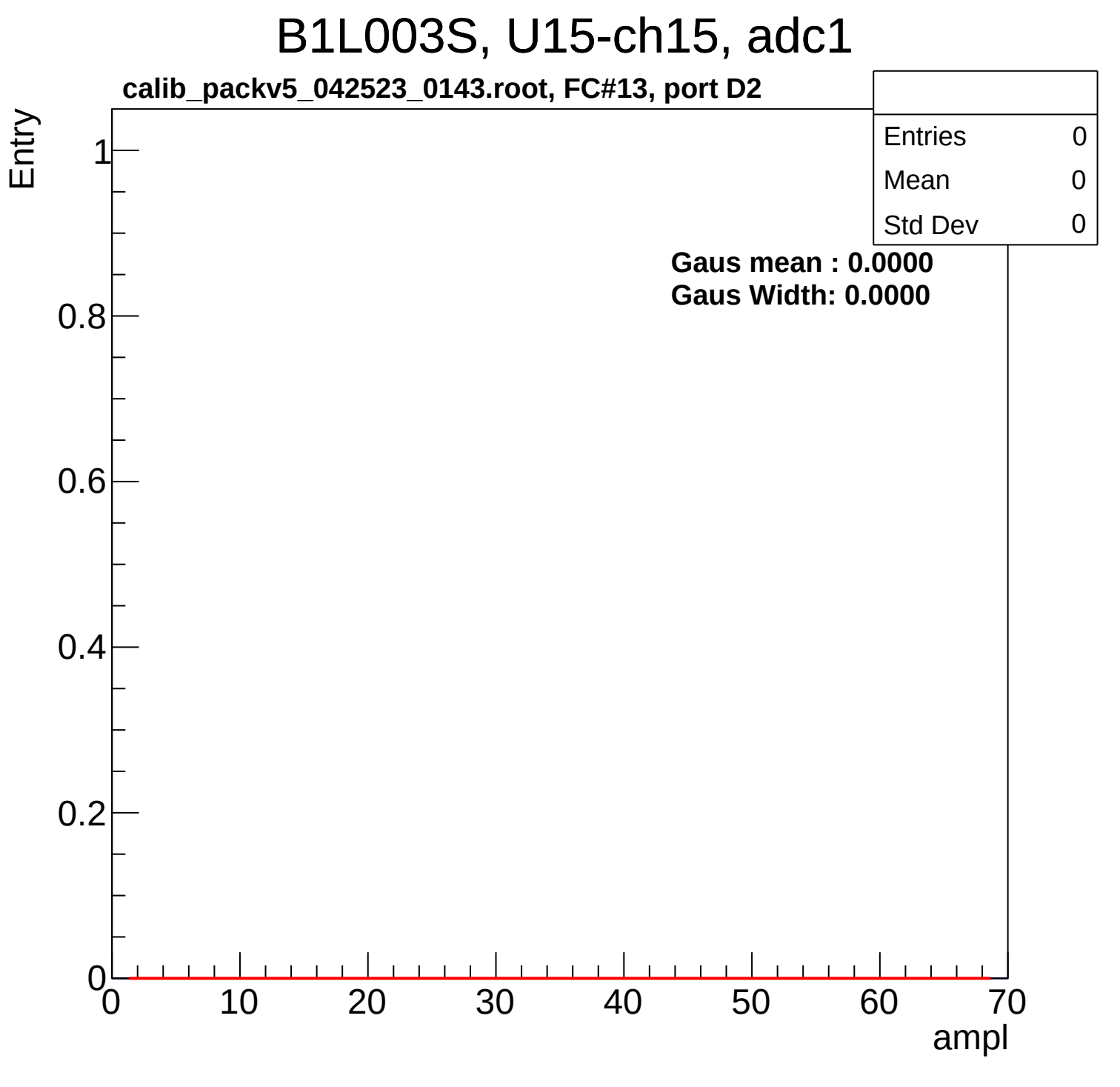
B1L003S, U15-ch15, adc1
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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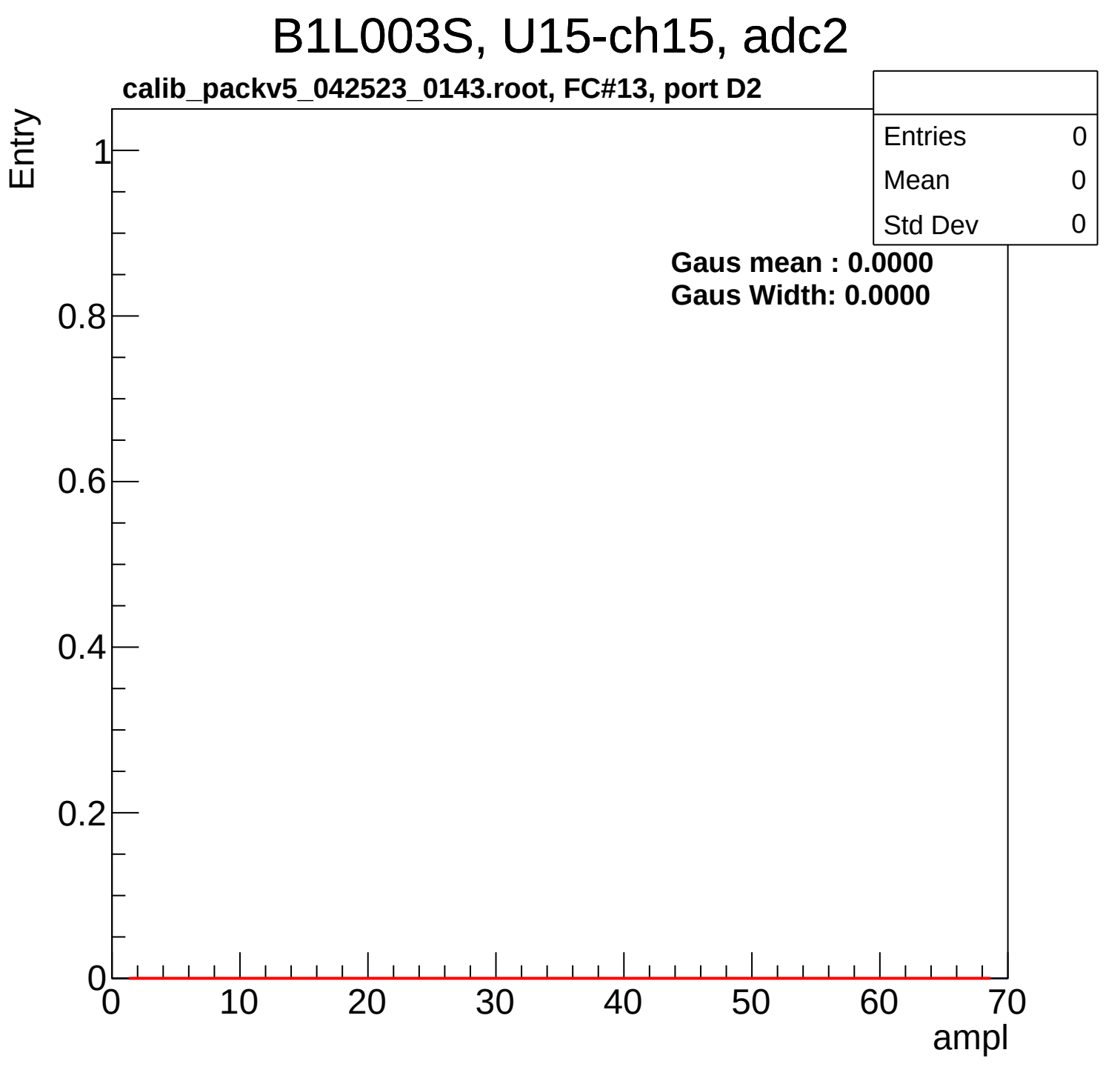
B1L003S, U15-ch15, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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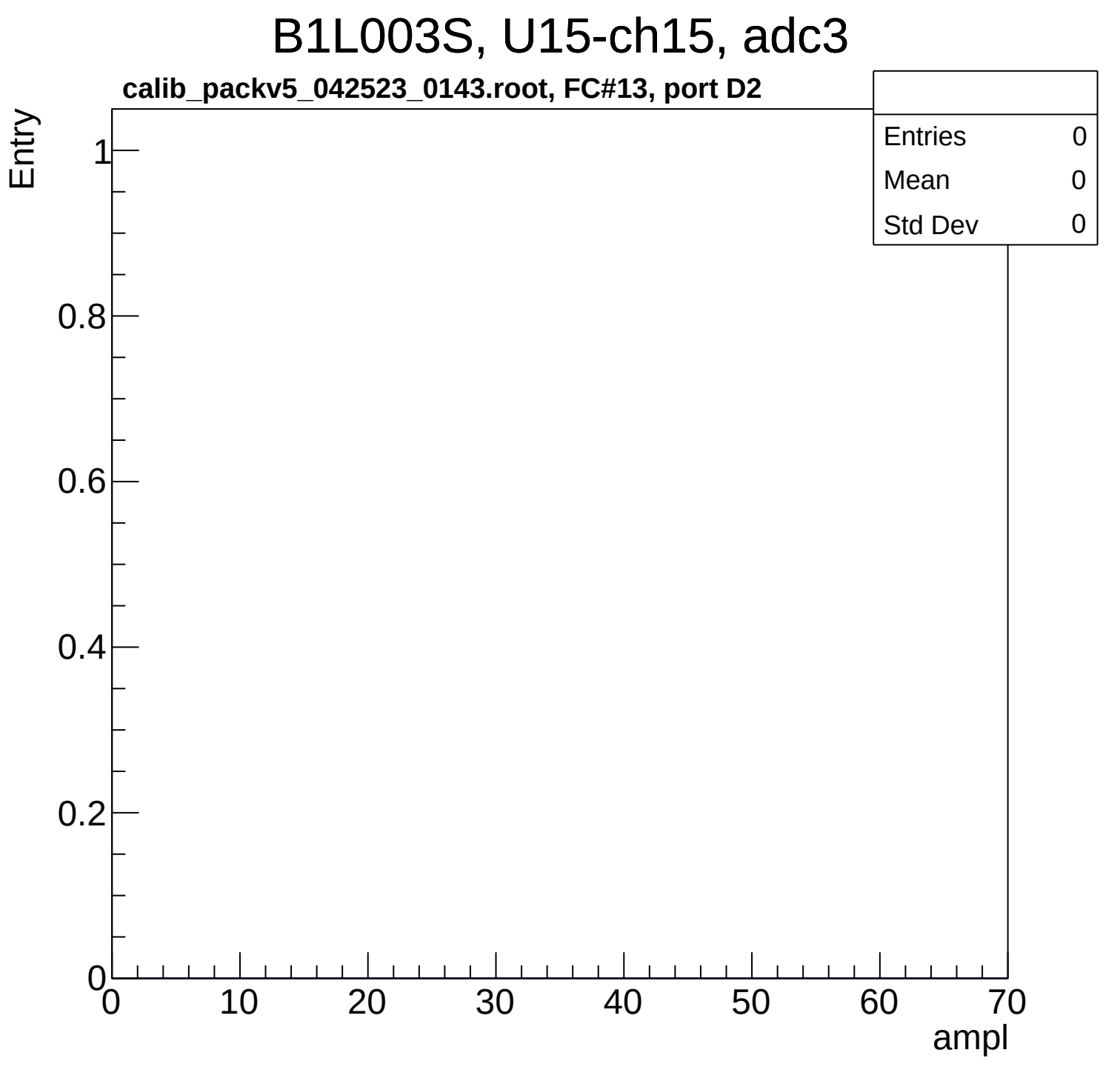
B1L003S, U15-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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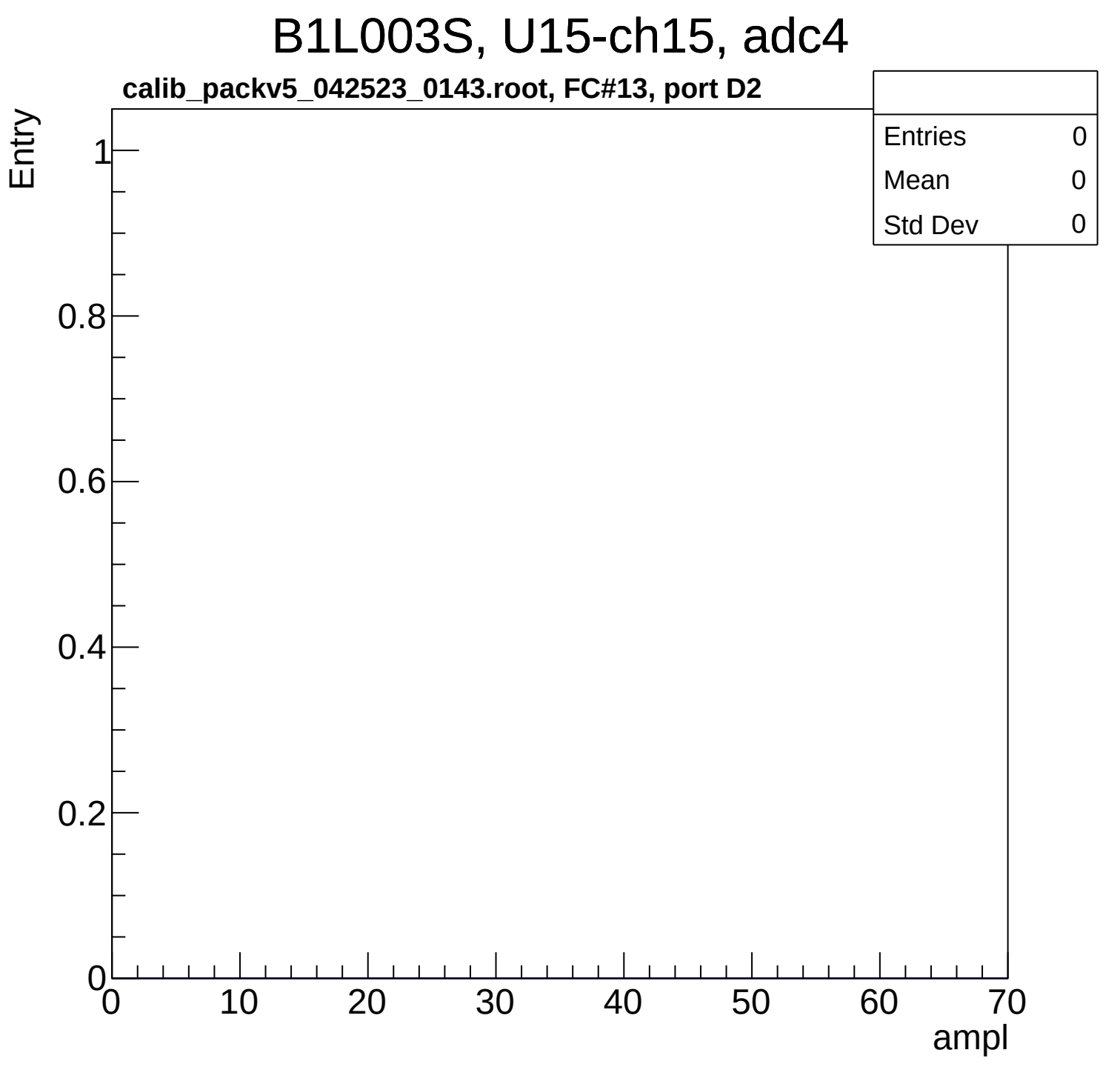
B1L003S, U15-ch15, adc4
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Entries 0
Mean 0
Std Dev 0
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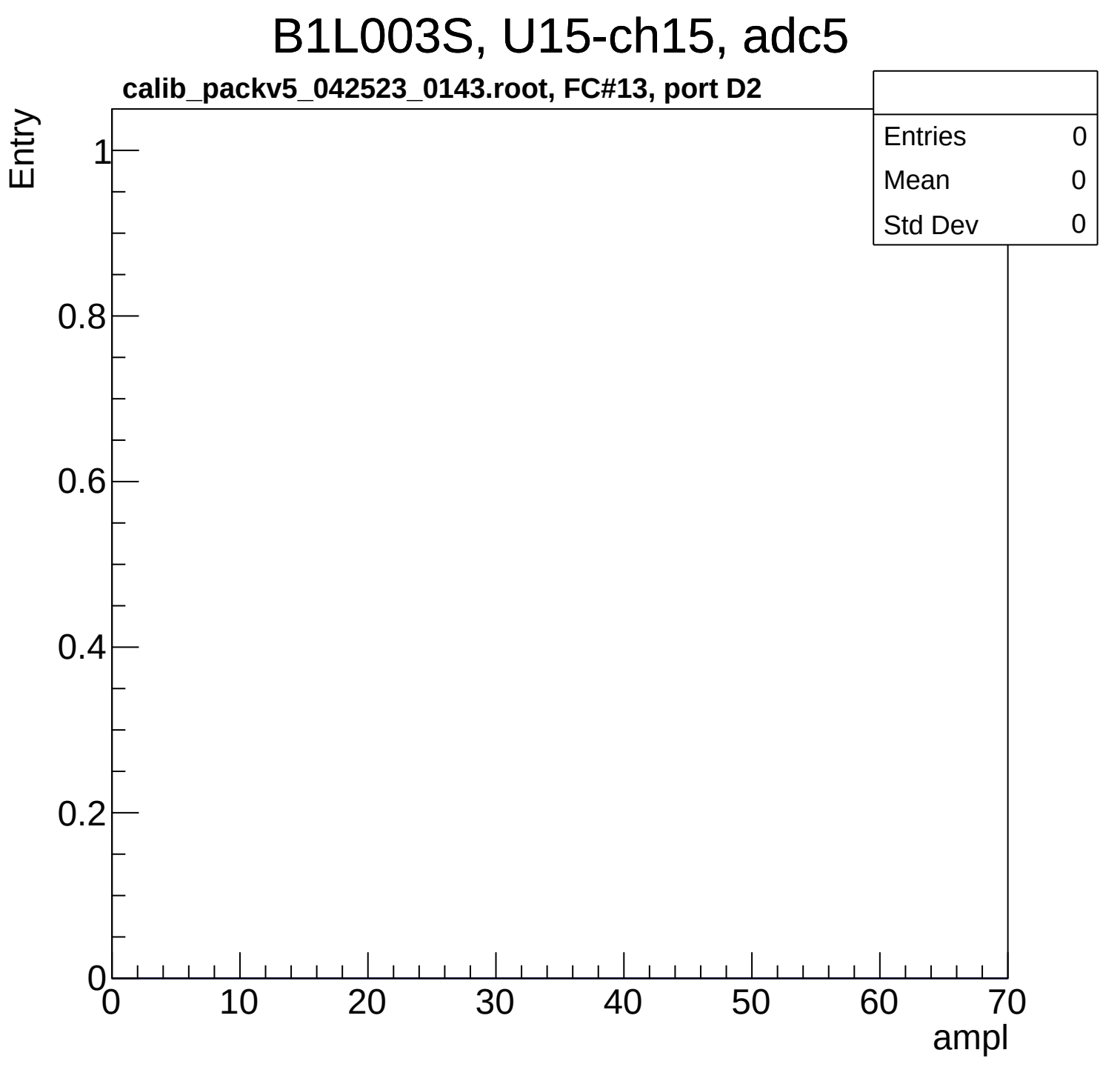
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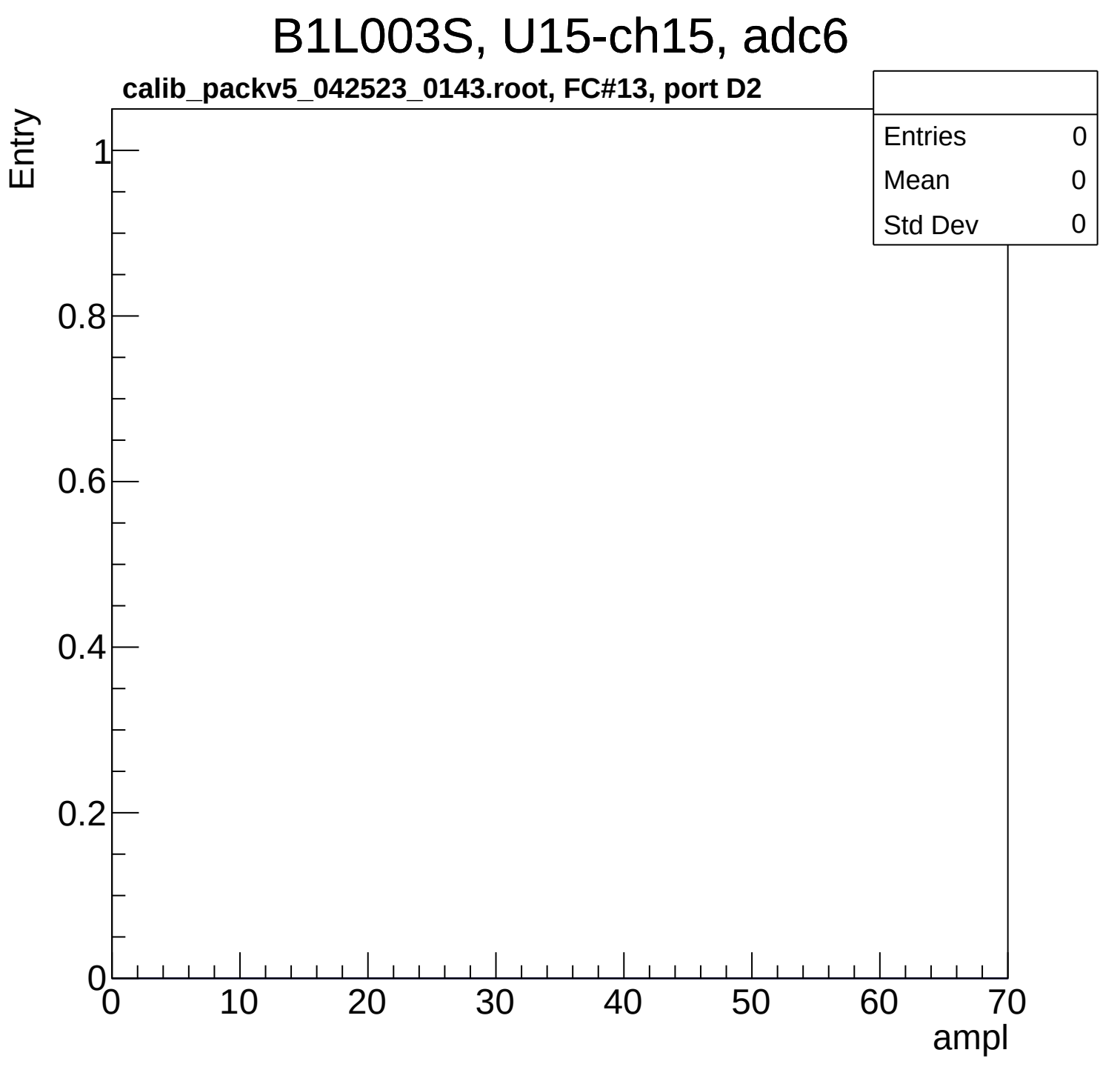
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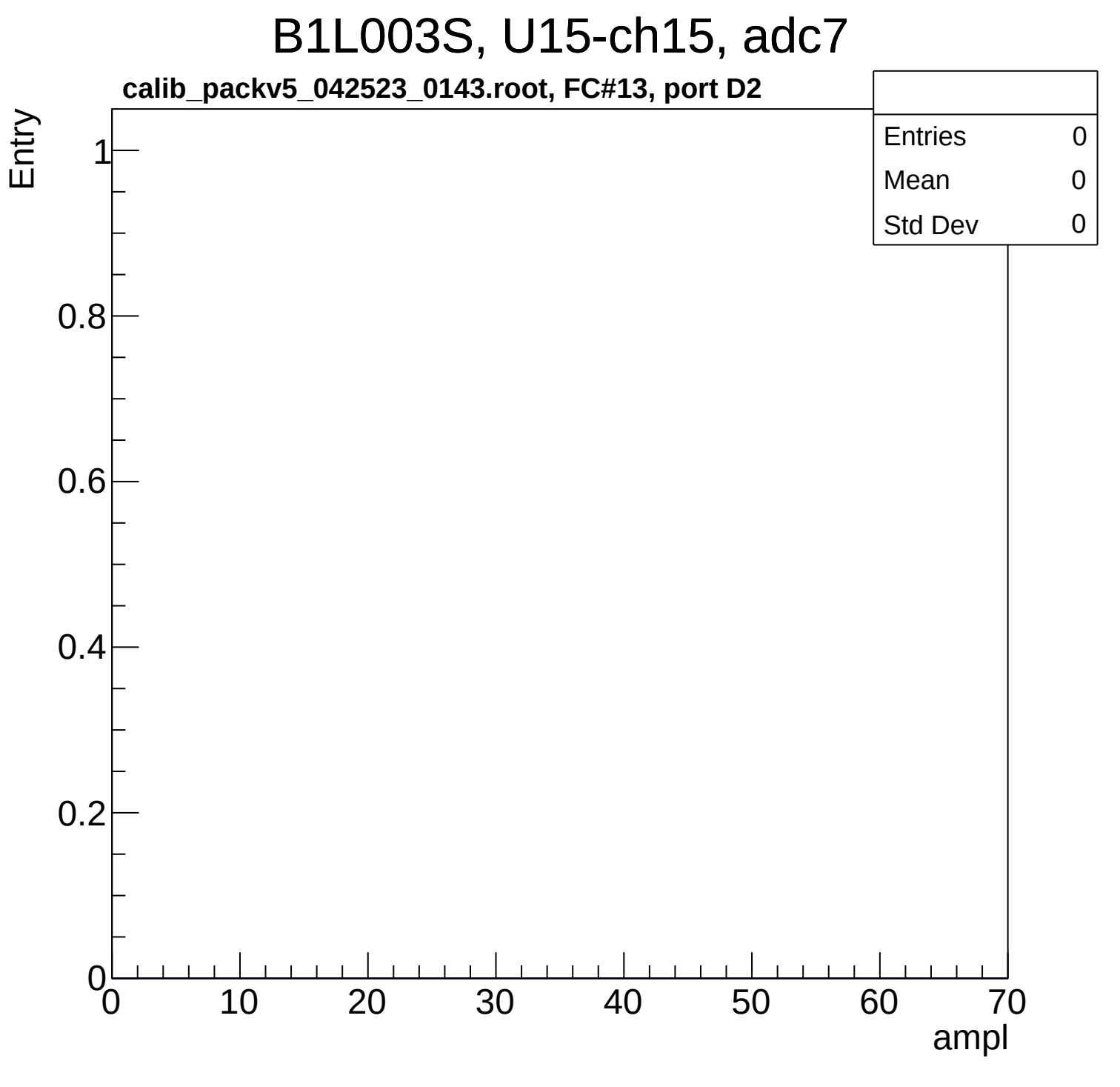
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Std Dev 0
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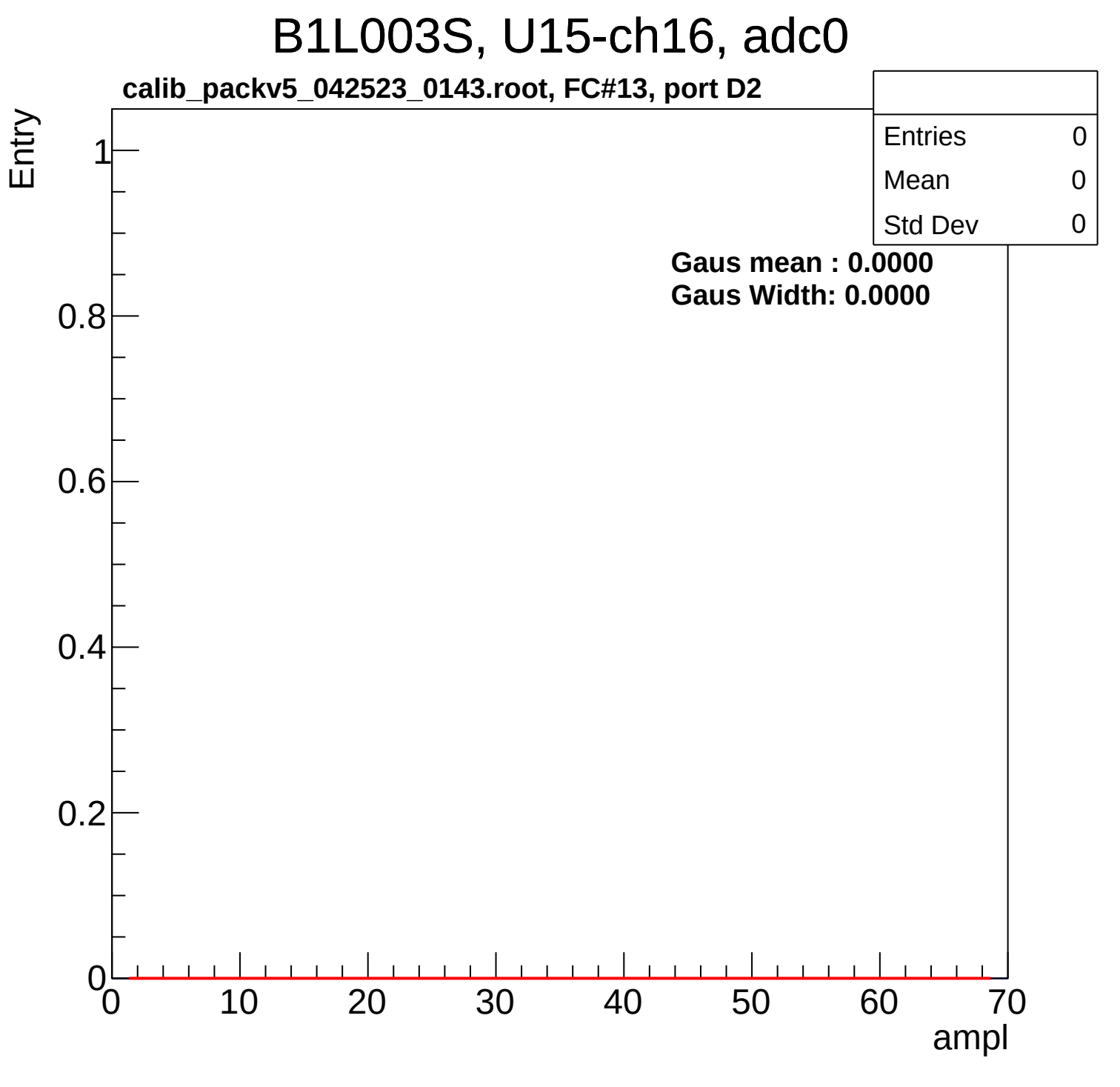
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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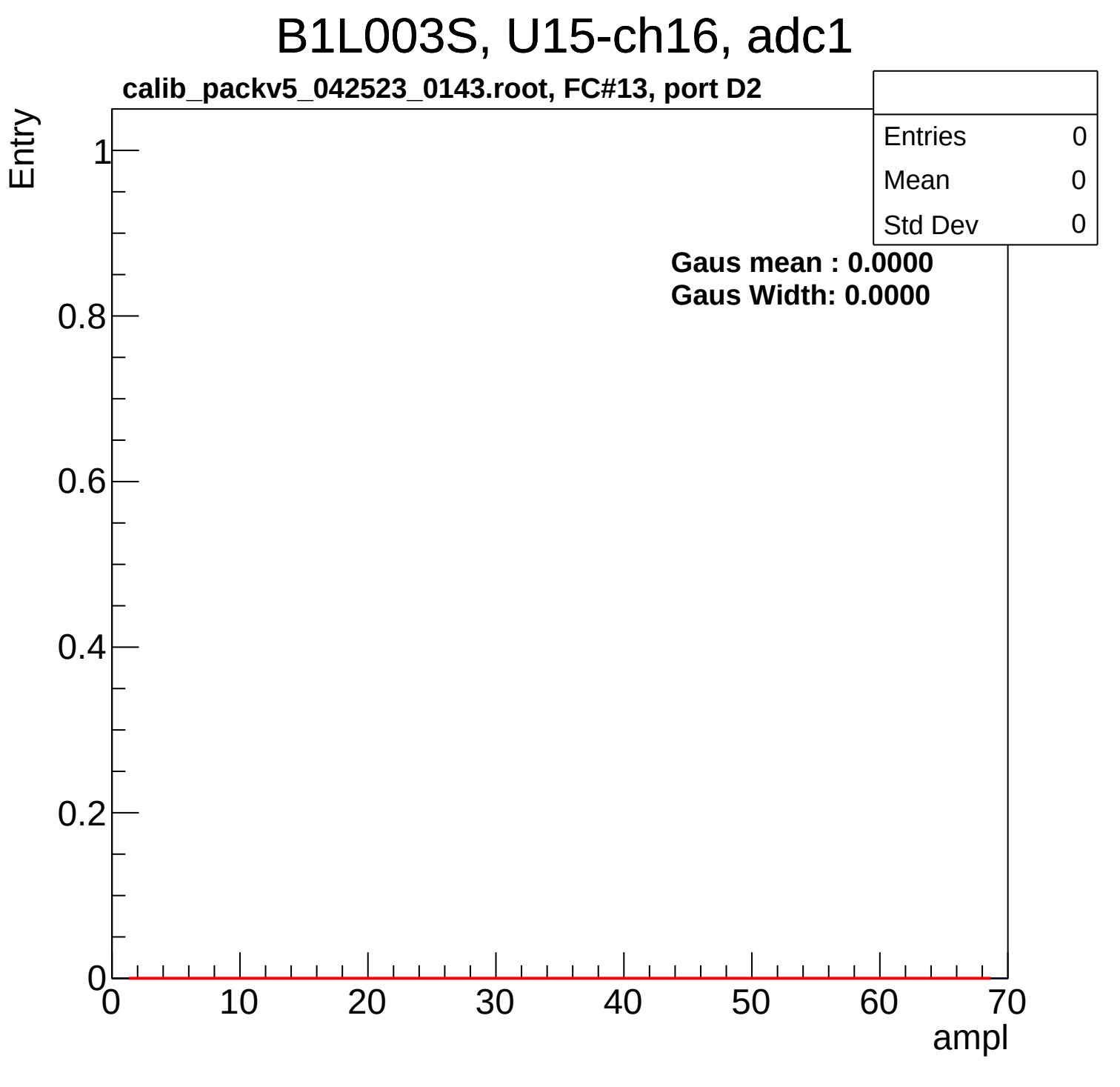
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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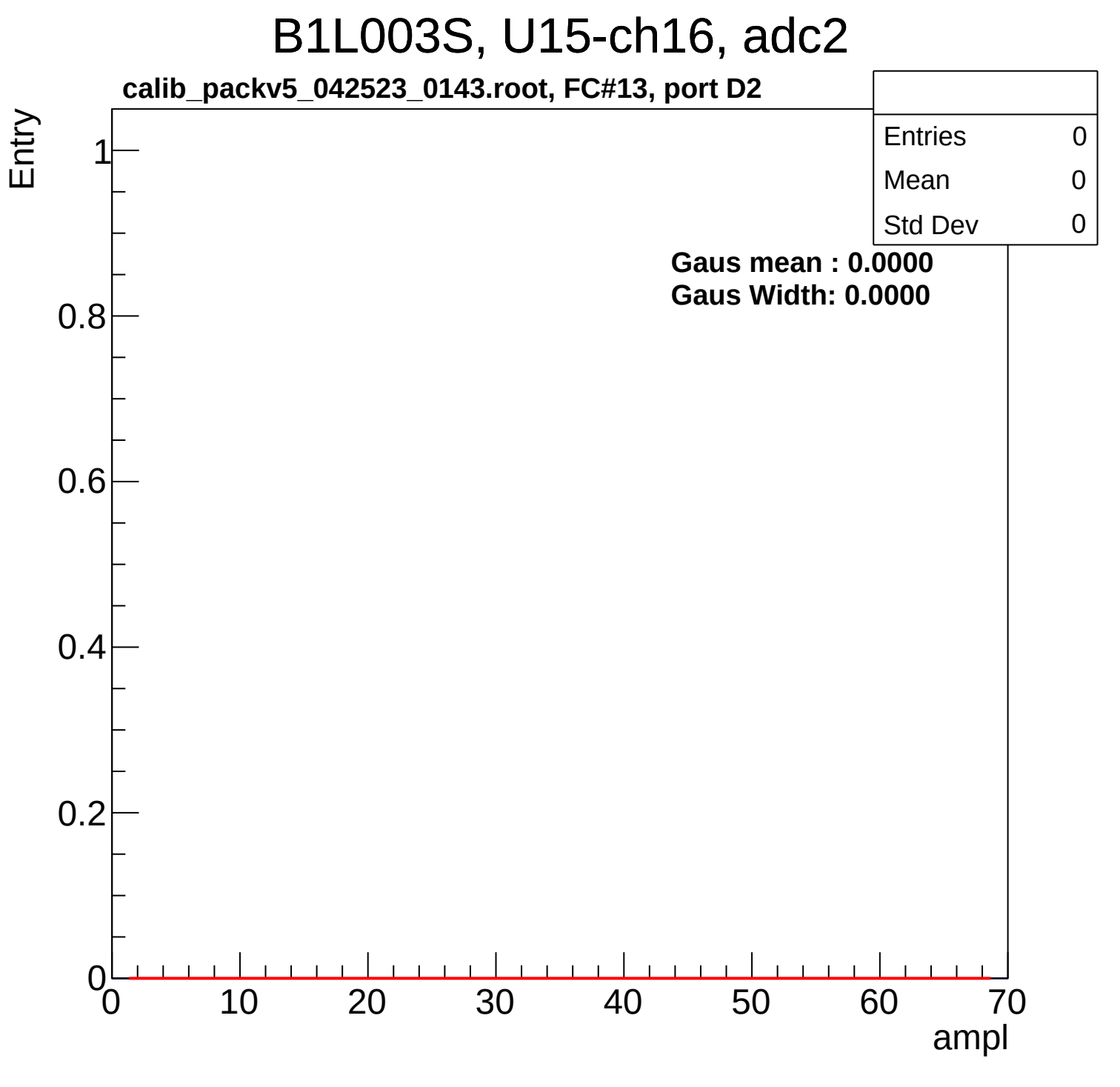
ampl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Gaus Width: 0.0000
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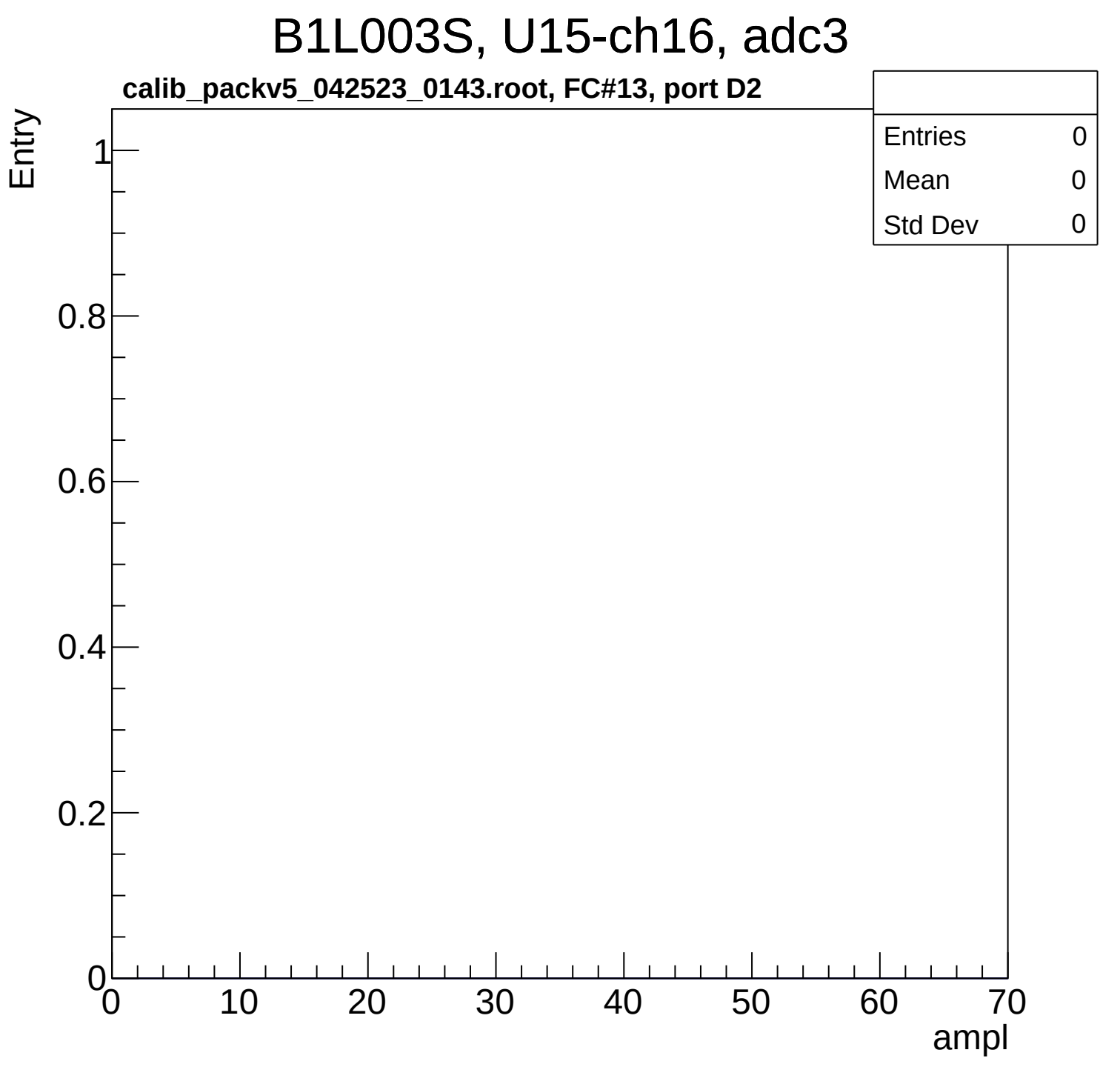
B1L003S, U15-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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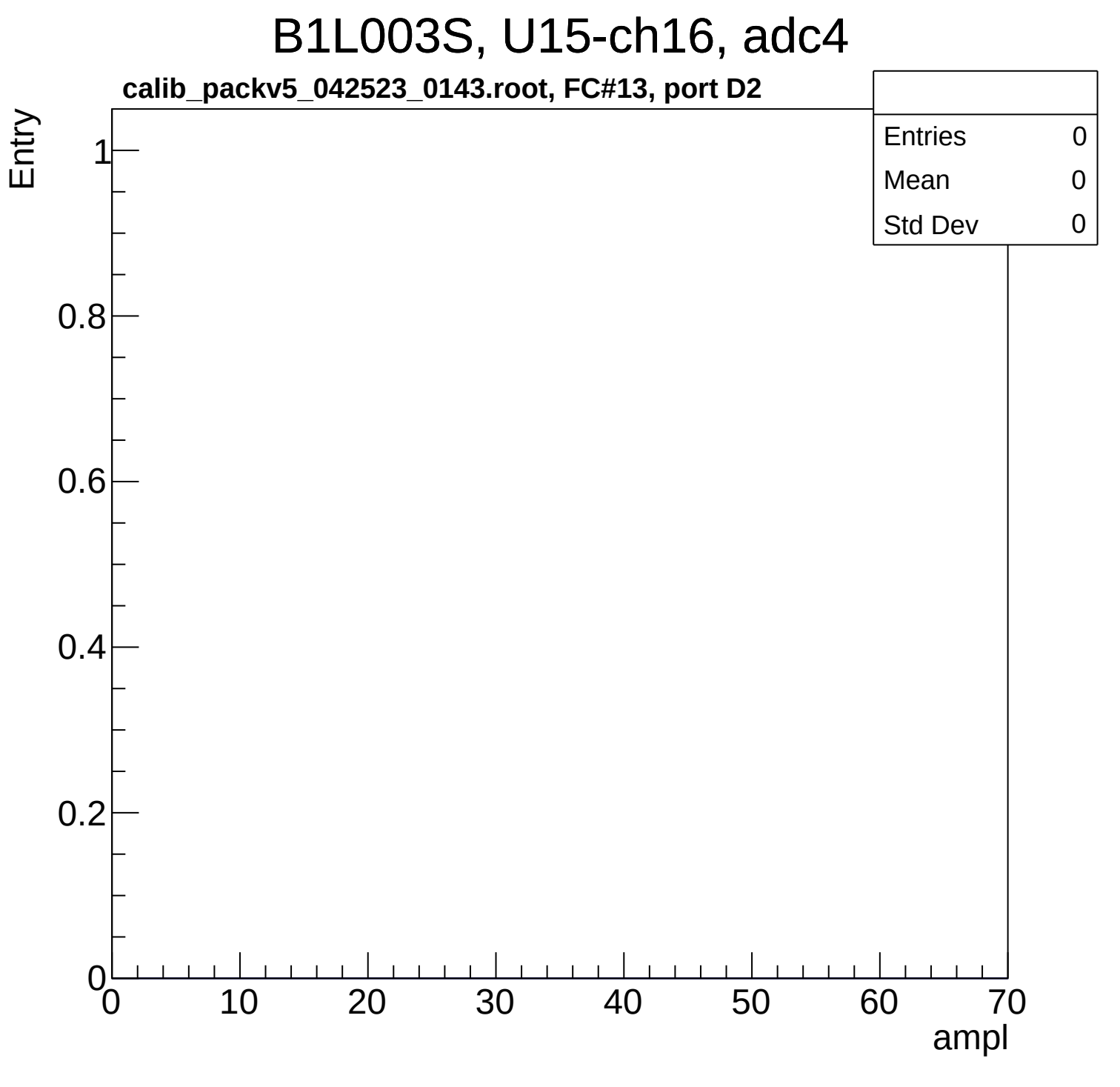
ampl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Mean 0
Std Dev 0
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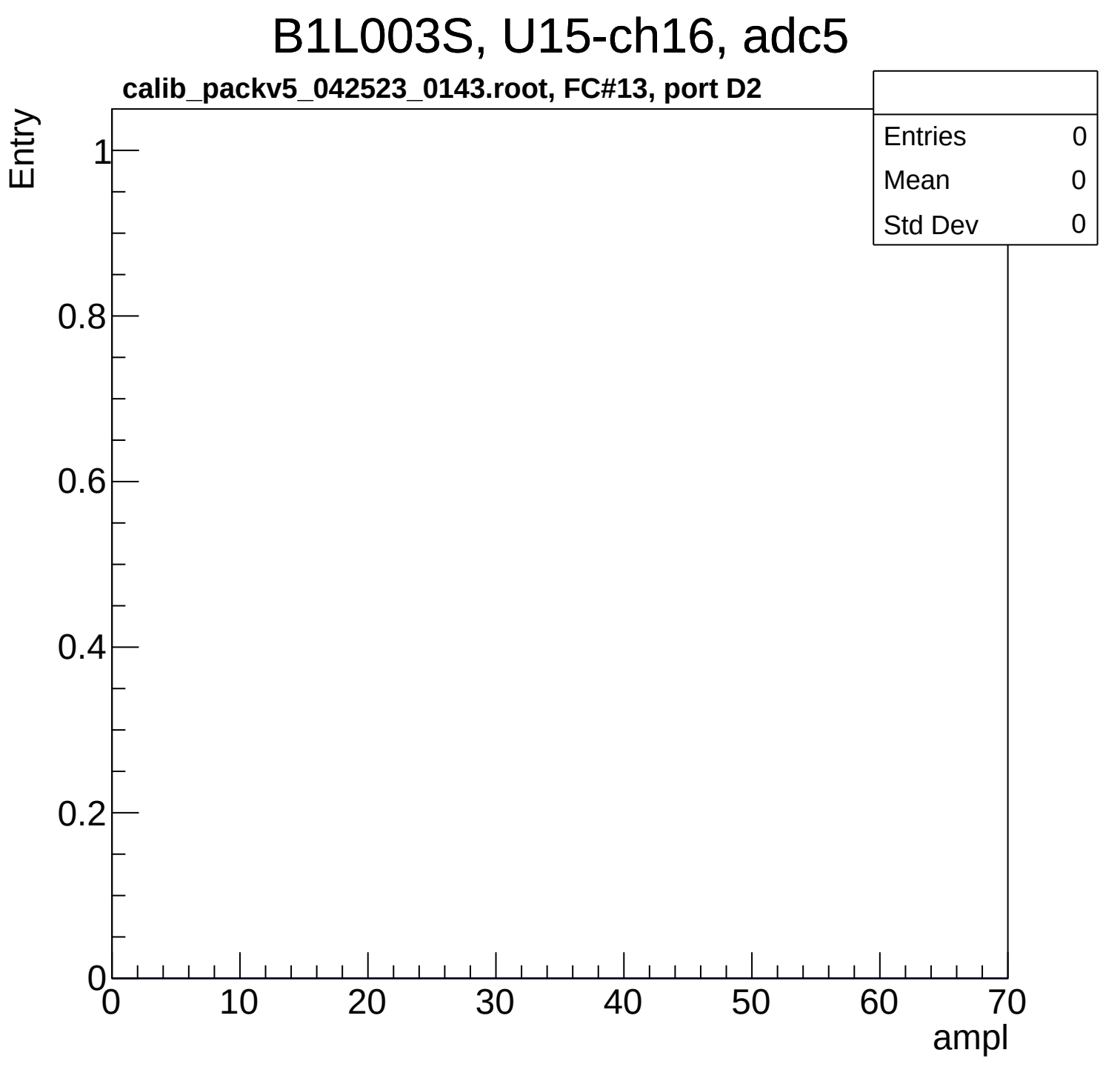
ampl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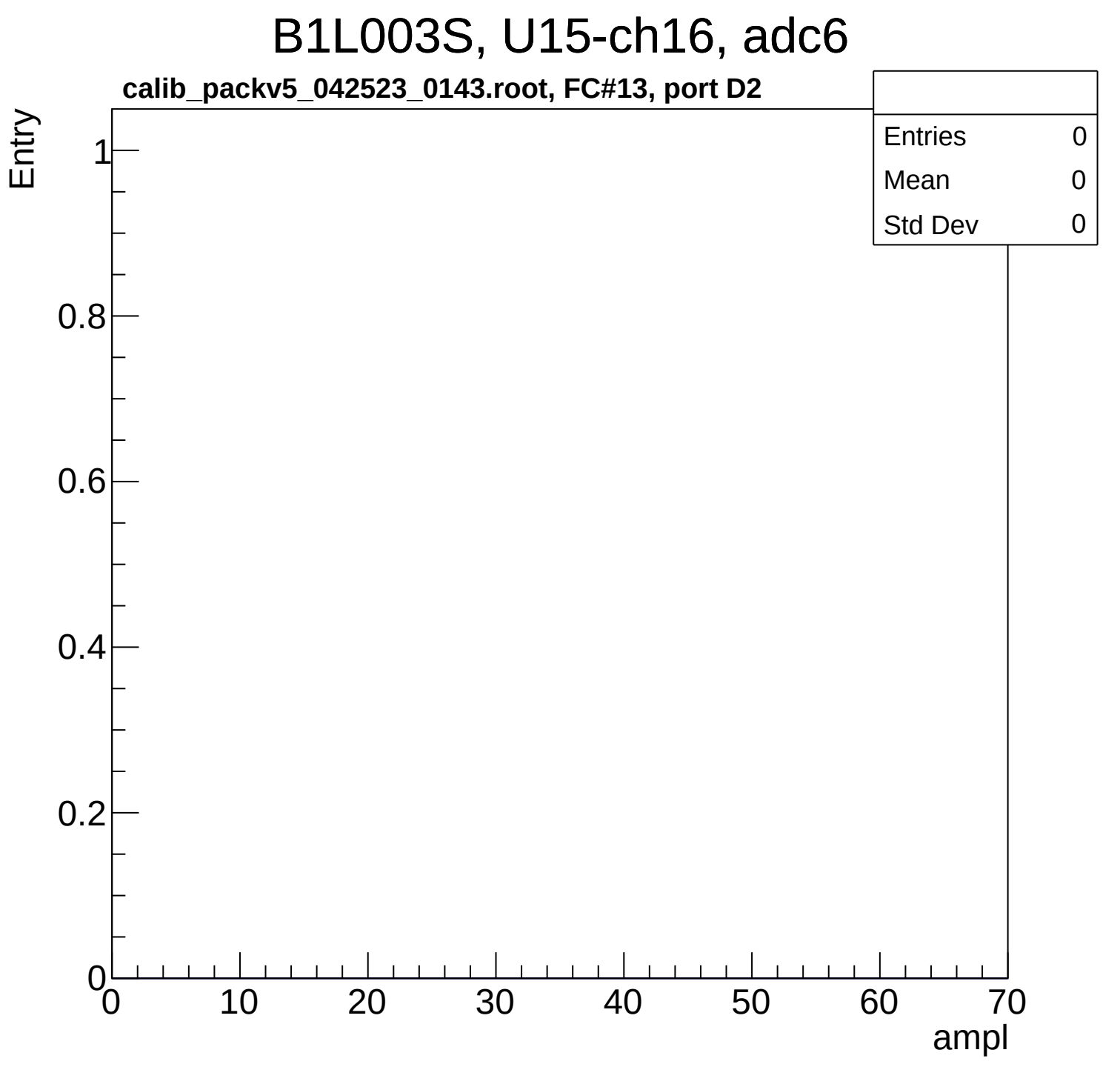
B1L003S, U15-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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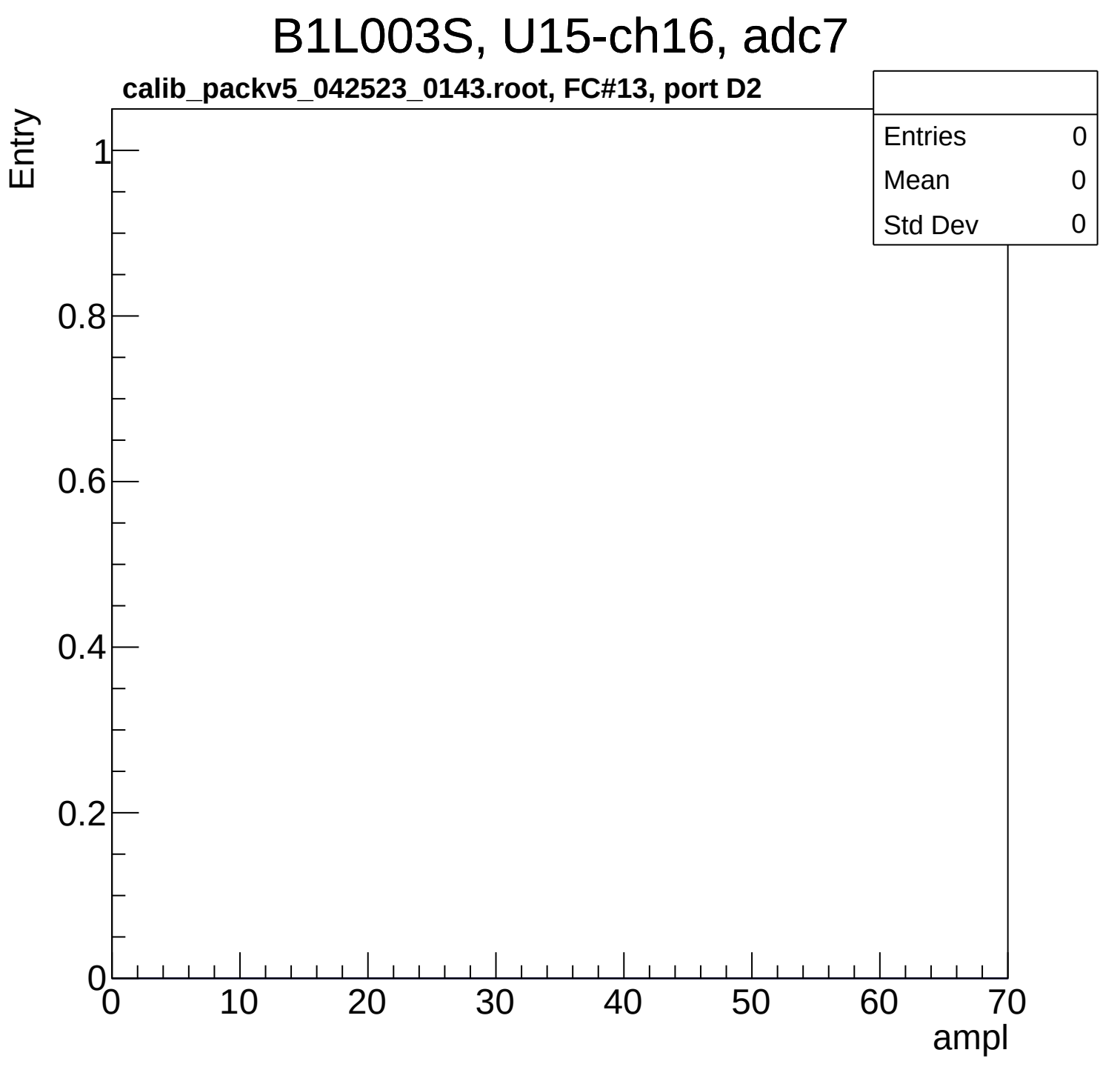
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Mean 0
Std Dev 0
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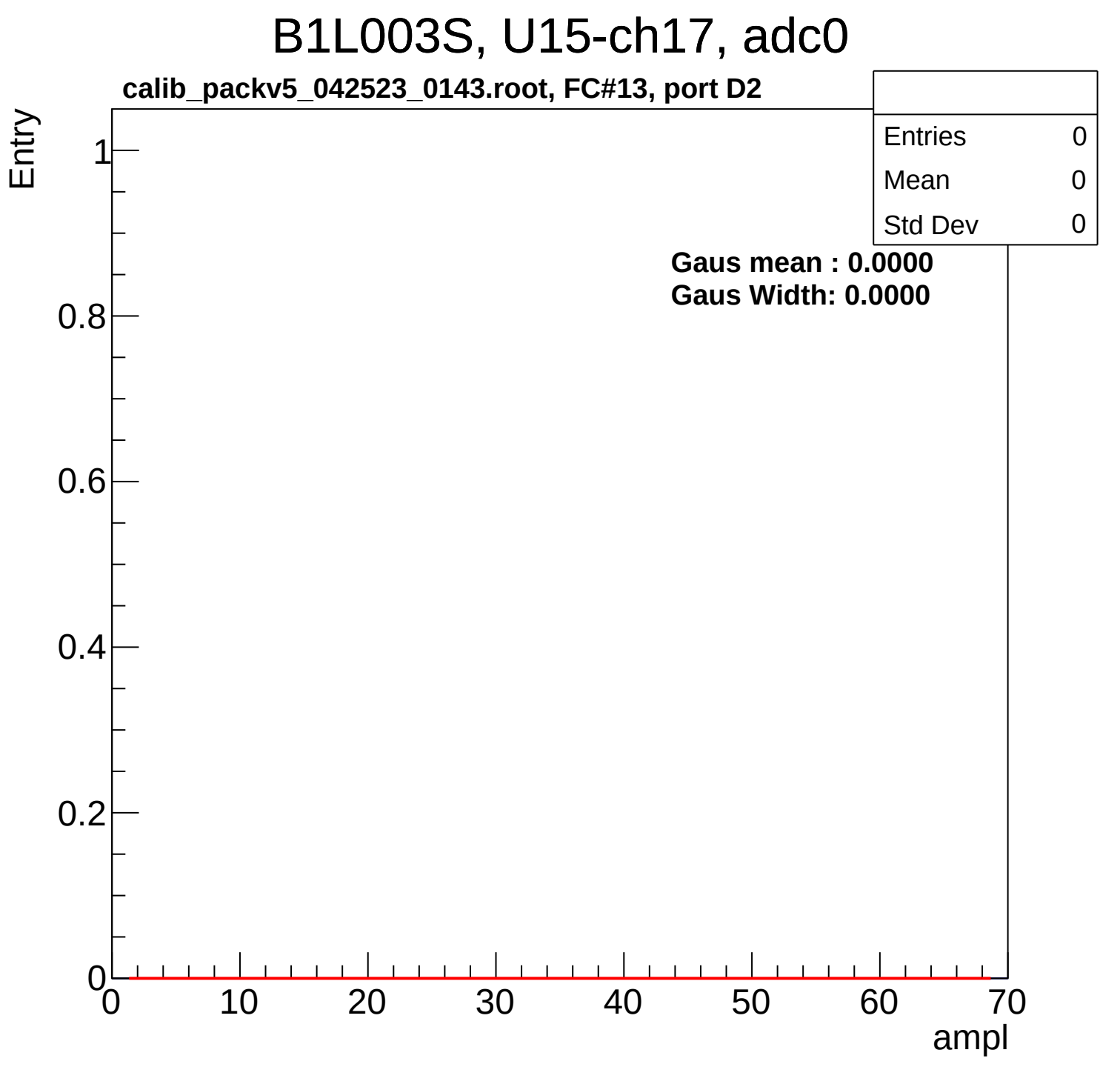
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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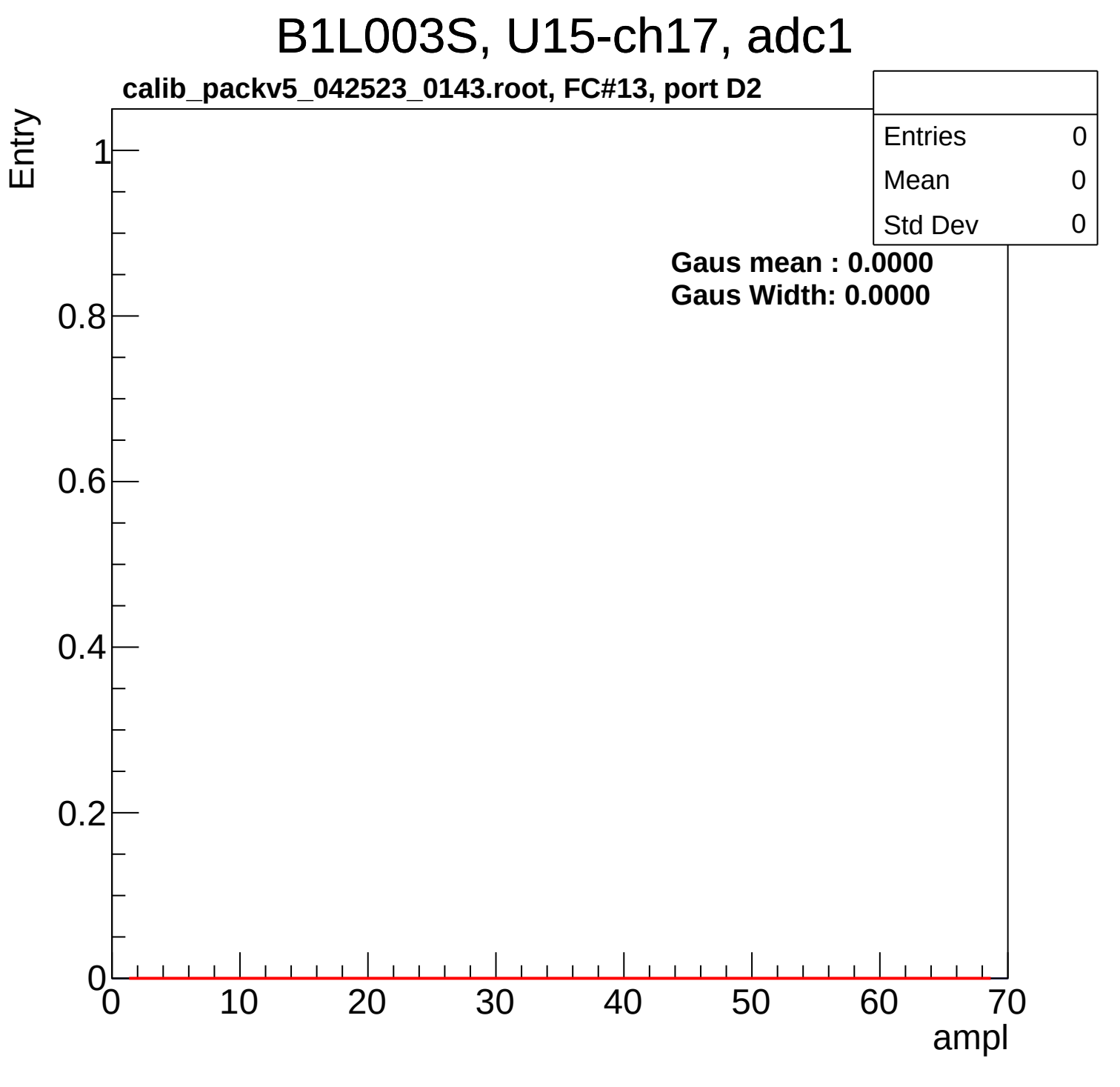
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Gaus Width: 0.0000
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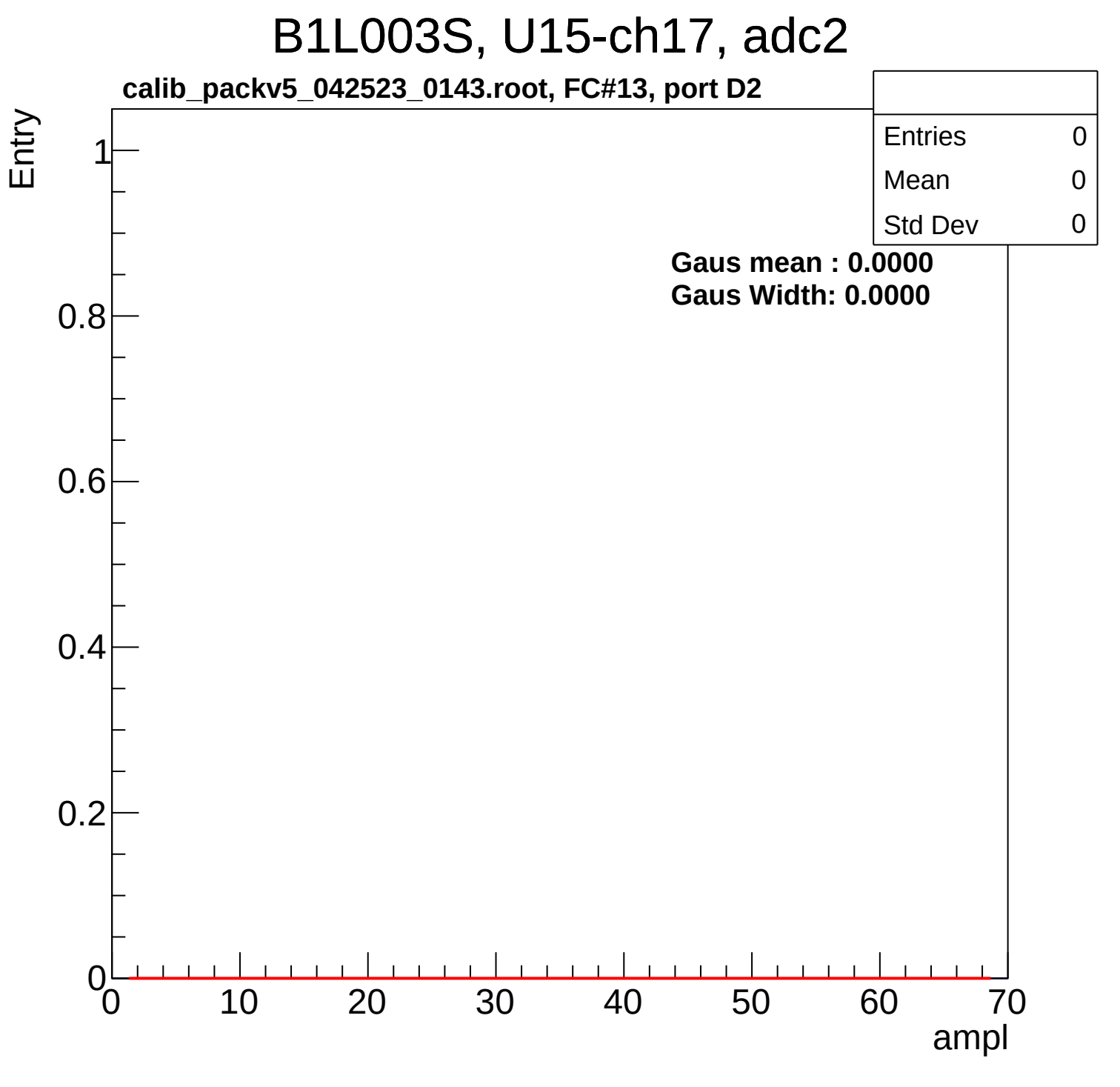
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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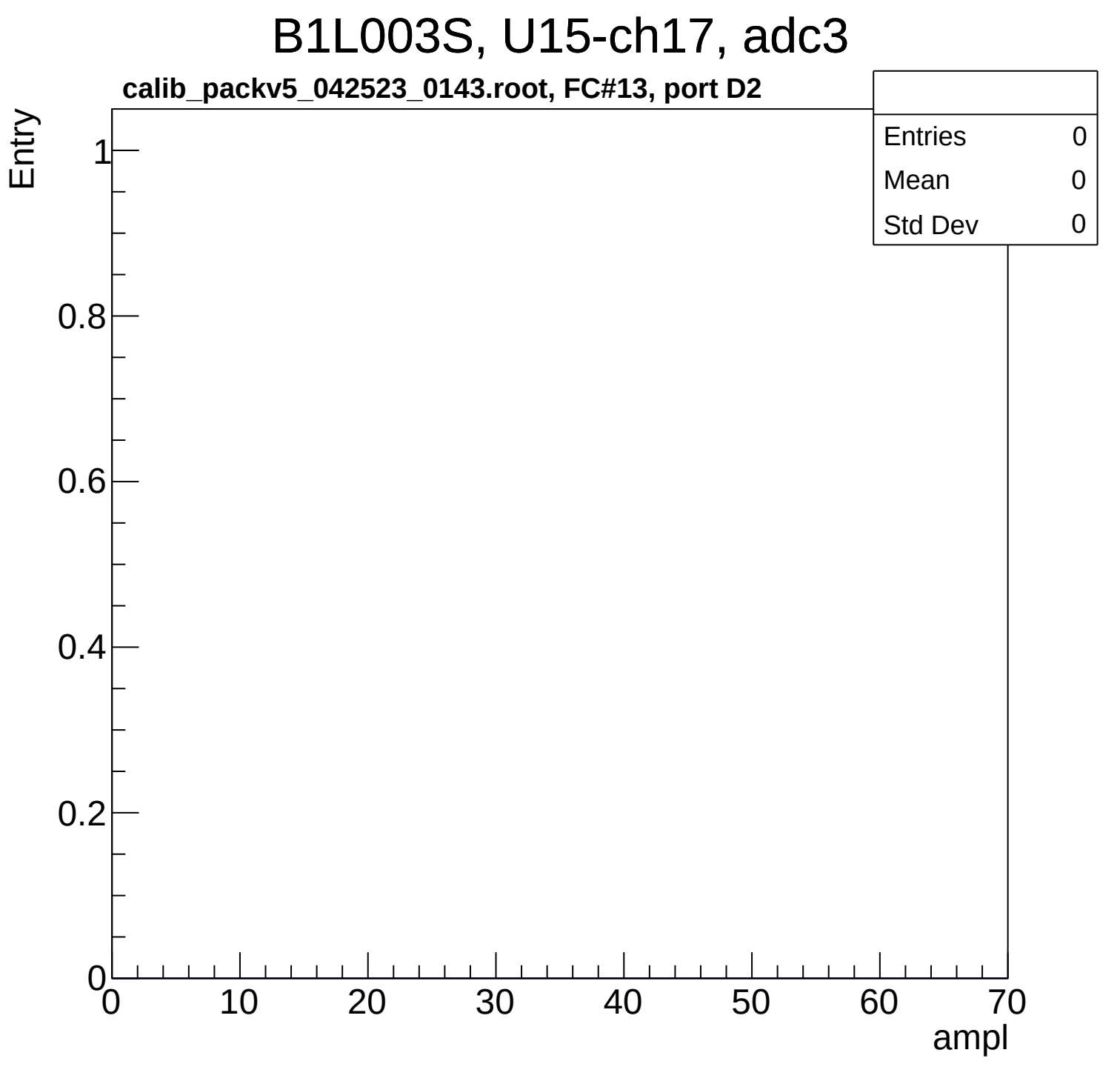
ampl


B1L003S, U15-ch17, adc3
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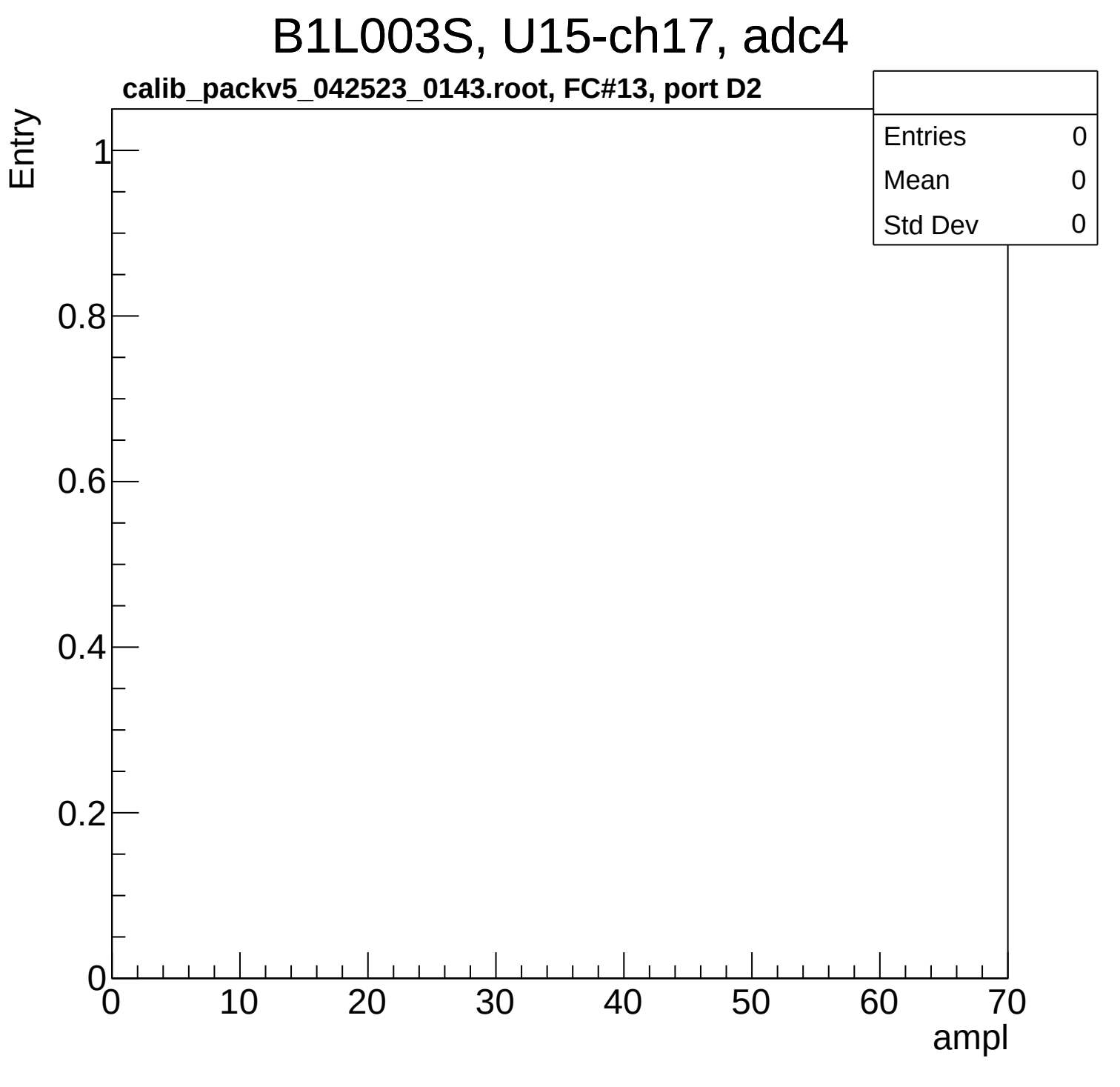
ampl


B1L003S, U15-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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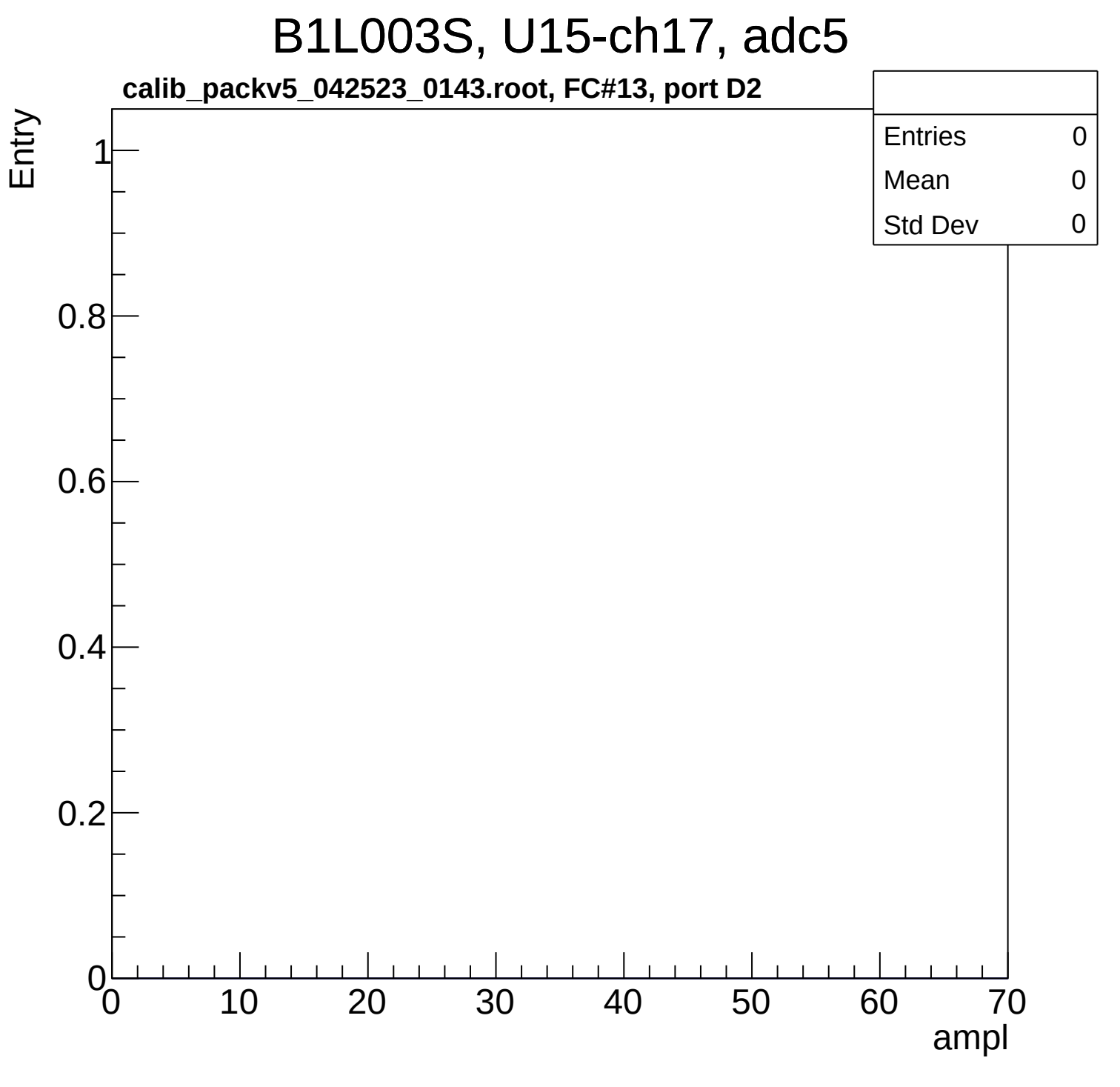
B1L003S, U15-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
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Std Dev 0
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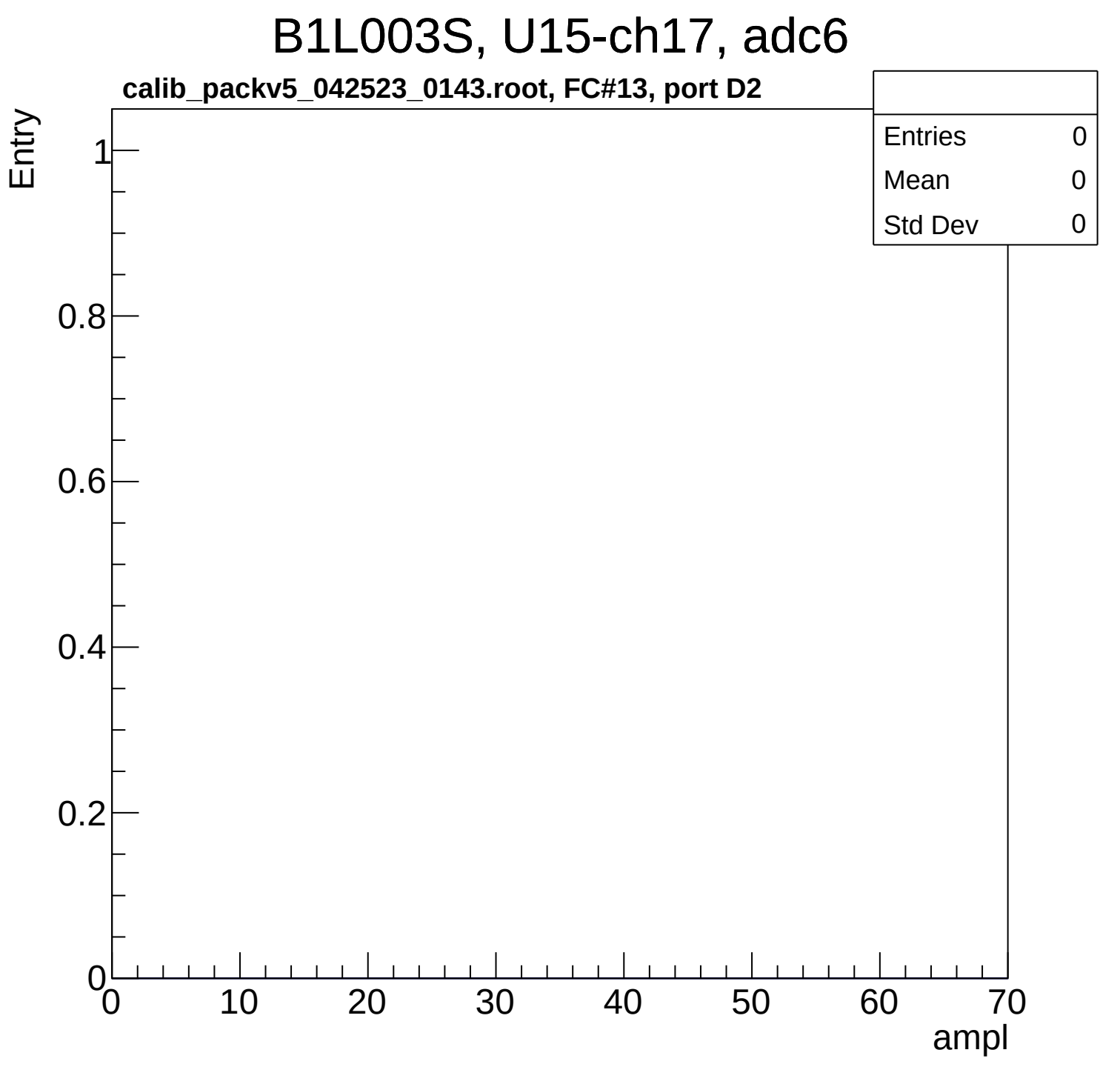
B1L003S, U15-ch17, adc6
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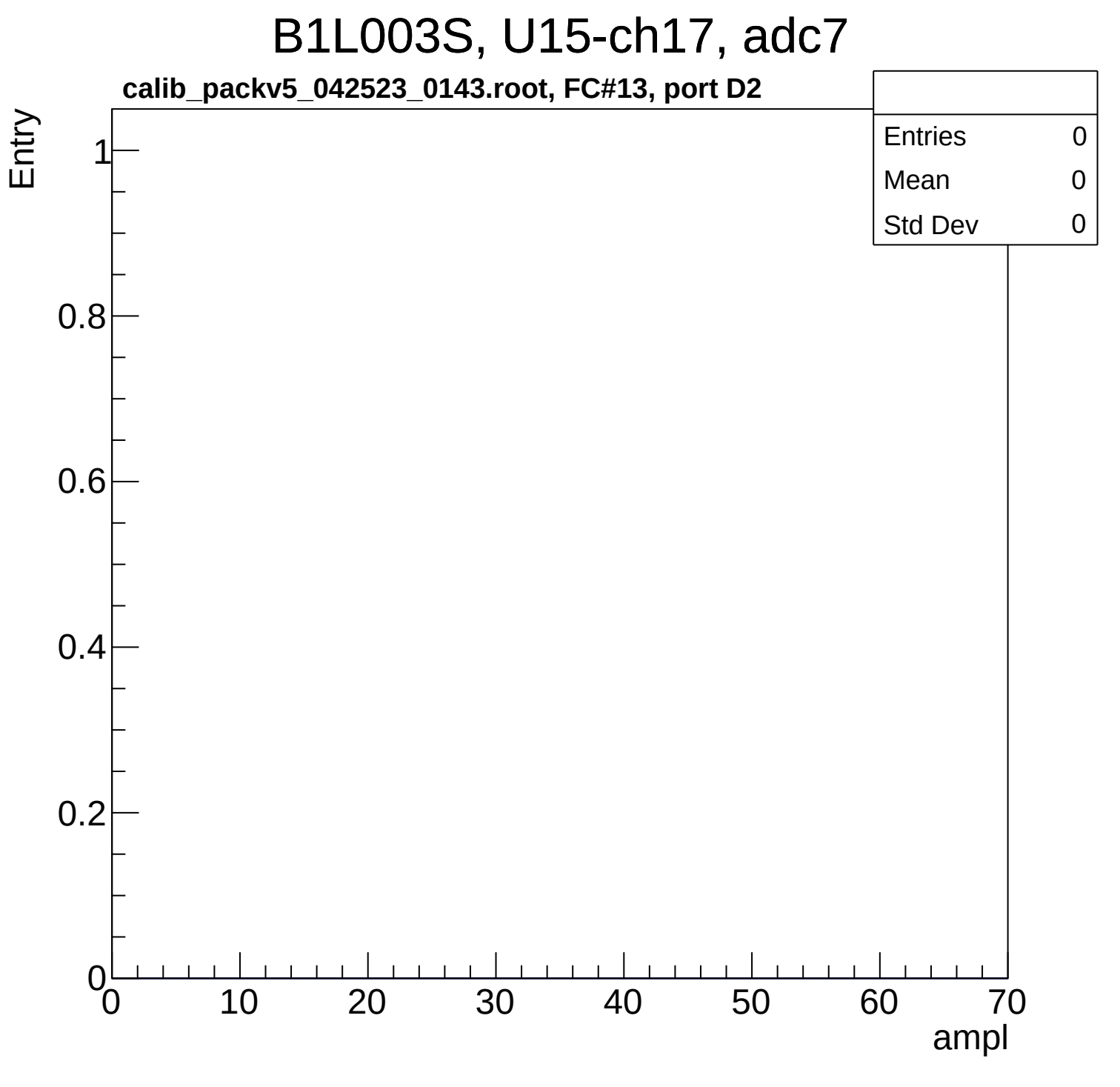
B1L003S, U15-ch17, adc7
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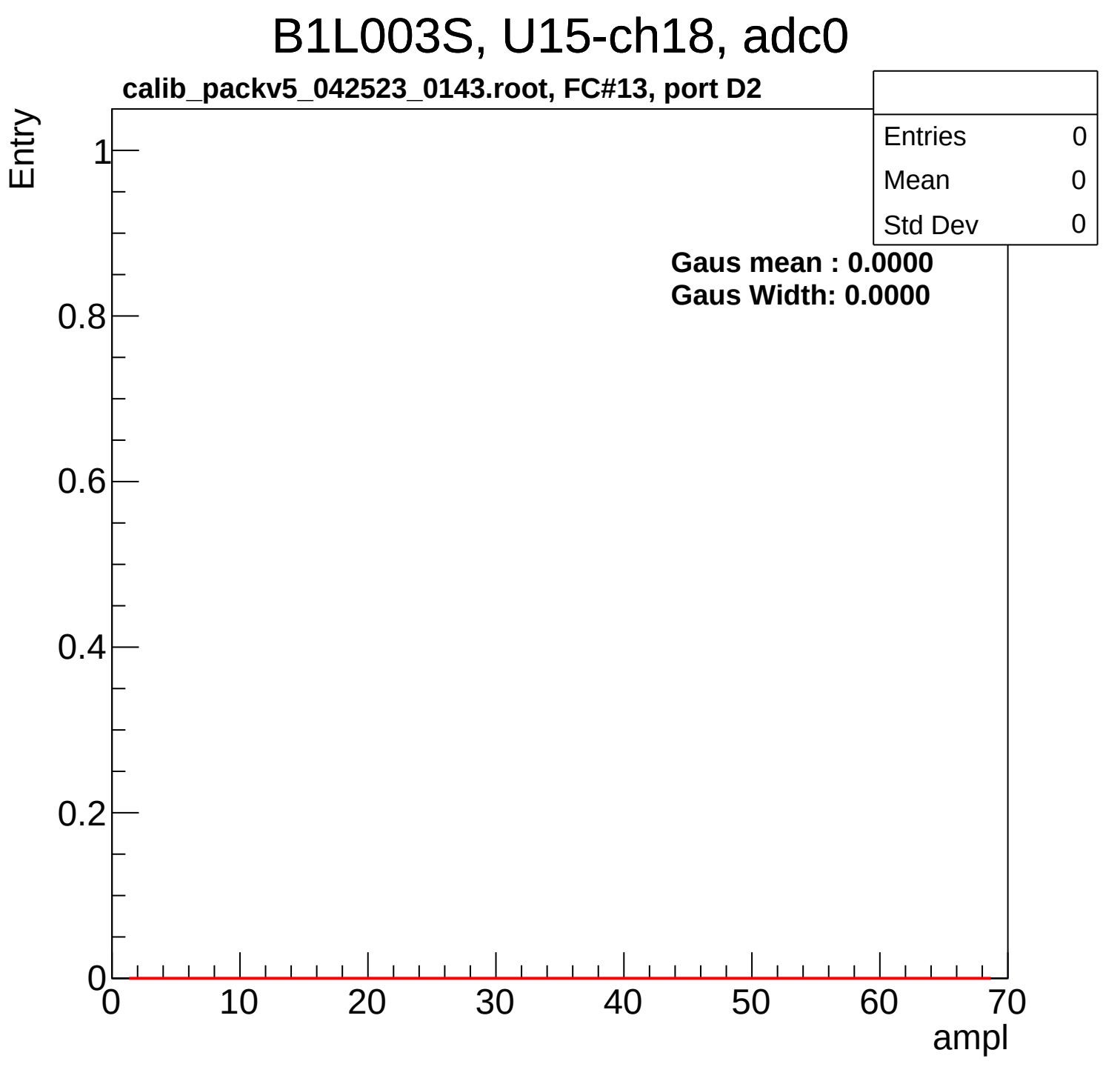
B1L003S, U15-ch18, adc0
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Std Dev 0
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Gaus Width: 0.0000
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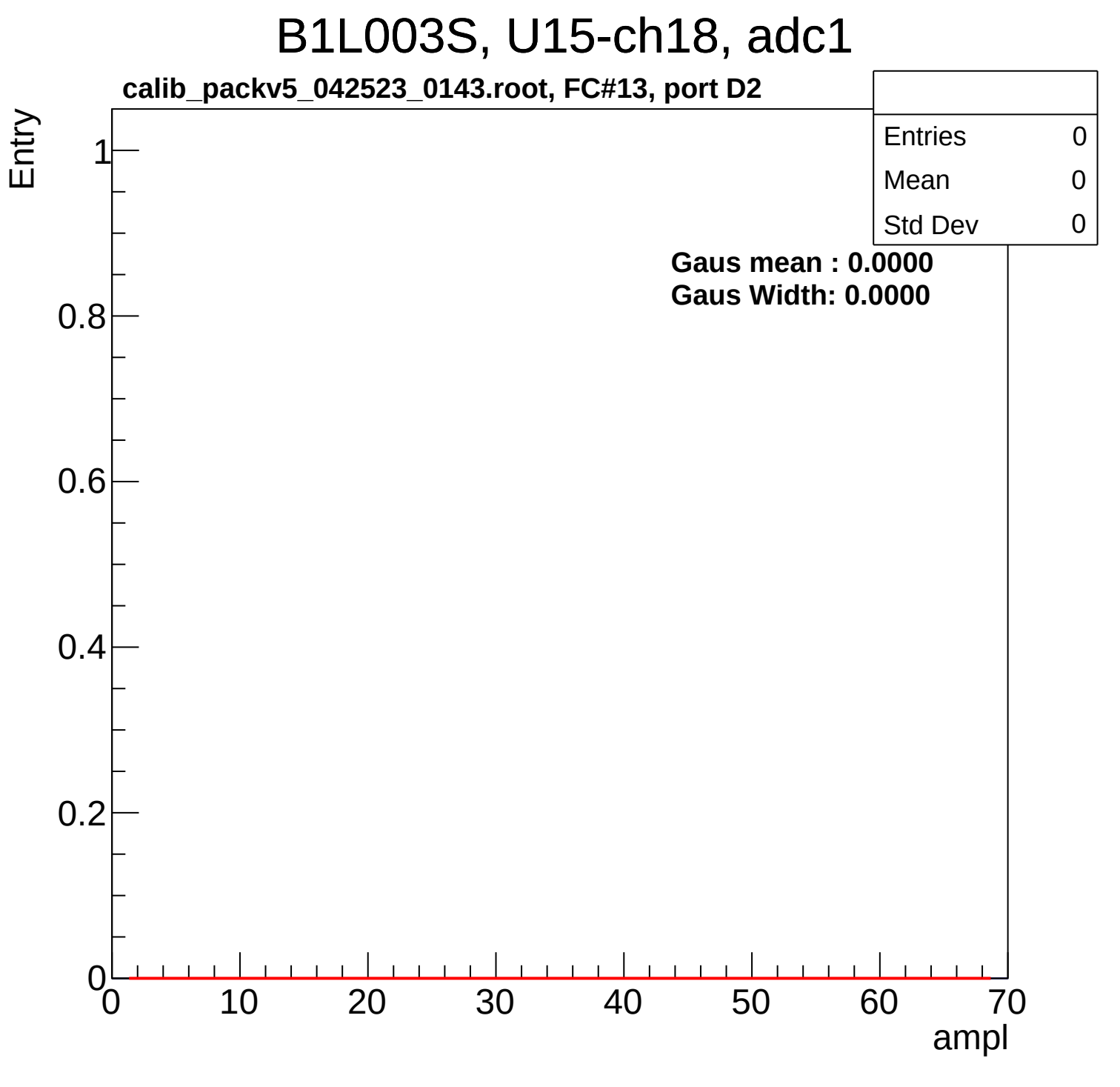
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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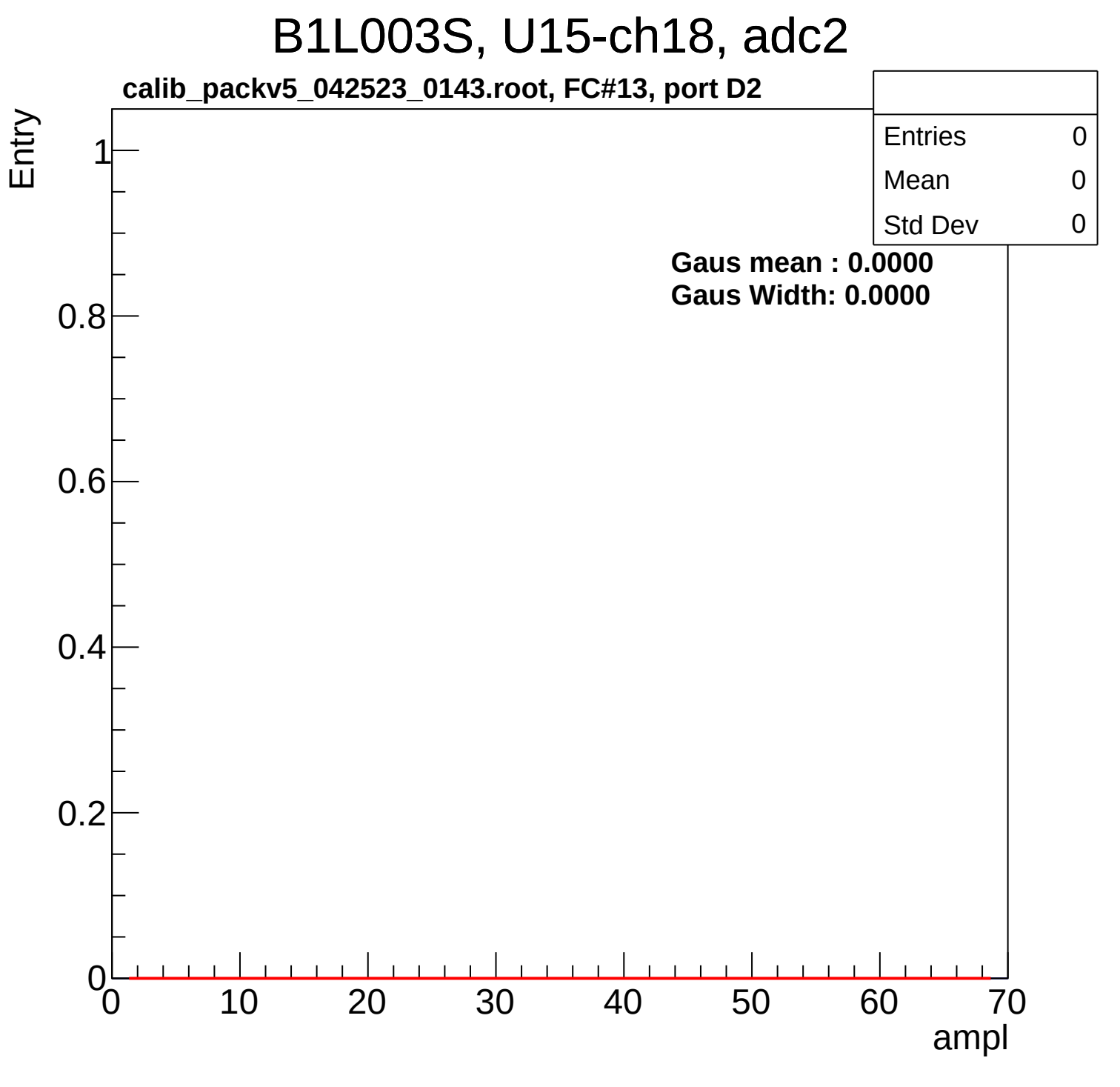
B1L003S, U15-ch18, adc2
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Std Dev 0
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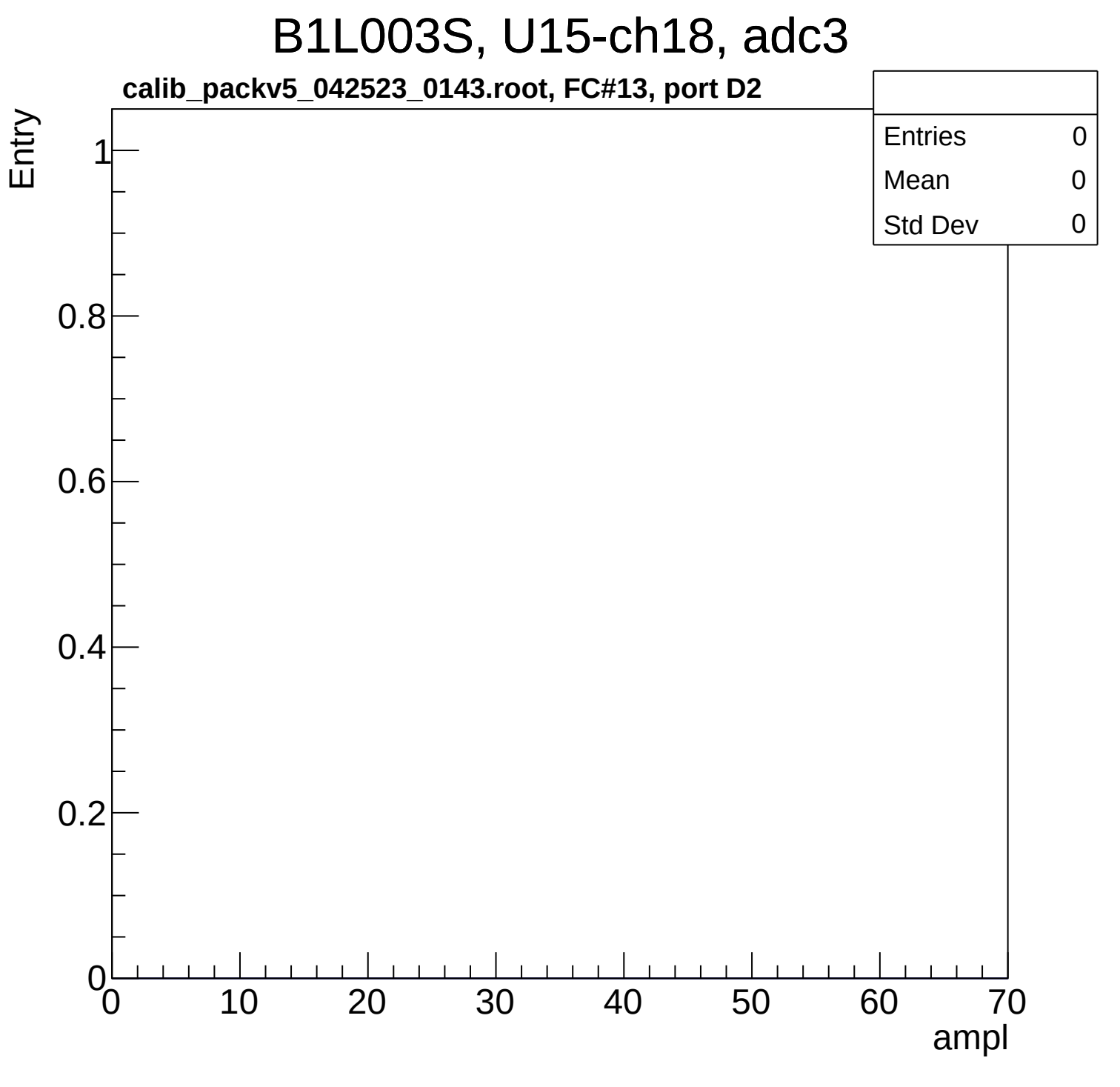
B1L003S, U15-ch18, adc3
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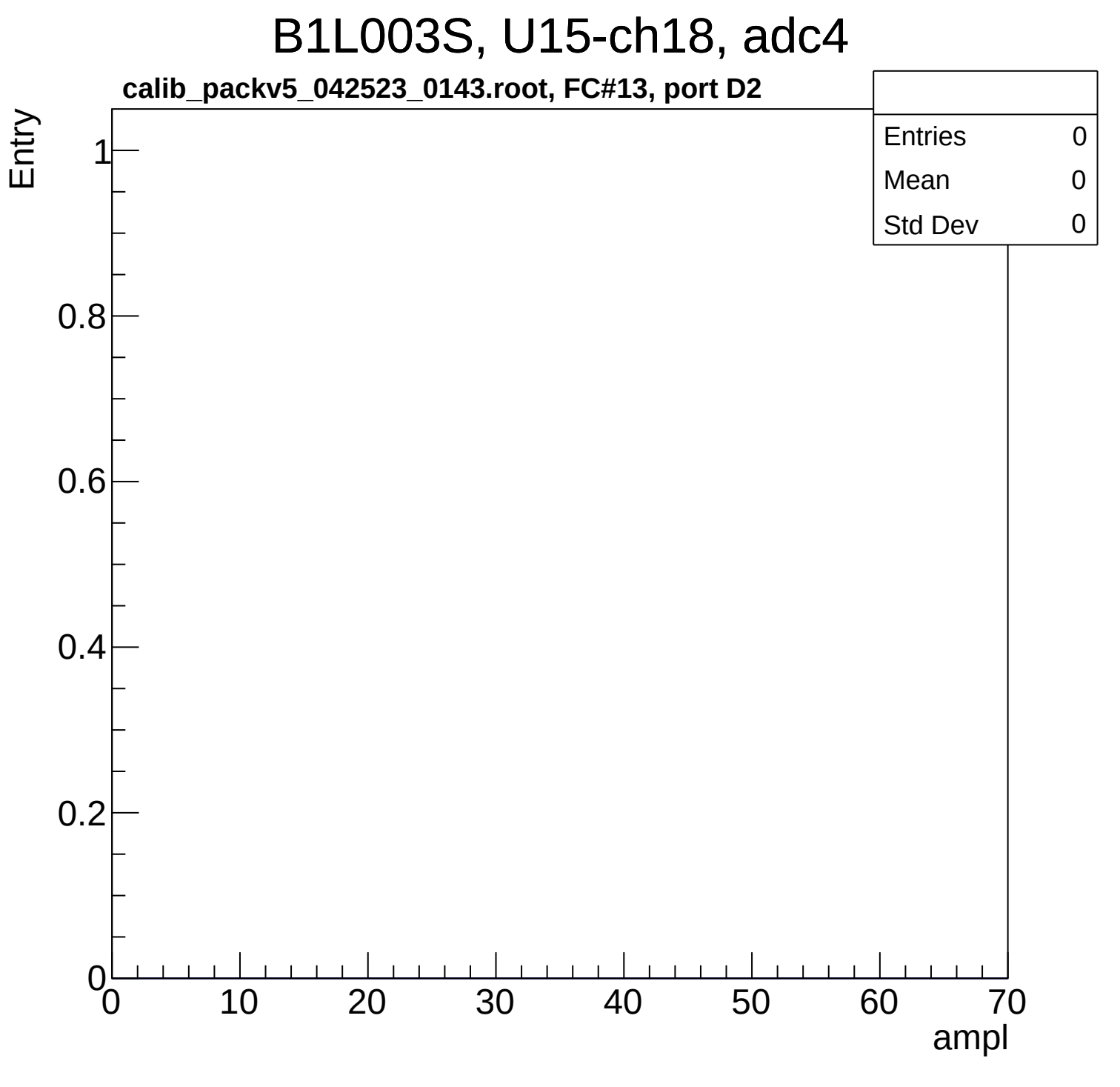
B1L003S, U15-ch18, adc4
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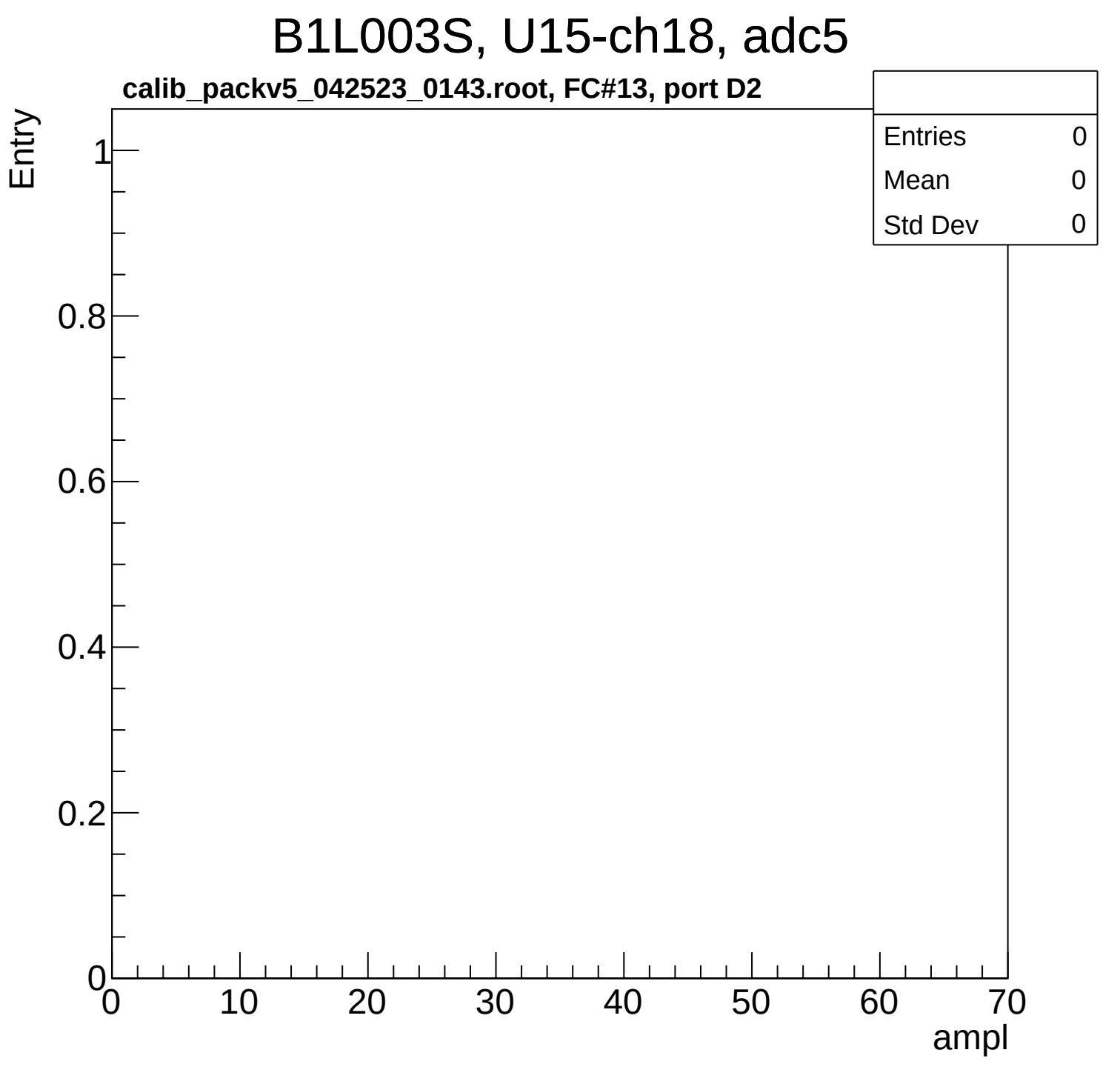
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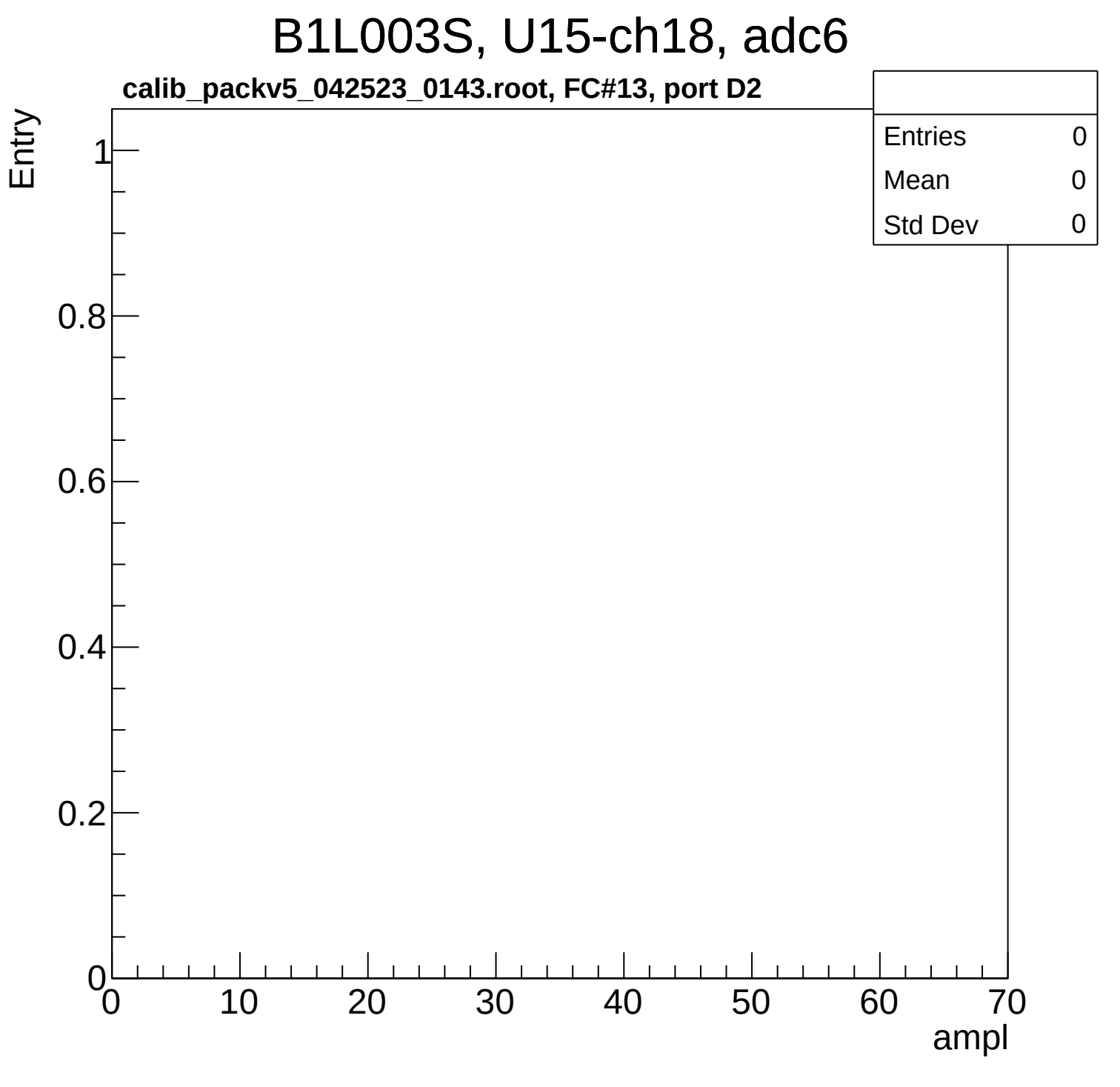
B1L003S, U15-ch18, adc6
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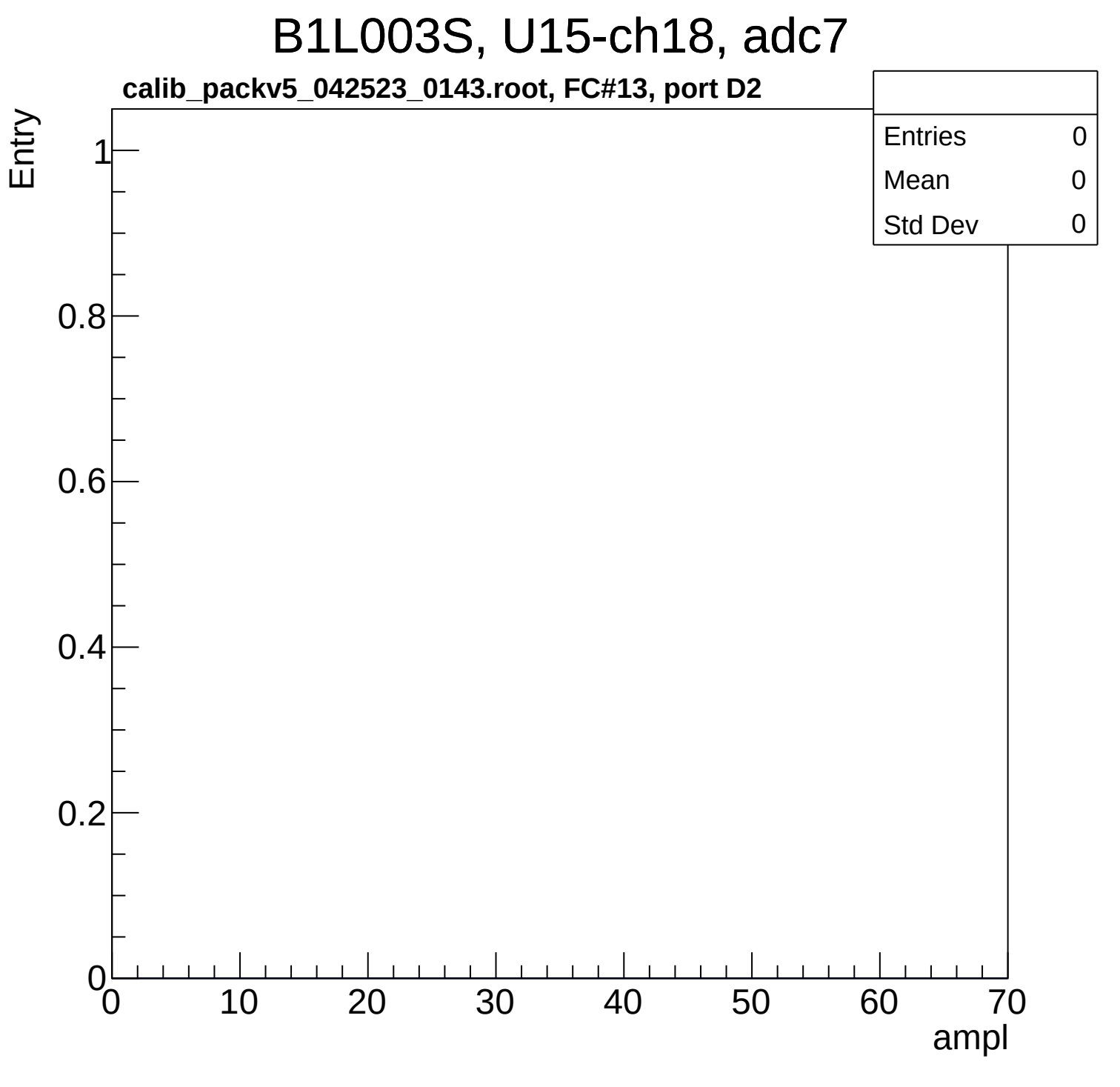
B1L003S, U15-ch18, adc7
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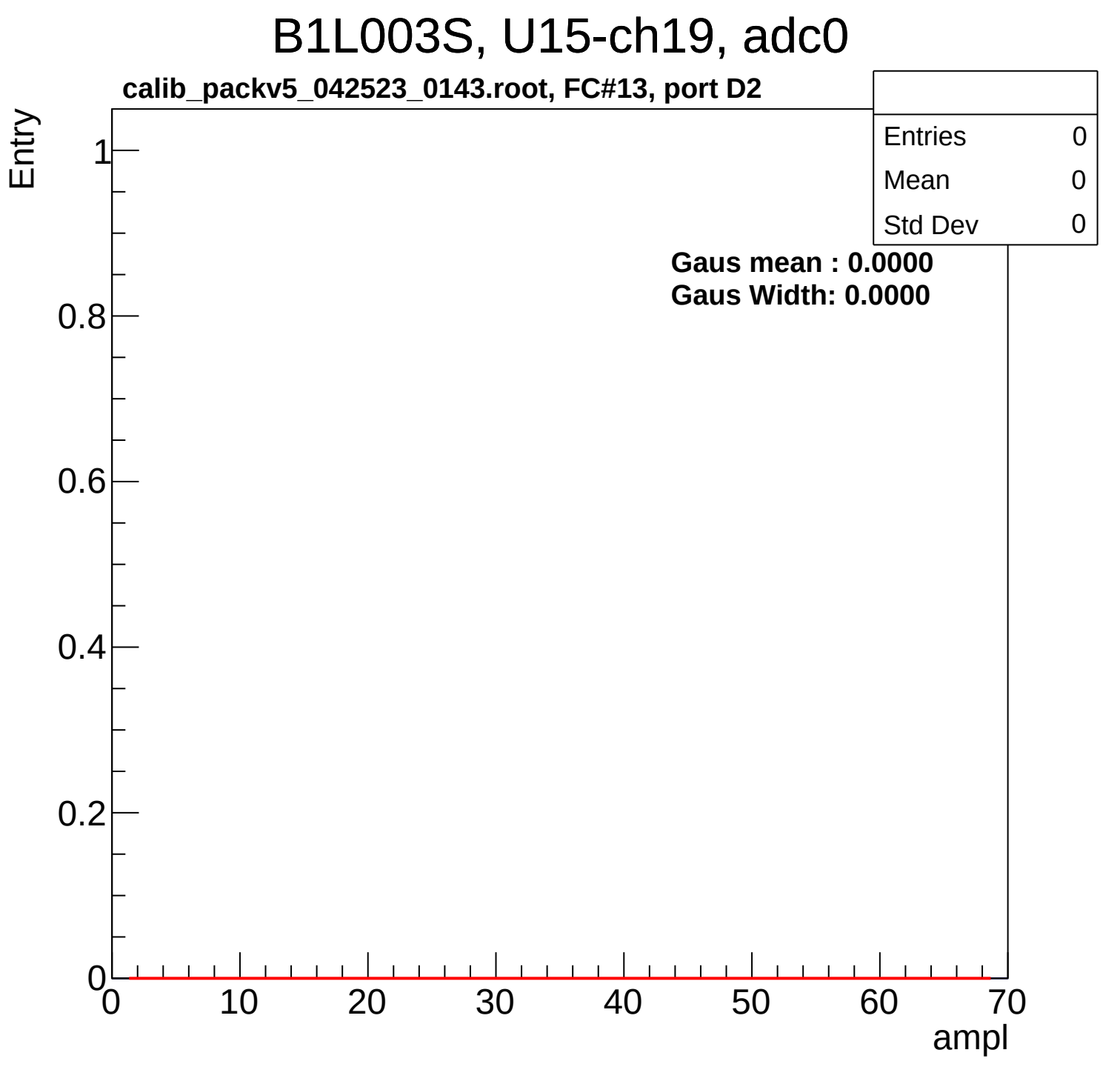
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Gaus Width: 0.0000
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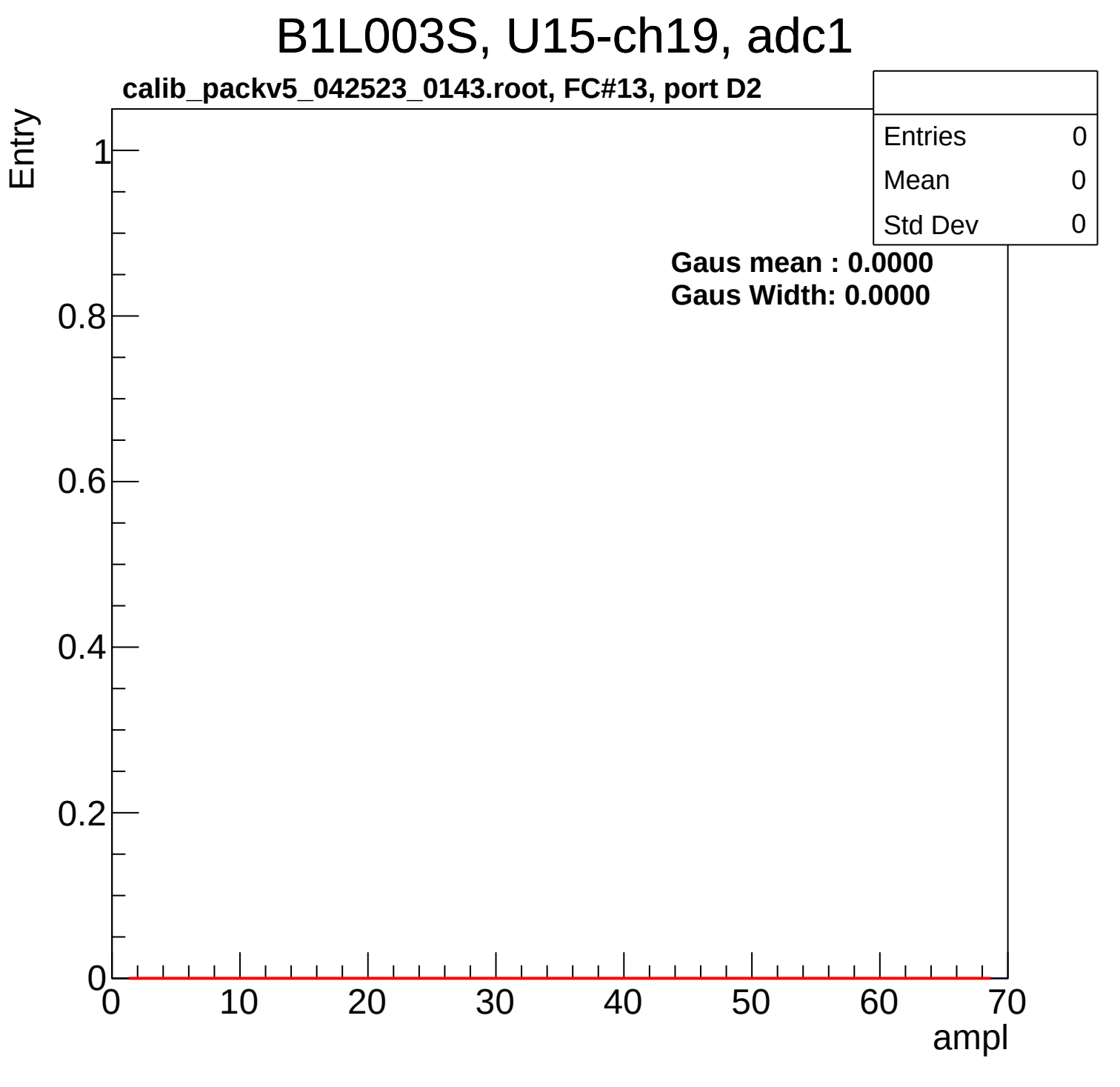
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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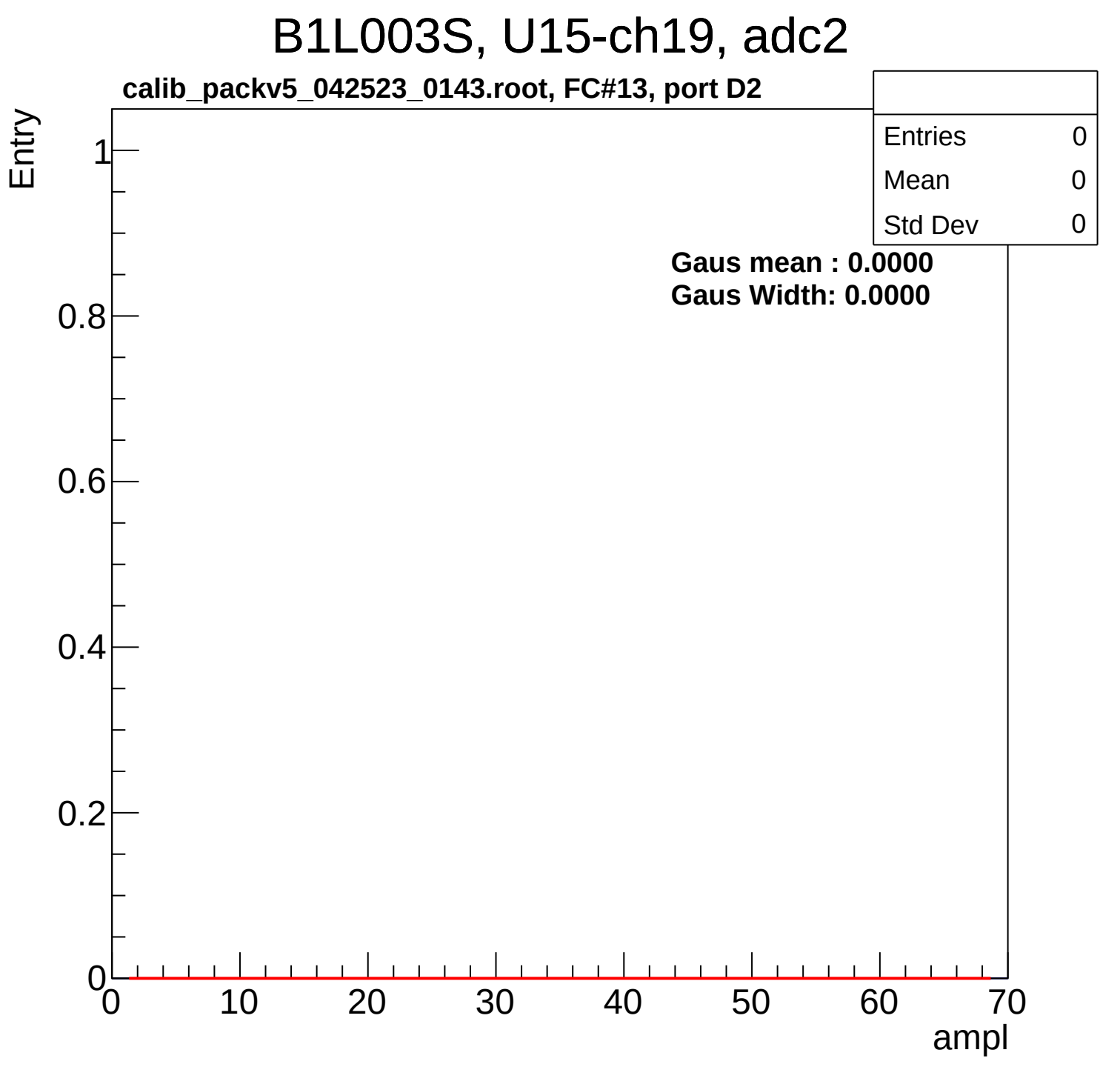
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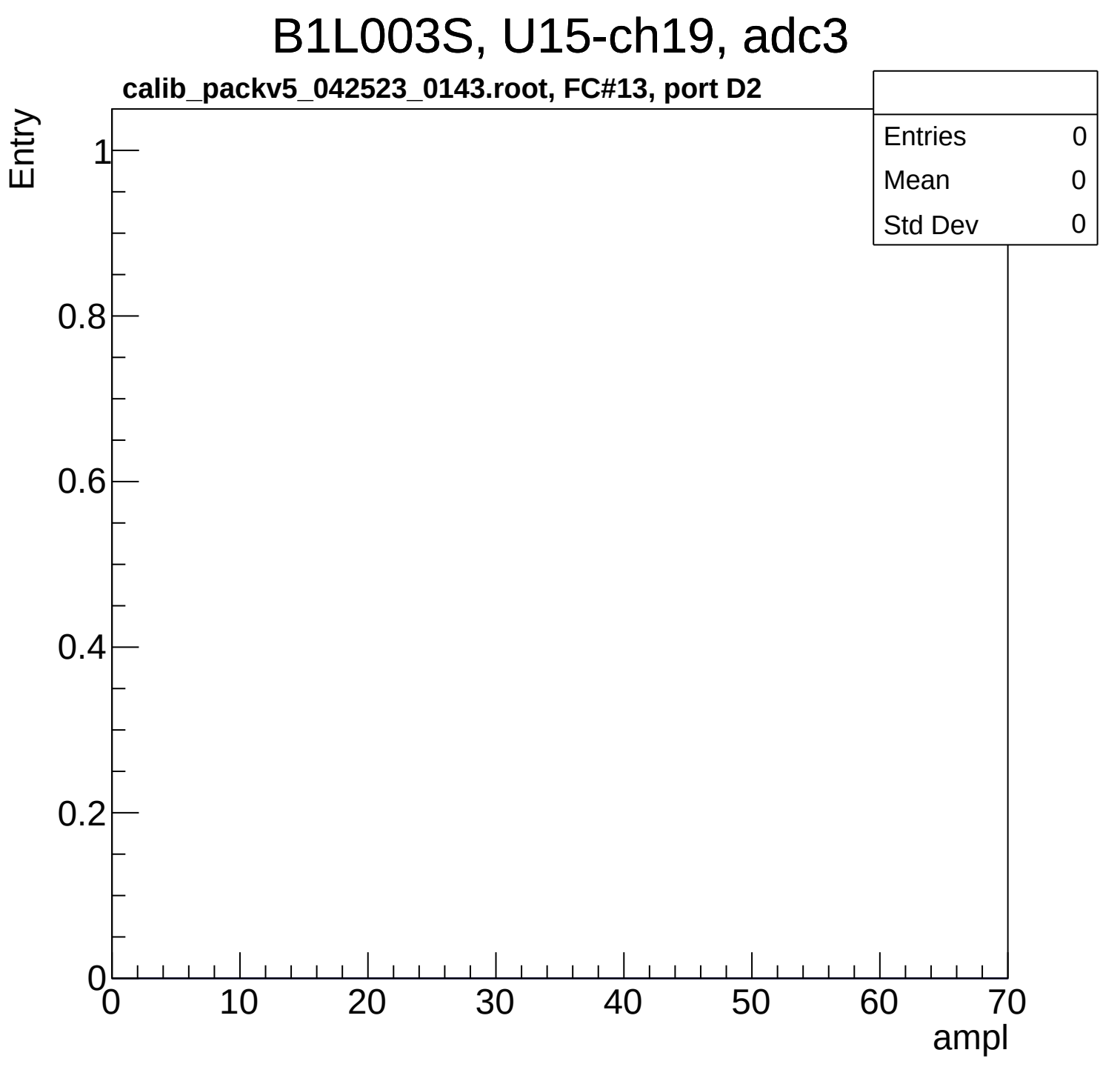
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Entries 0
Mean 0
Std Dev 0
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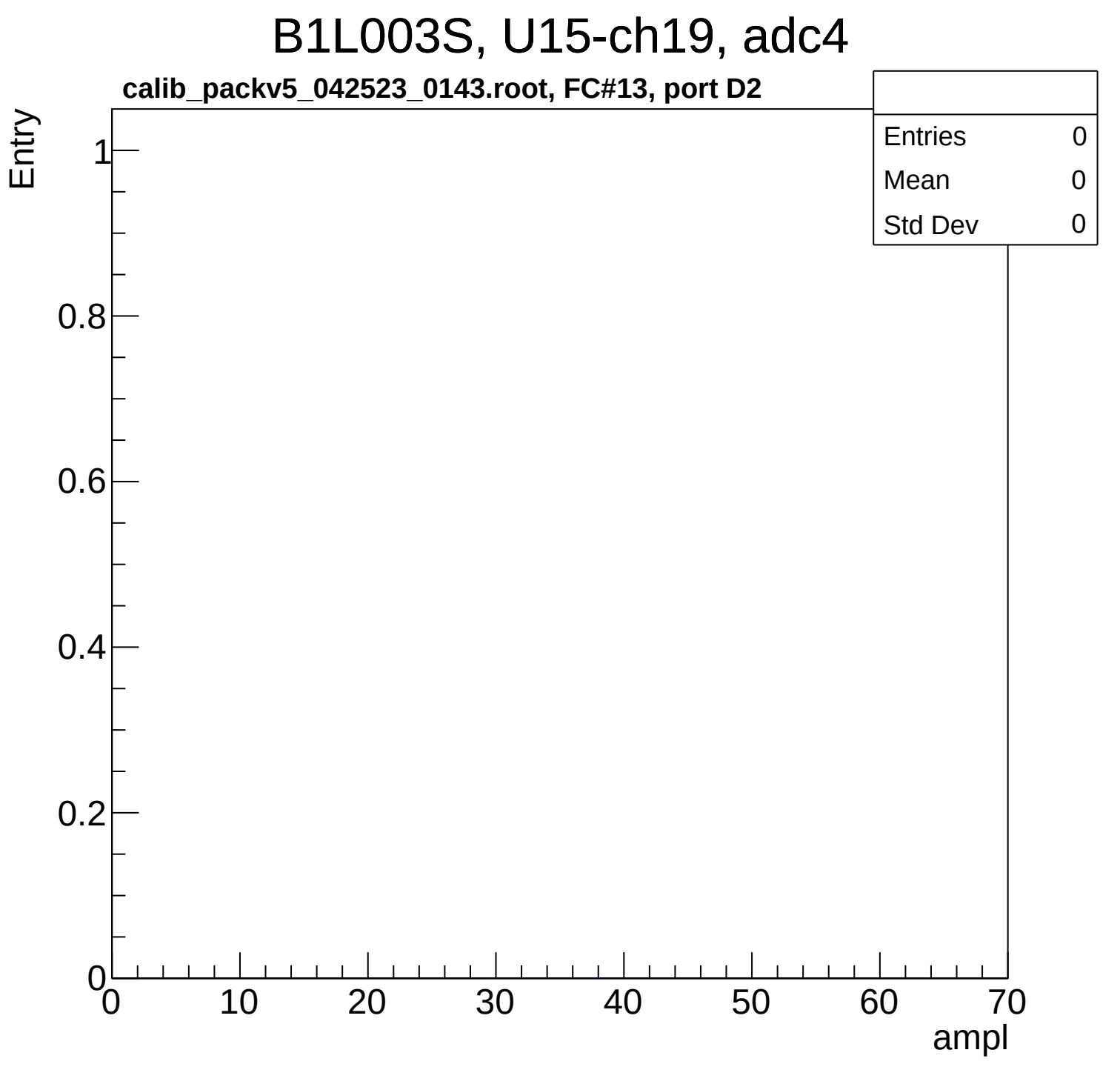
B1L003S, U15-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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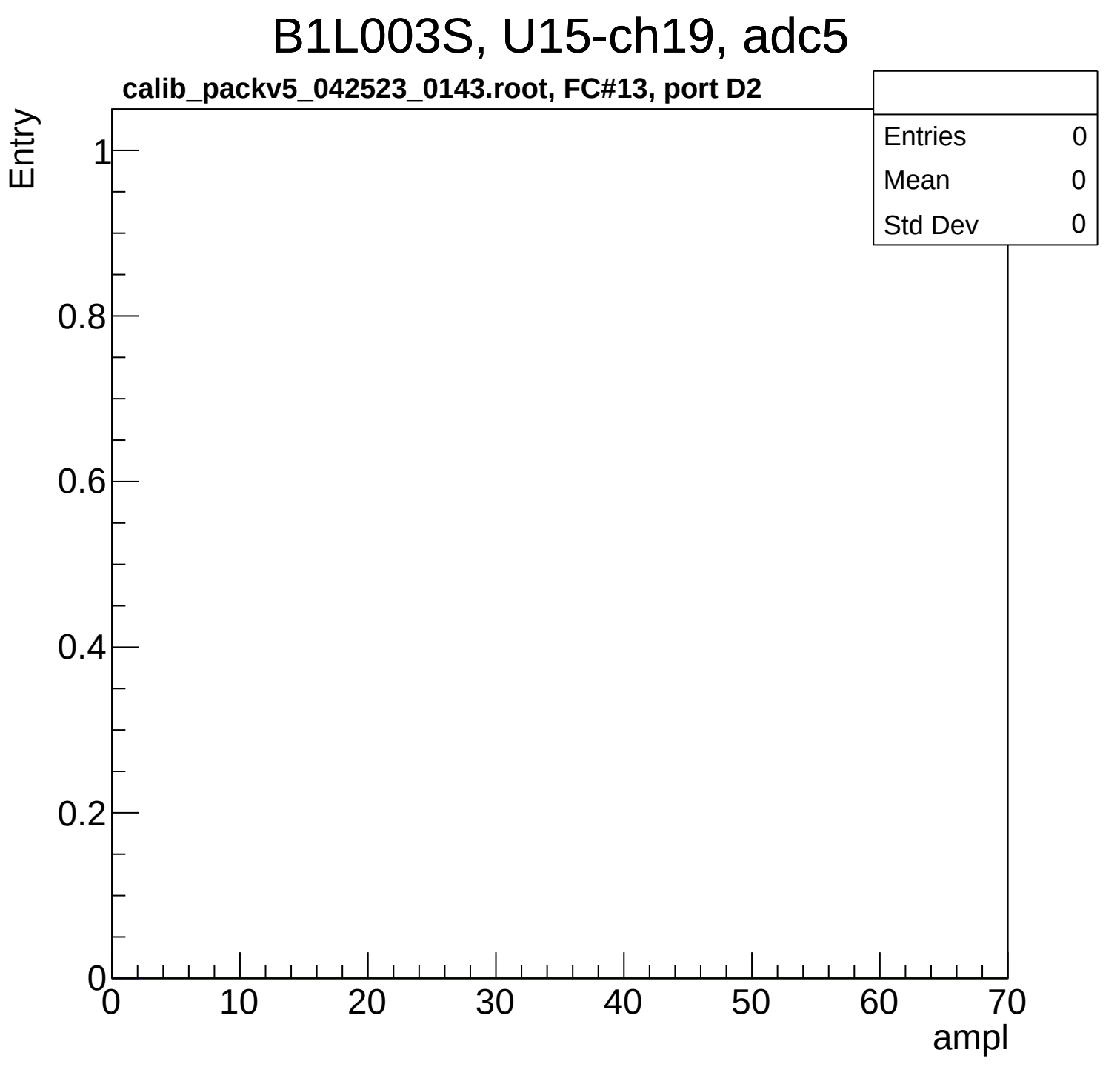
B1L003S, U15-ch19, adc5
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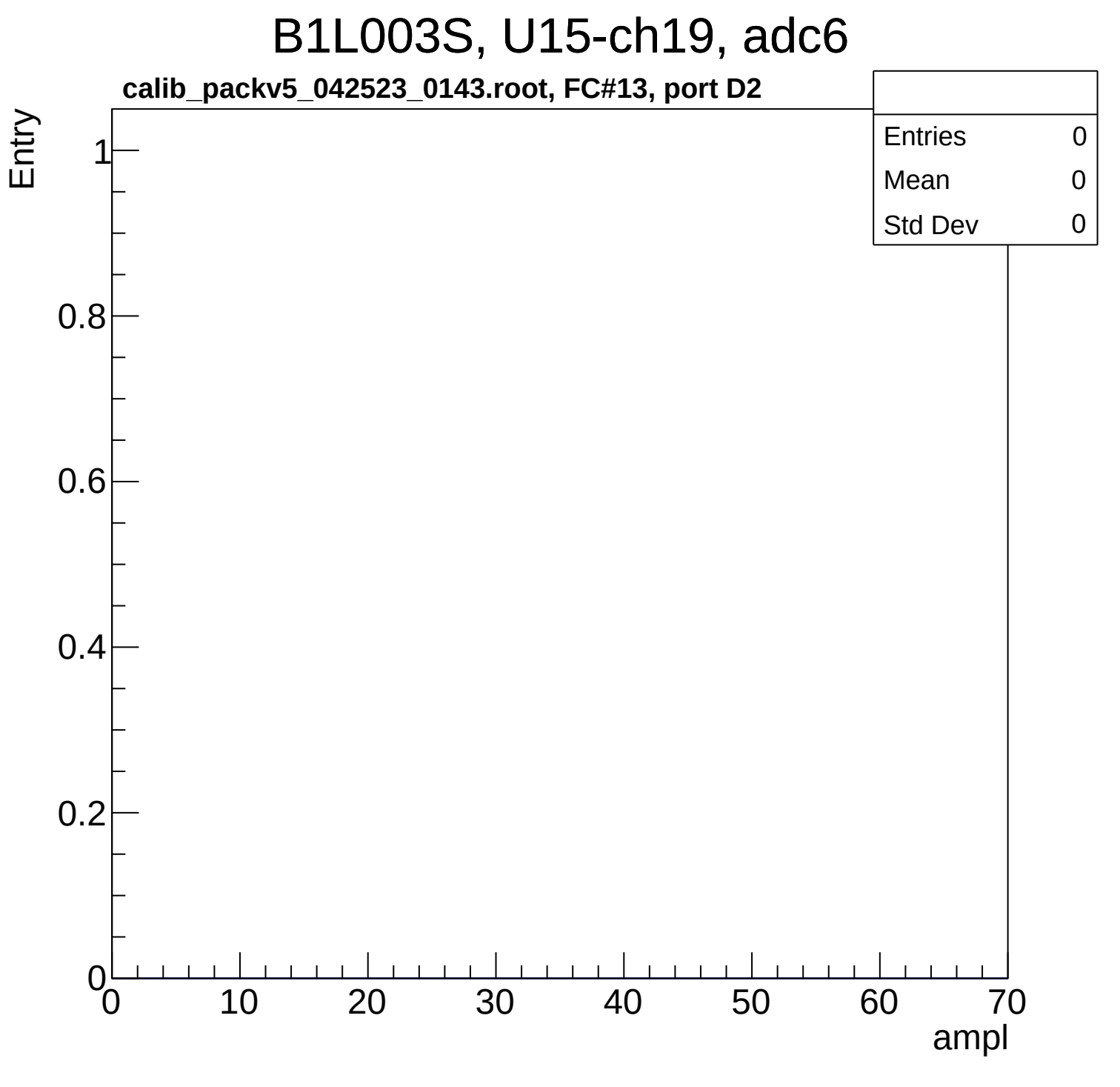
B1L003S, U15-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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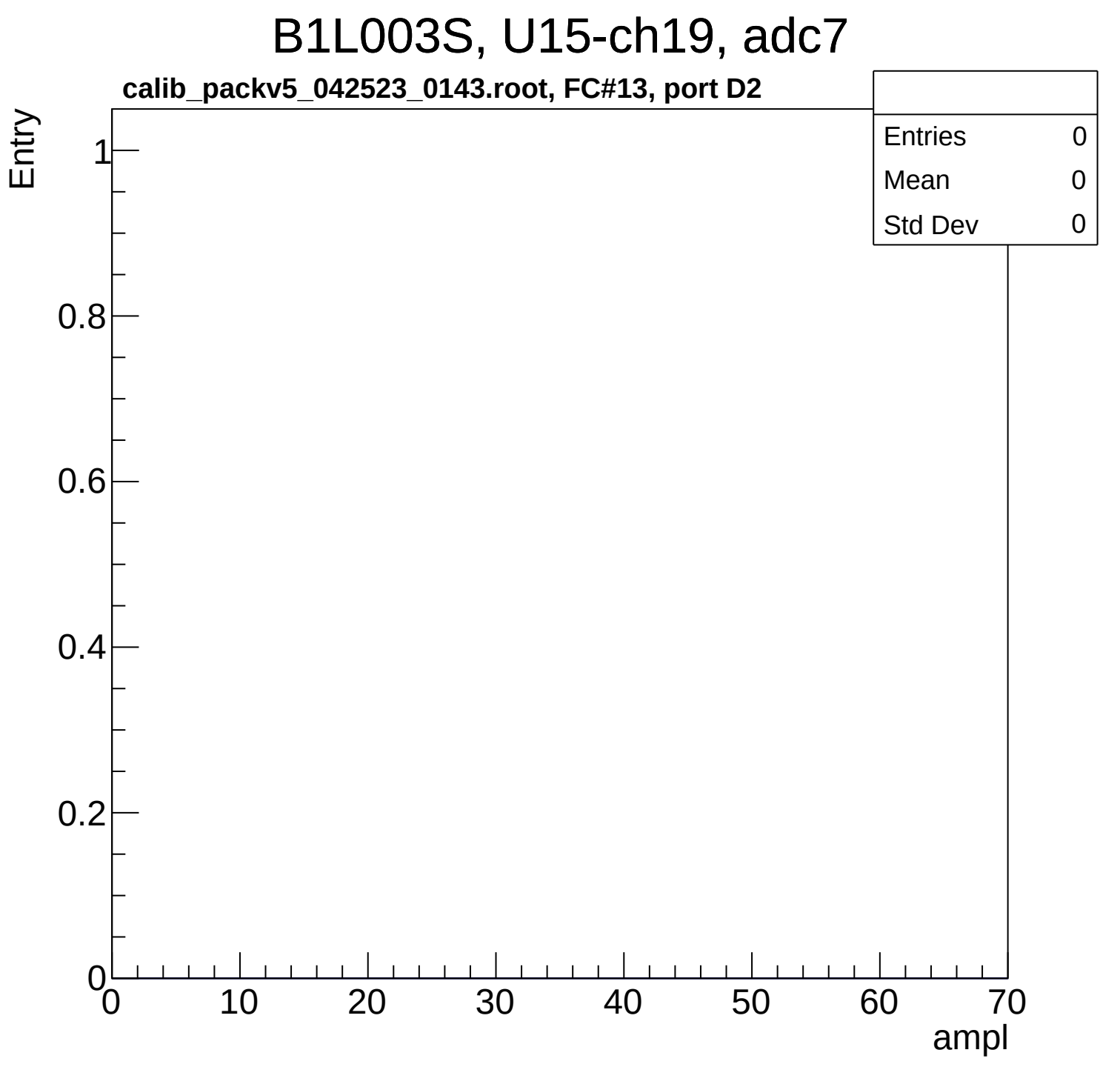
ampl


B1L003S, U15-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
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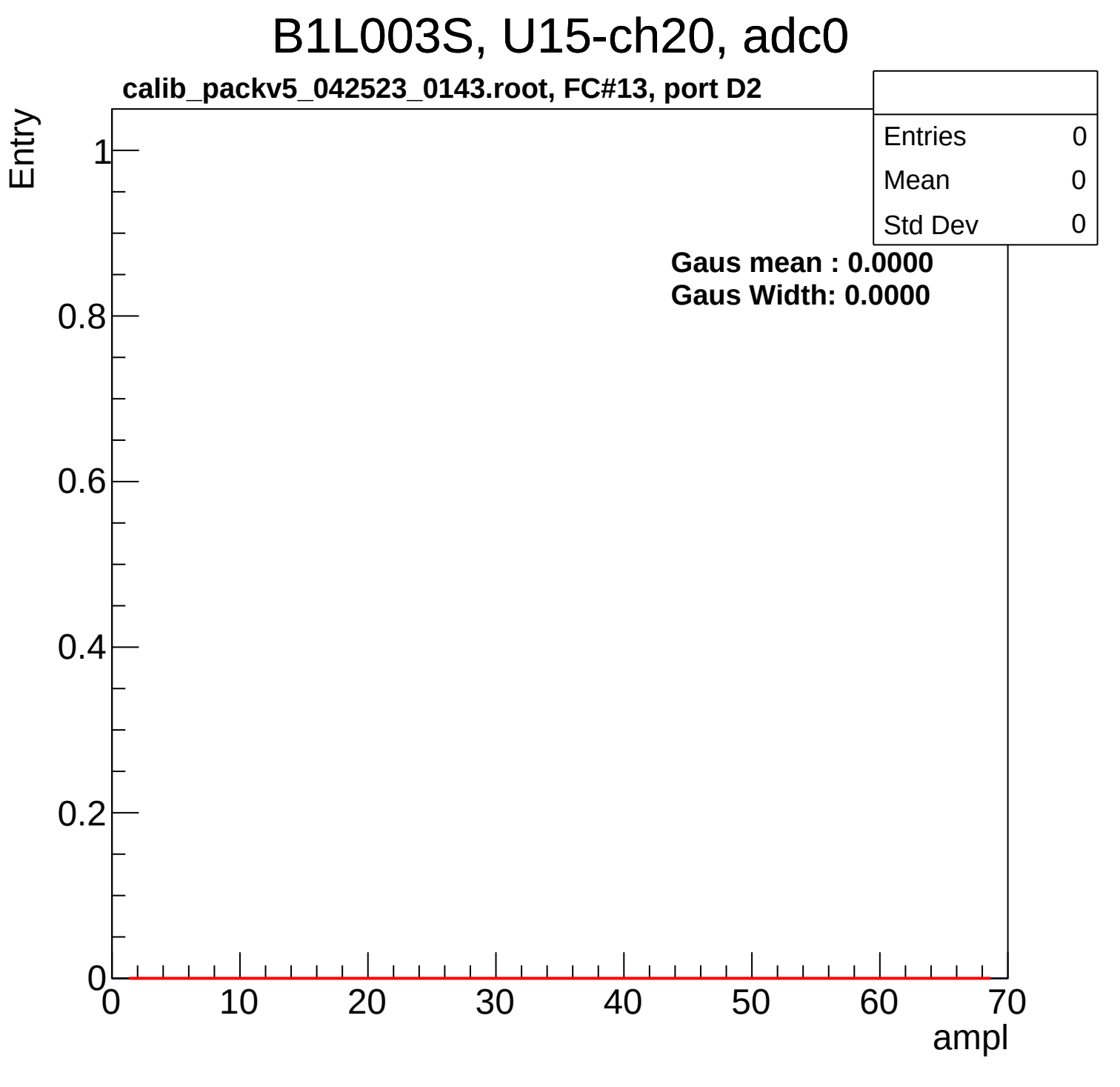
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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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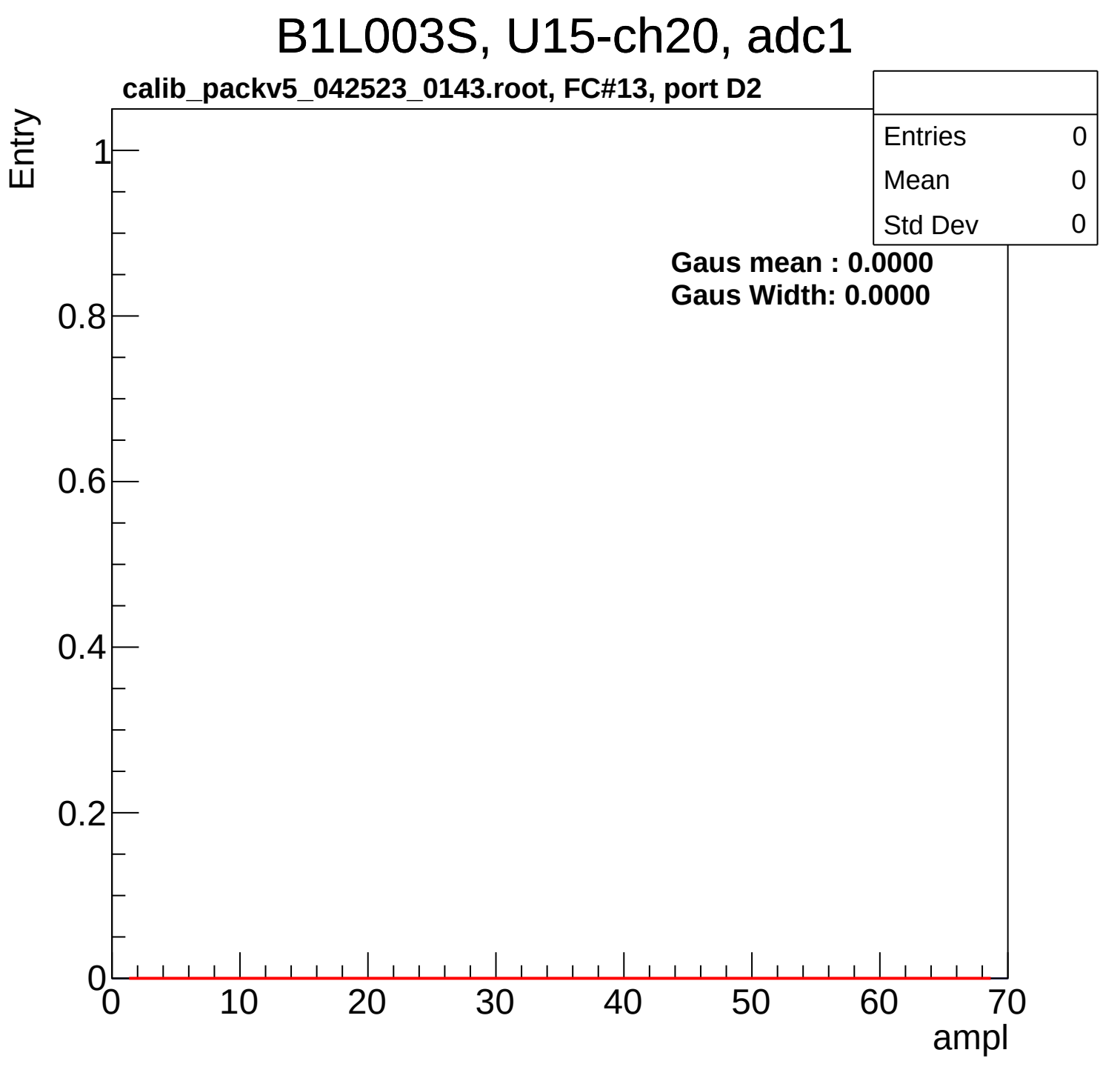
ampl


B1L003S, U15-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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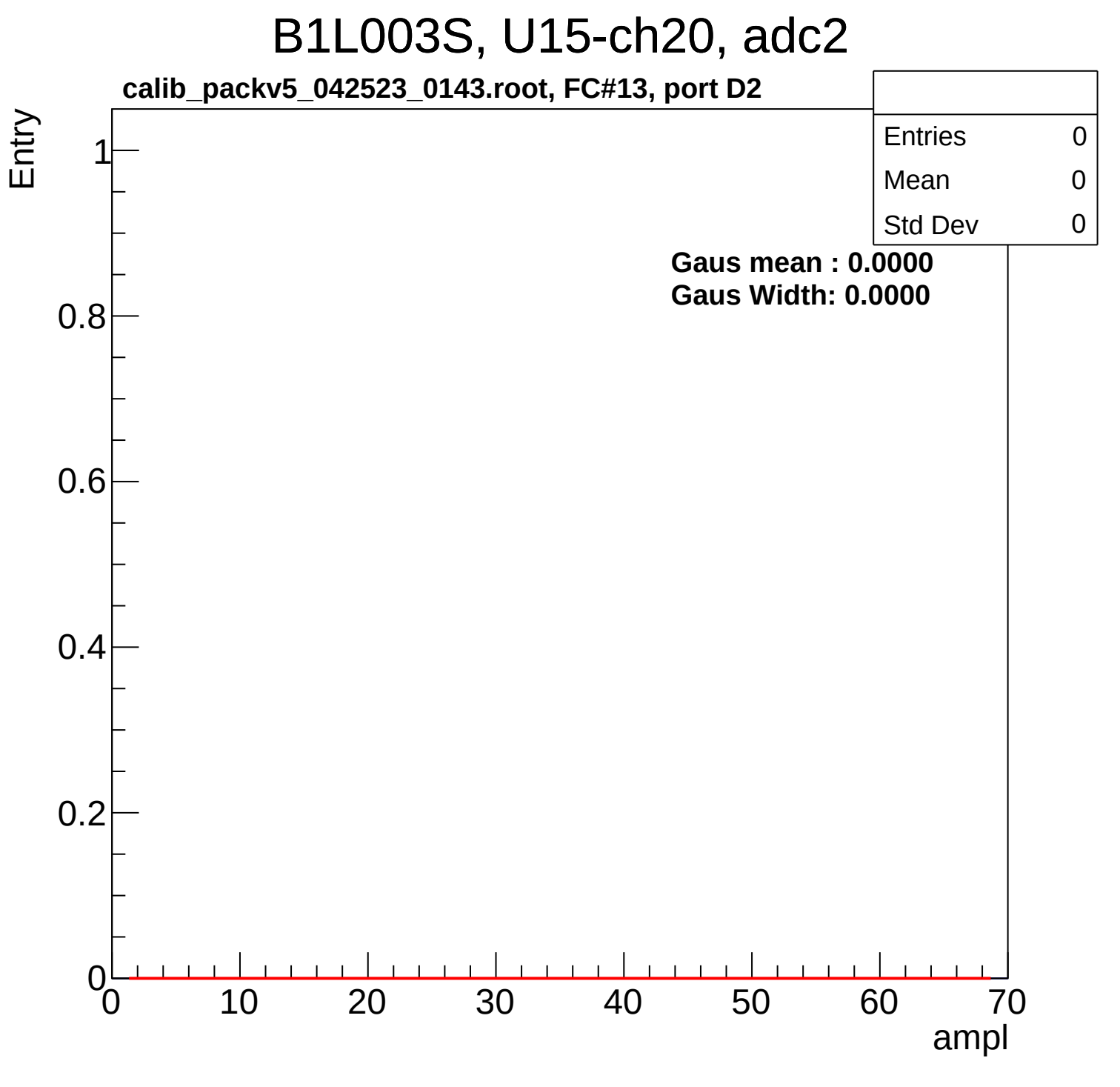
ampl


B1L003S, U15-ch20, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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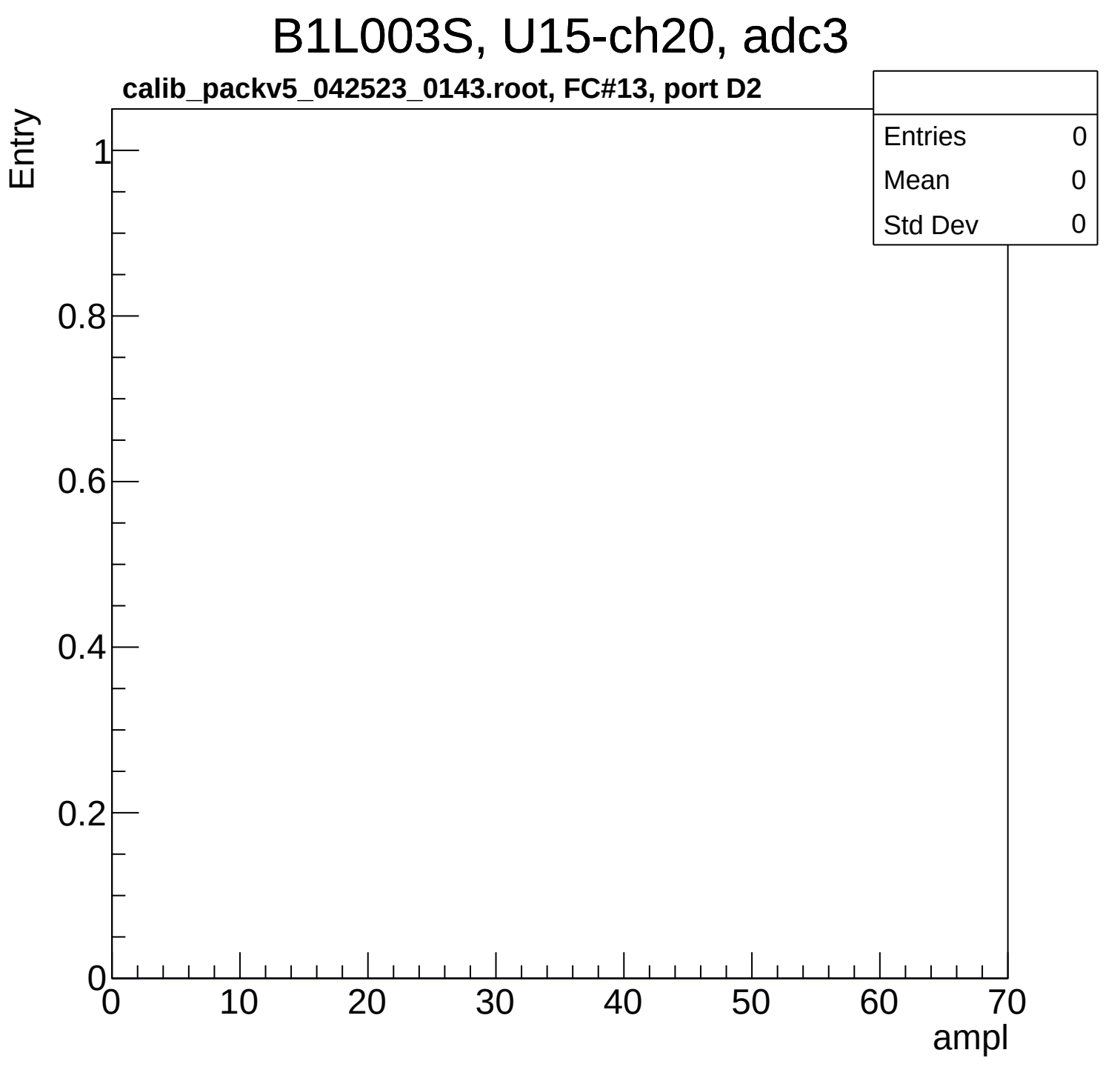
ampl


B1L003S, U15-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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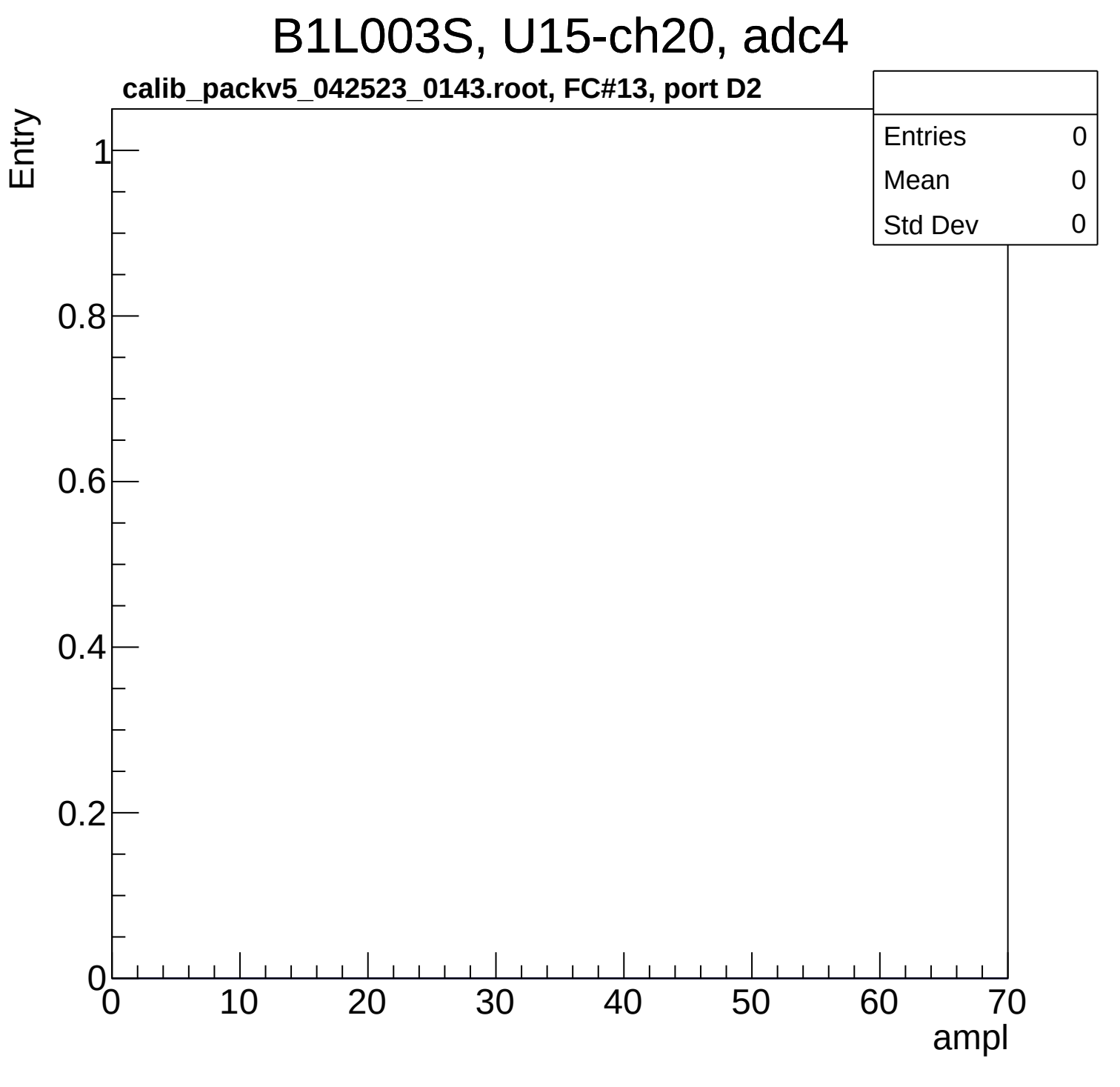
ampl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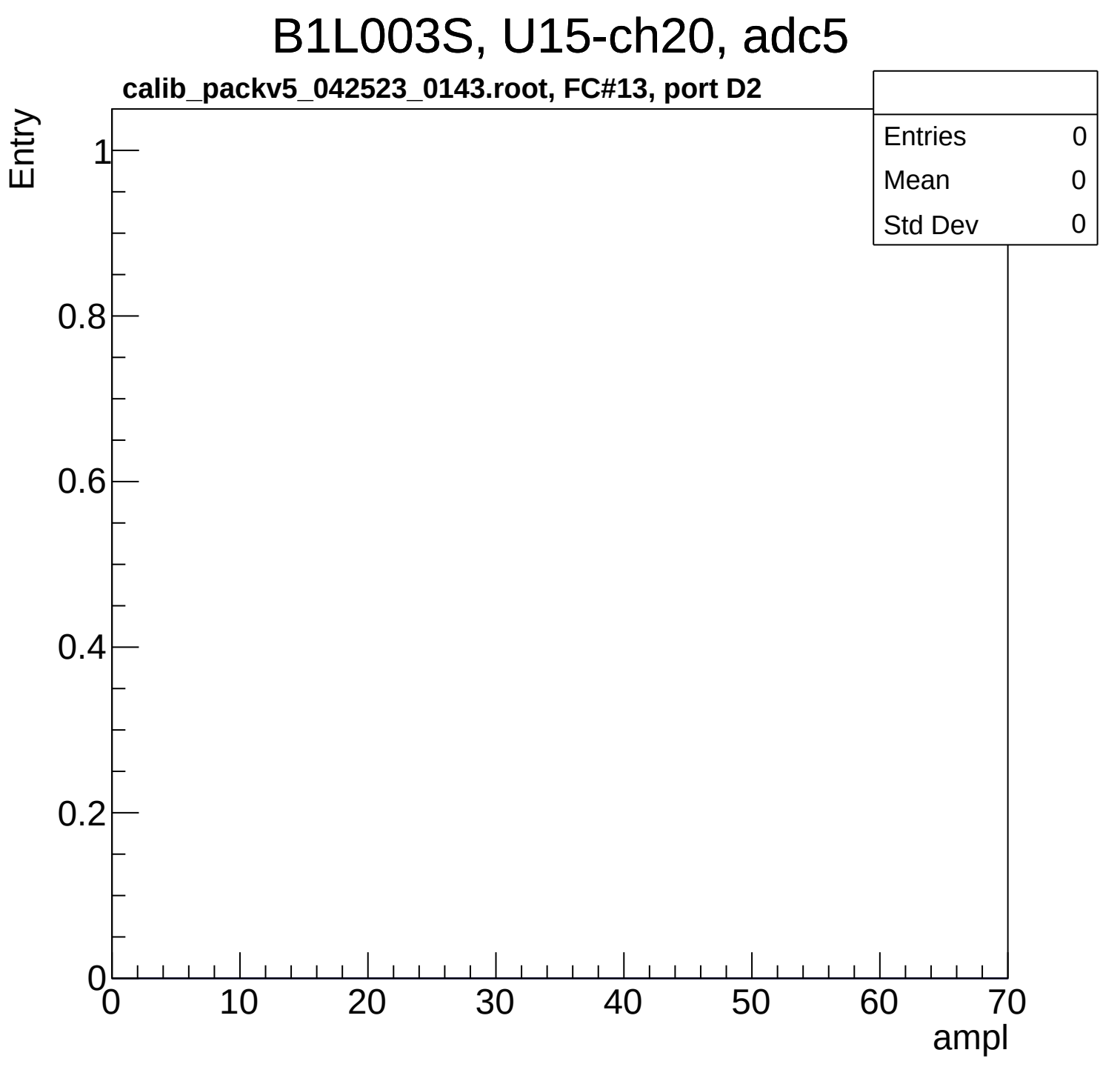
ampl


B1L003S, U15-ch20, adc5
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Std Dev 0
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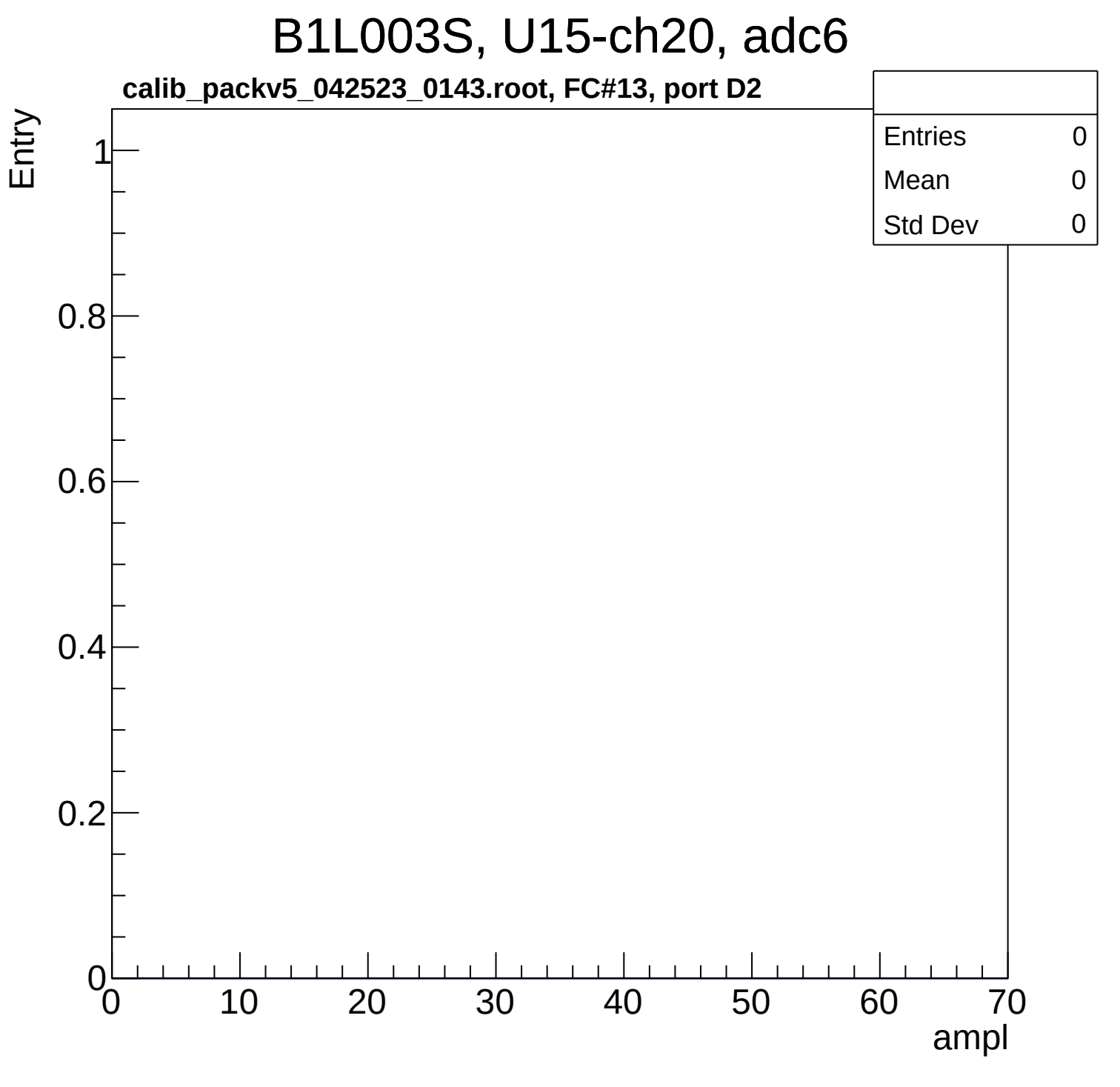
ampl


B1L003S, U15-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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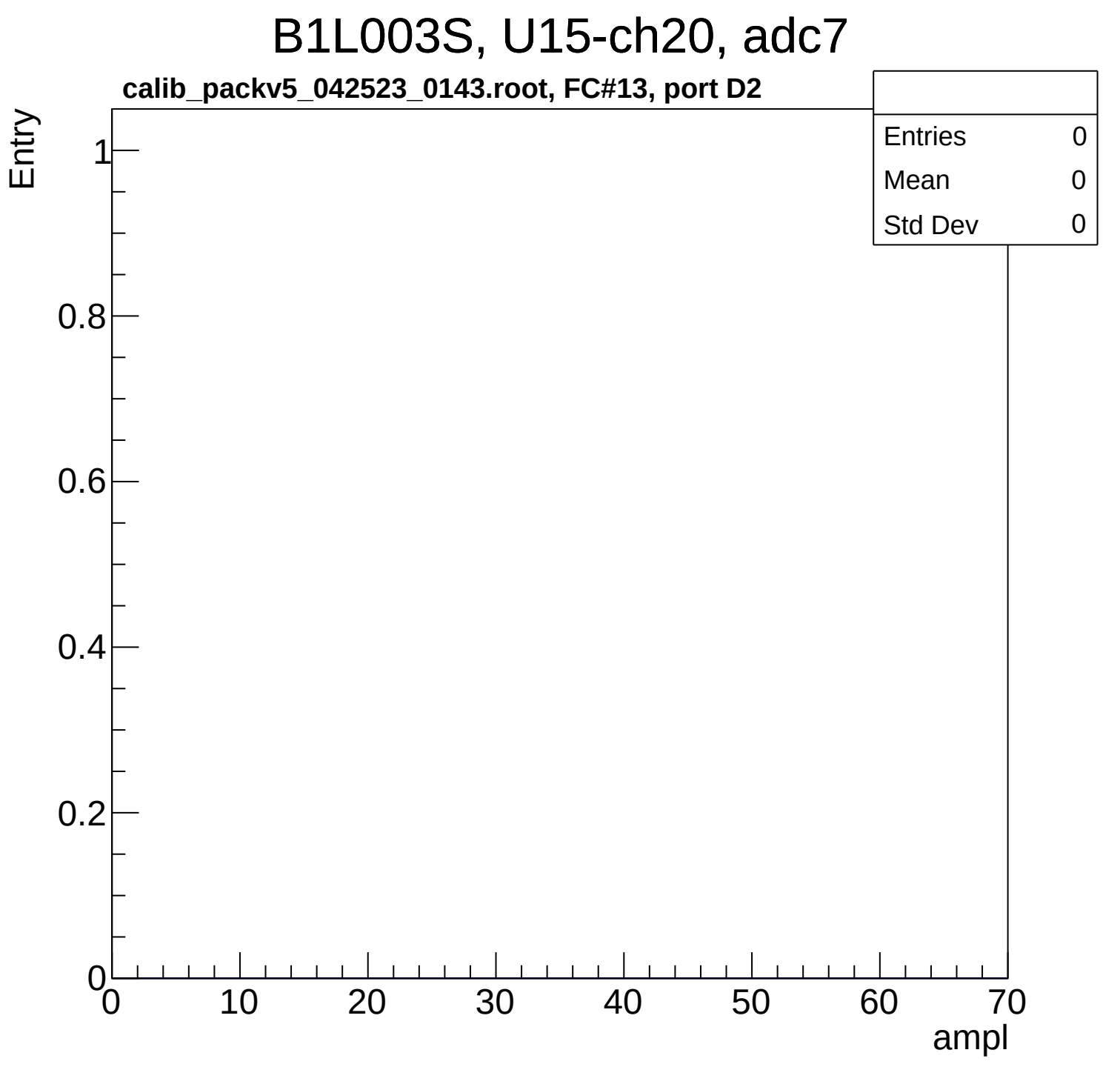
ampl


B1L003S, U15-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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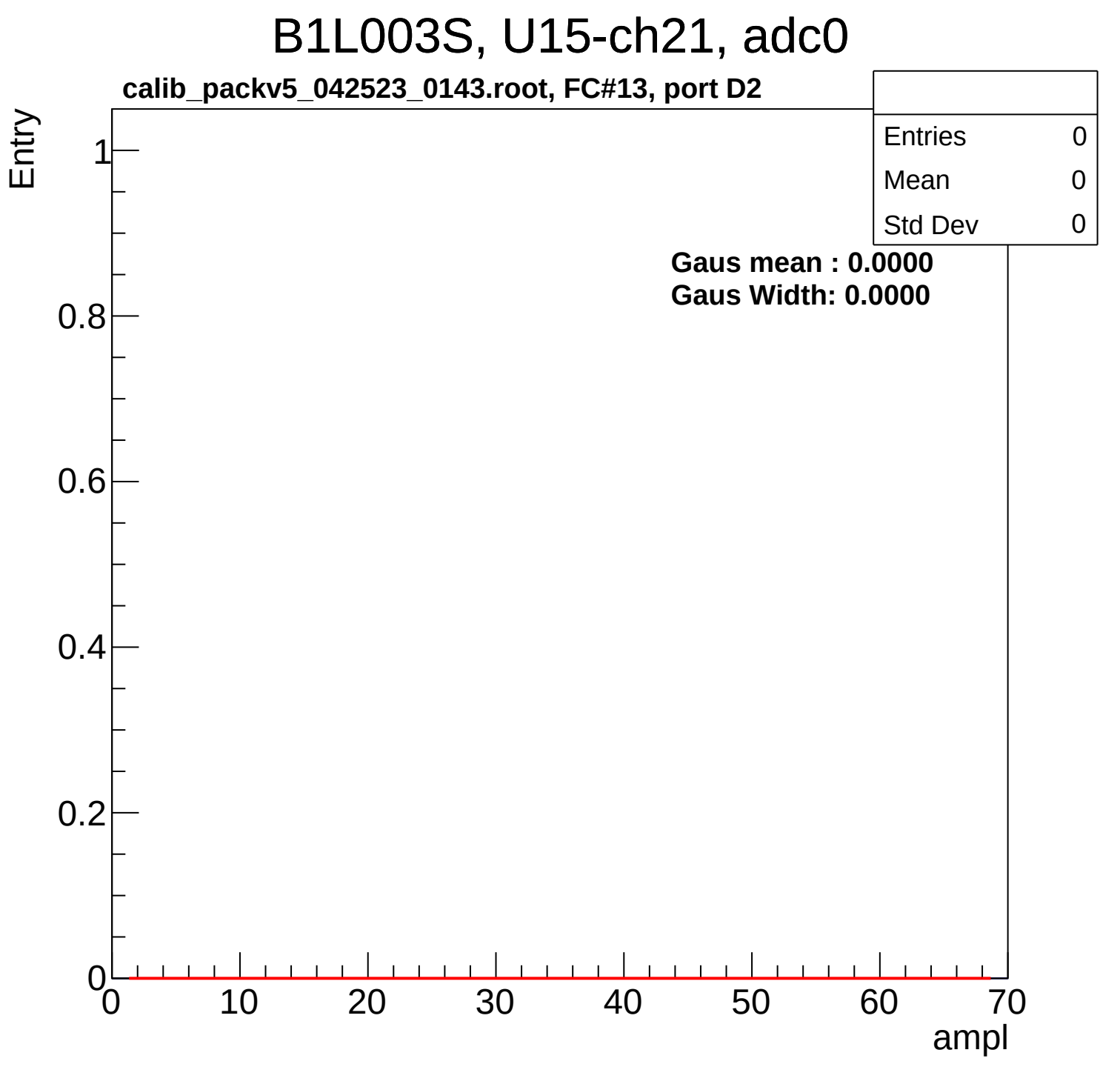
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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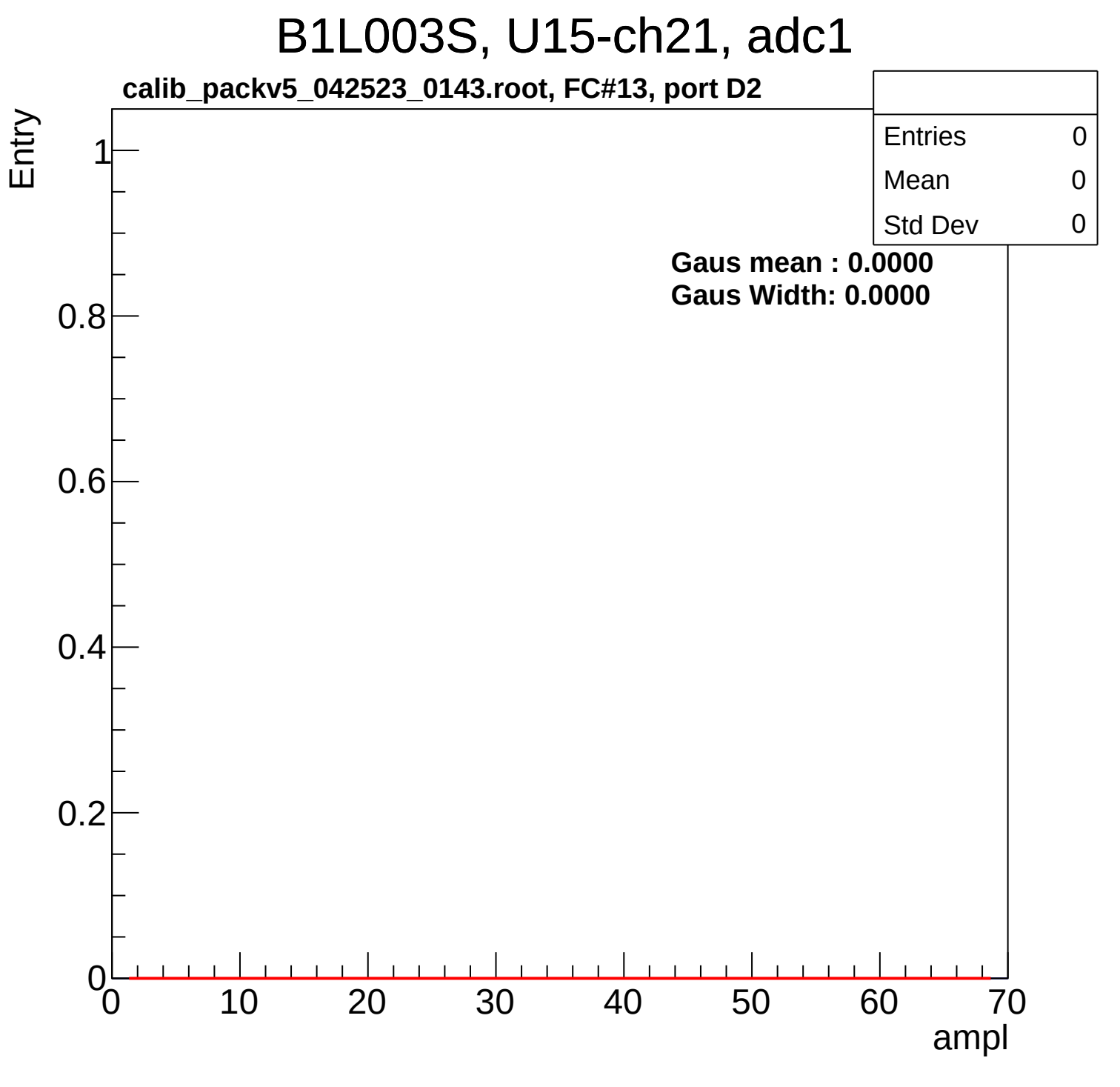
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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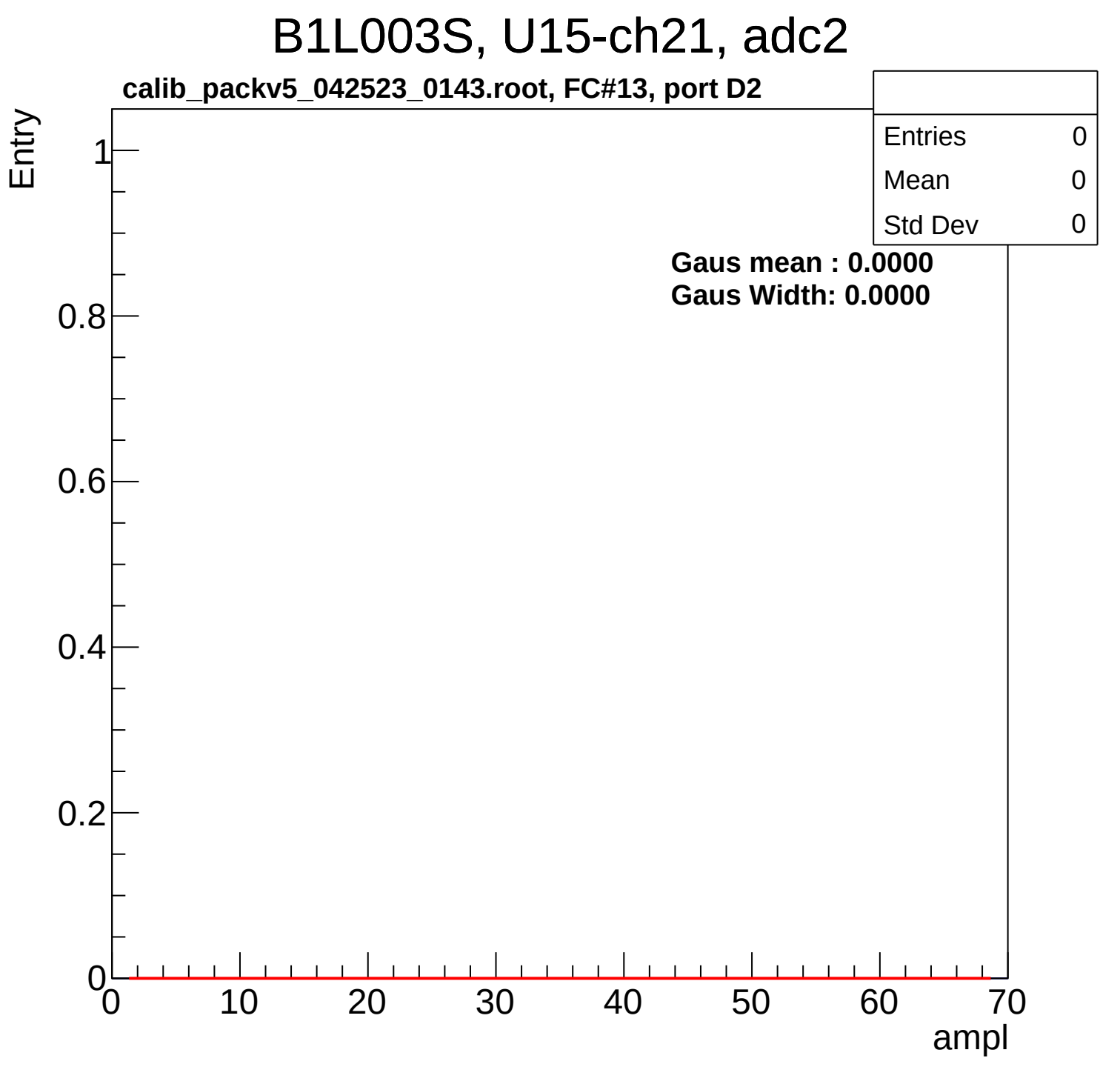
ampl


B1L003S, U15-ch21, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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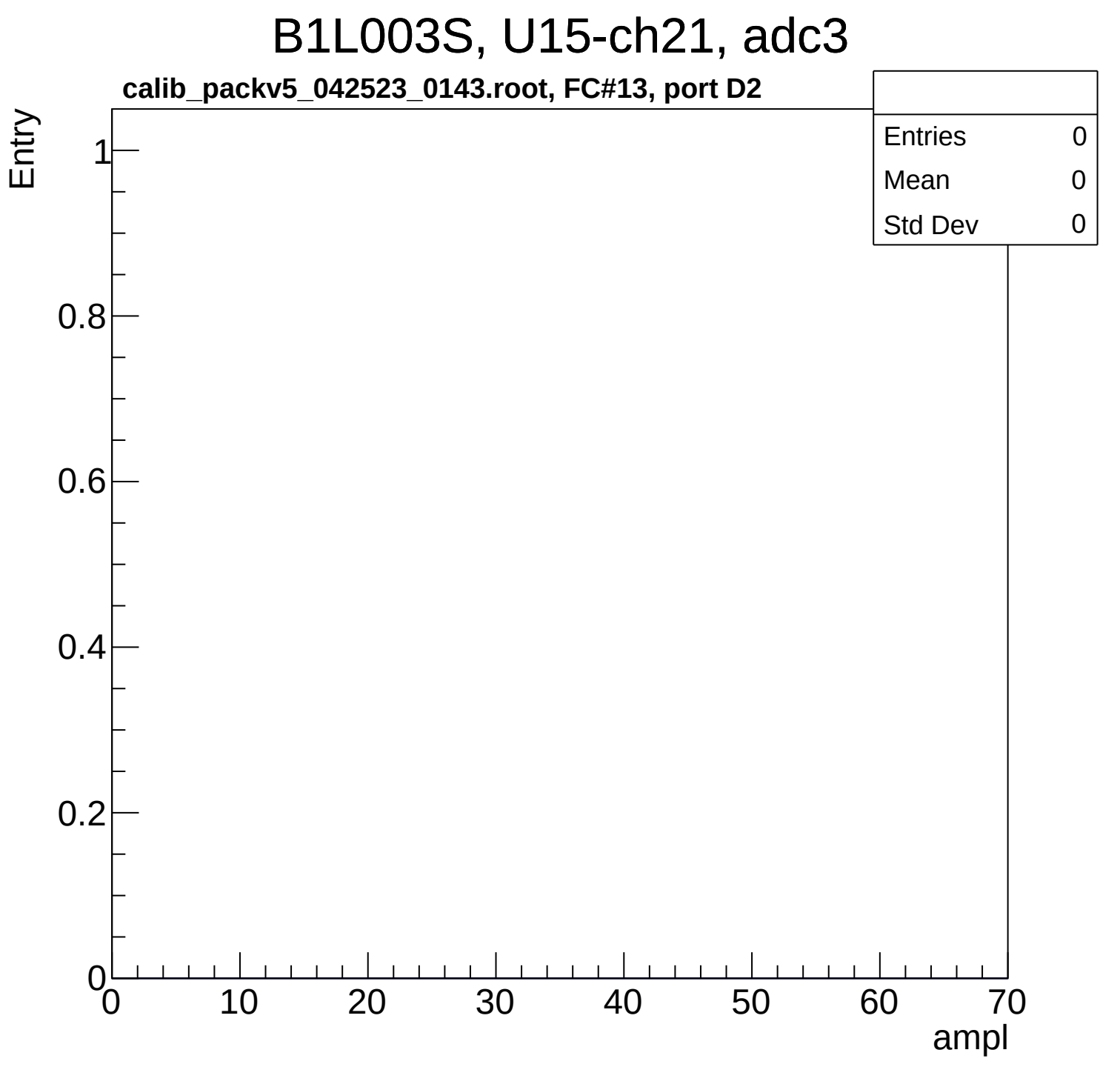
B1L003S, U15-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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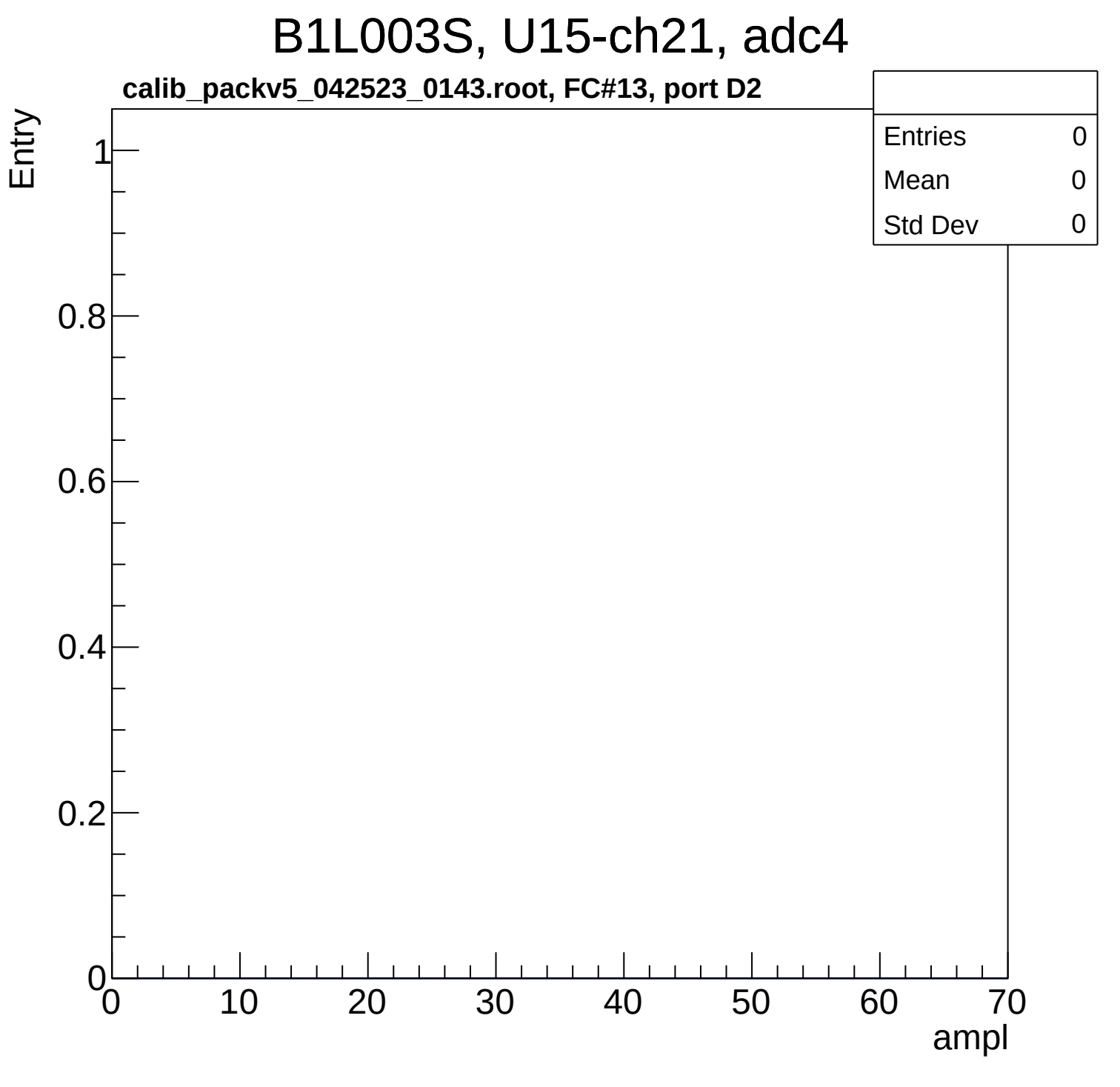
B1L003S, U15-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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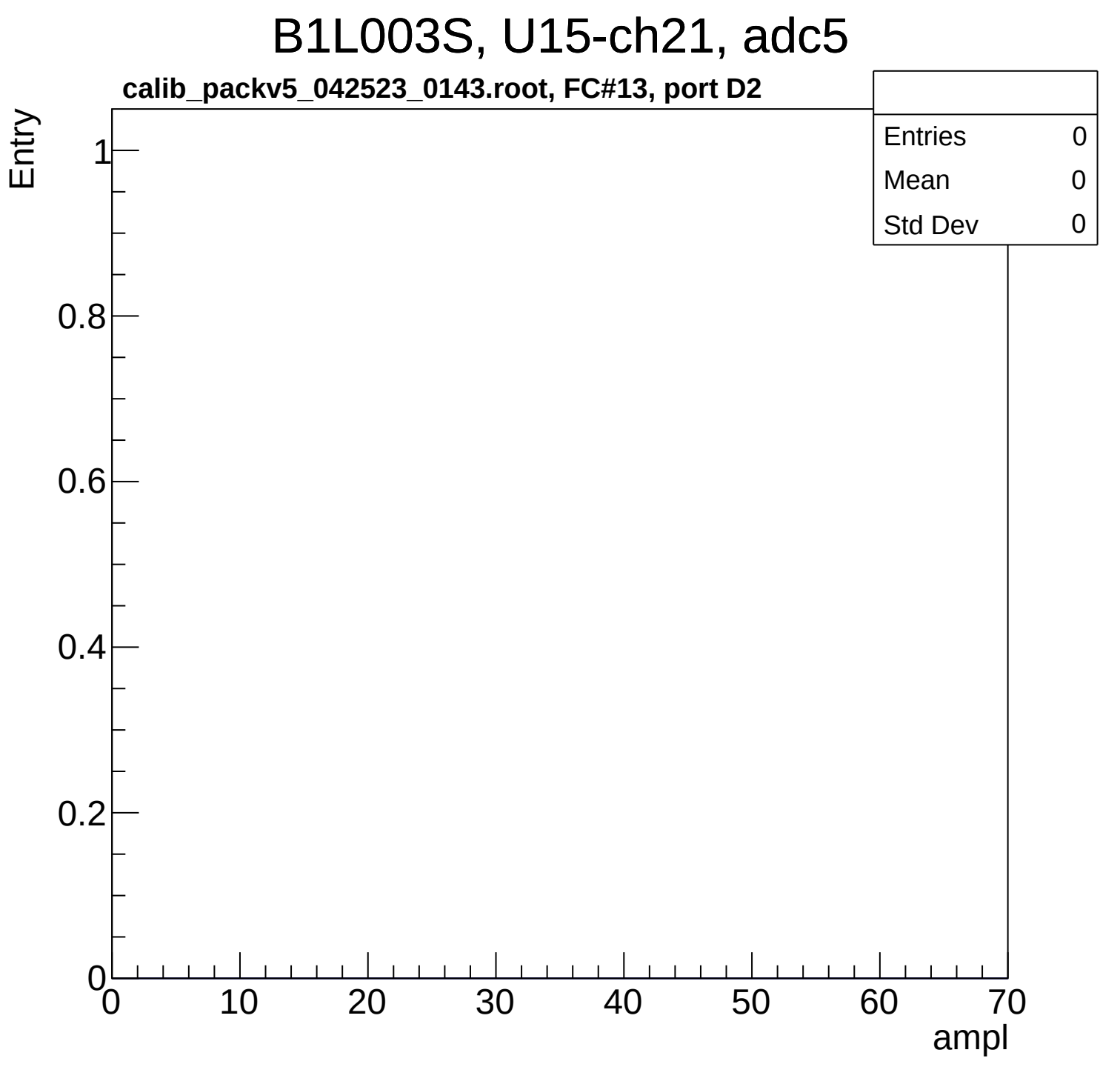
ampl


B1L003S, U15-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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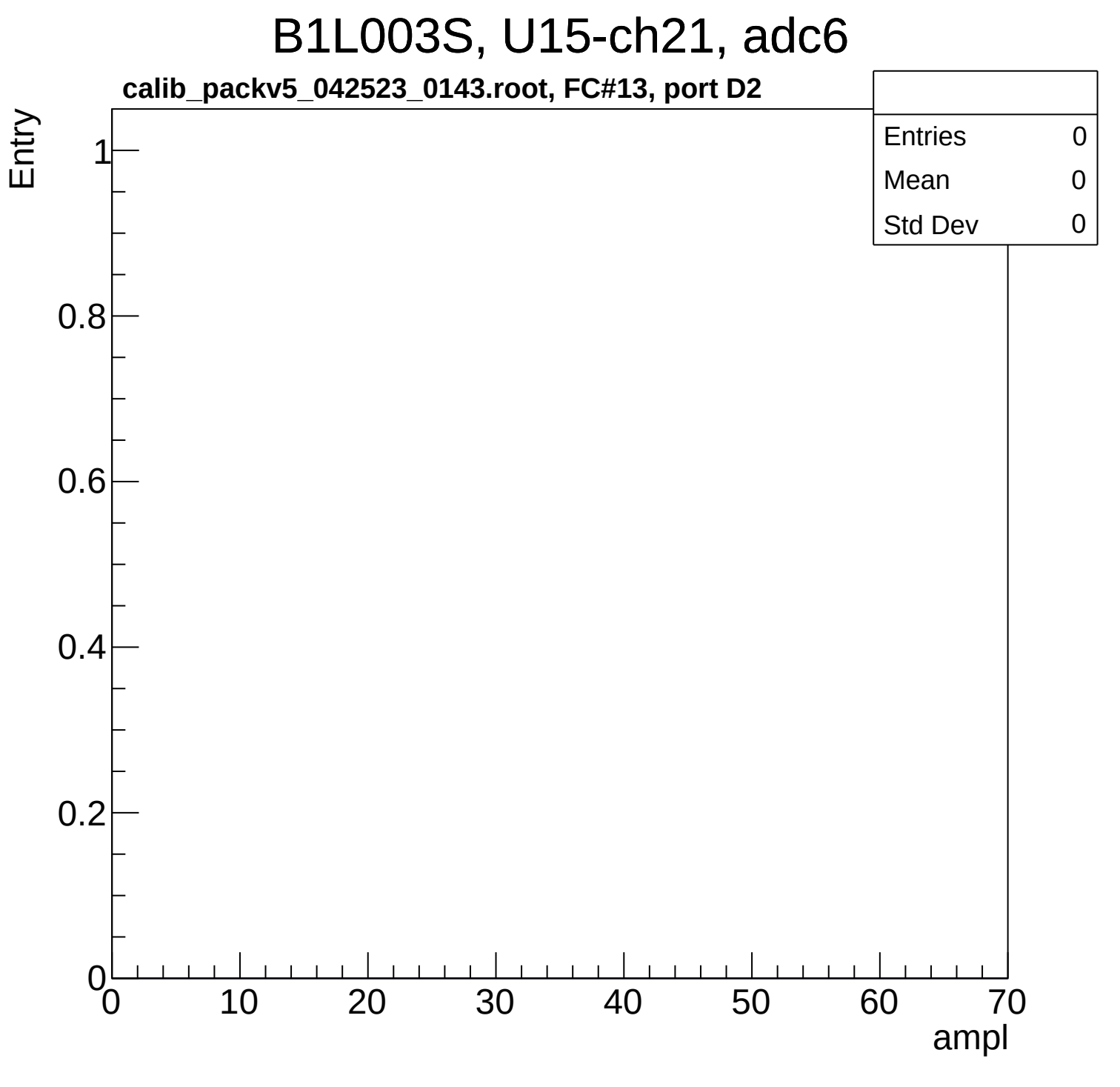
B1L003S, U15-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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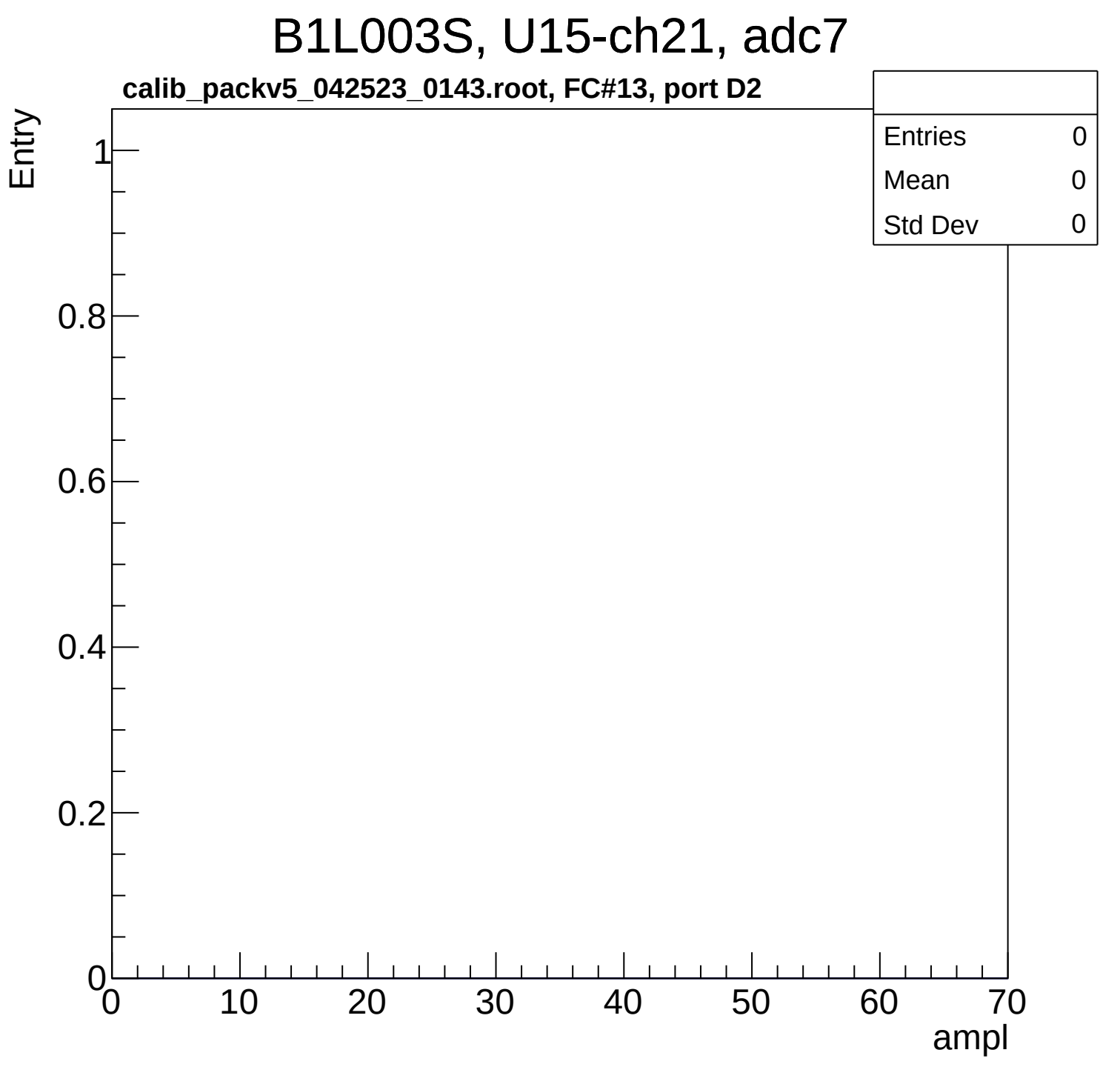
B1L003S, U15-ch21, adc7
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Mean 0
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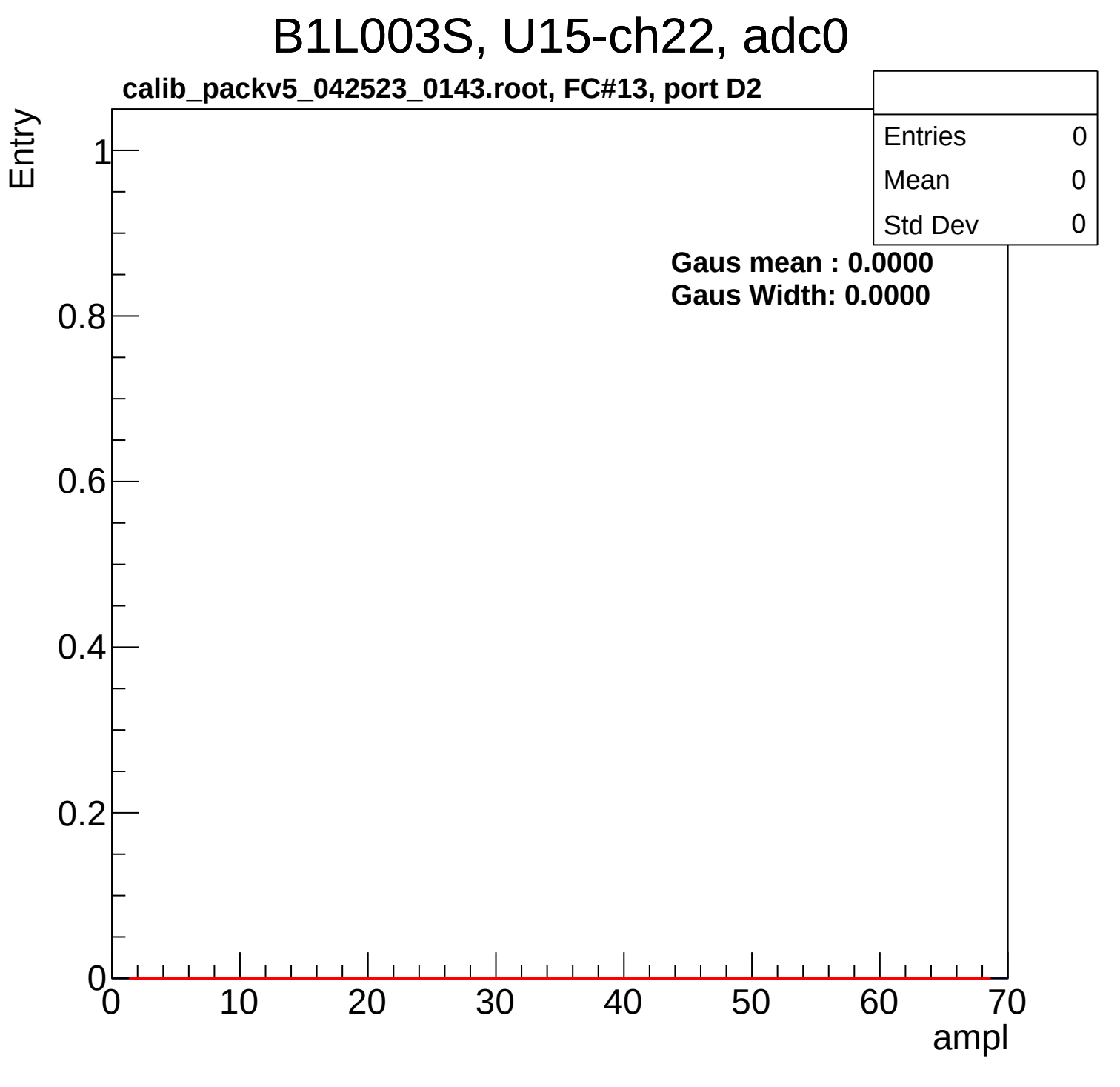
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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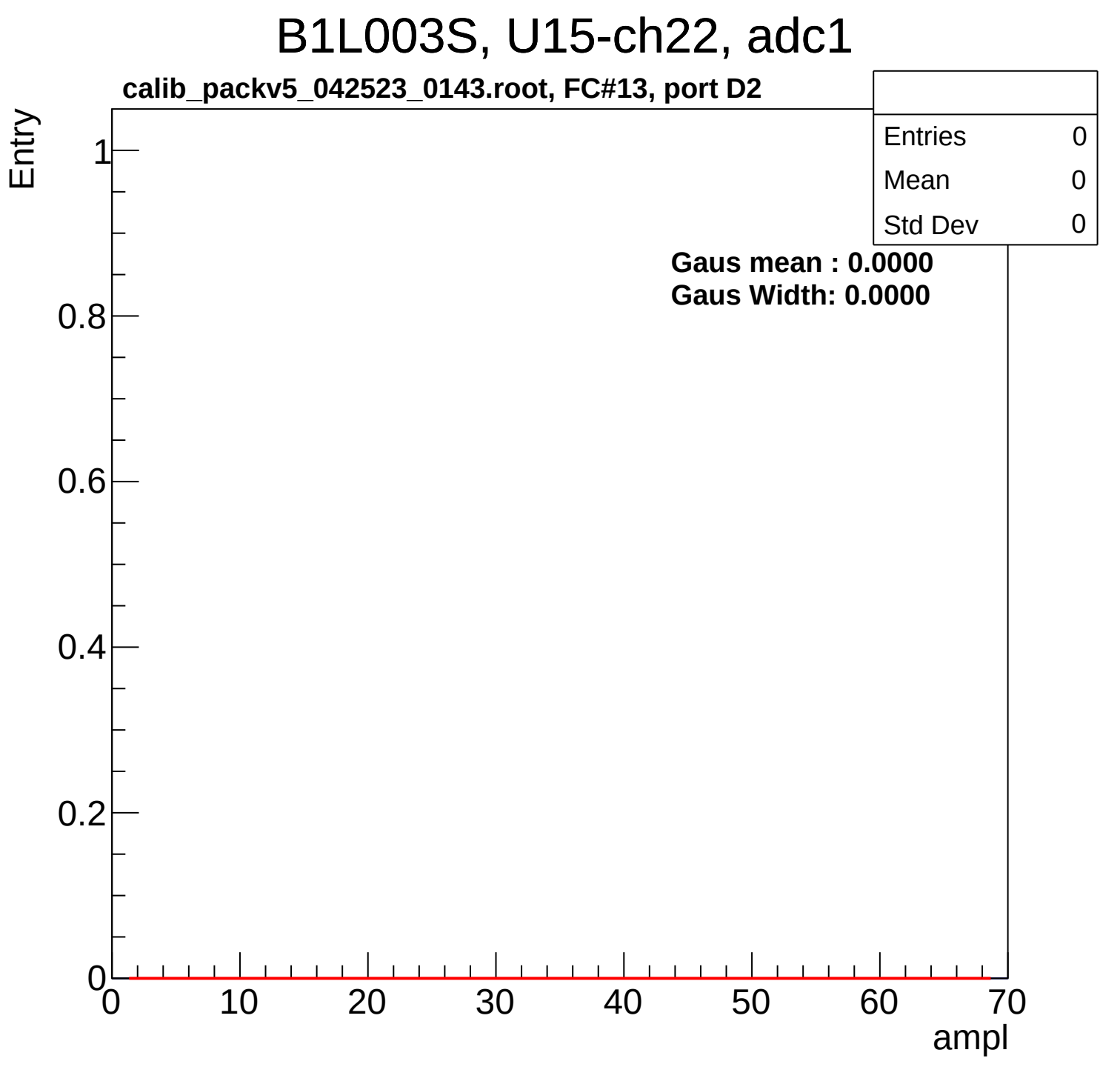
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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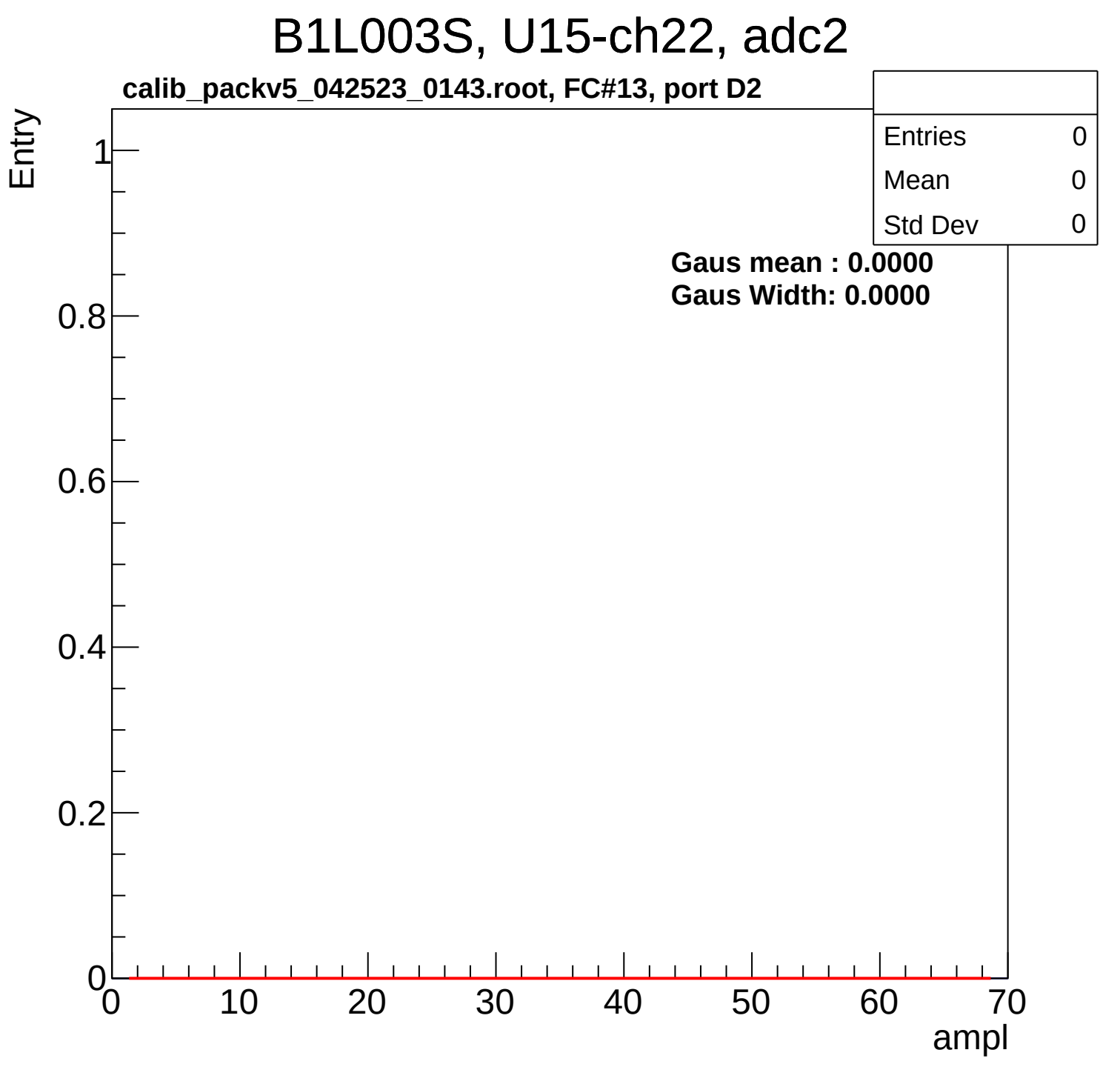
B1L003S, U15-ch22, adc2
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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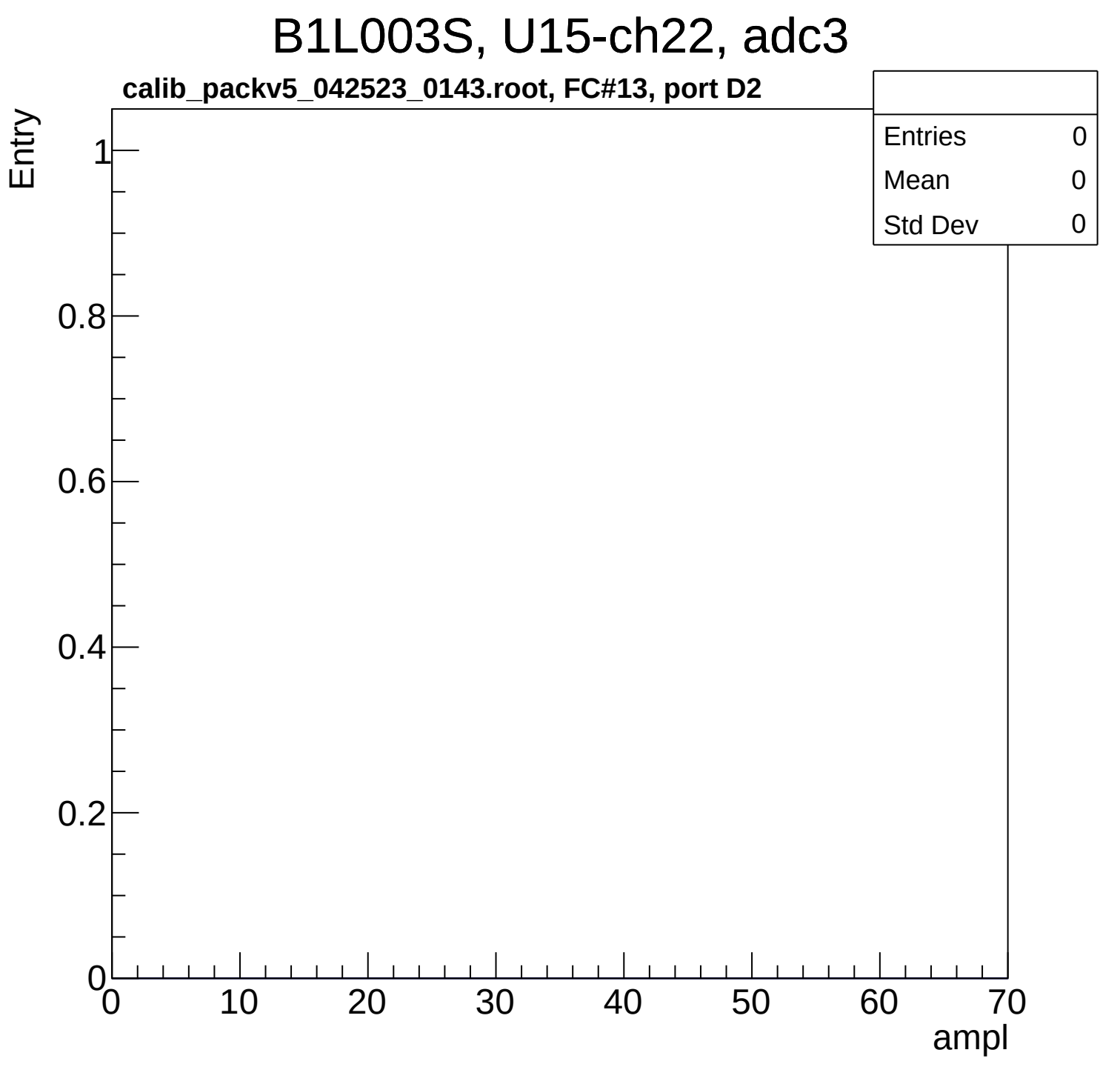
B1L003S, U15-ch22, adc3
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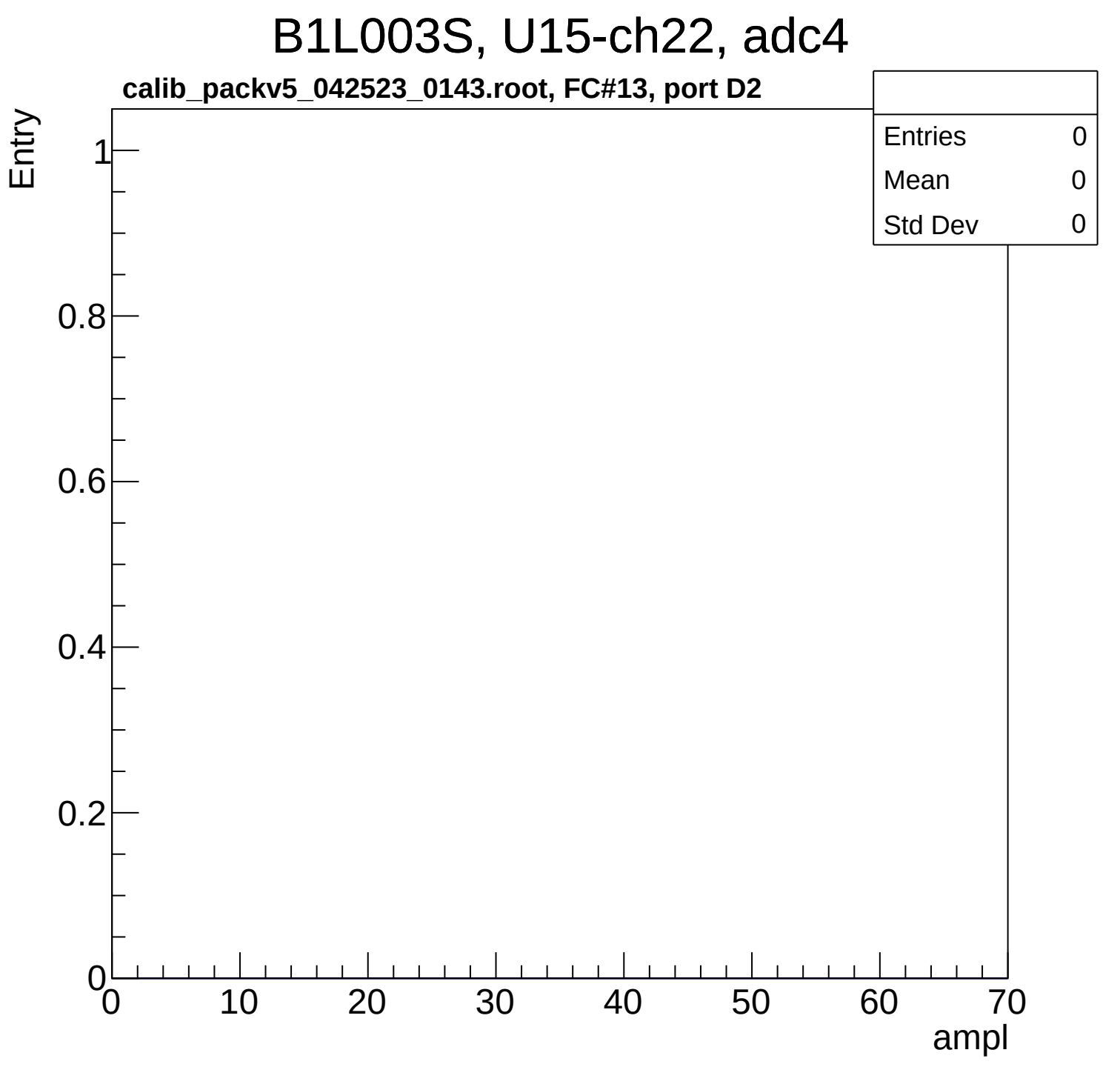
B1L003S, U15-ch22, adc4
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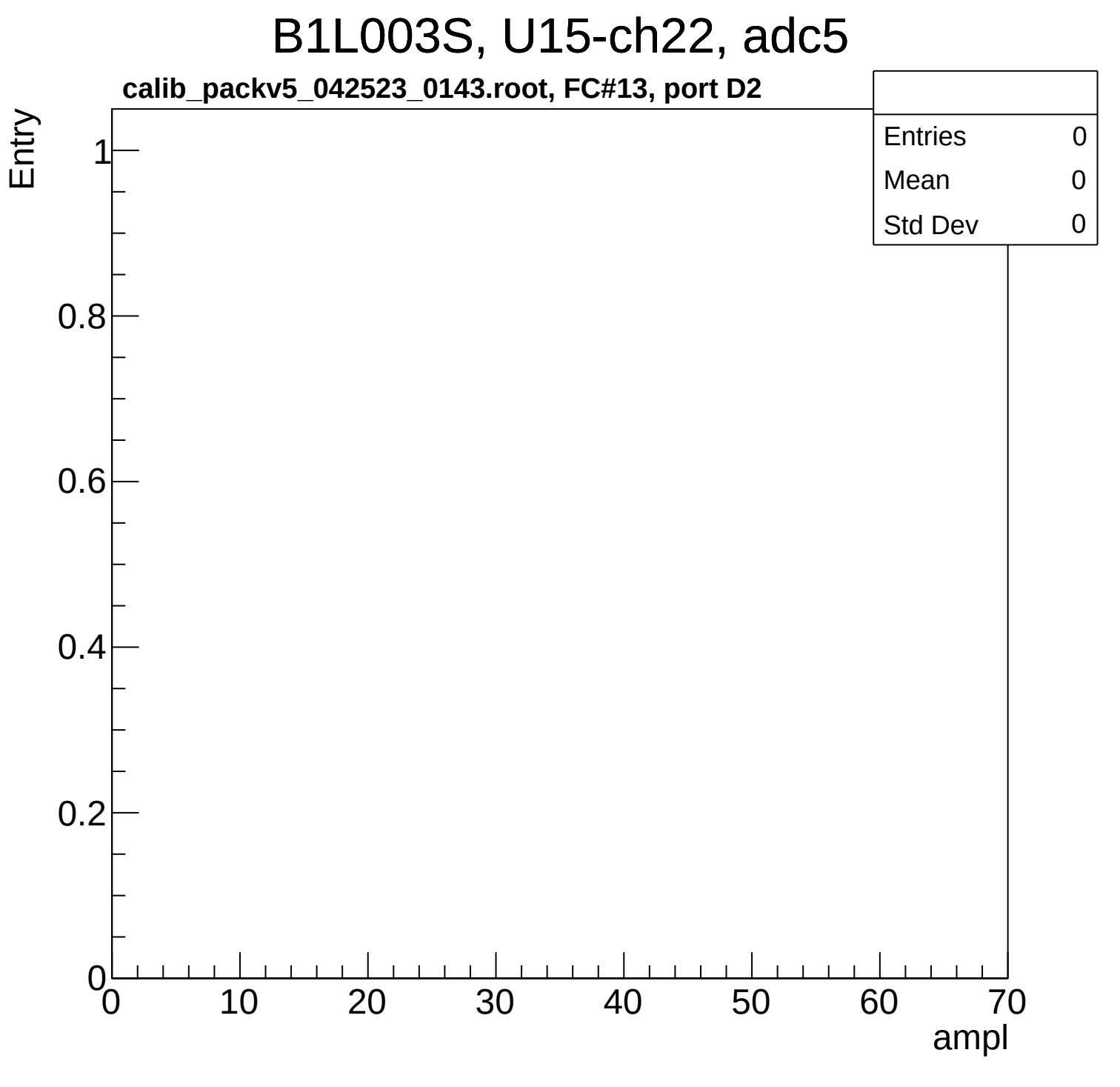
B1L003S, U15-ch22, adc5
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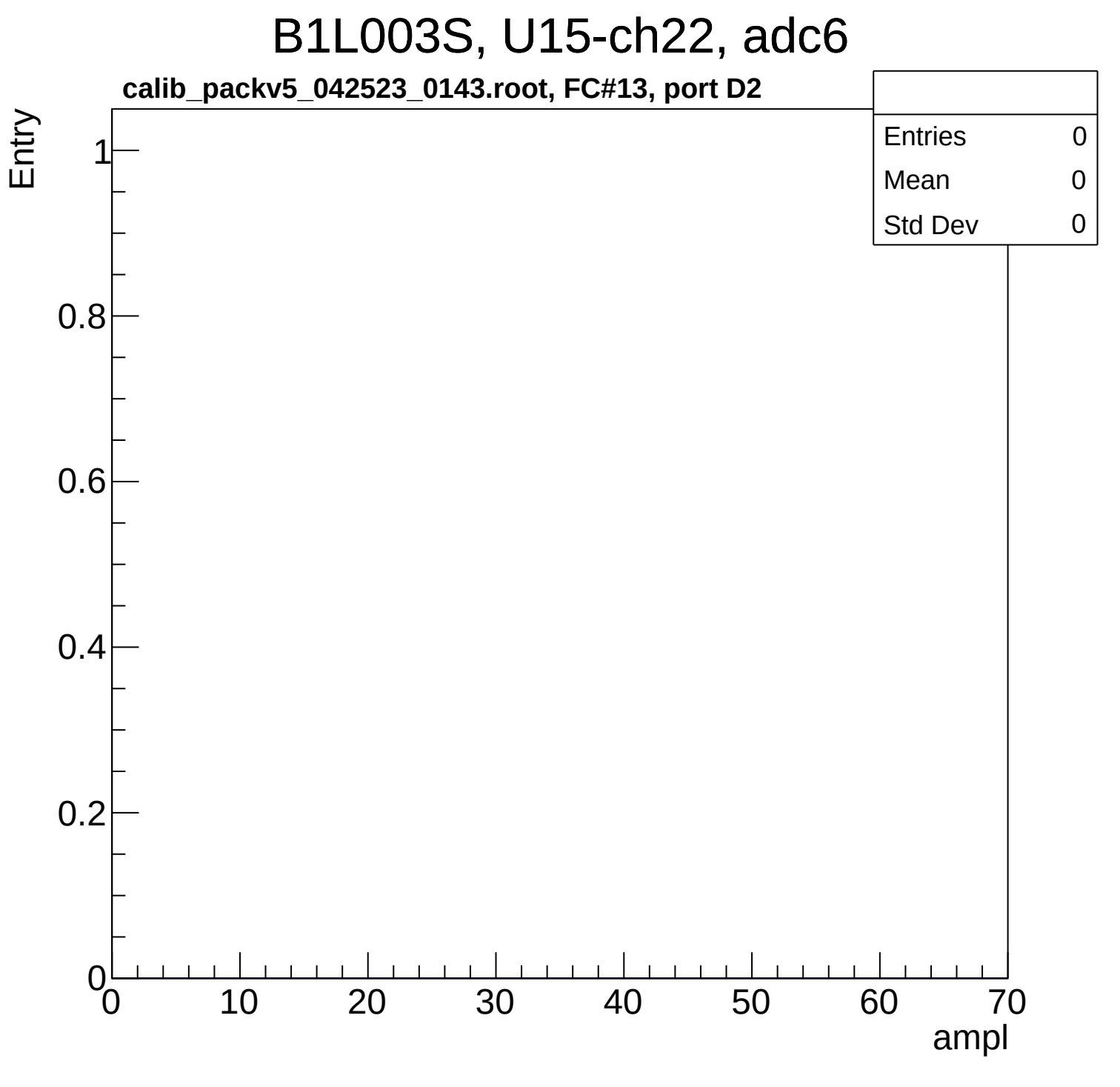
B1L003S, U15-ch22, adc6
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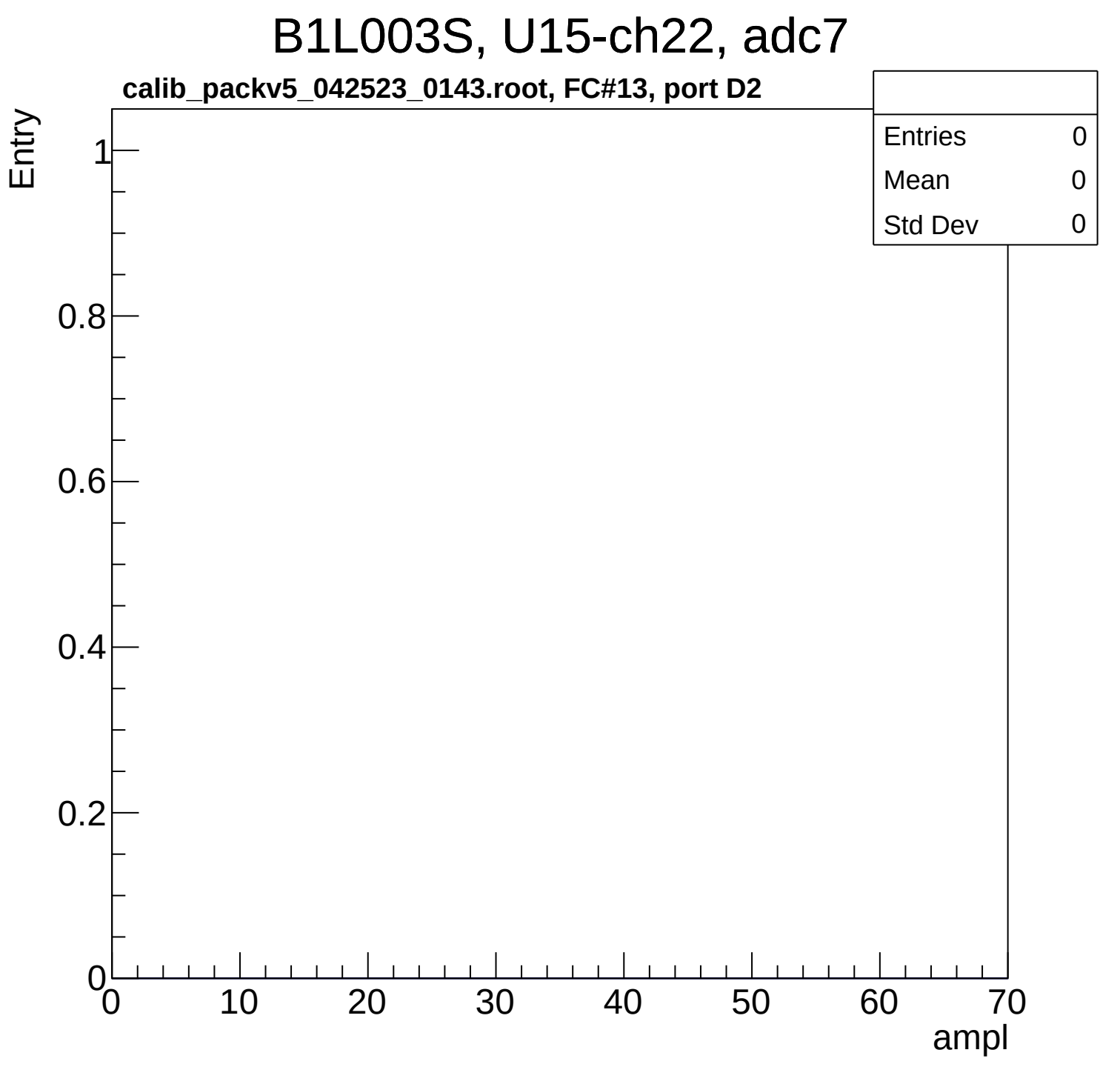
B1L003S, U15-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
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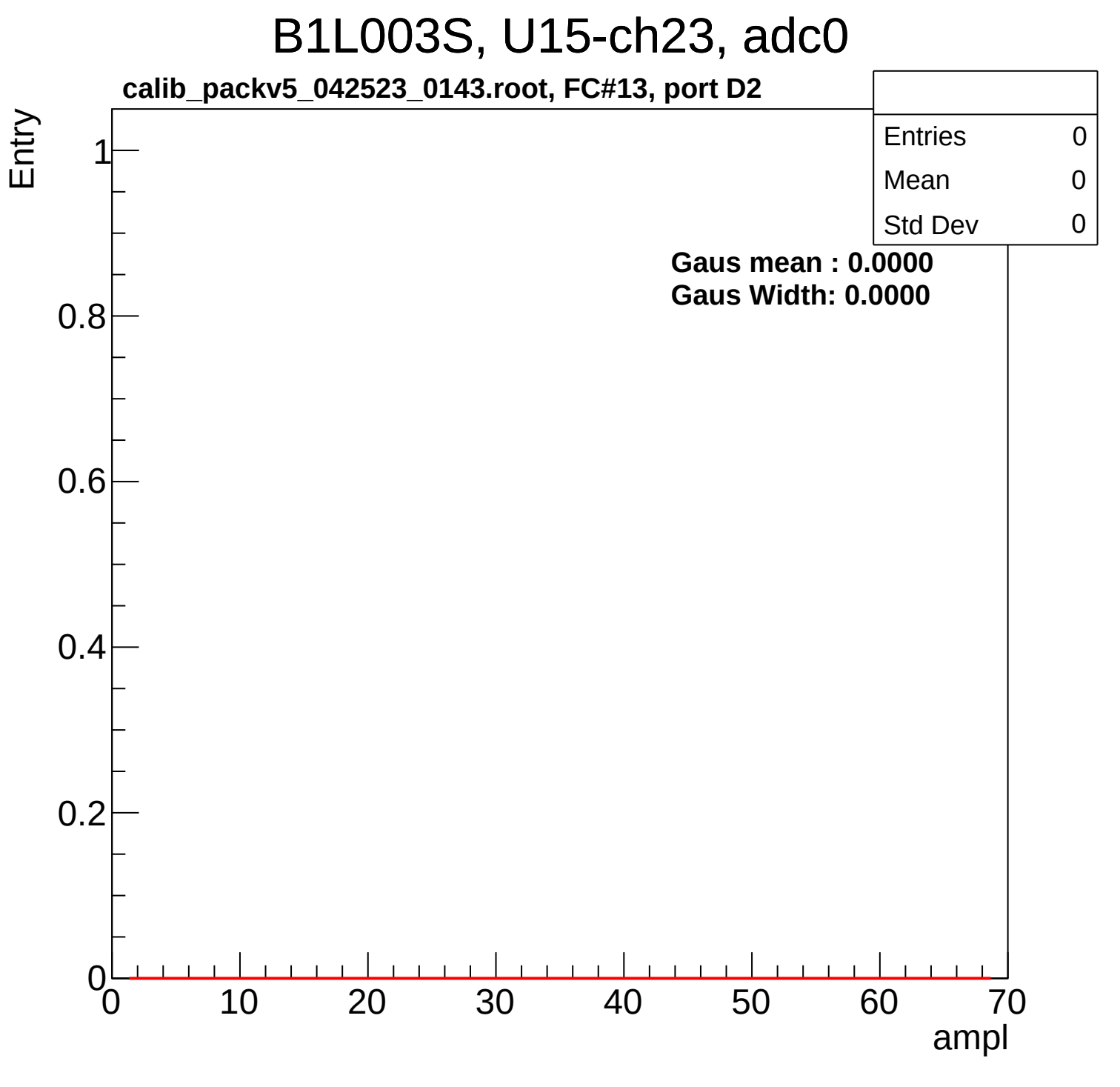
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Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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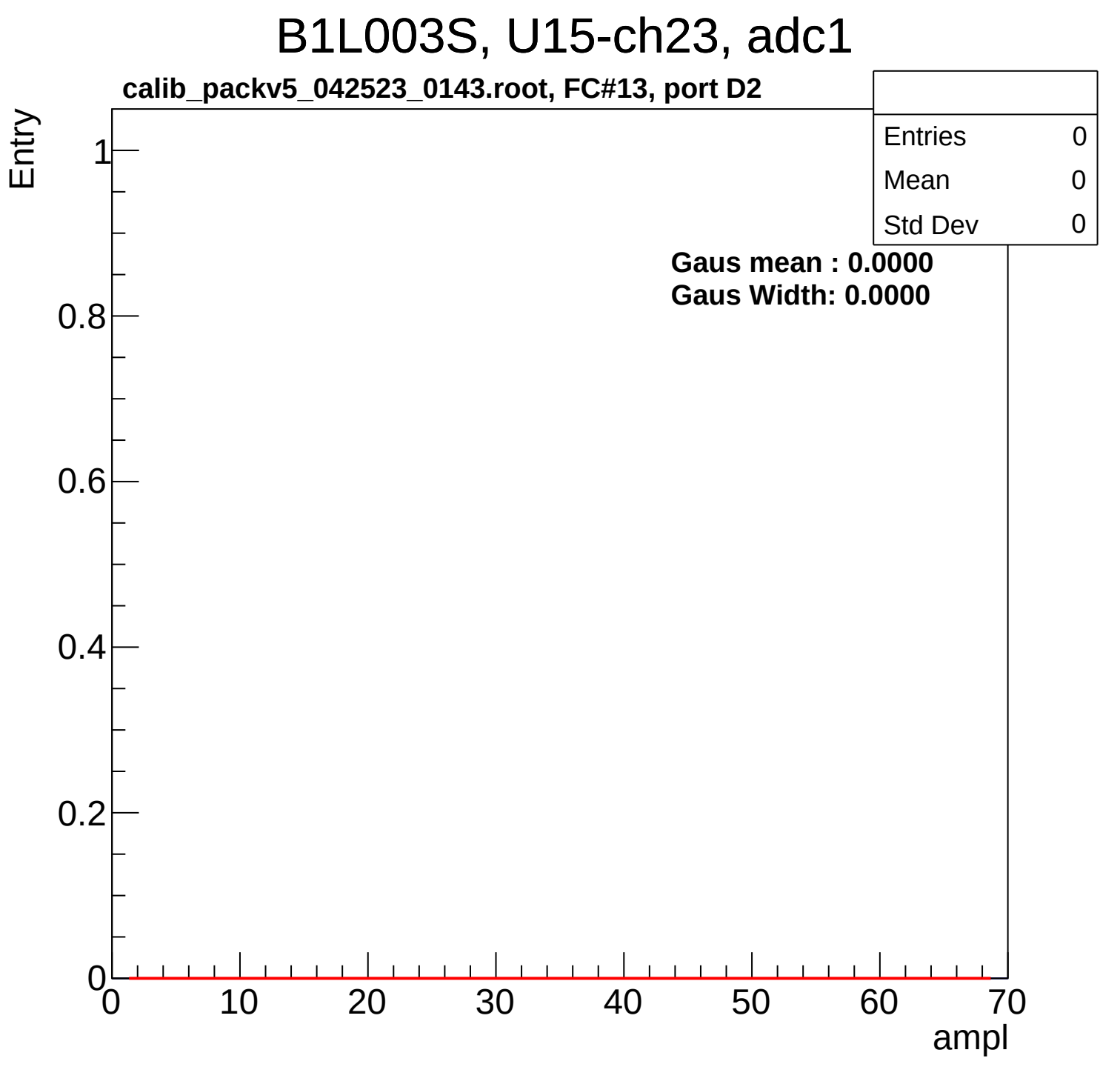
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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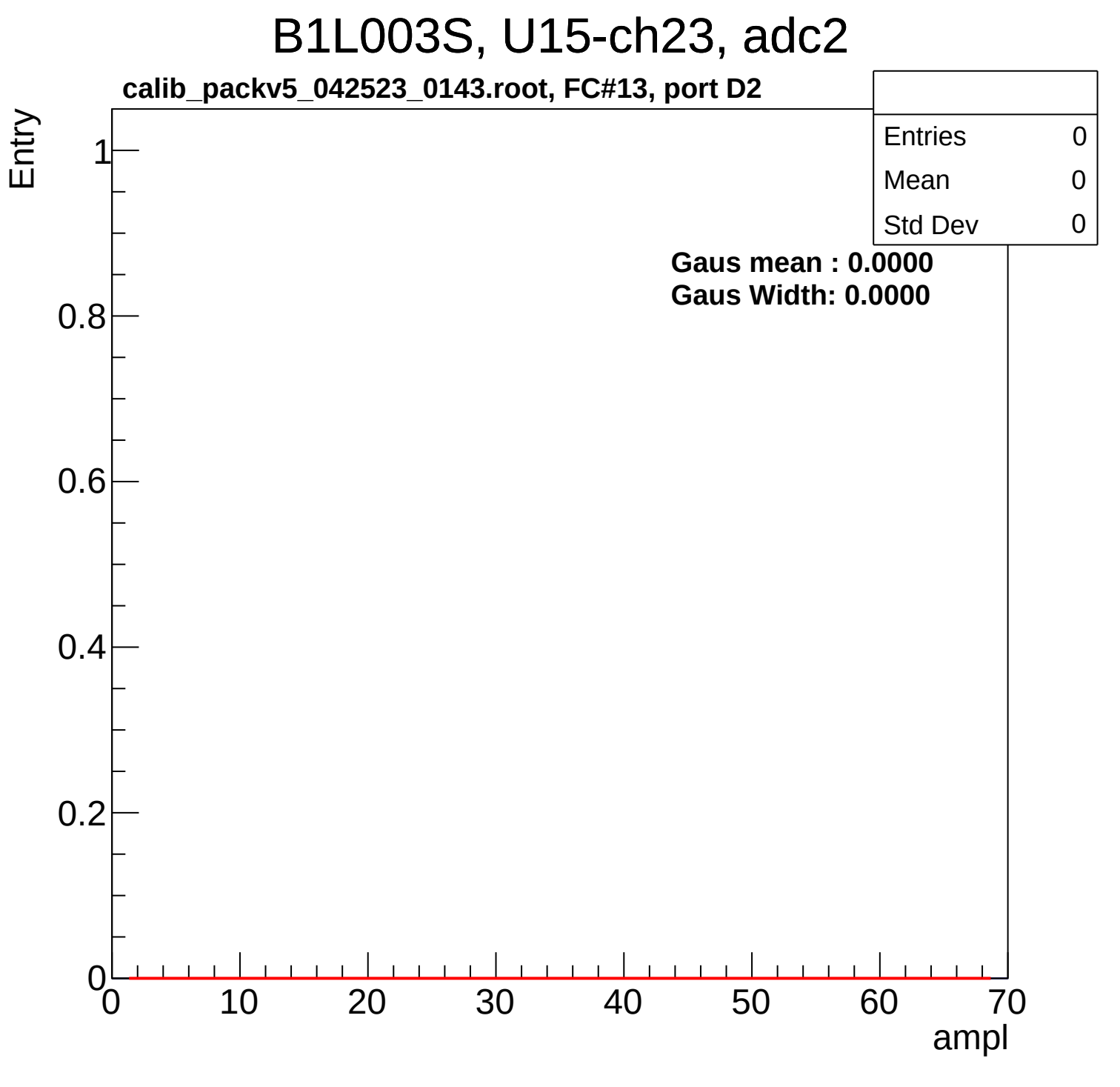
B1L003S, U15-ch23, adc2
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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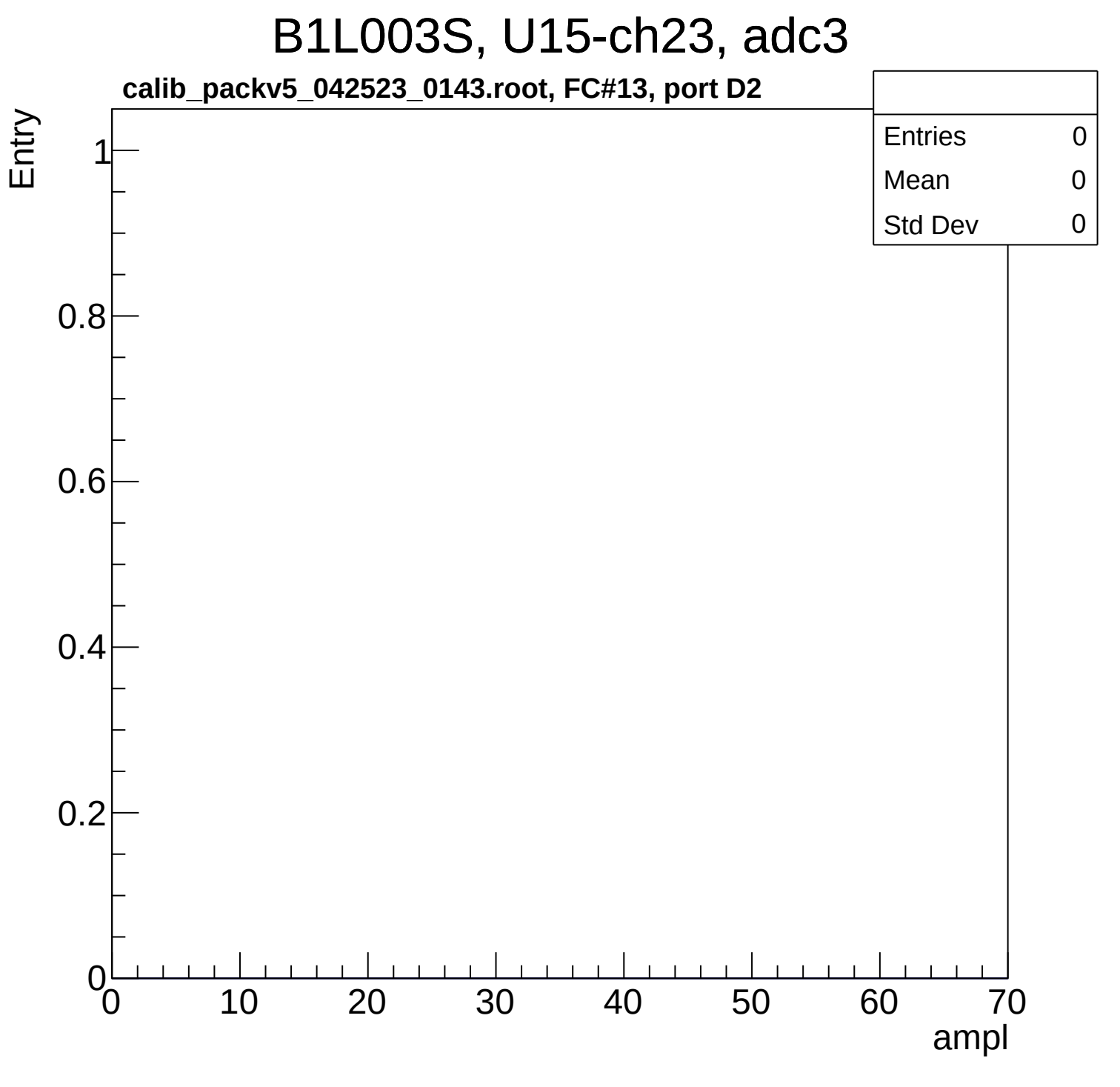
ampl


B1L003S, U15-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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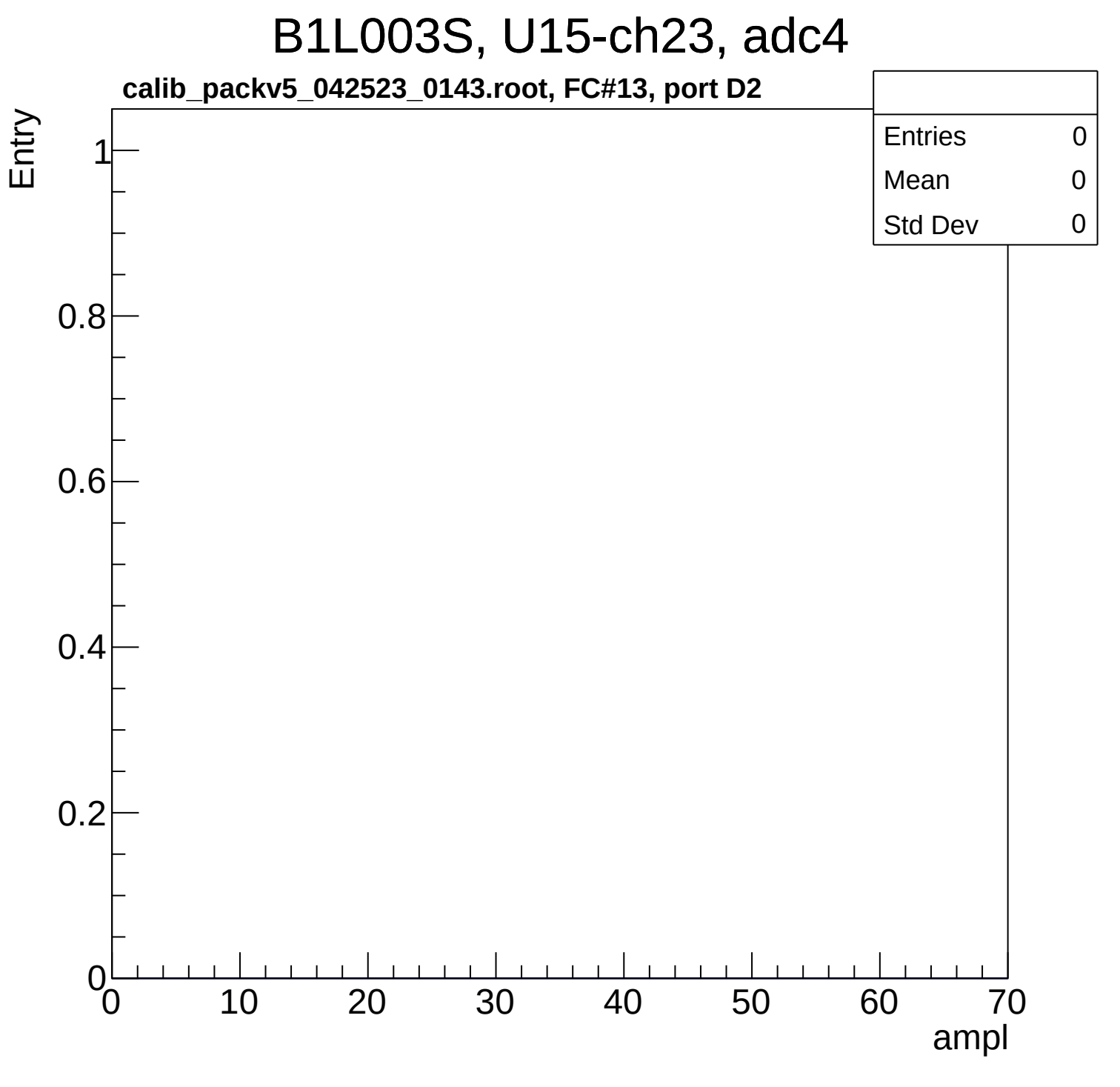
ampl


B1L003S, U15-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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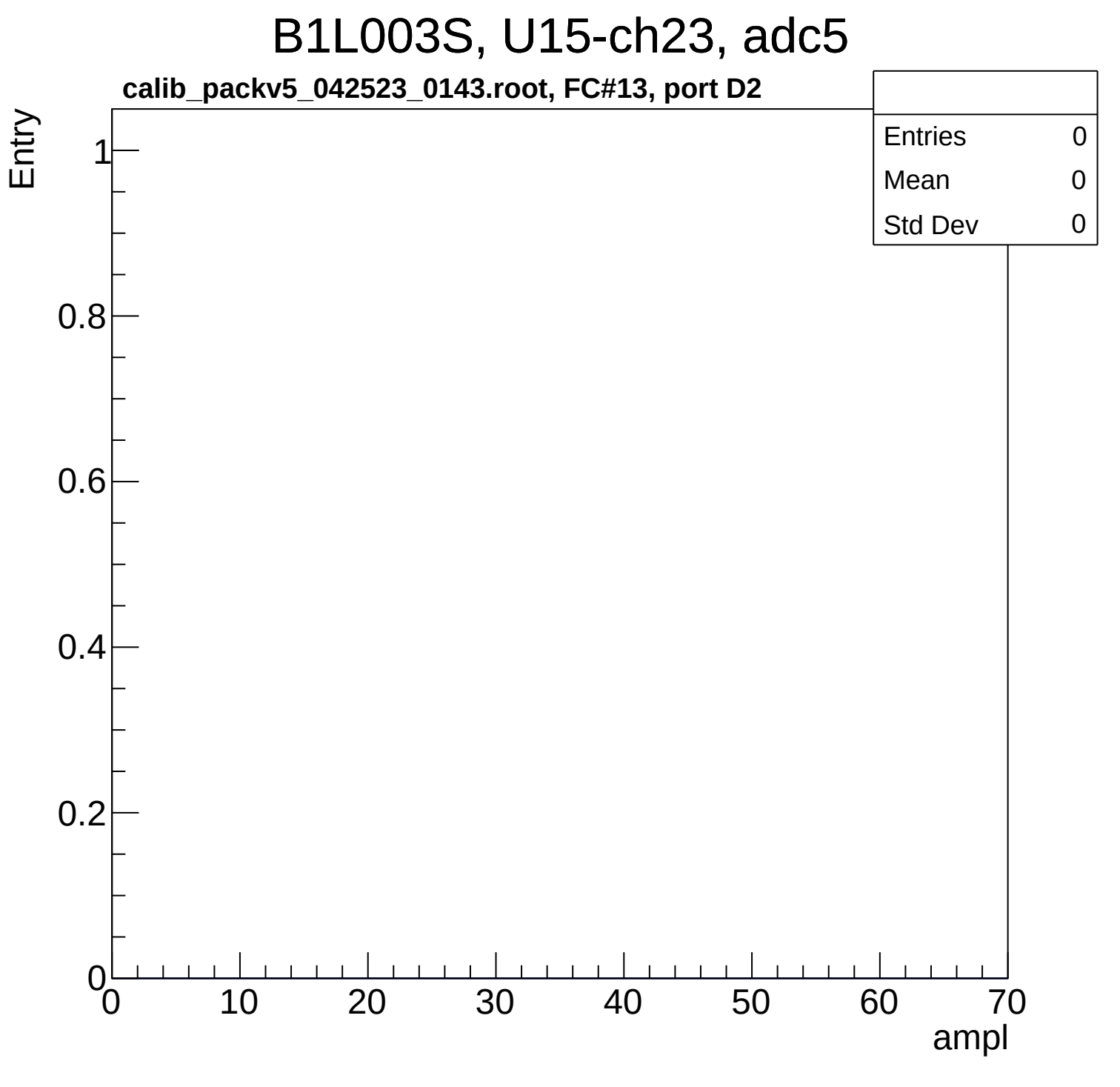
ampl


B1L003S, U15-ch23, adc5
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Entries 0
Mean 0
Std Dev 0
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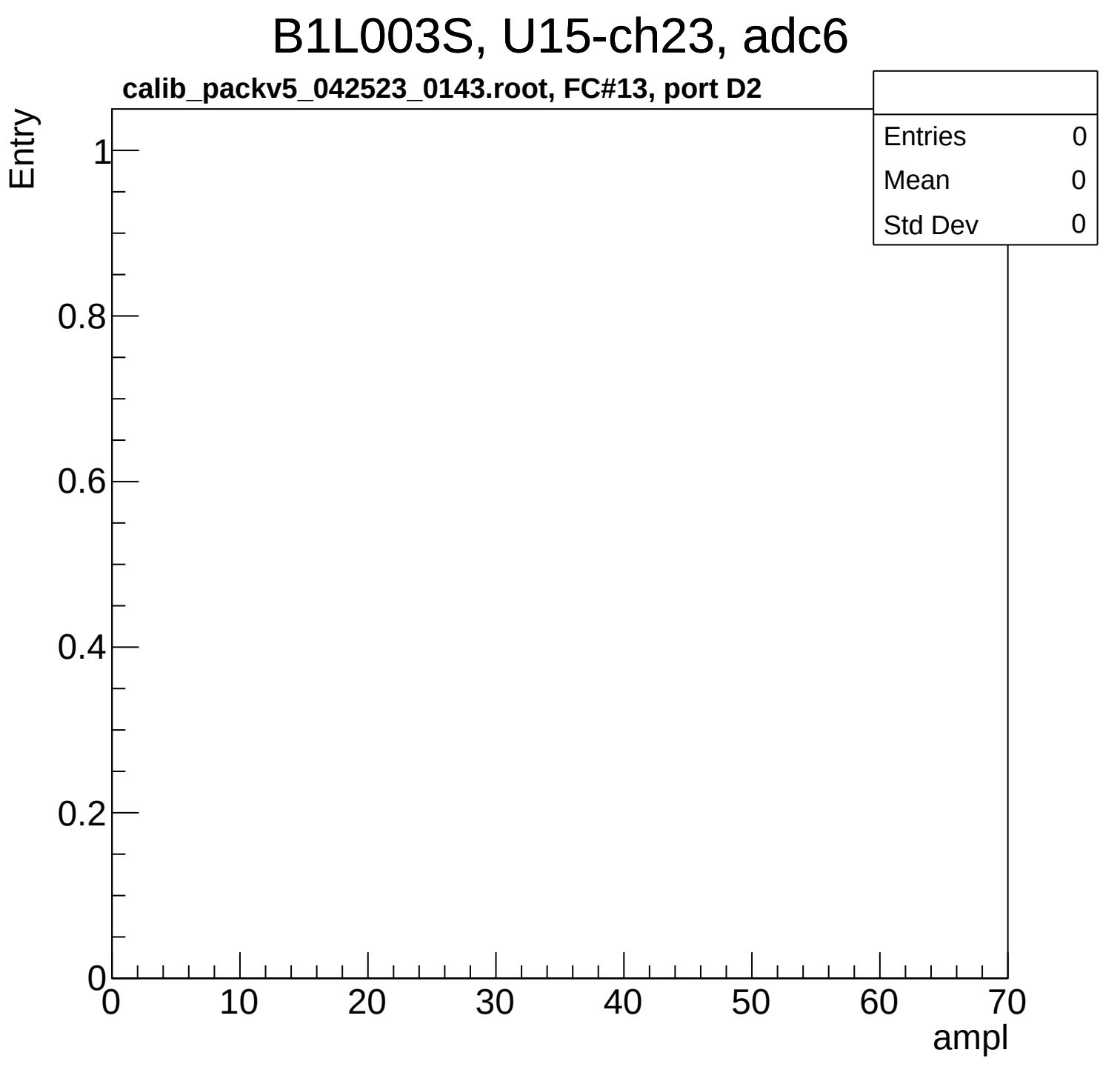
ampl


B1L003S, U15-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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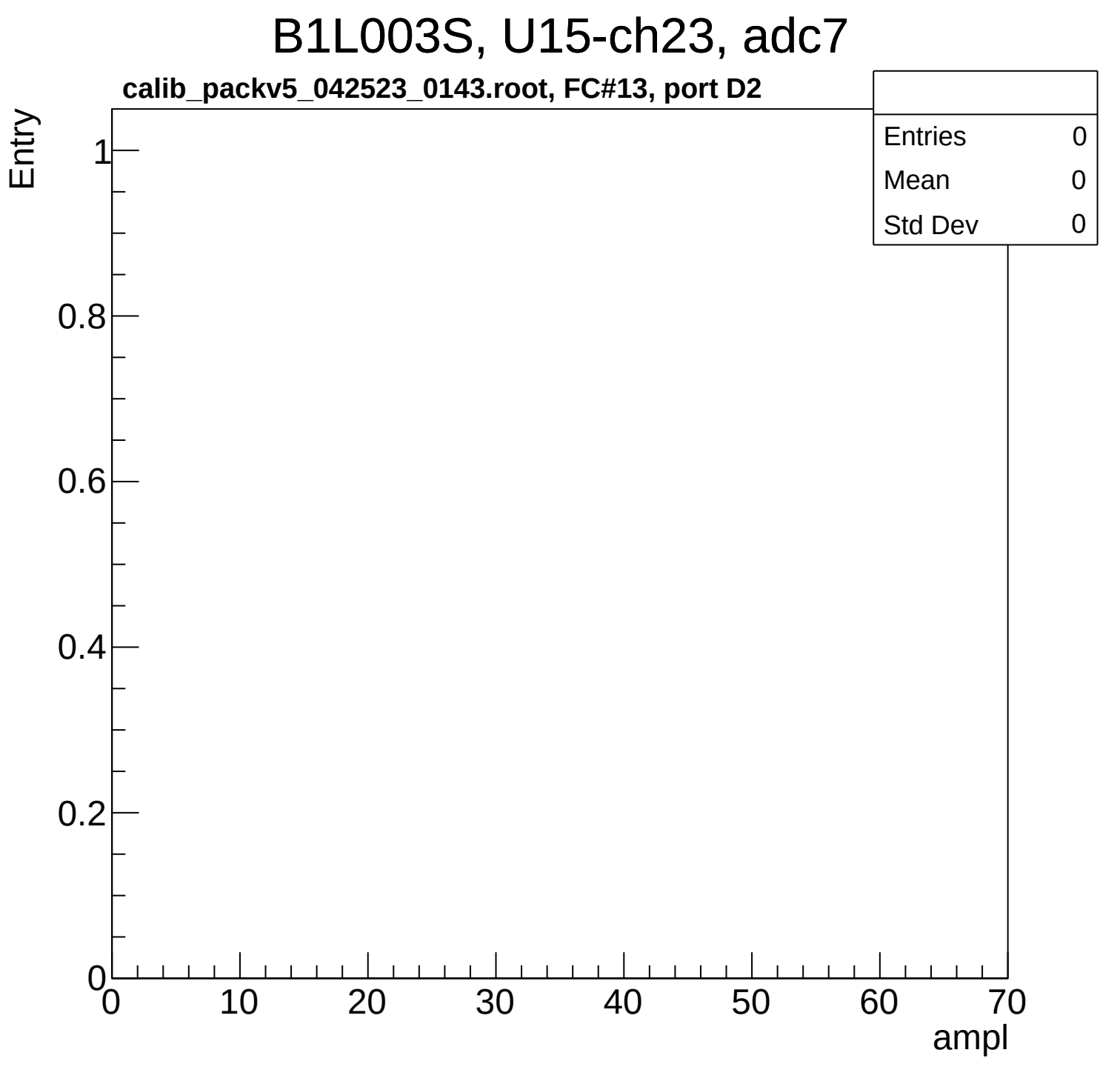
ampl


B1L003S, U15-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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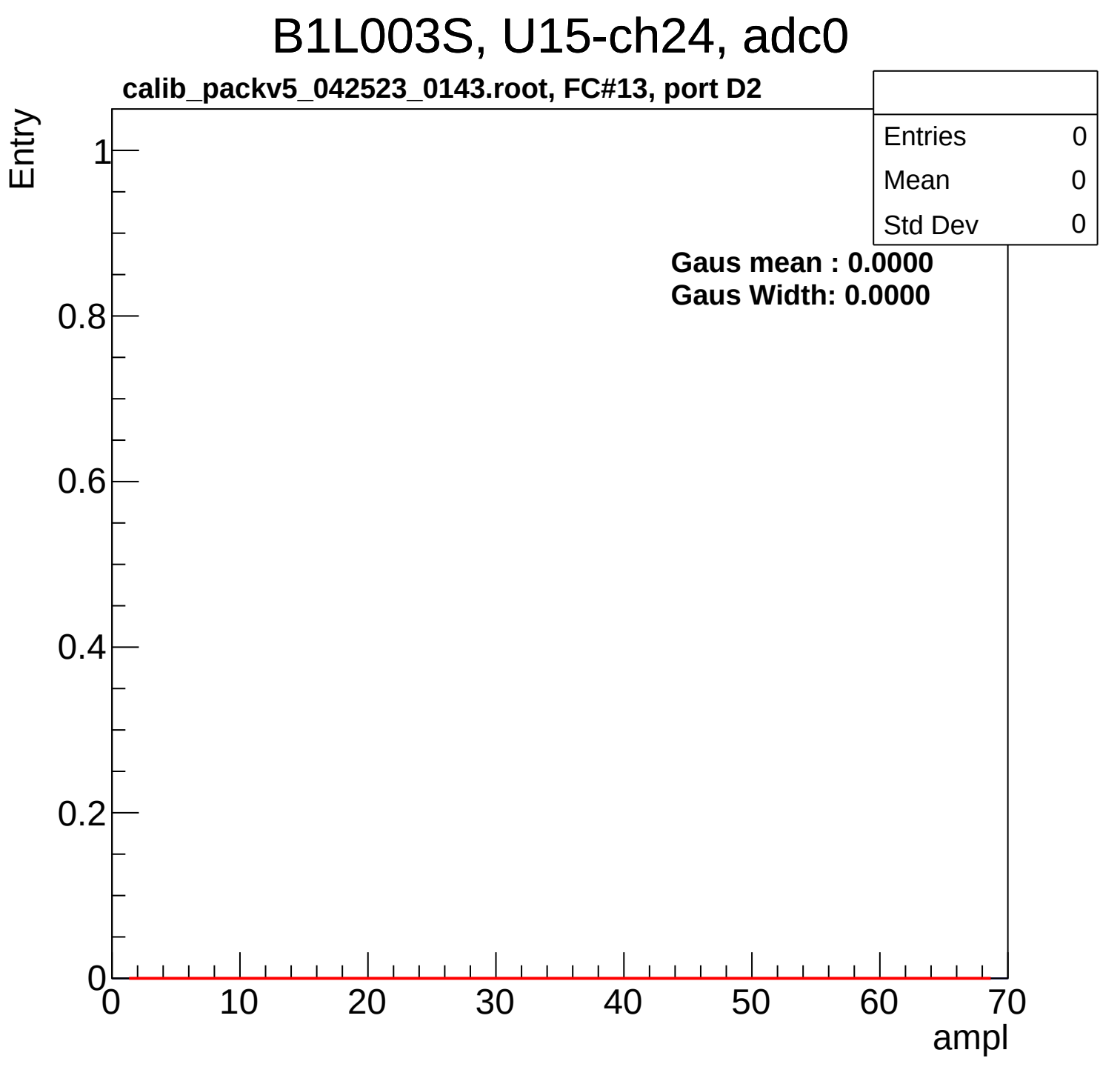
ampl


B1L003S, U15-ch24, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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0.2
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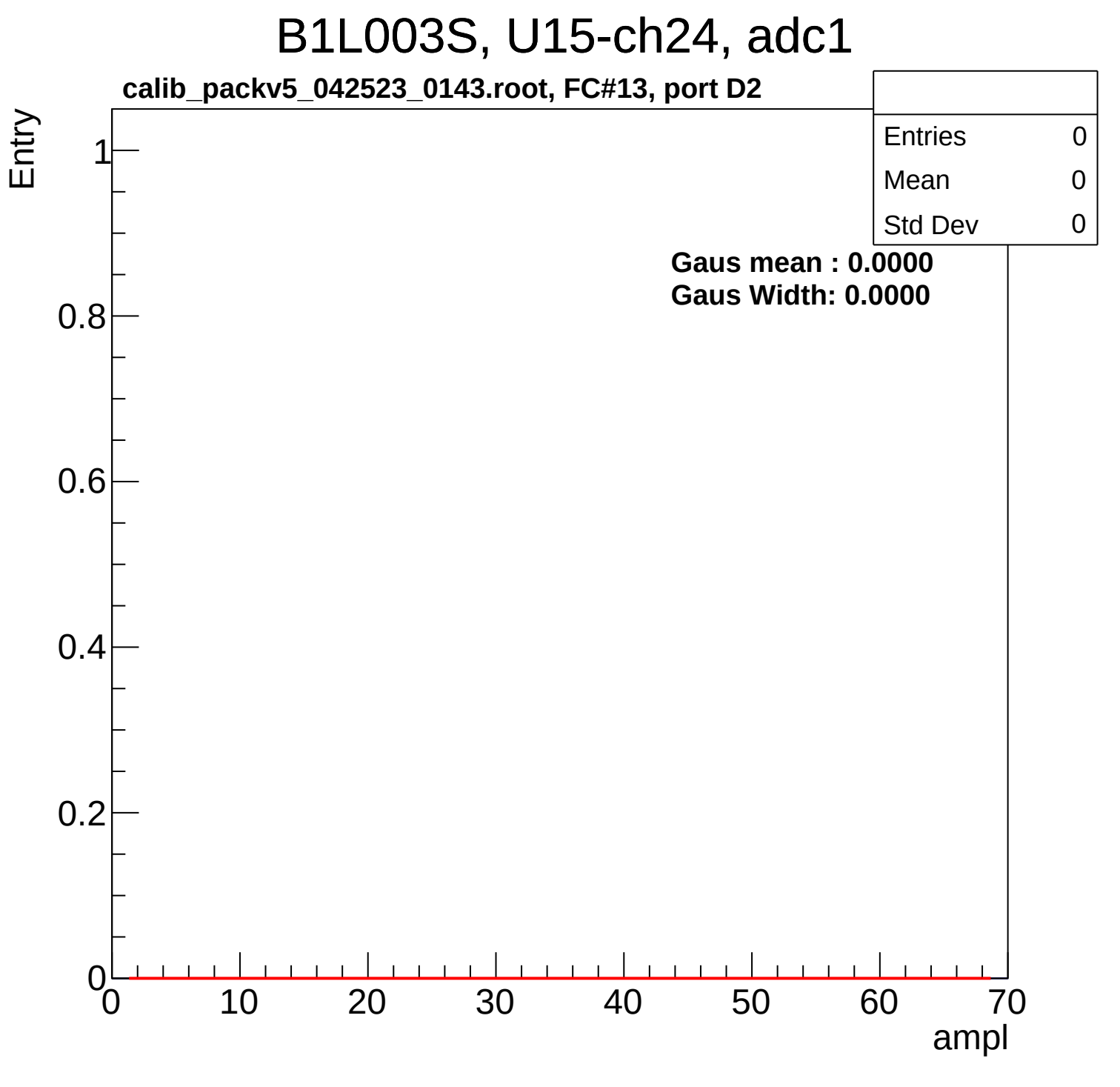
ampl


B1L003S, U15-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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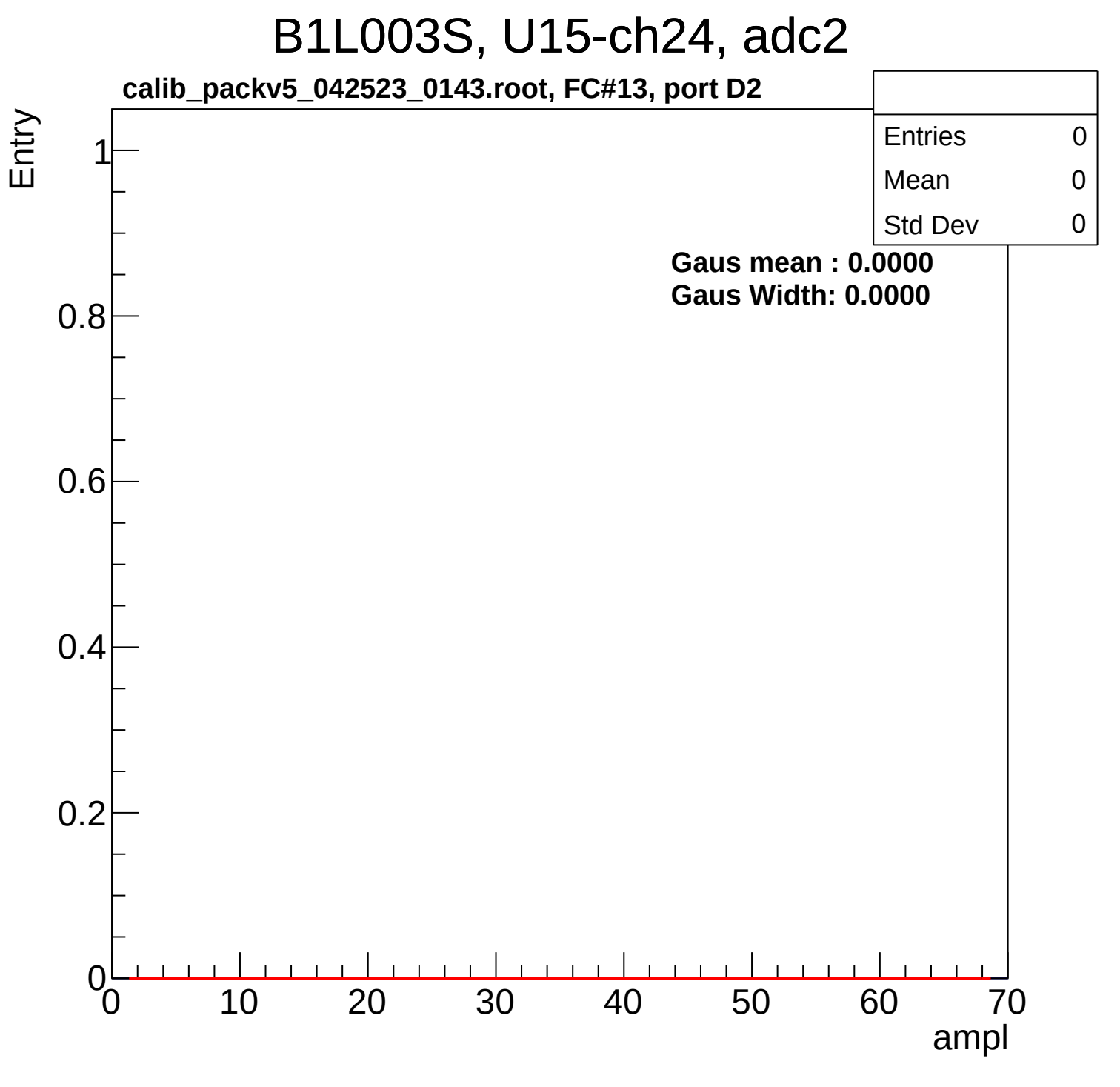
ampl


B1L003S, U15-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.4
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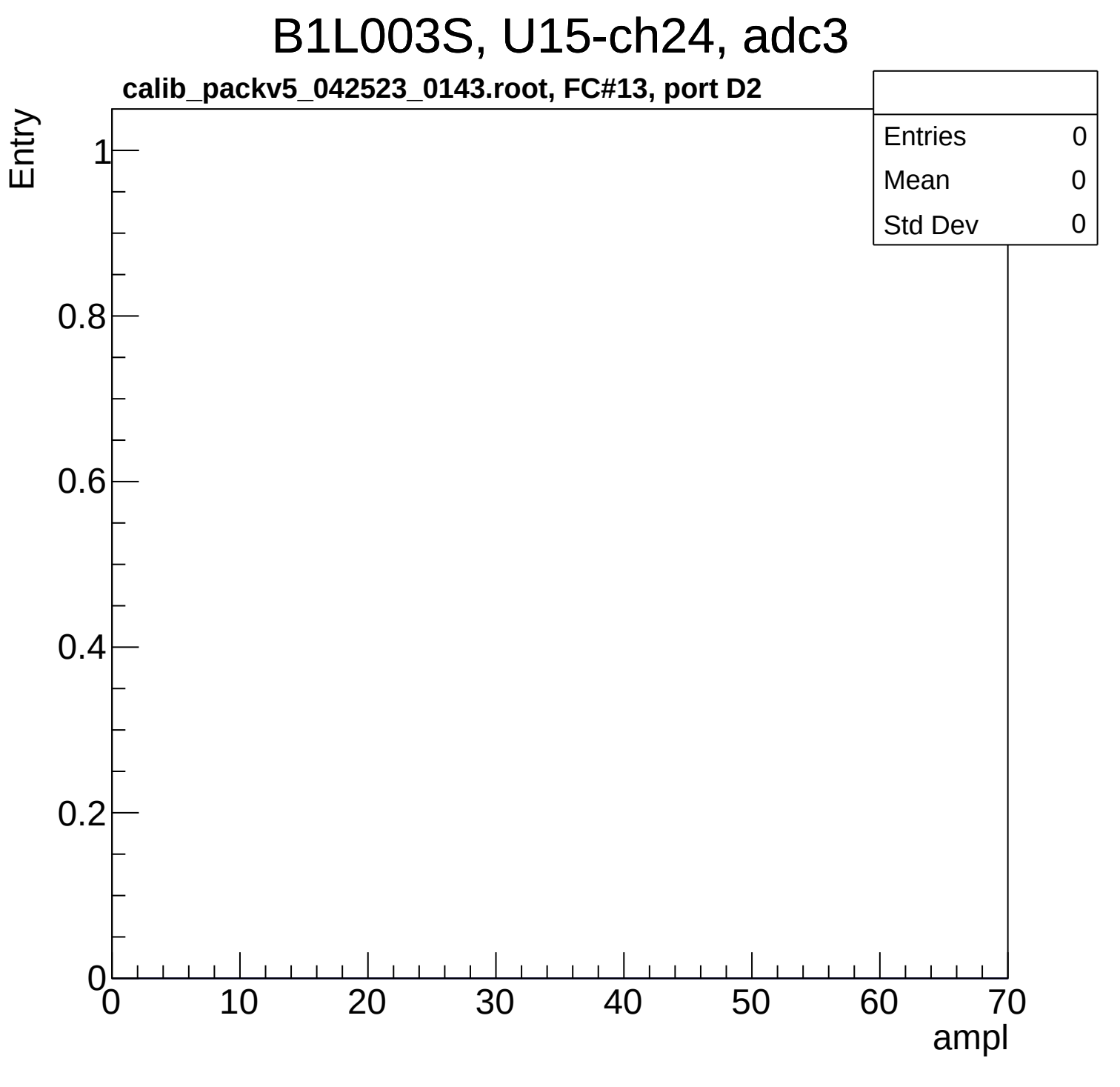
ampl


B1L003S, U15-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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Mean 0
Std Dev 0
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0.6
0.4
0.2
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60
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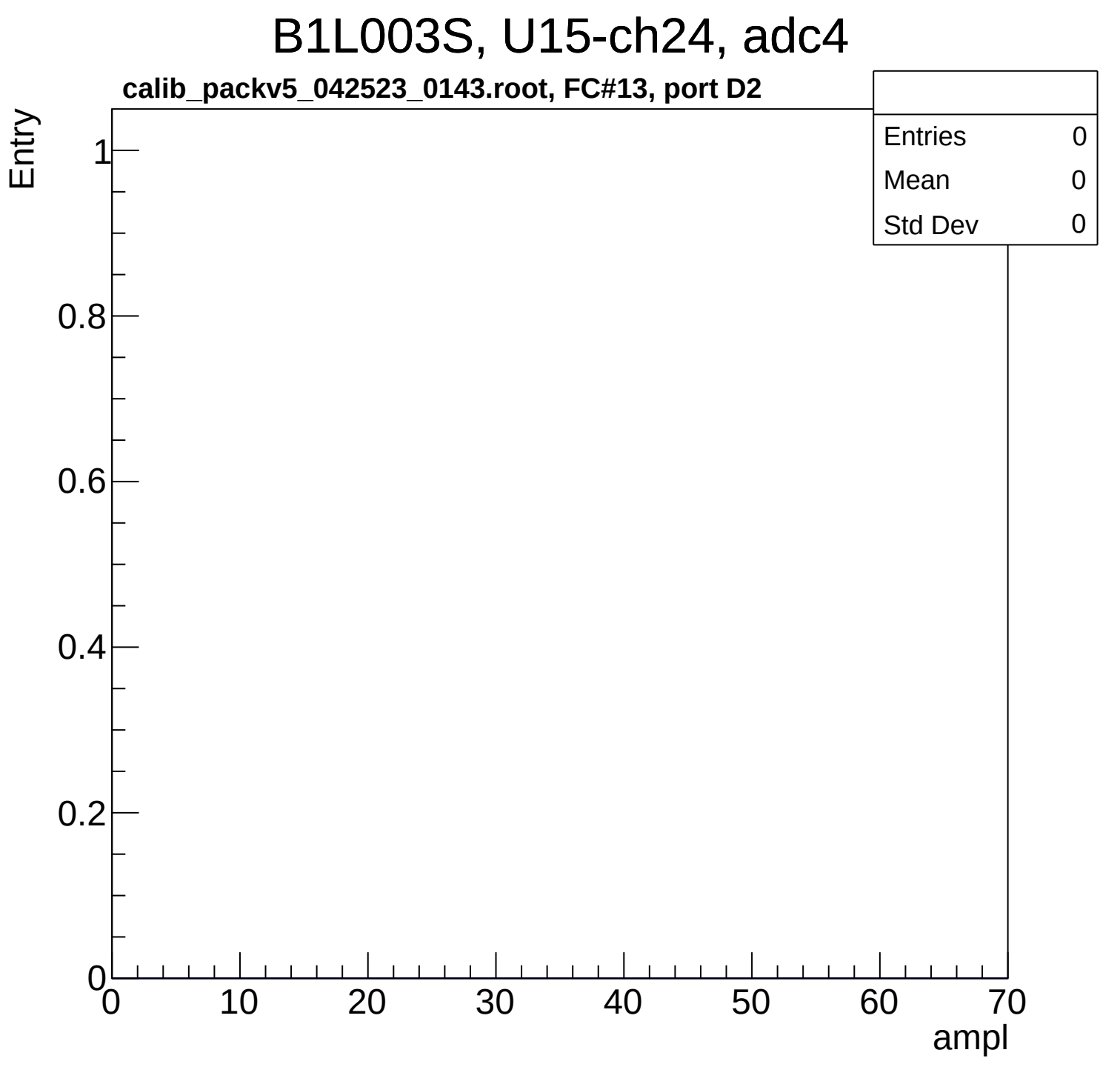
ampl


B1L003S, U15-ch24, adc4
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Entries 0
Mean 0
Std Dev 0
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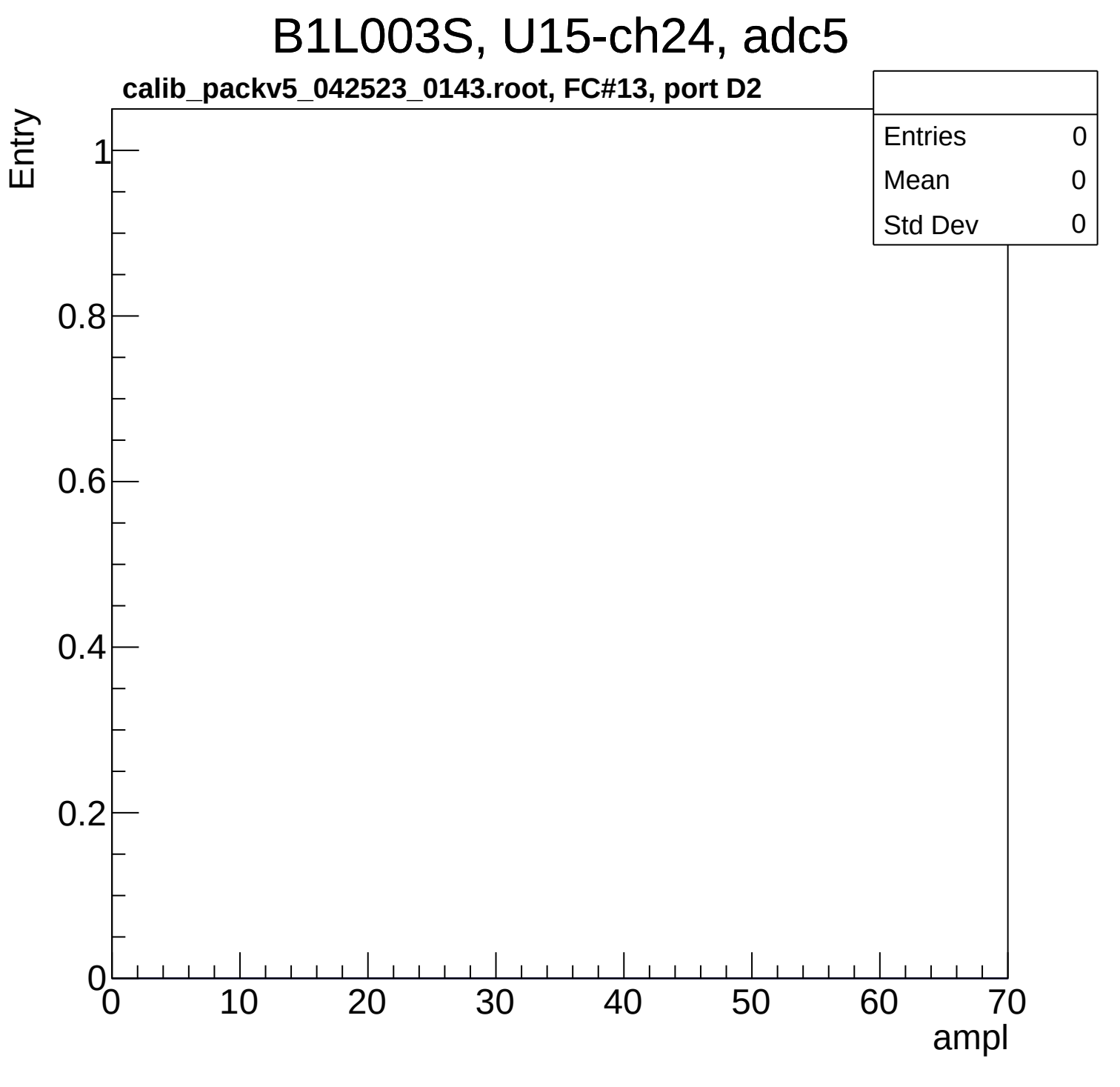
ampl


B1L003S, U15-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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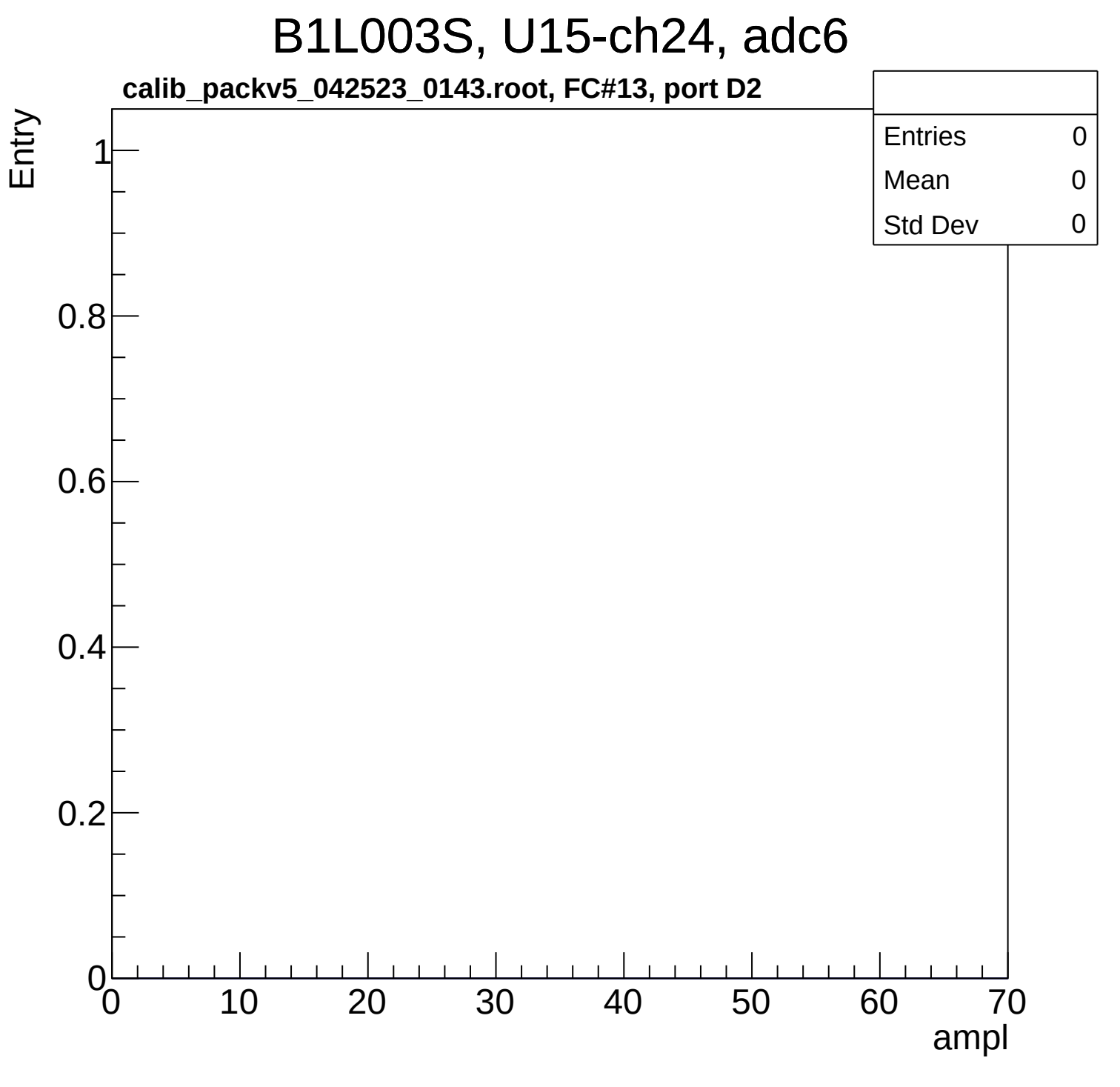
ampl


B1L003S, U15-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
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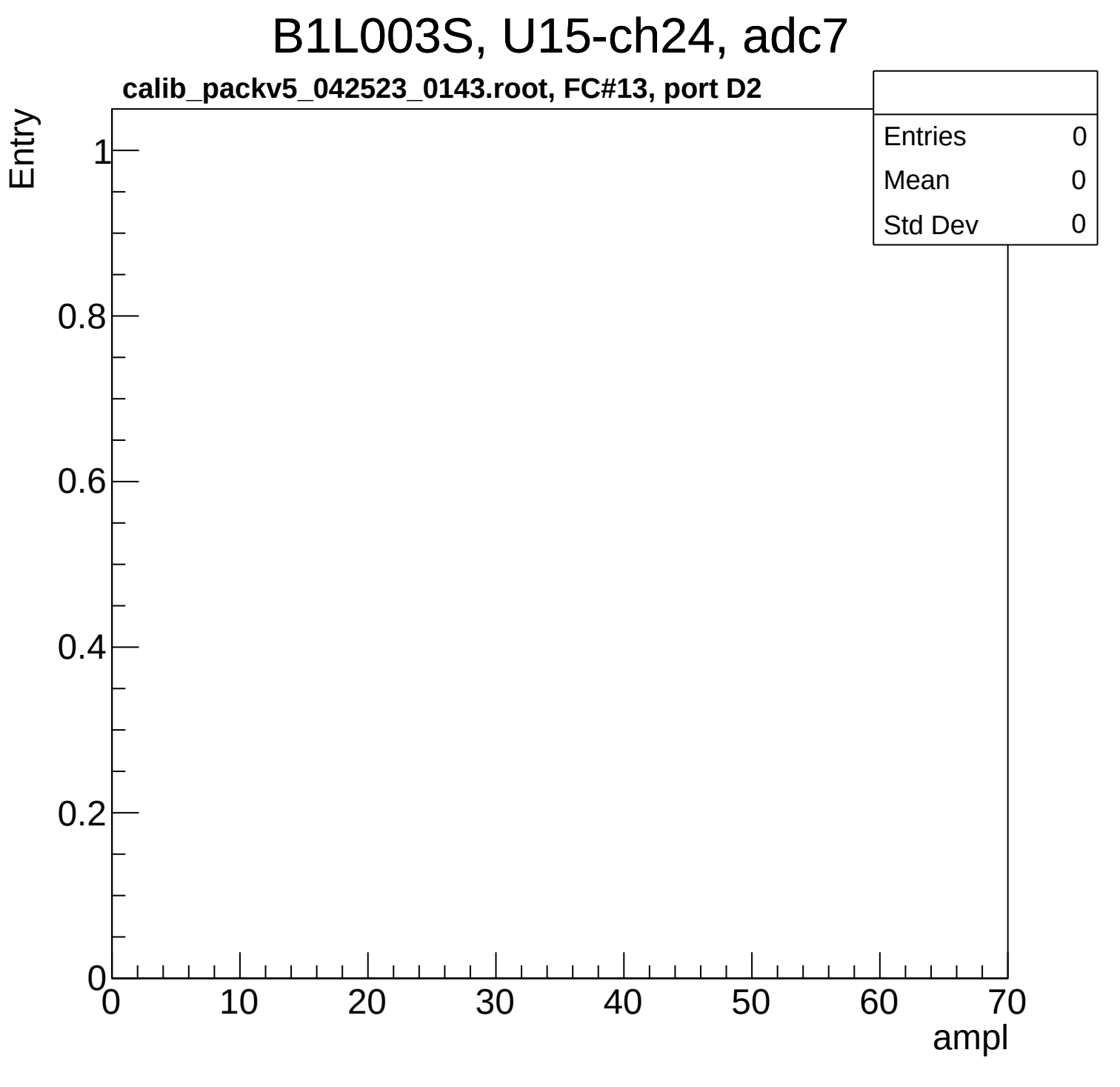
50
60
70
ampl


B1L003S, U15-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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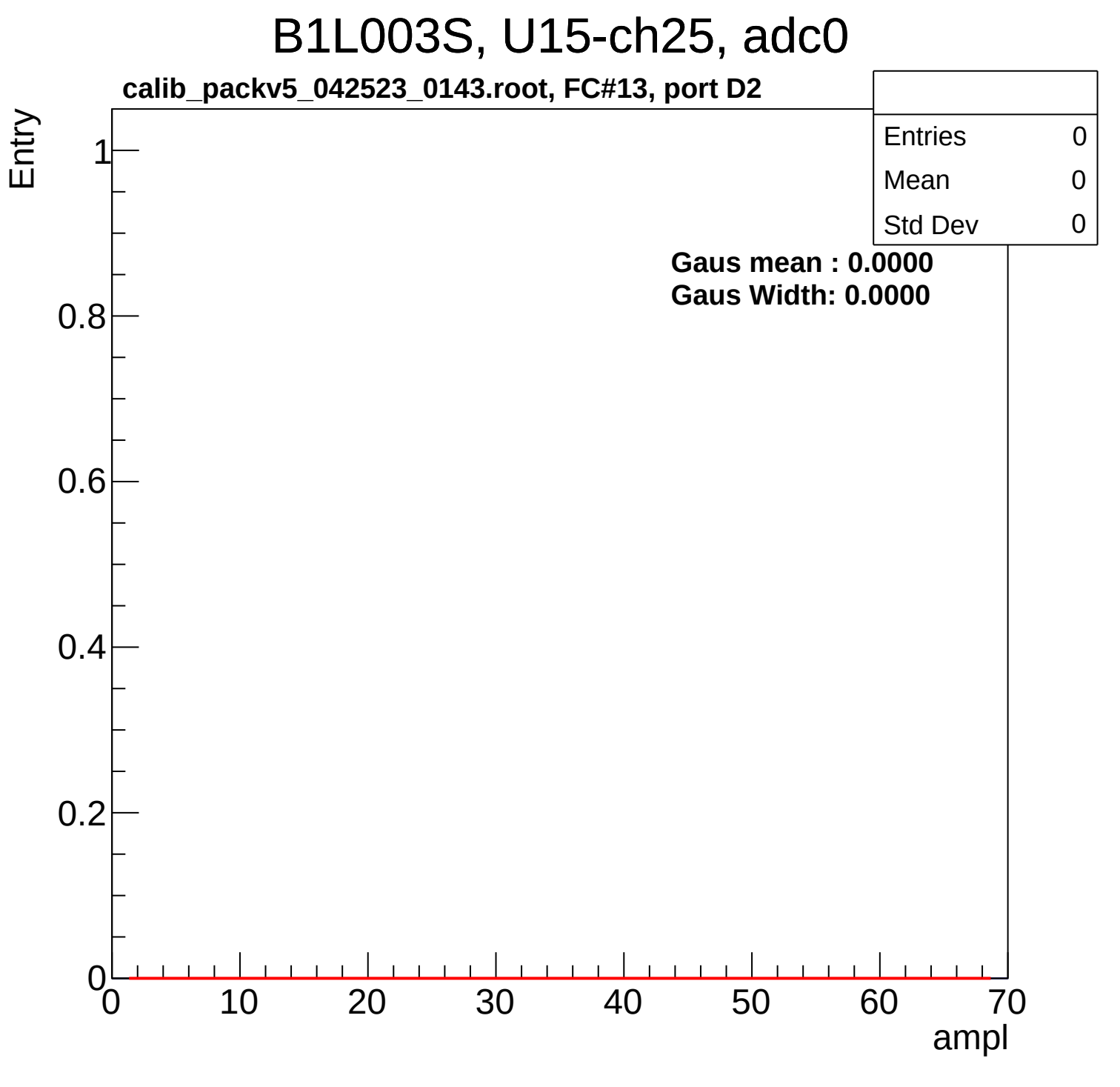
ampl


B1L003S, U15-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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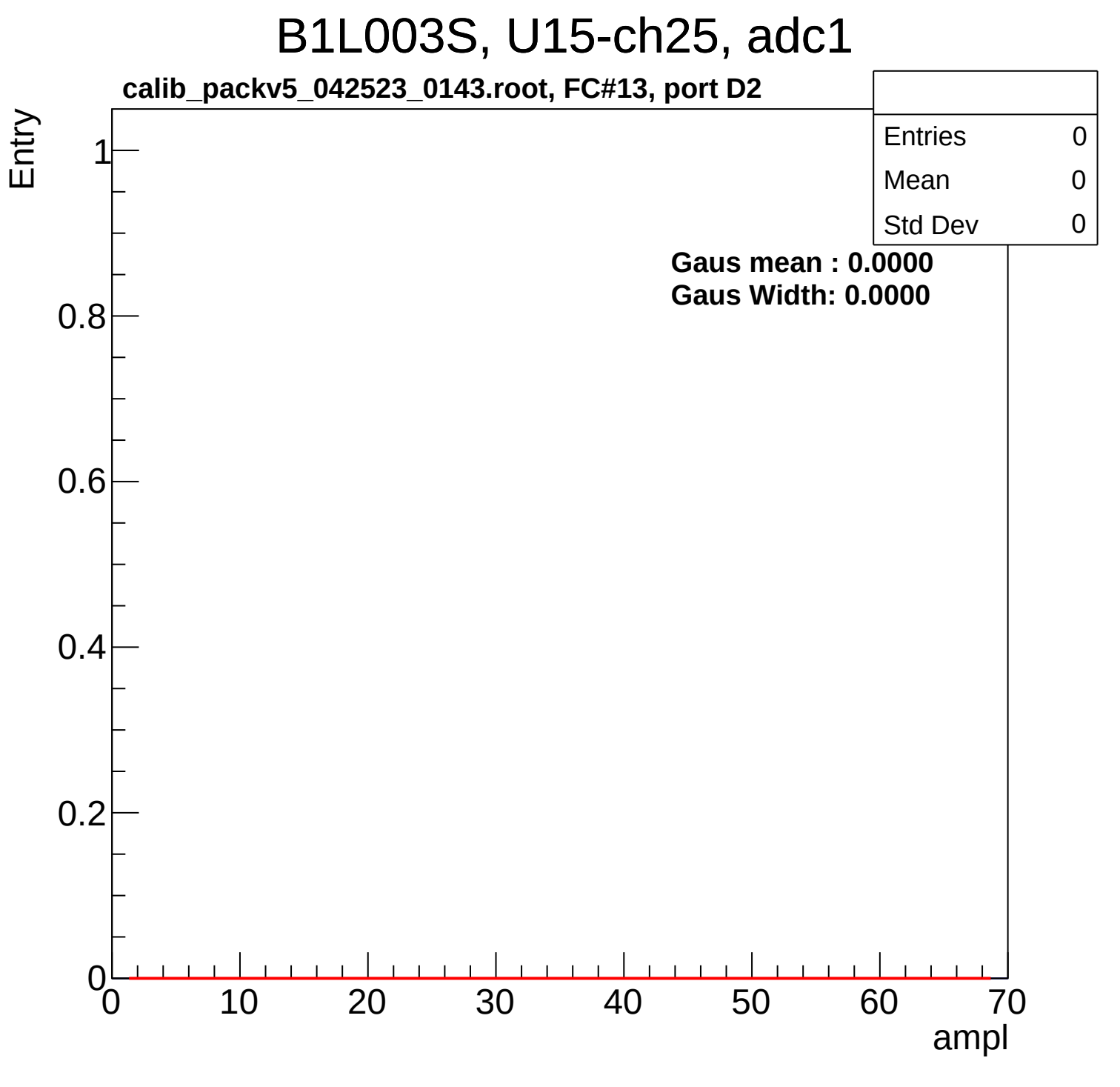
ampl


B1L003S, U15-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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0.6
0.4
0.2
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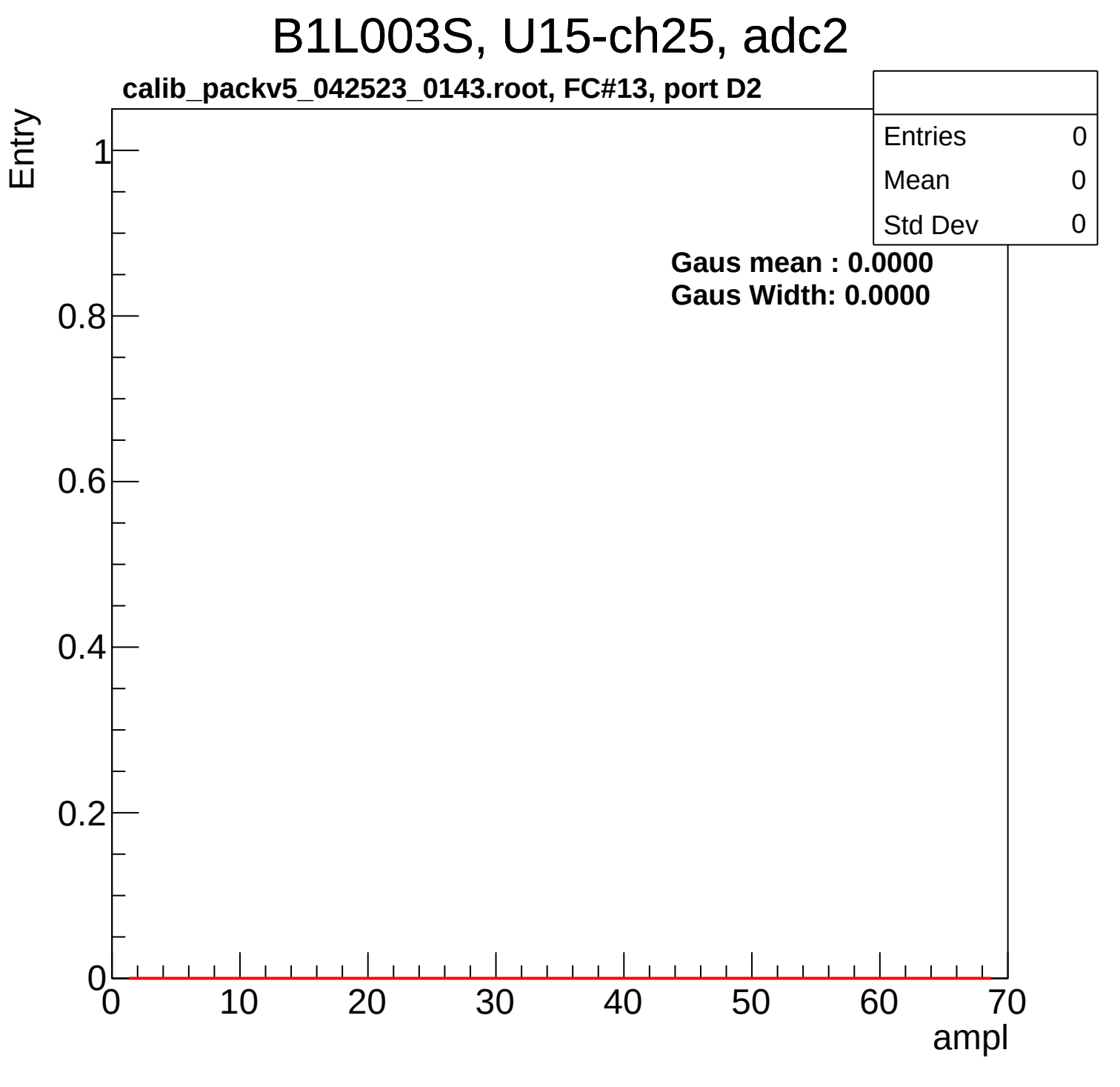
ampl


B1L003S, U15-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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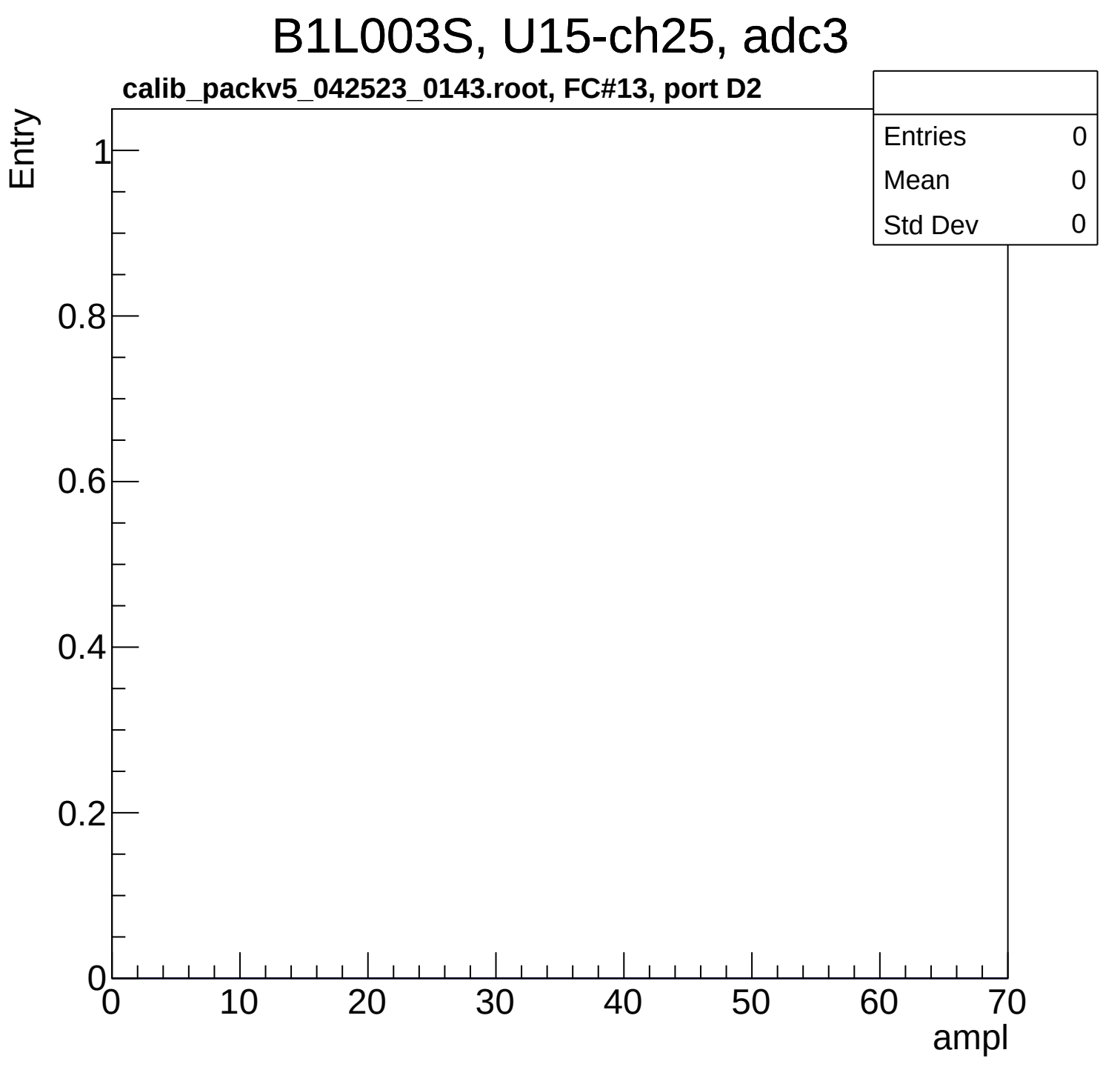
ampl


B1L003S, U15-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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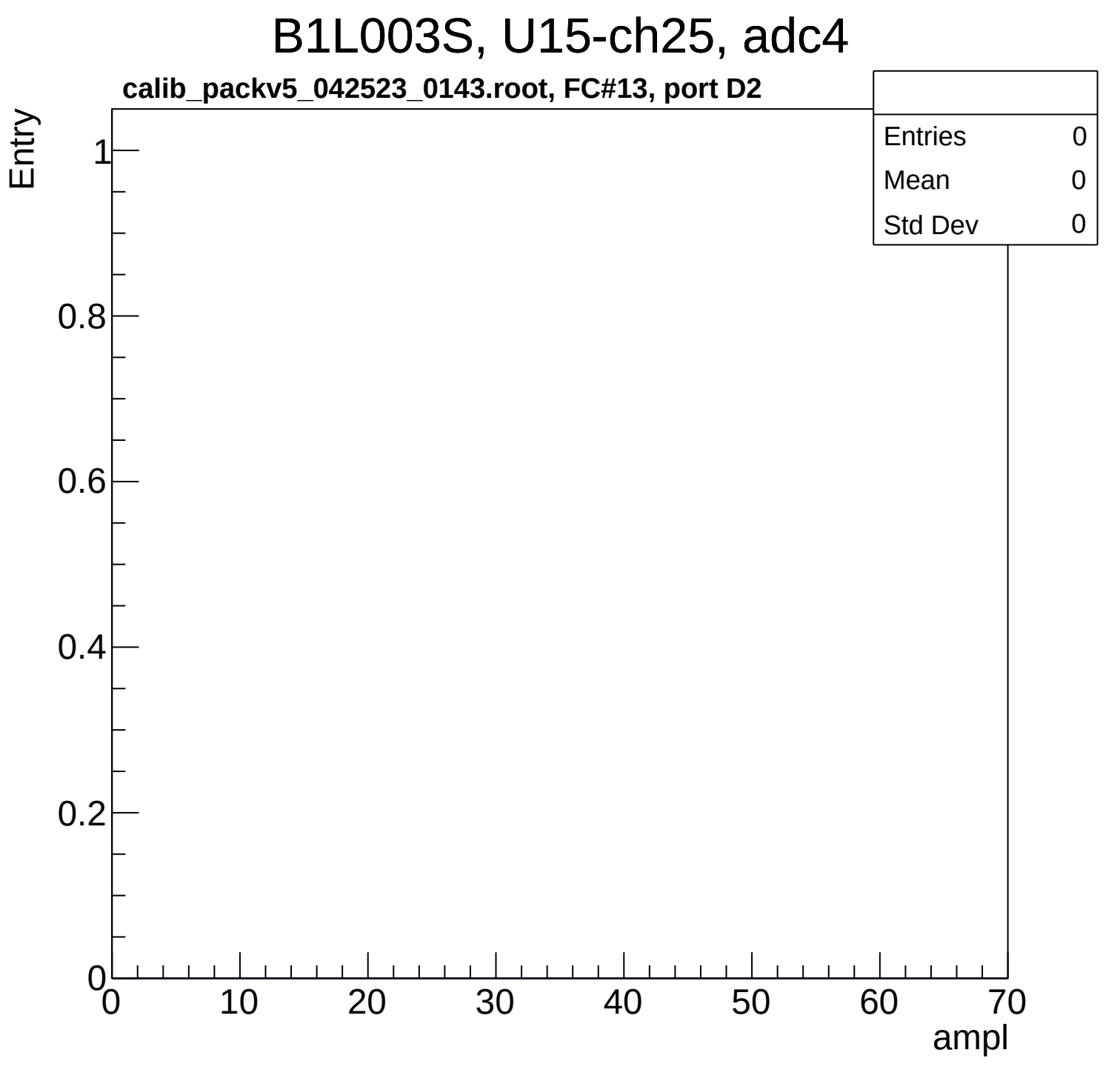
ampl


B1L003S, U15-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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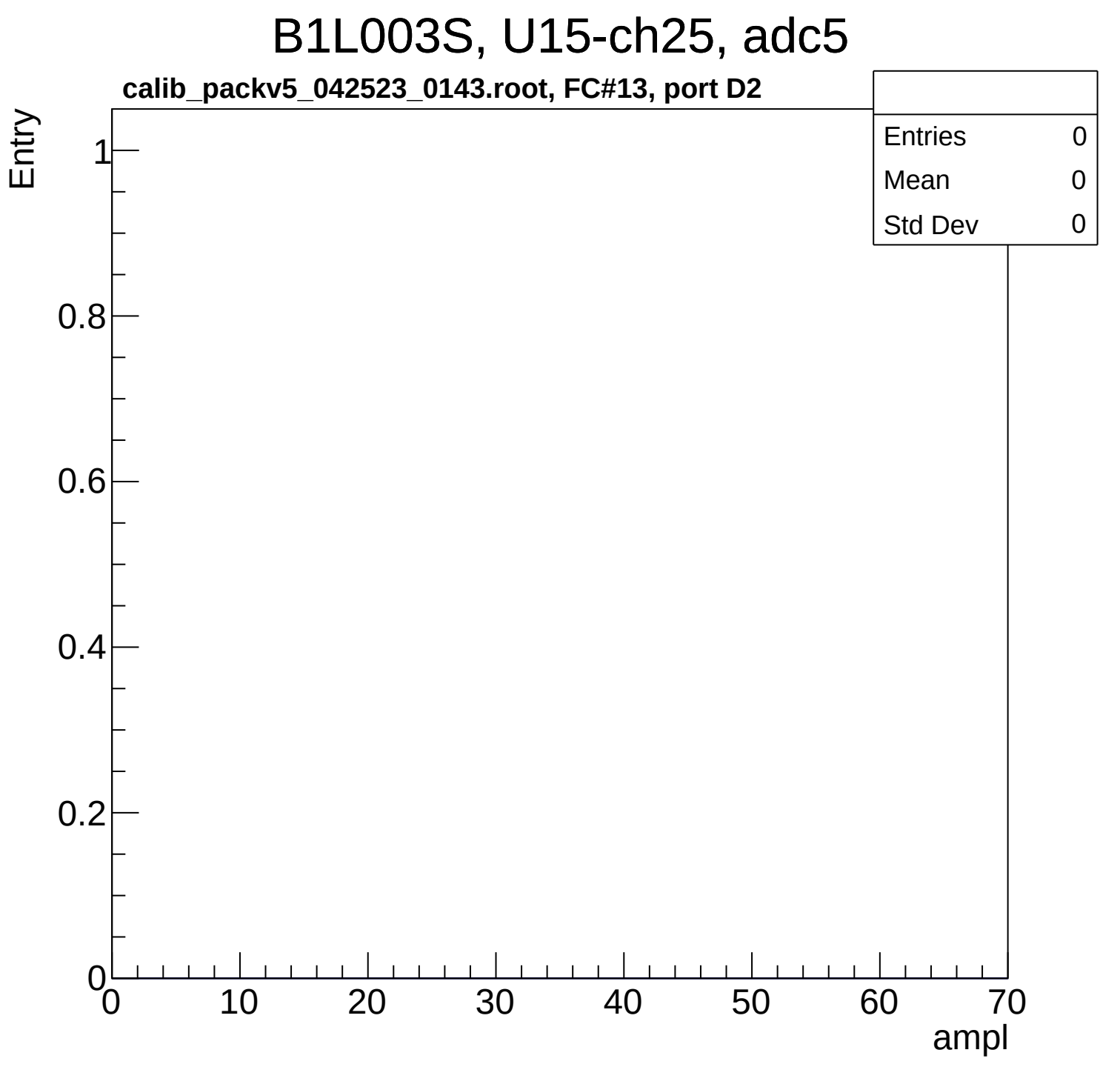
ampl


B1L003S, U15-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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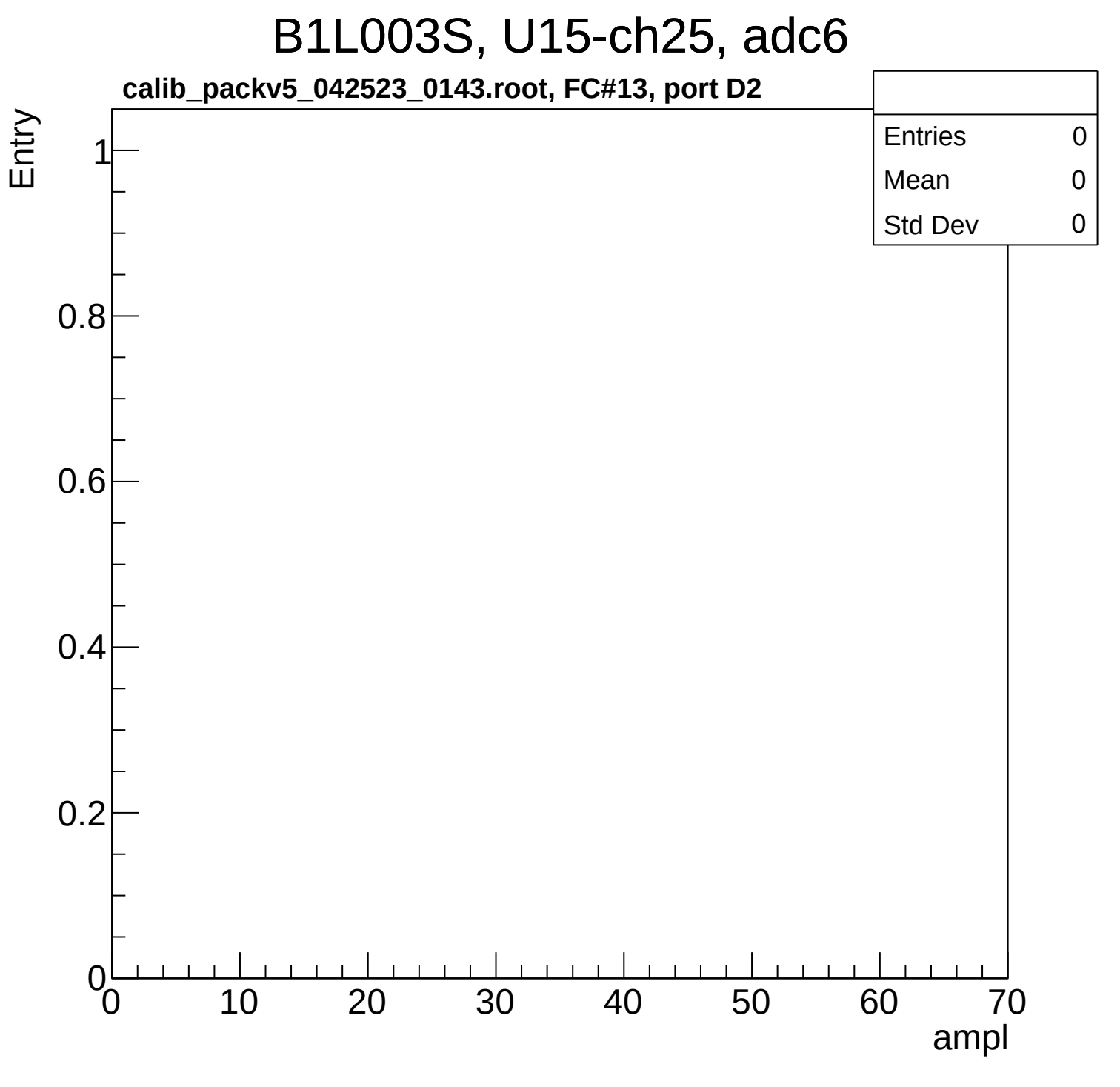
B1L003S, U15-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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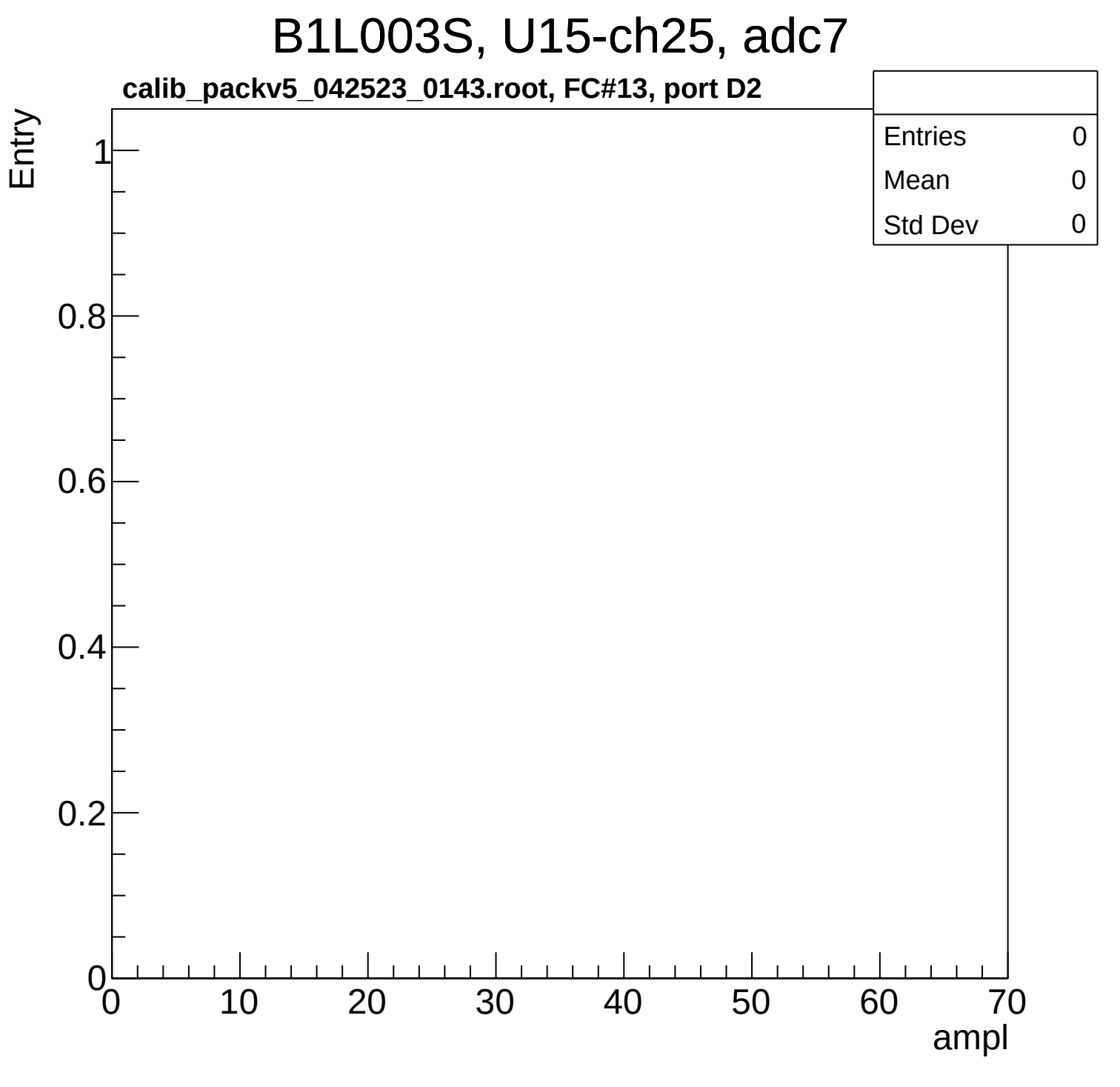
ampl


B1L003S, U15-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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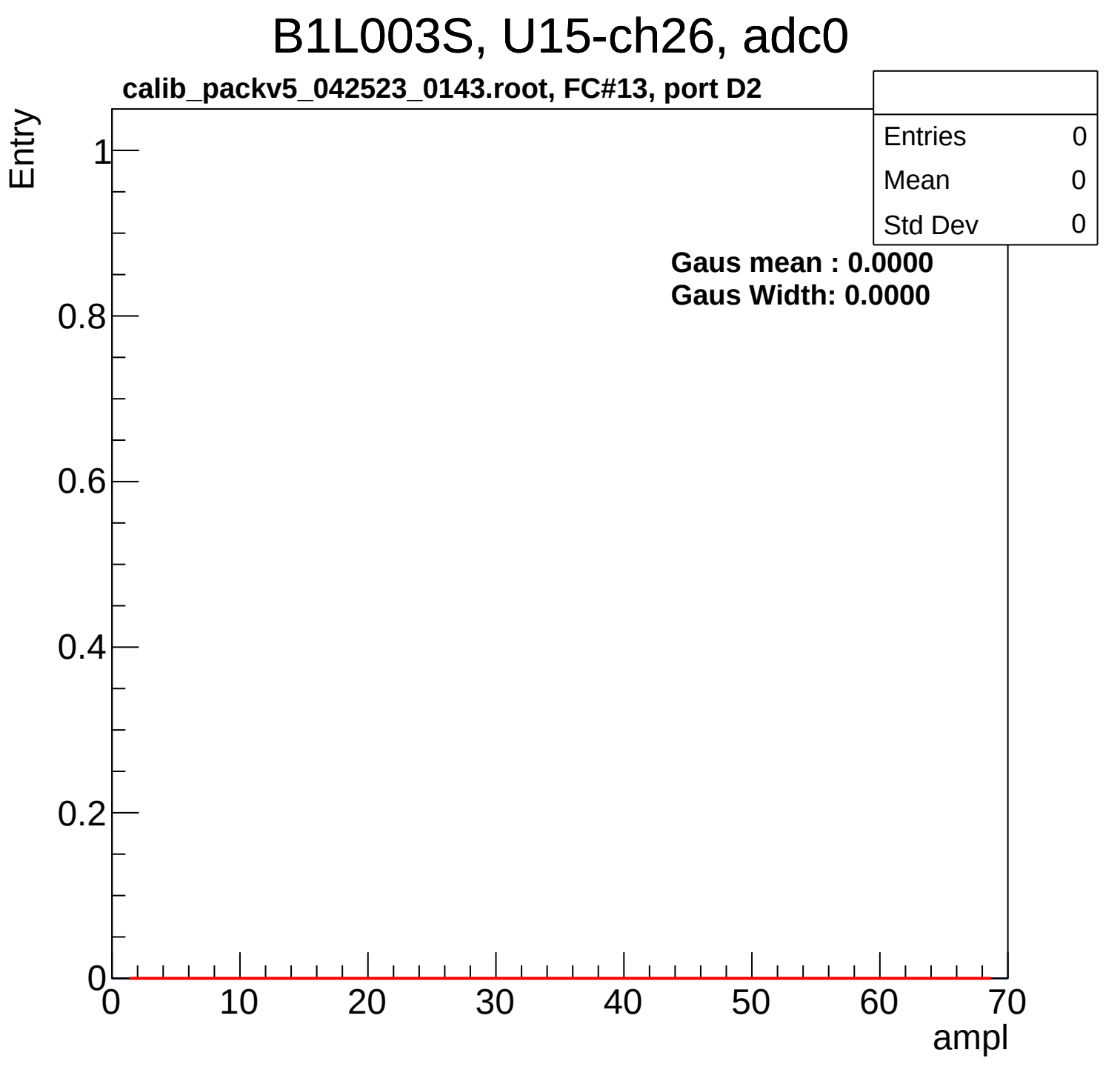
ampl


B1L003S, U15-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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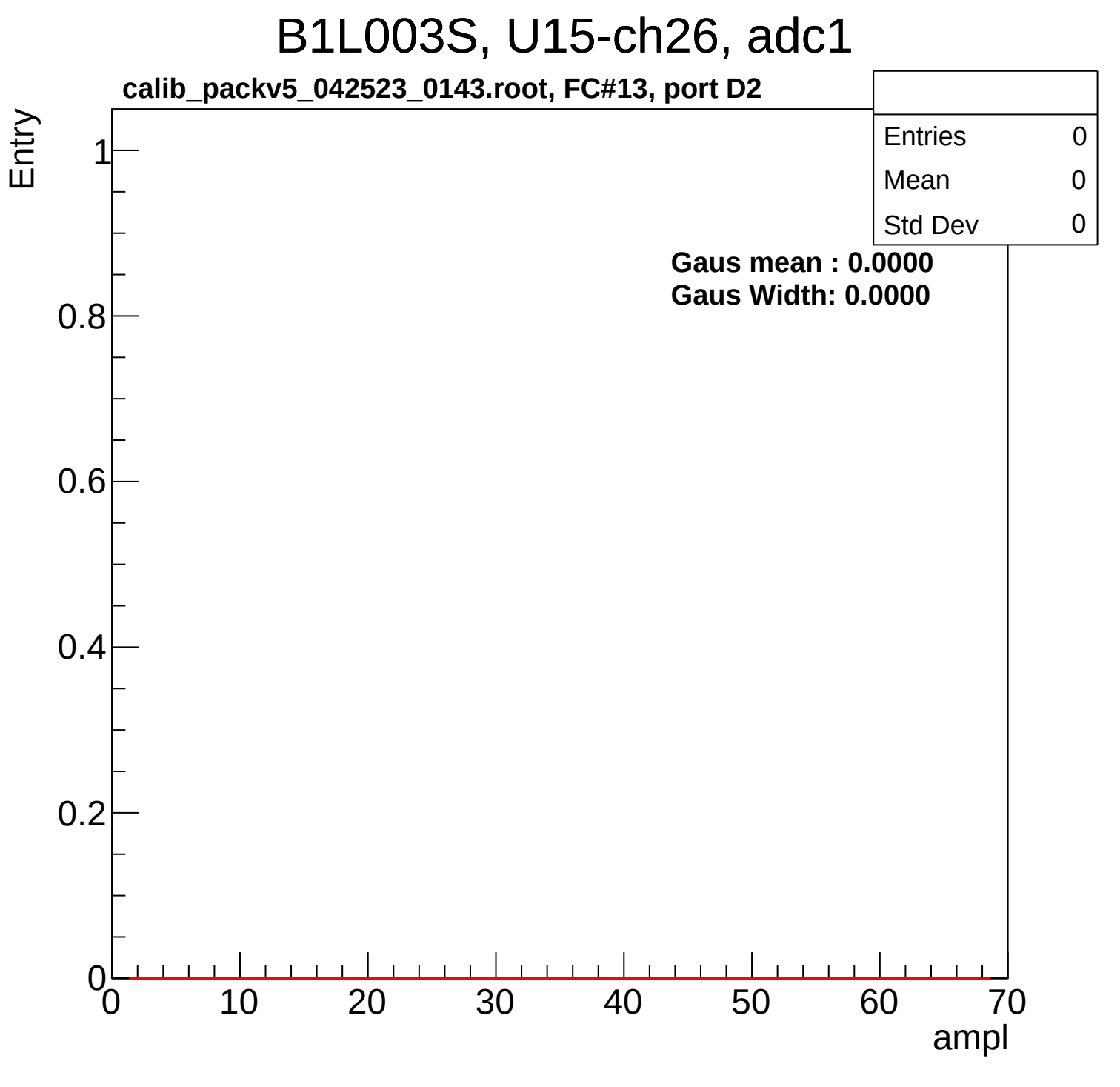
ampl


B1L003S, U15-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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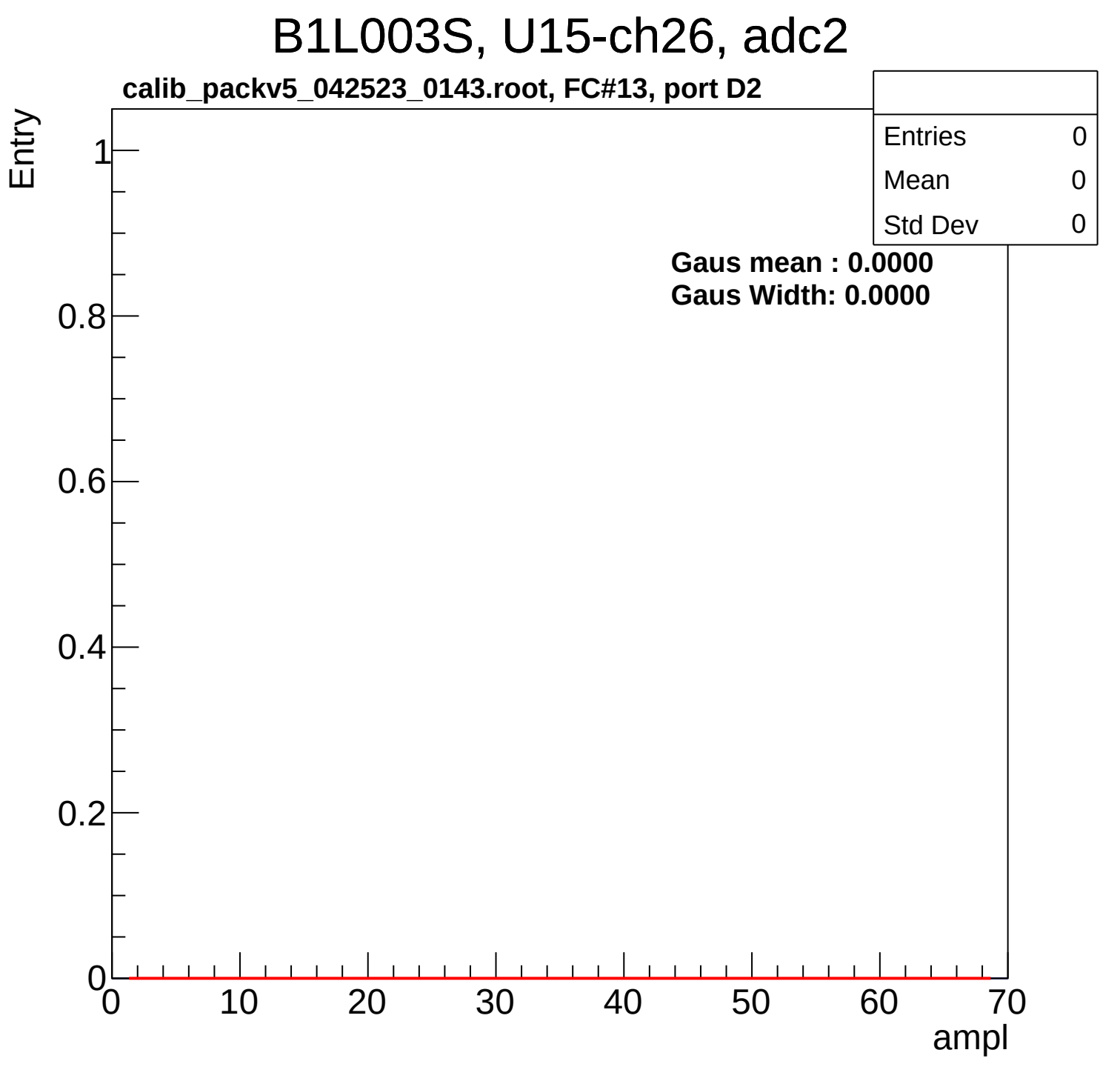
ampl


B1L003S, U15-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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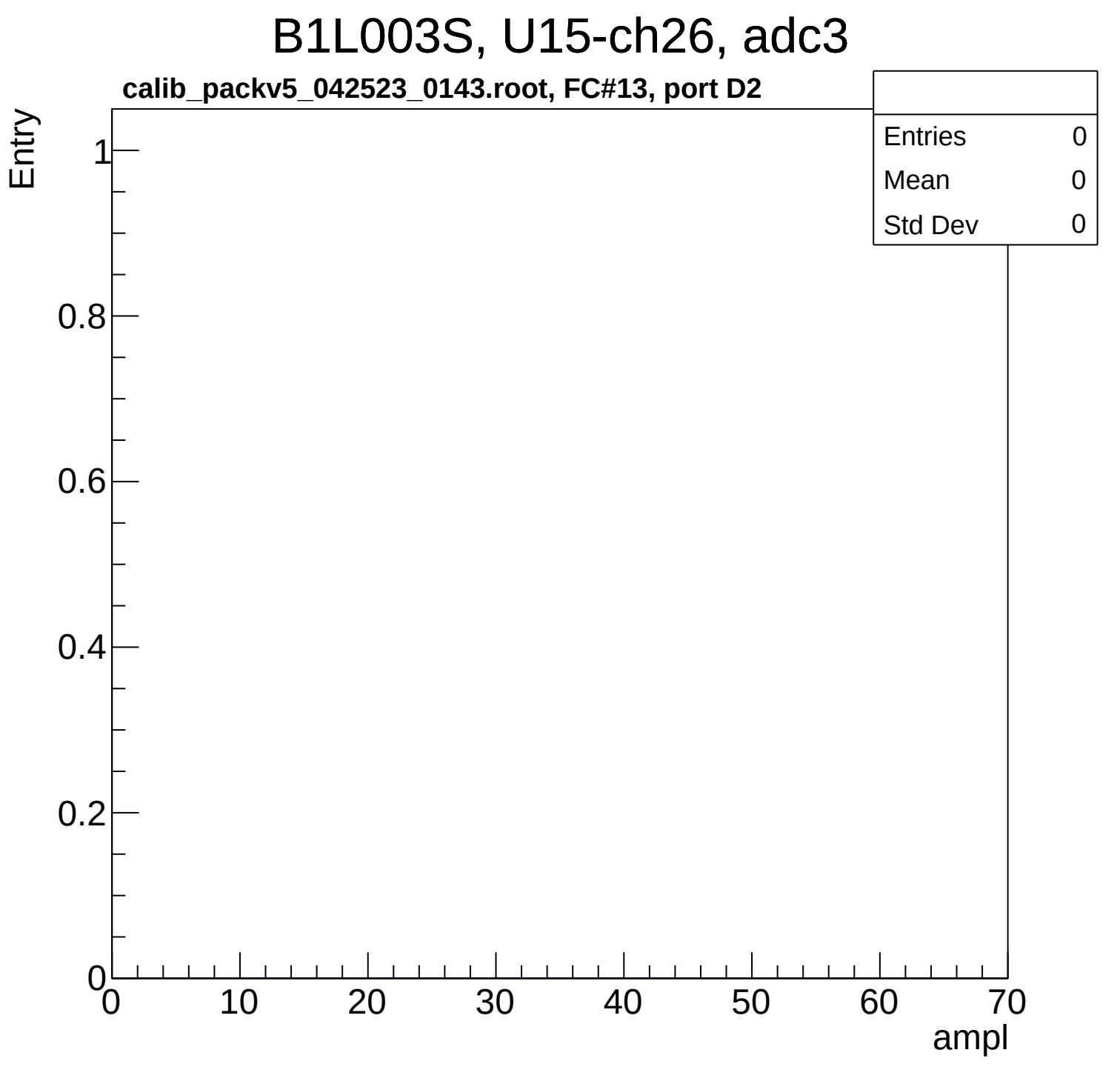
ampl


B1L003S, U15-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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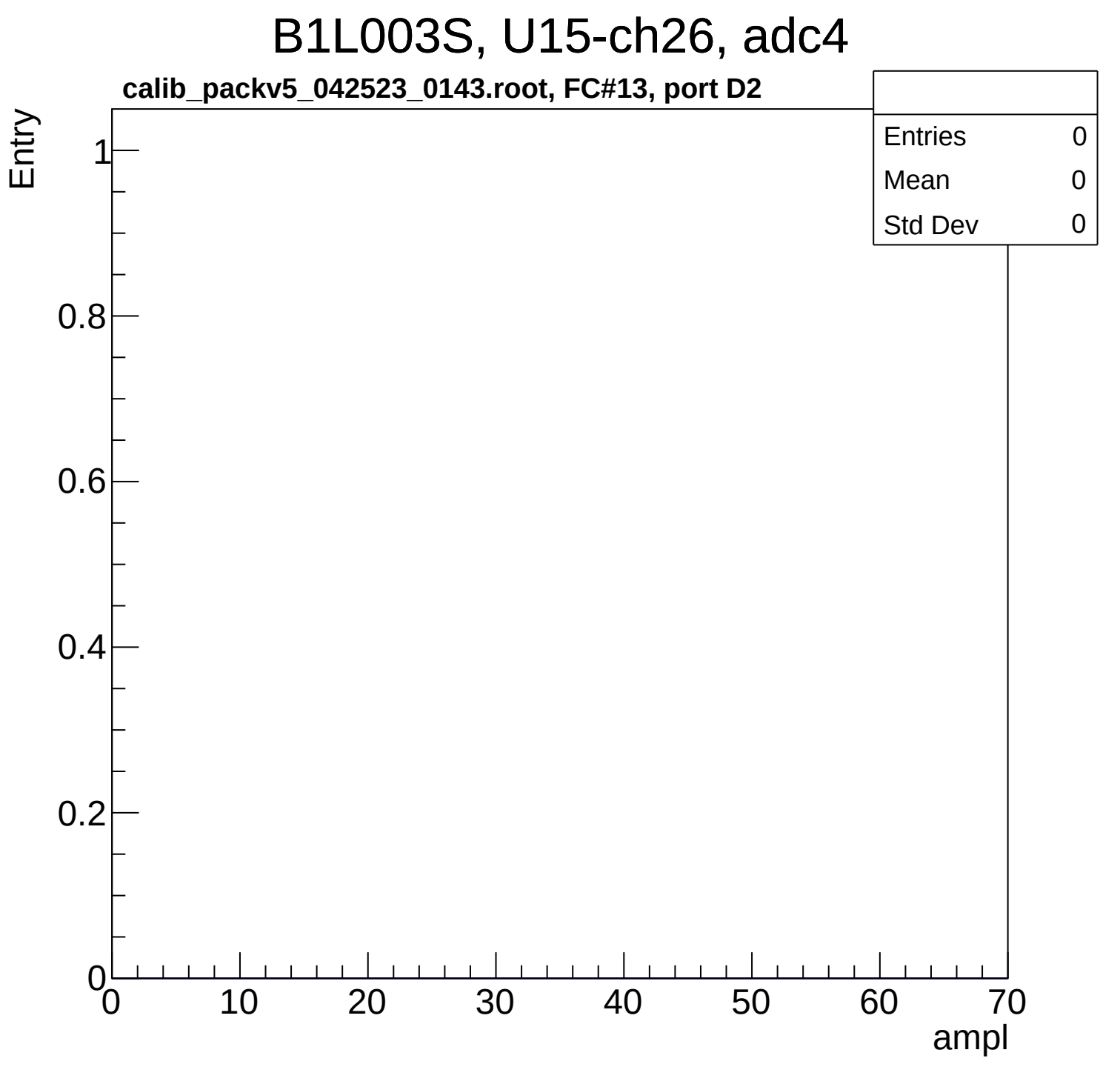
ampl


B1L003S, U15-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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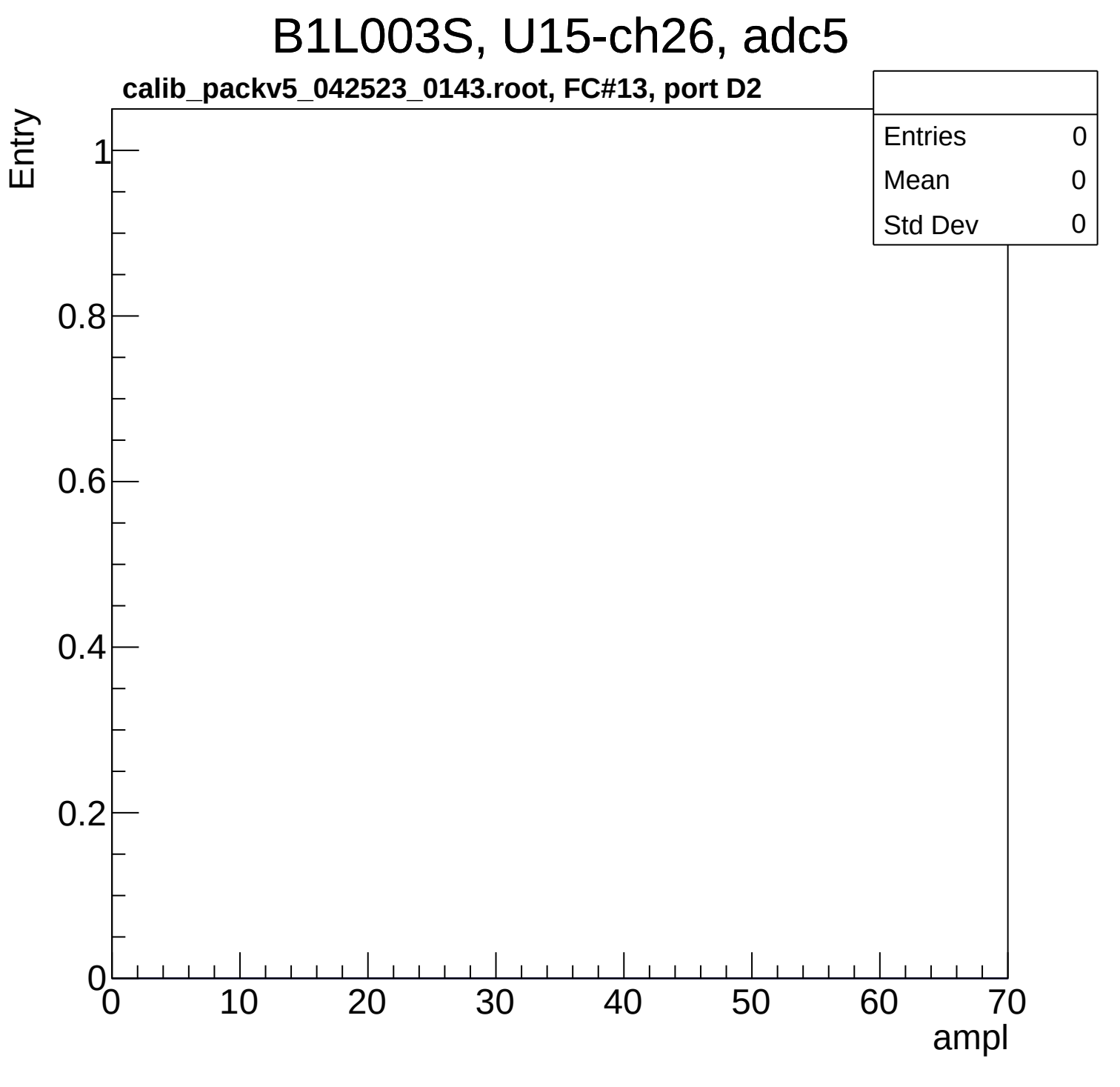
ampl


B1L003S, U15-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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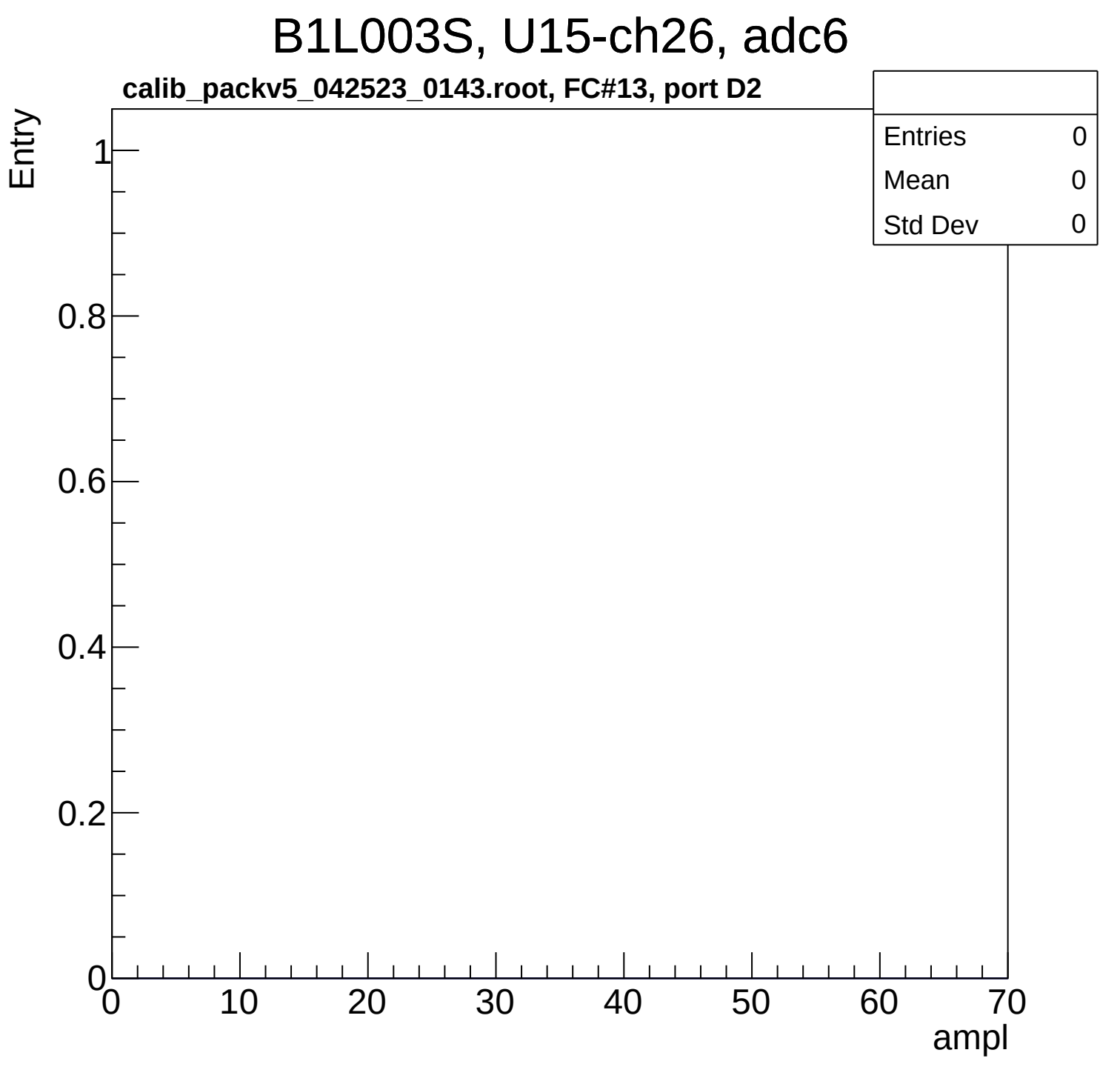
ampl


B1L003S, U15-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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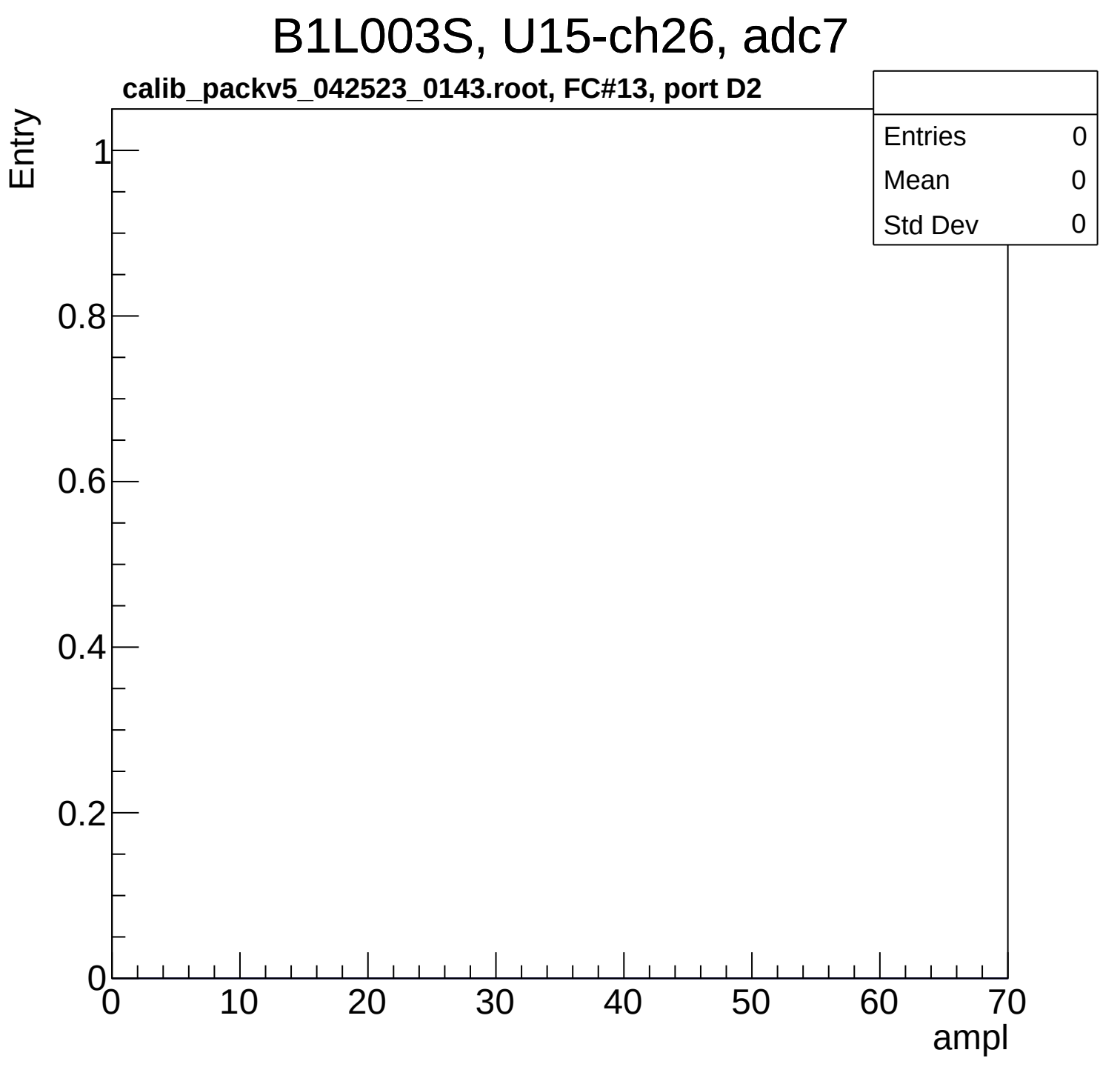
ampl


B1L003S, U15-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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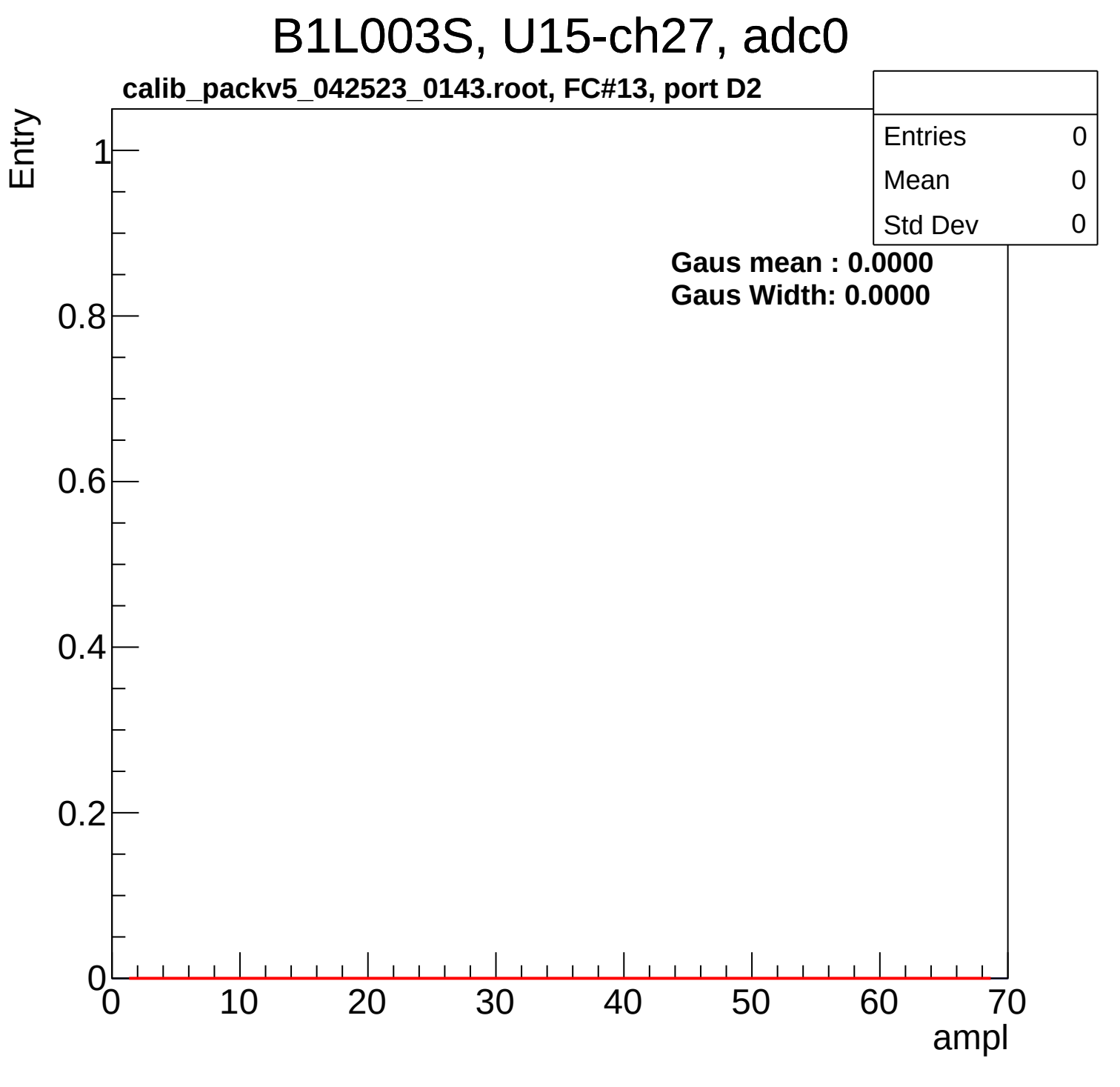
ampl


B1L003S, U15-ch27, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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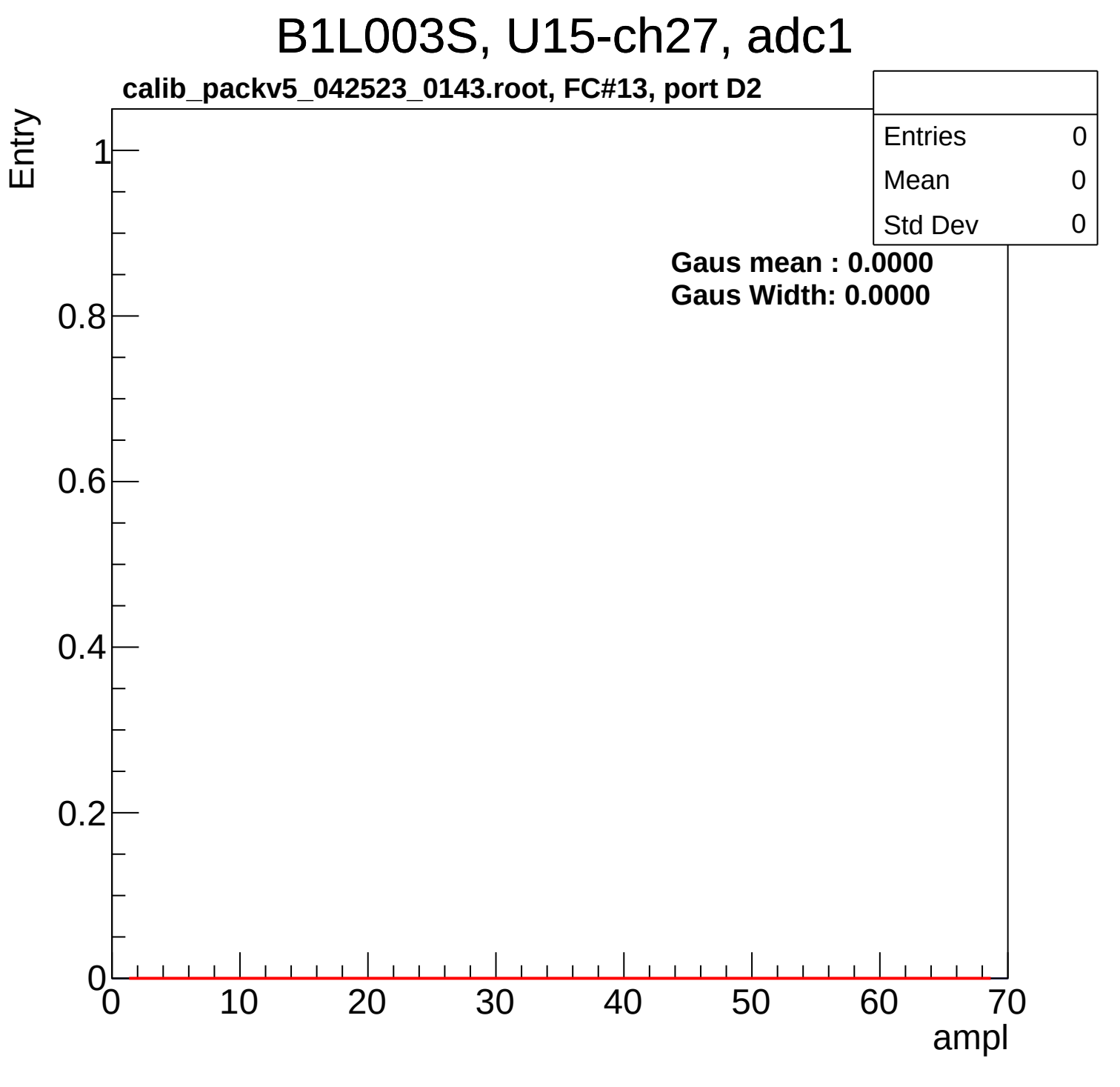
ampl


B1L003S, U15-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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0.6
0.4
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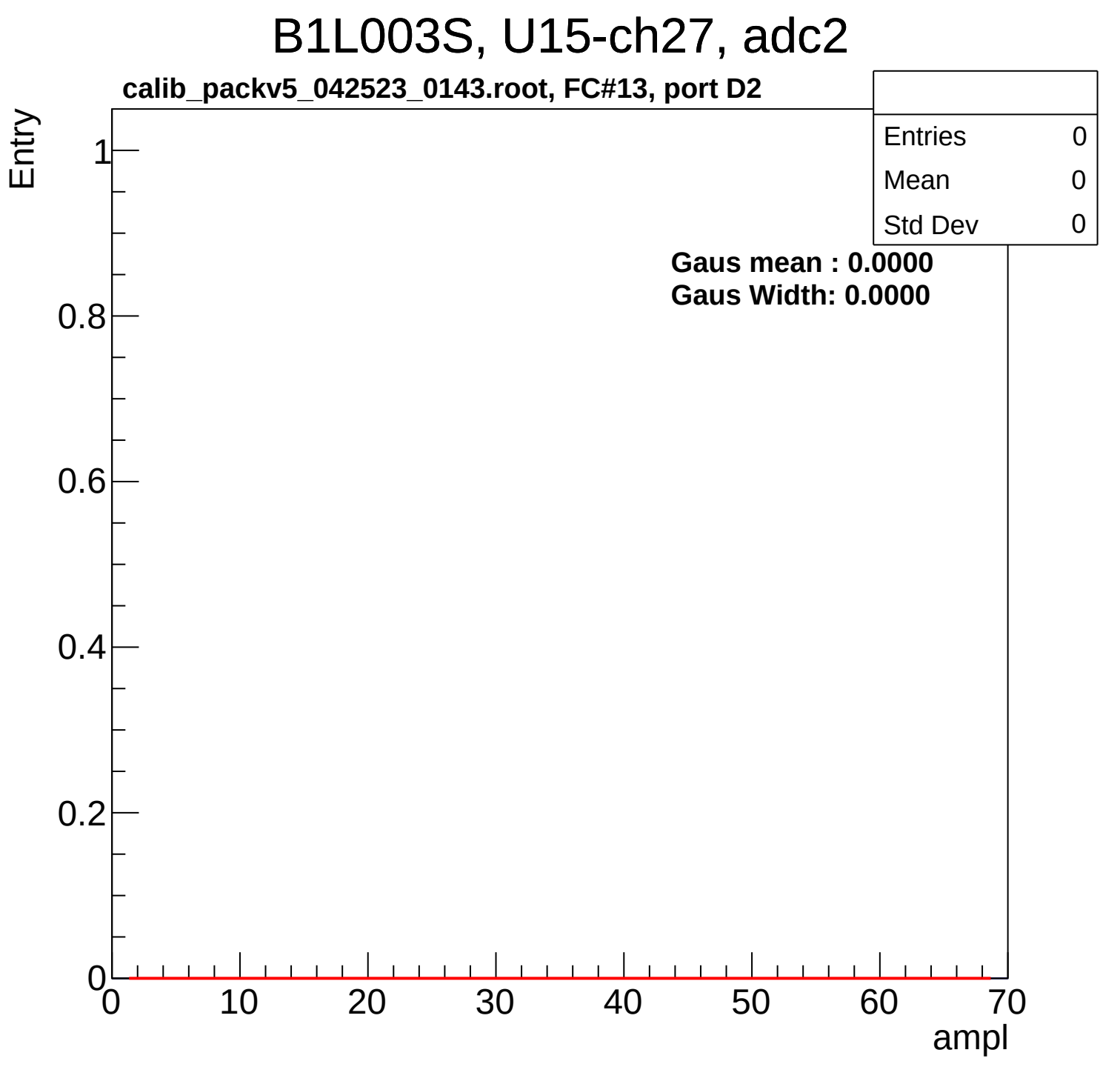
ampl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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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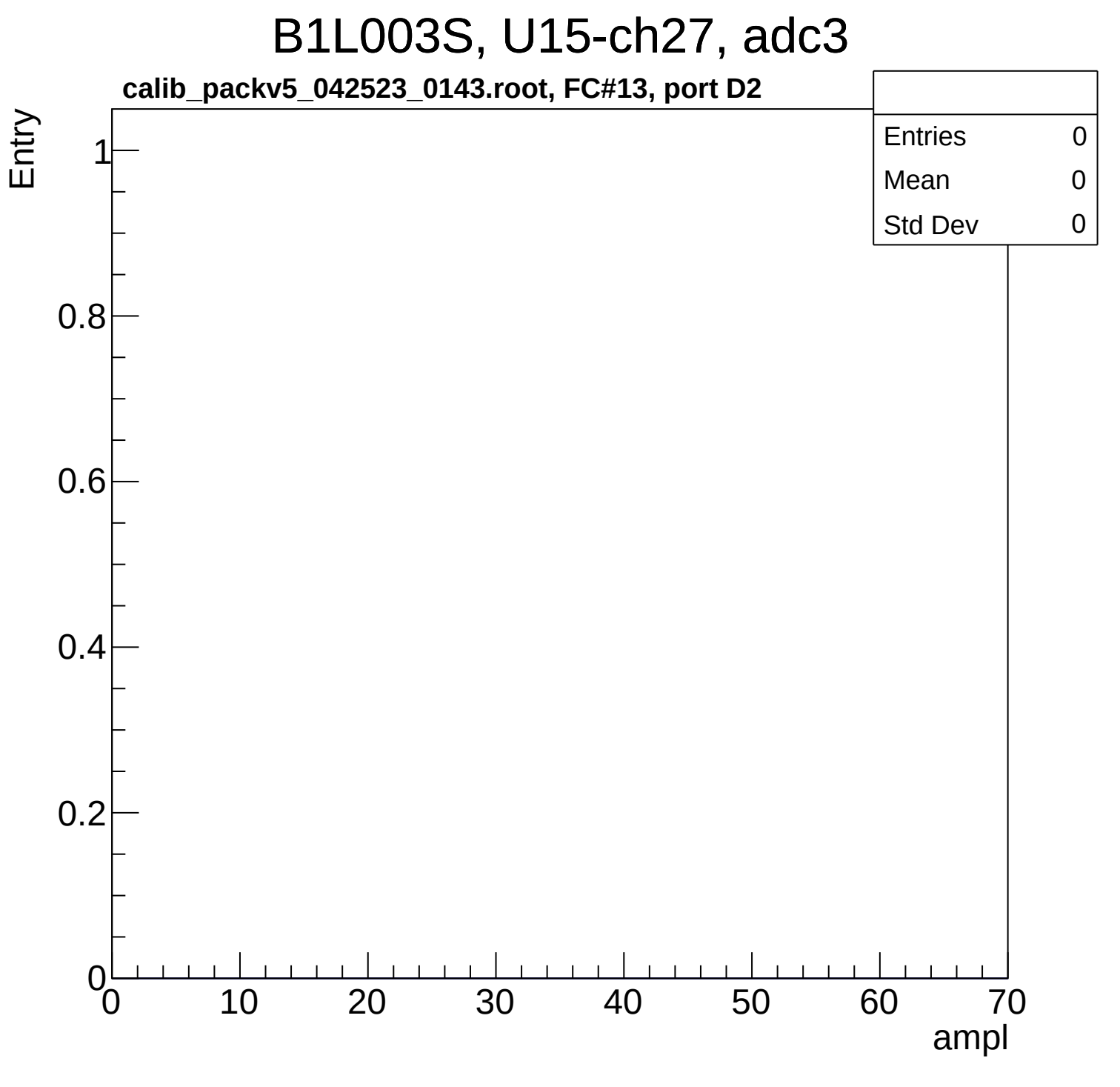
B1L003S, U15-ch27, adc3
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Std Dev 0
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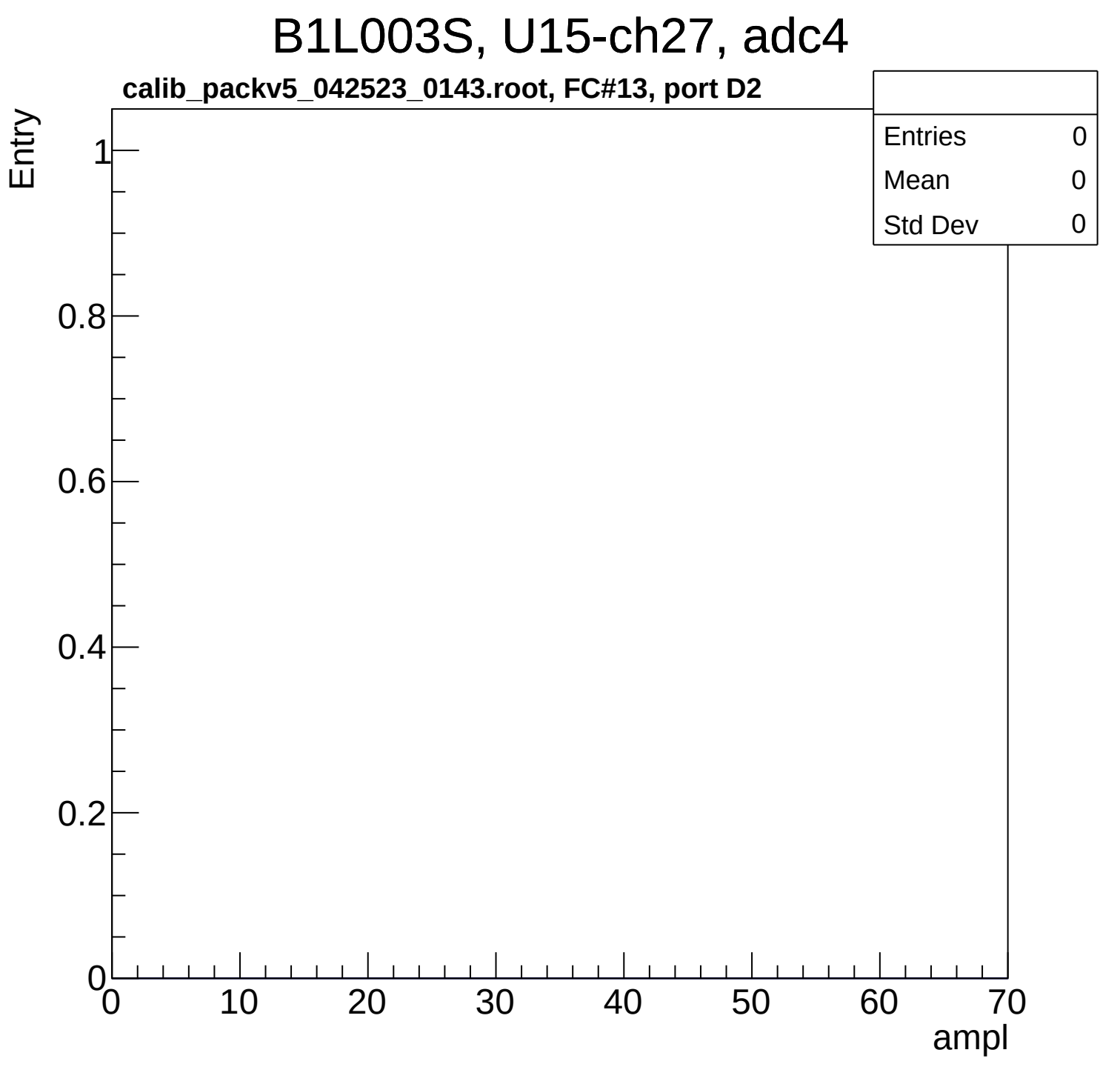
ampl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Mean 0
Std Dev 0
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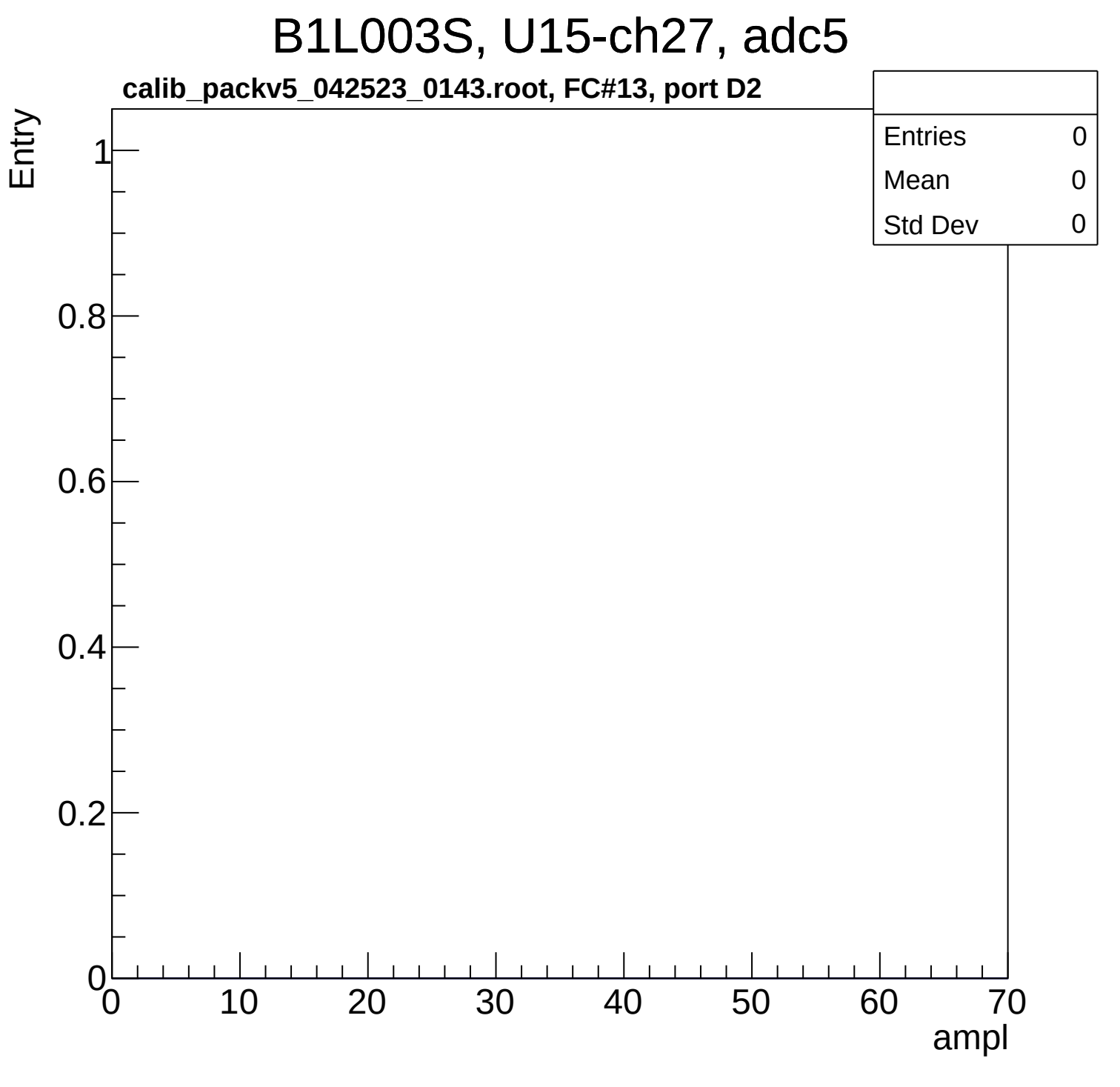
ampl


B1L003S, U15-ch27, adc5
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Std Dev 0
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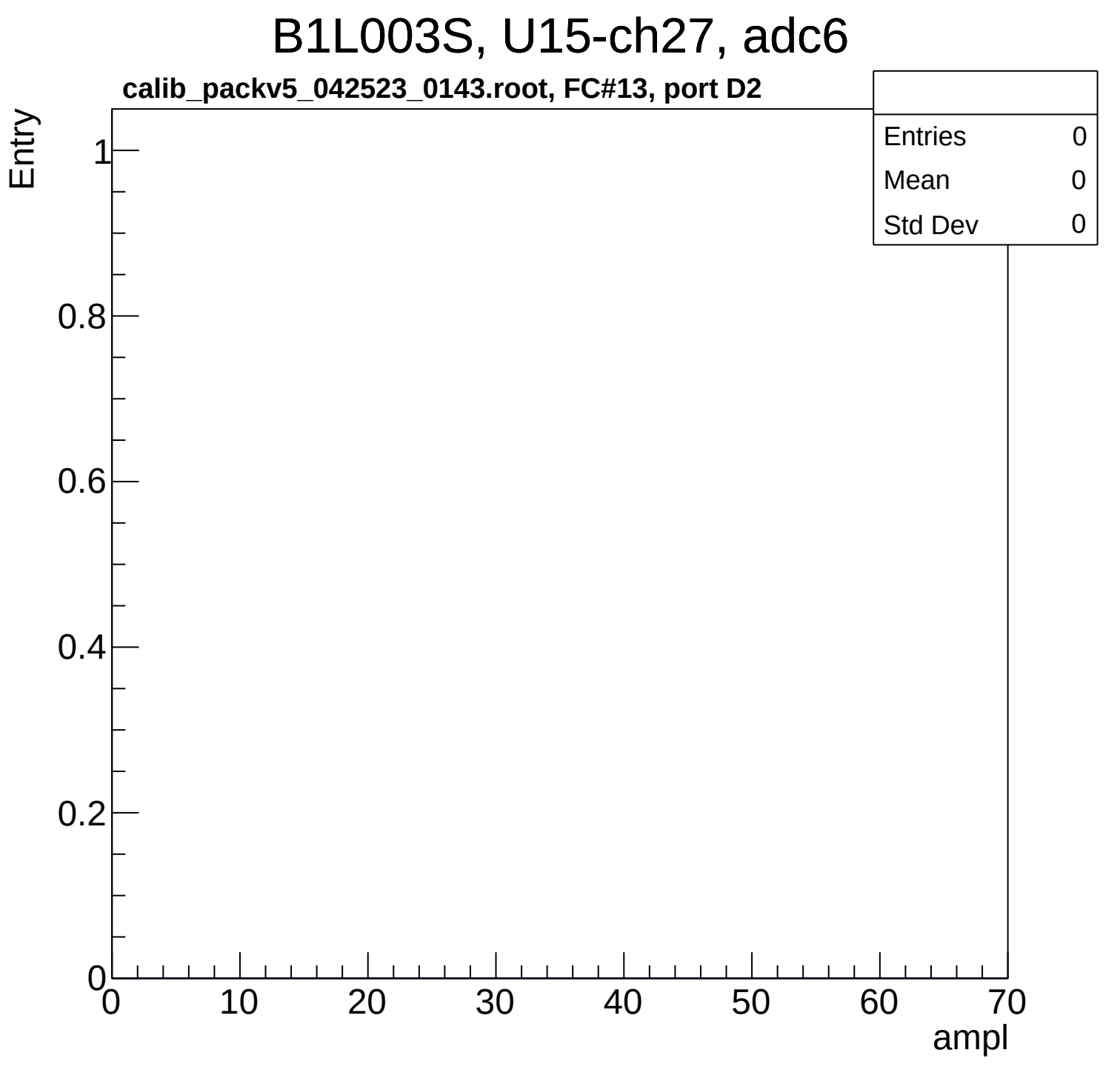
ampl


B1L003S, U15-ch27, adc6
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Mean 0
Std Dev 0
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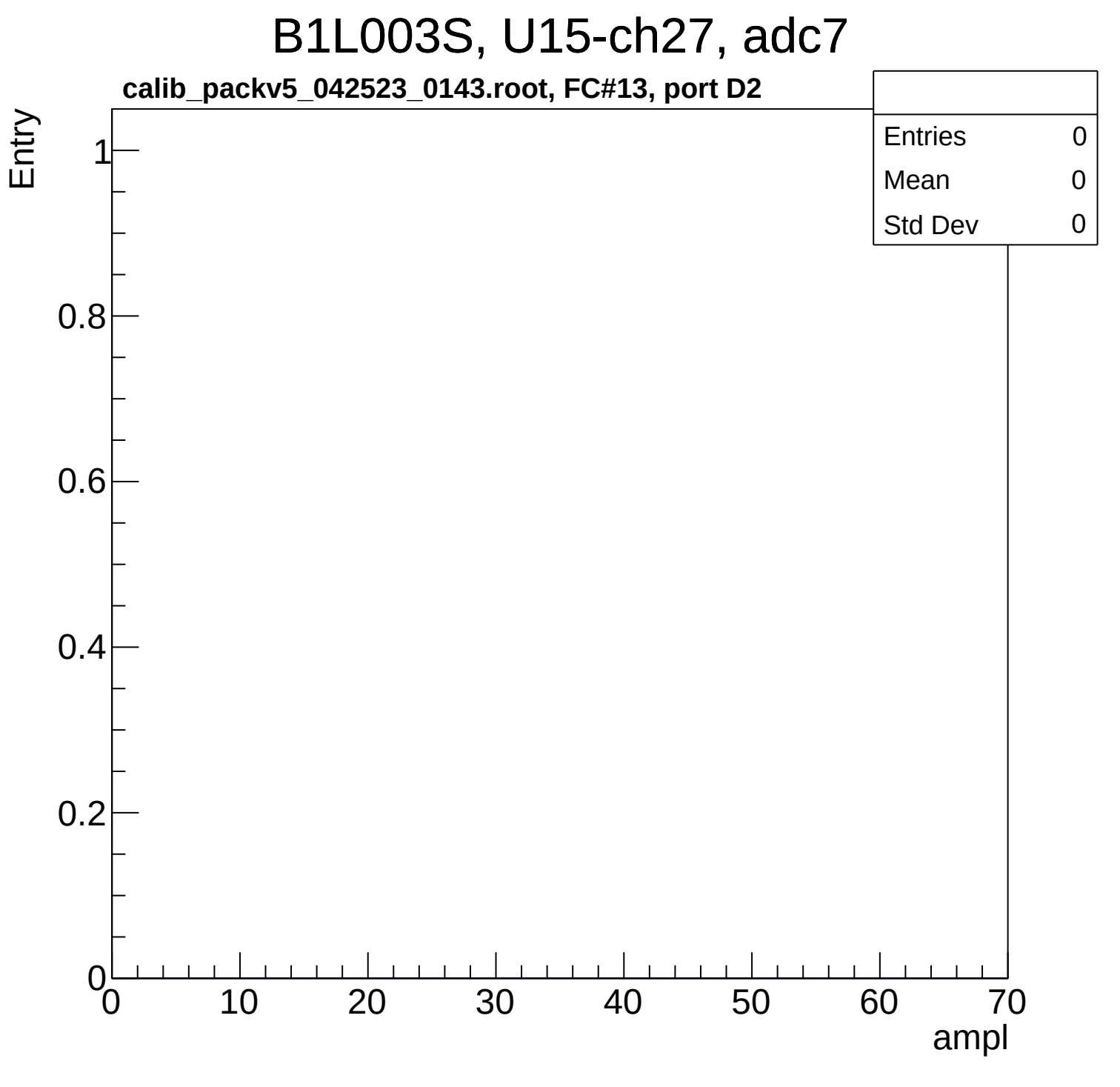
ampl


B1L003S, U15-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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0.6
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0.2
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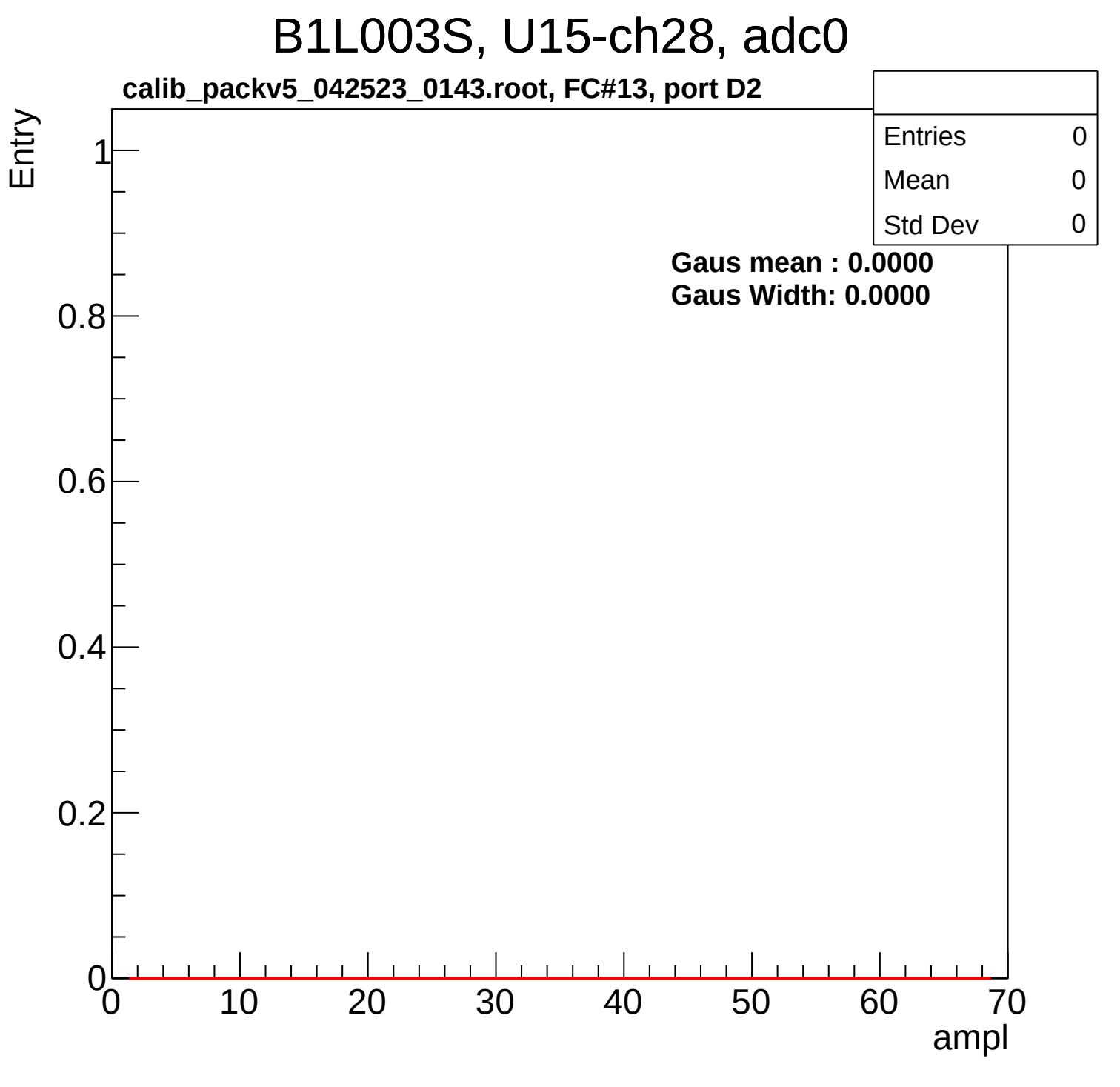
ampl


B1L003S, U15-ch28, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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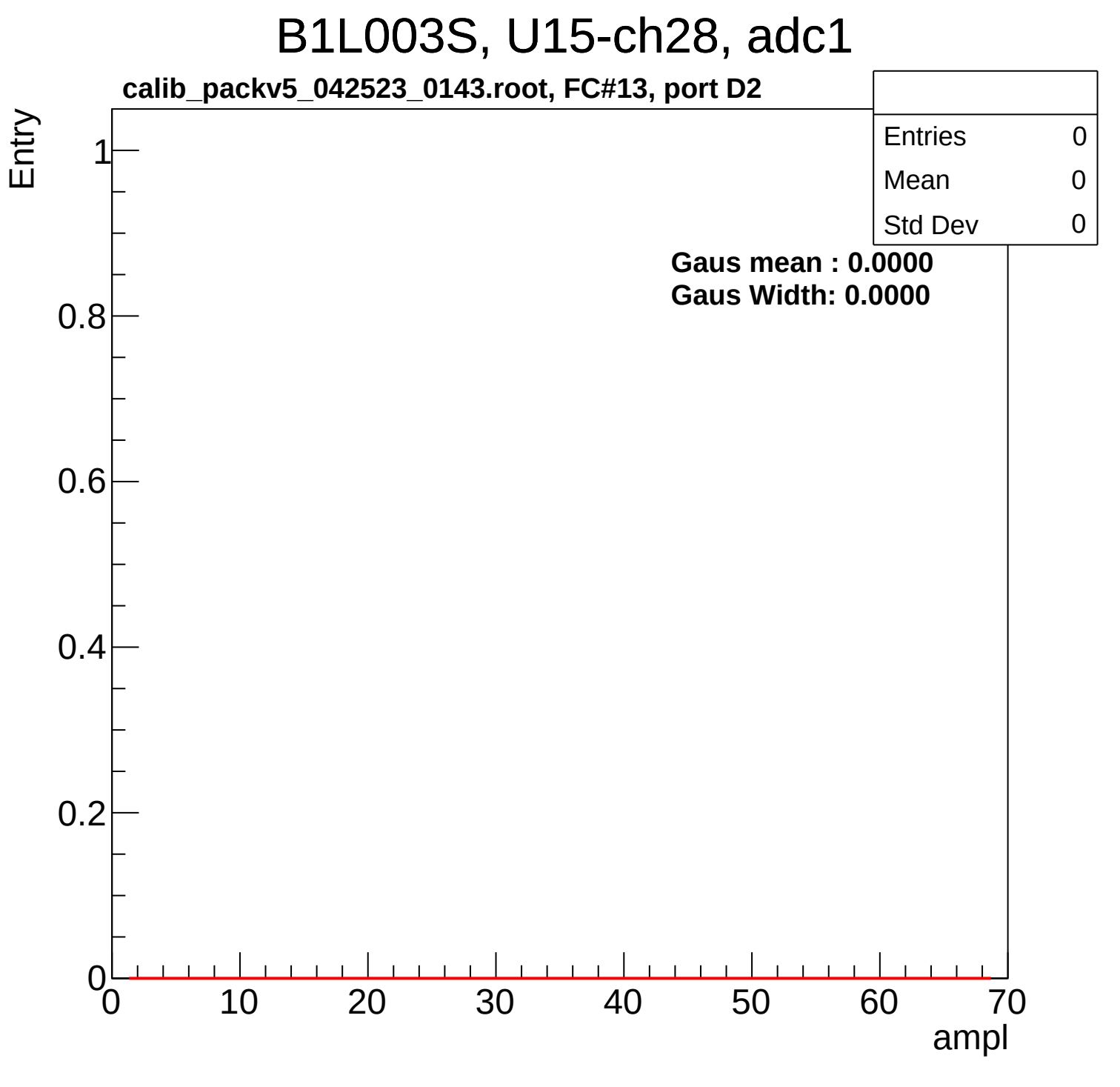
ampl


B1L003S, U15-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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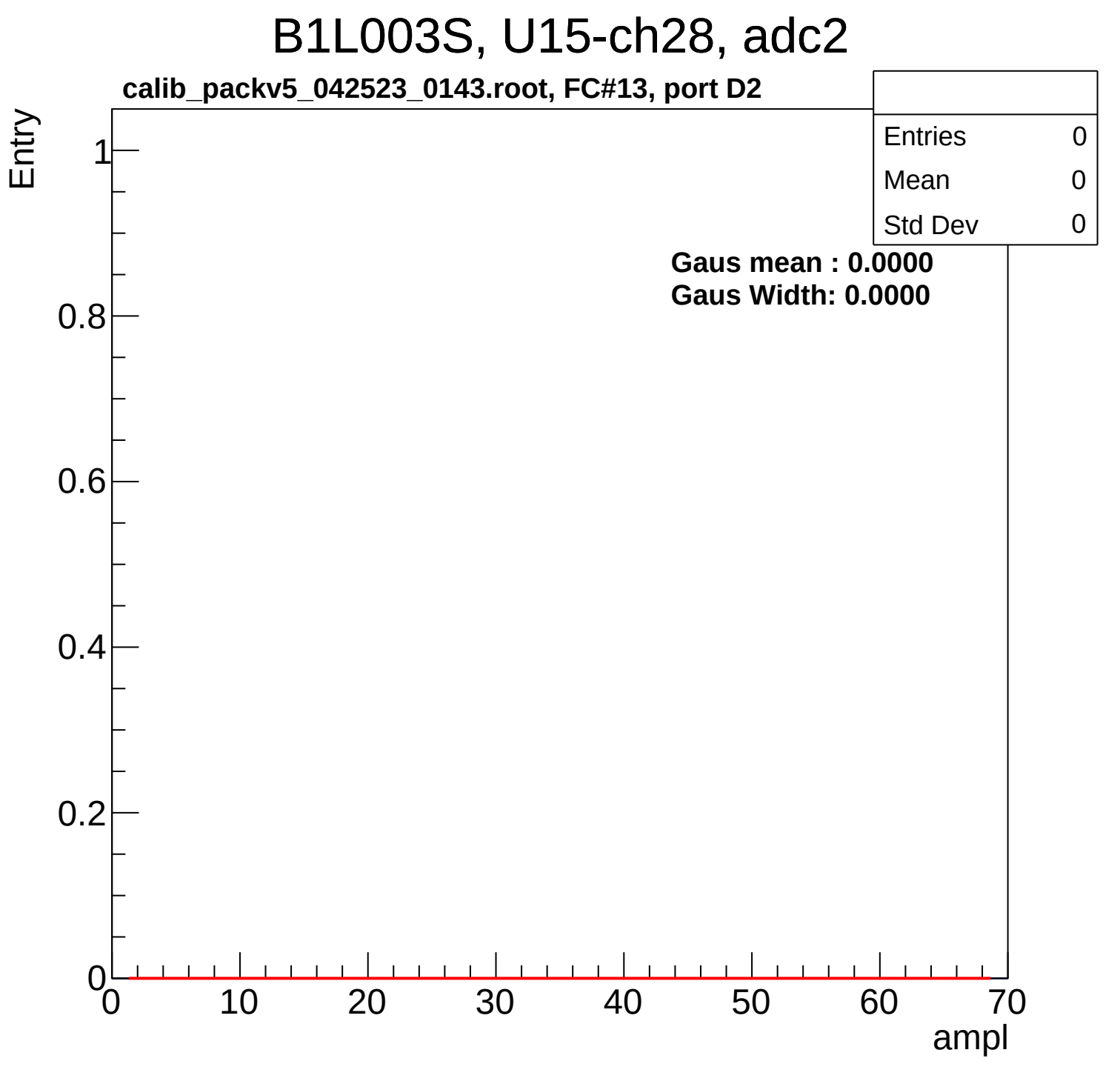
ampl


B1L003S, U15-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.2
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10
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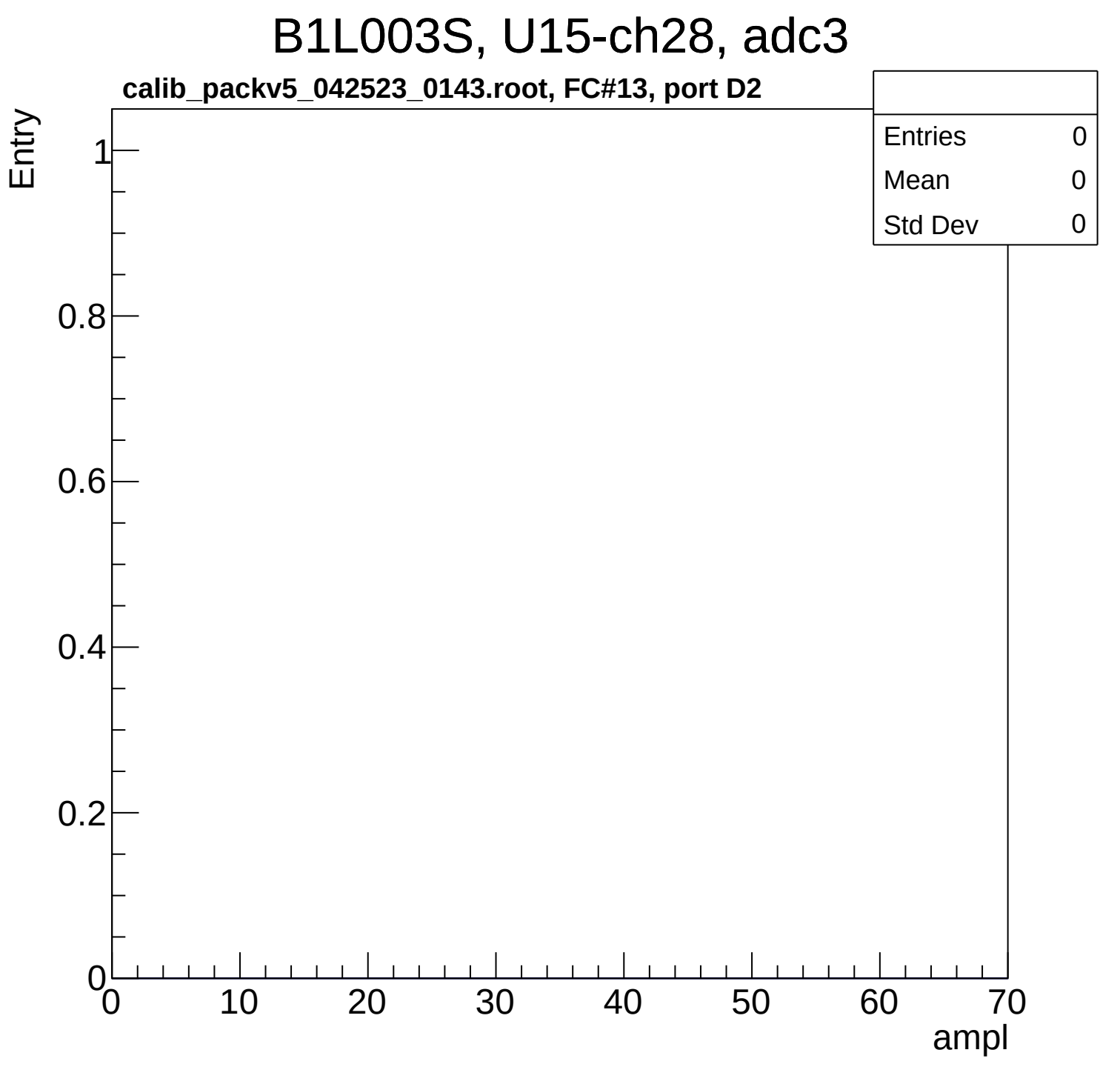
ampl


B1L003S, U15-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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0.6
0.4
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10
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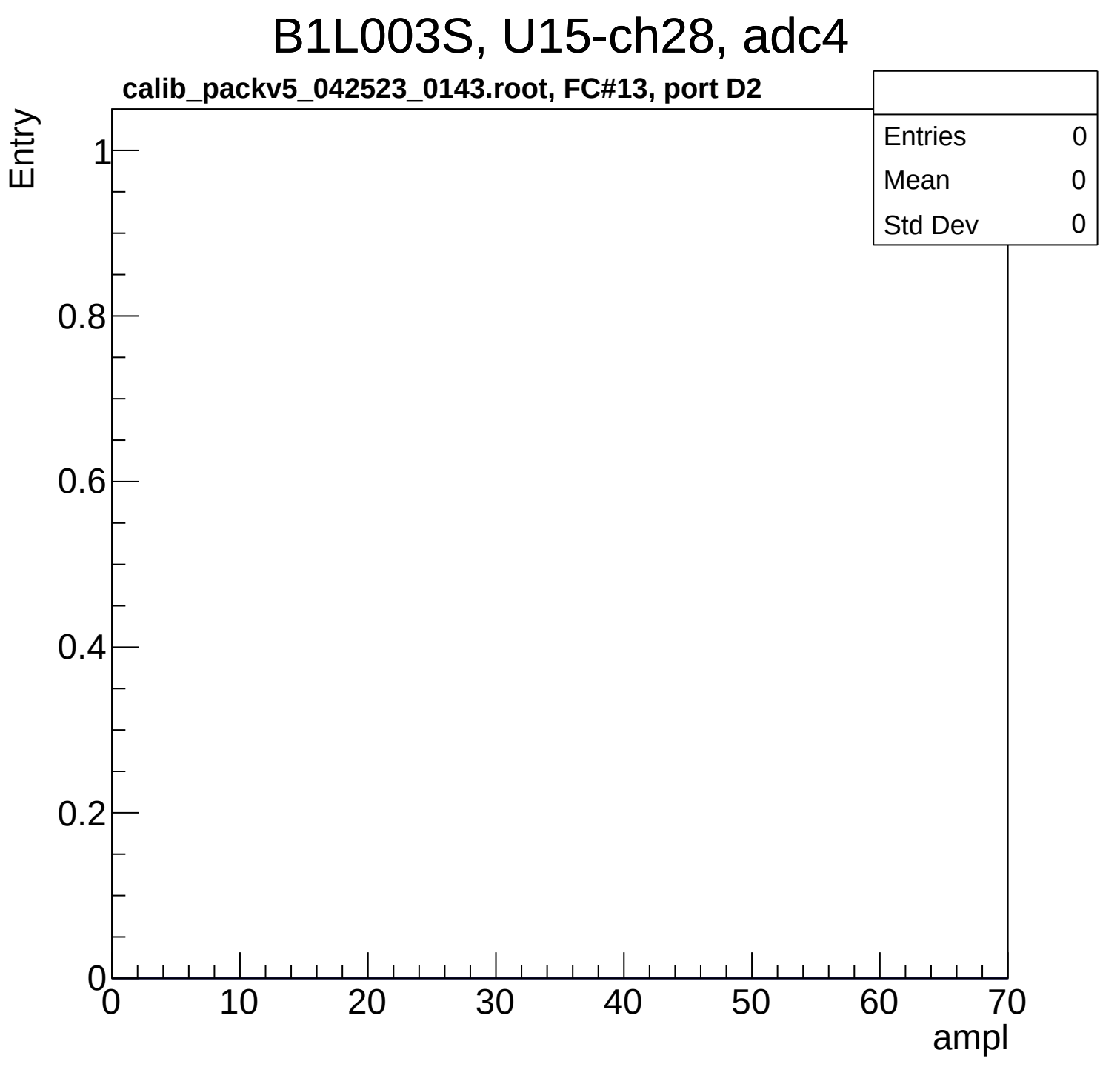
60
70
ampl


B1L003S, U15-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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0.6
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0.2
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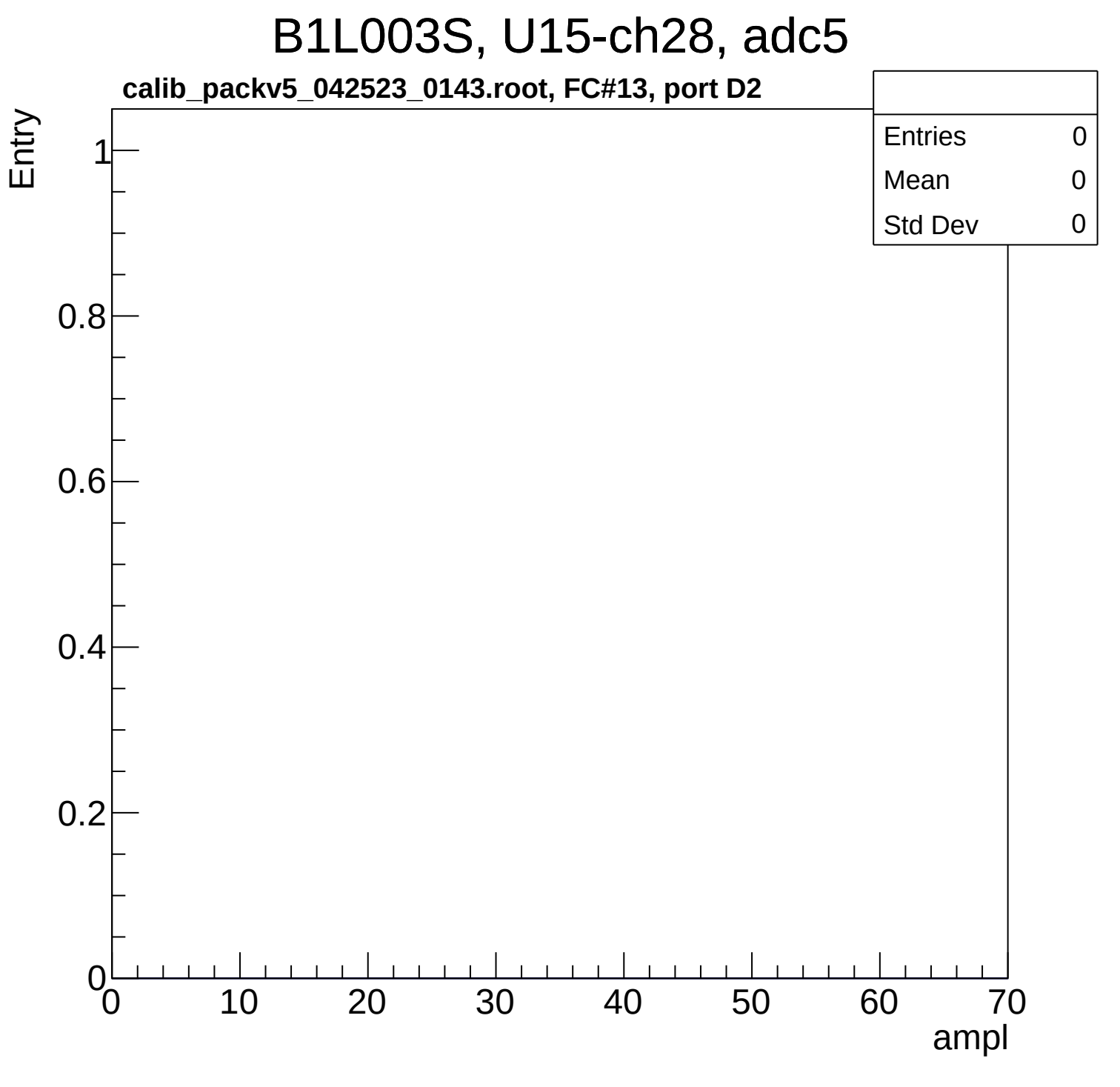
ampl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calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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0.2
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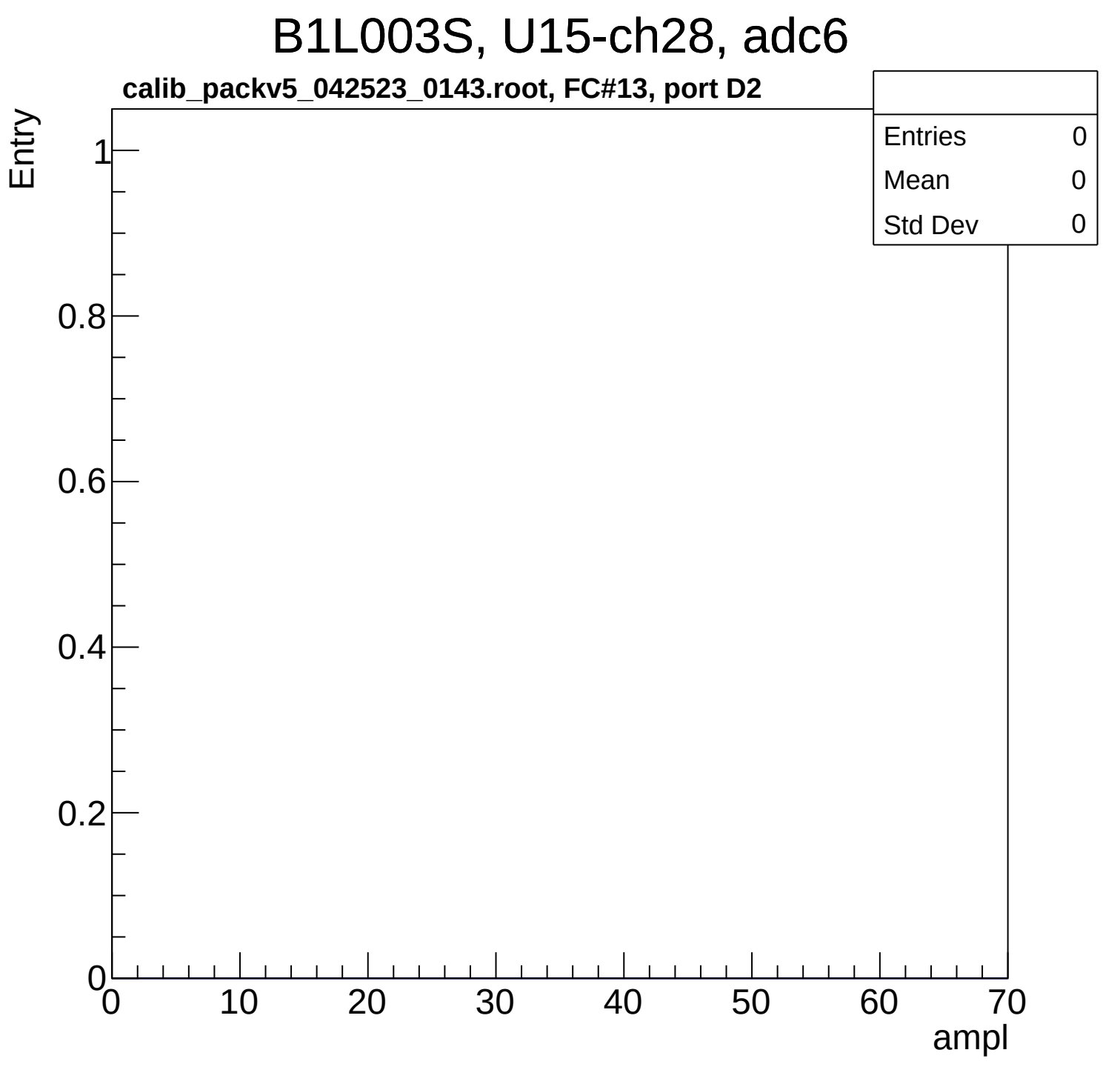
ampl


B1L003S, U15-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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0.4
0.2
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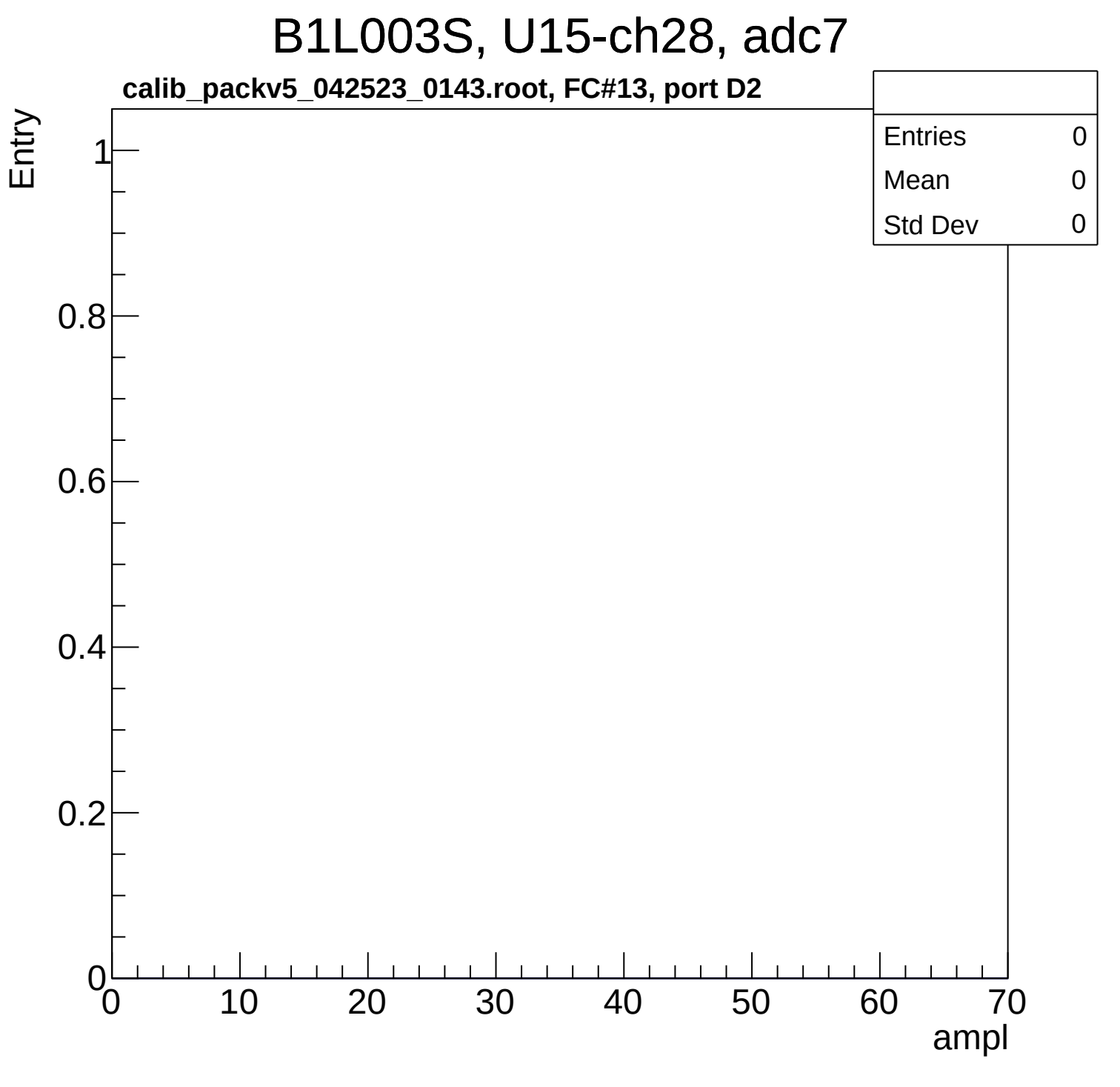
ampl


B1L003S, U15-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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0.6
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0.2
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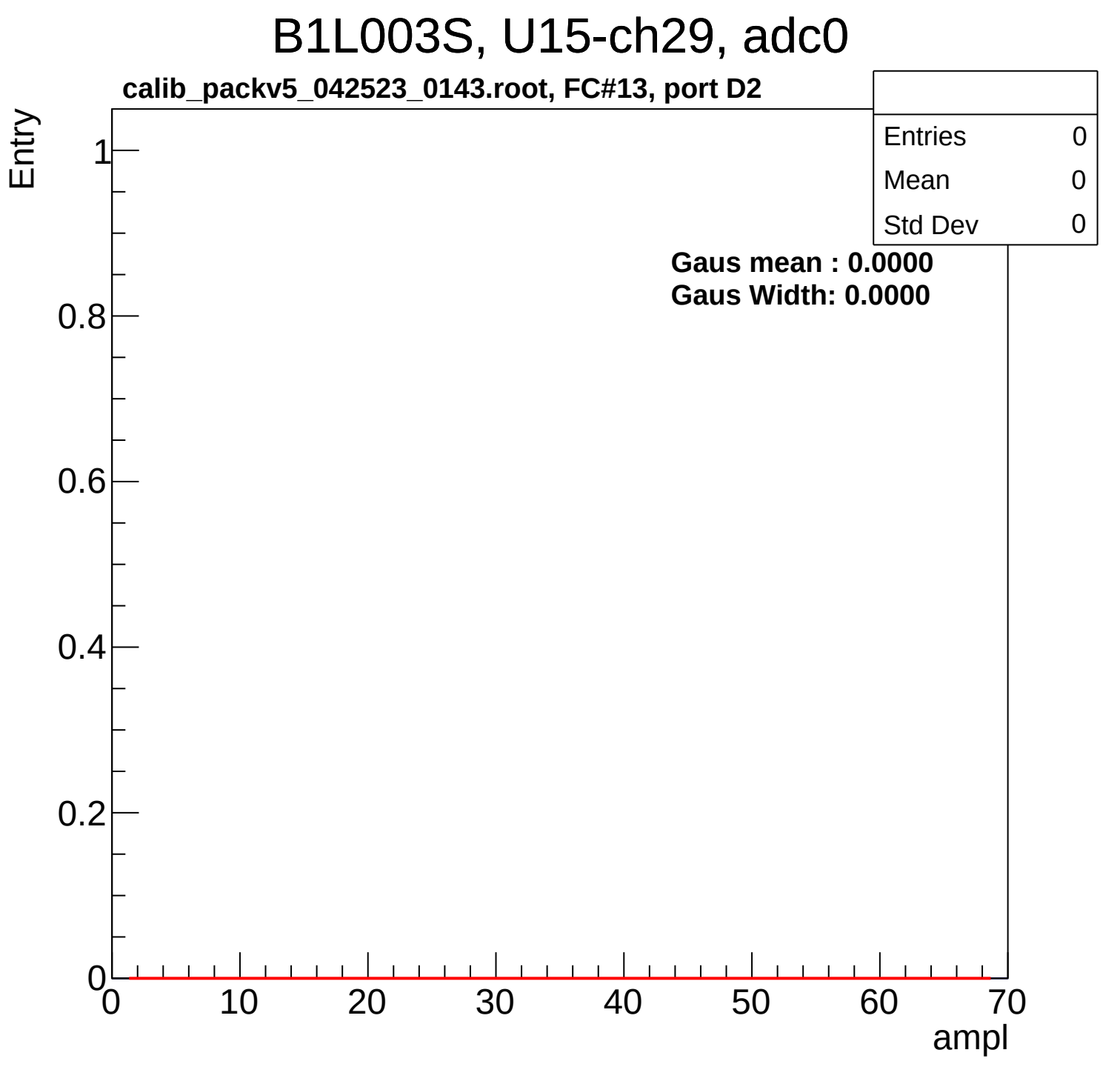
ampl


B1L003S, U15-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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0.6
0.4
0.2
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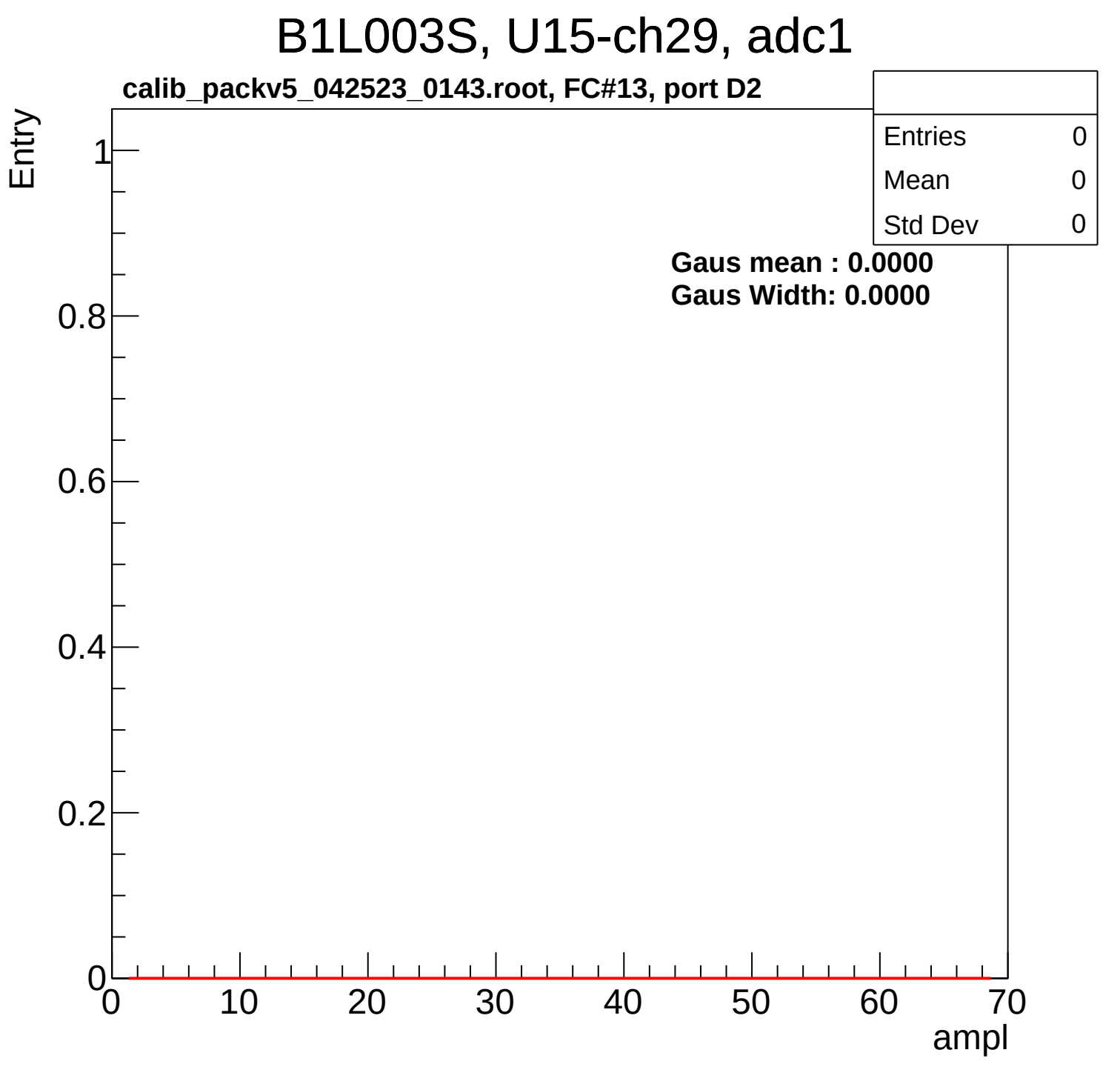
ampl


B1L003S, U15-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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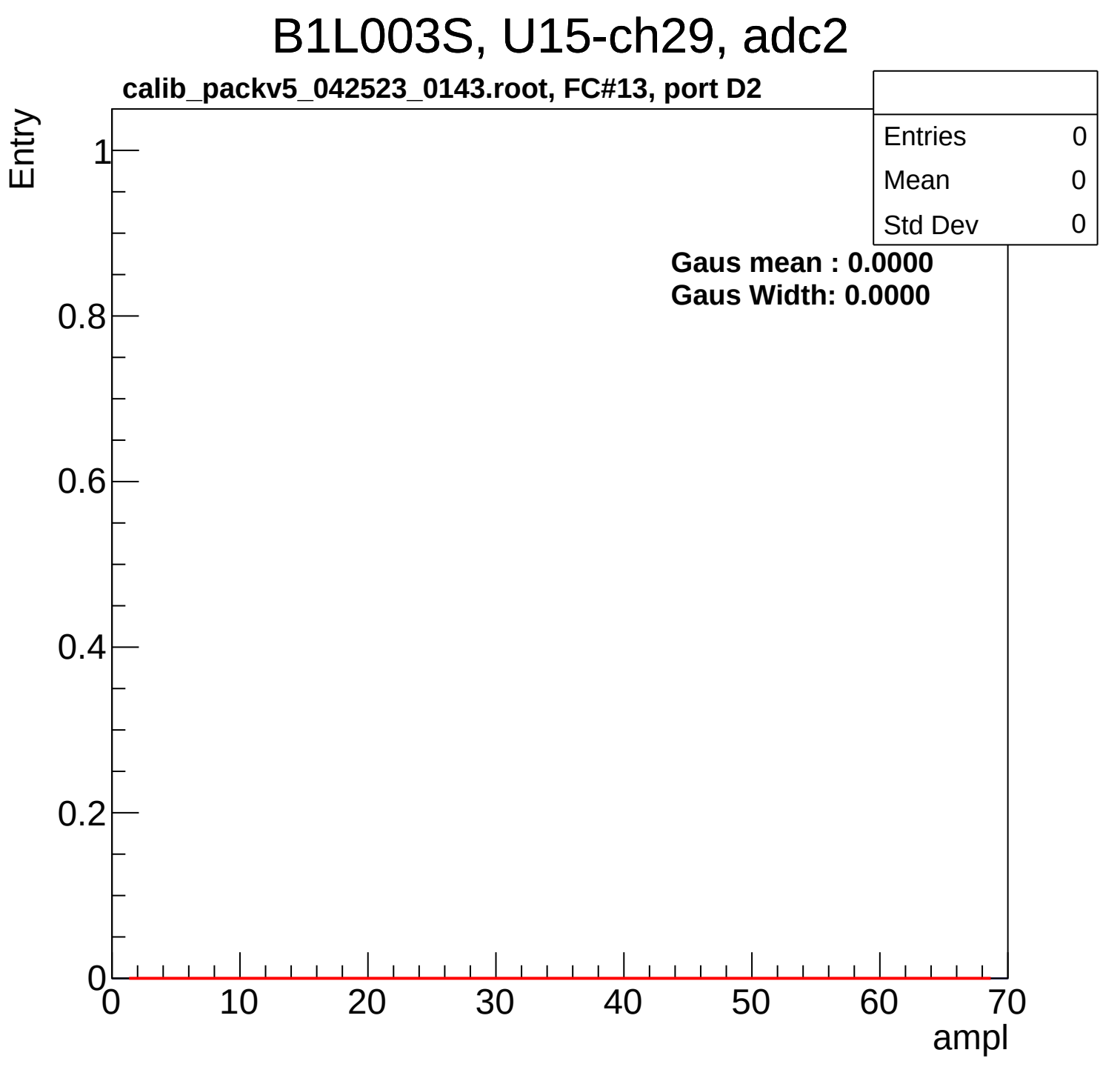
ampl


B1L003S, U15-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0
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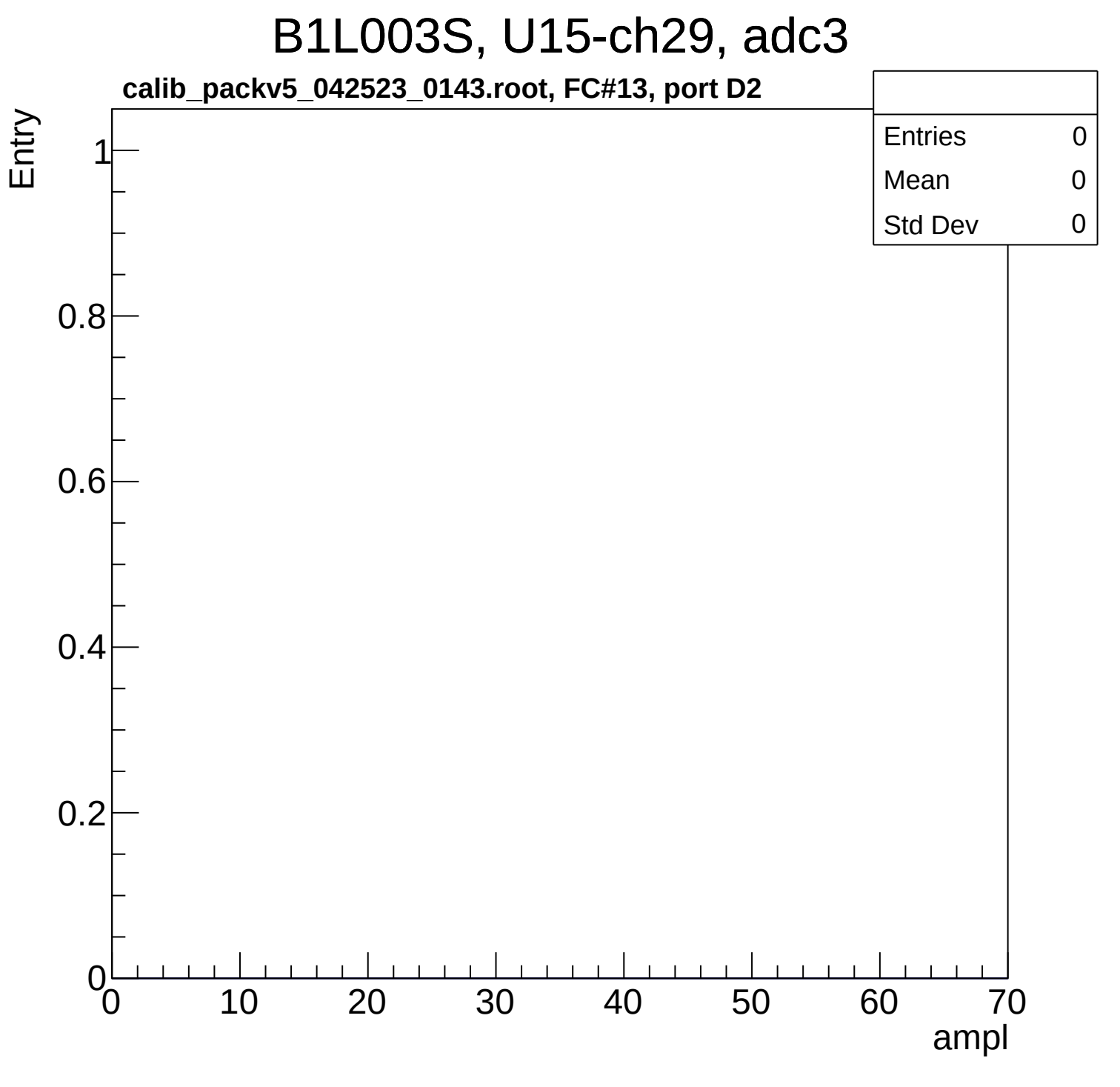
ampl


B1L003S, U15-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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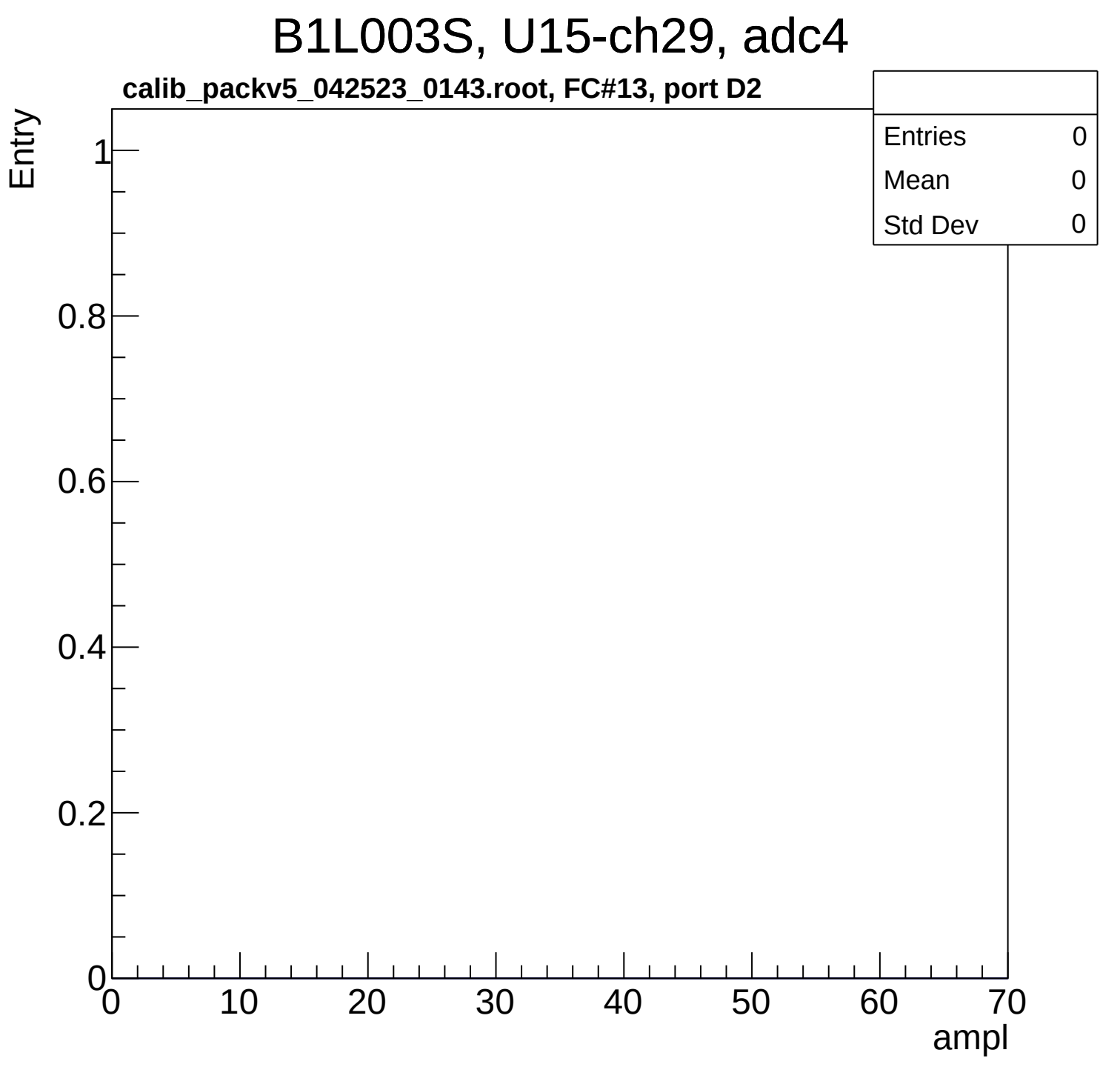
70
ampl


B1L003S, U15-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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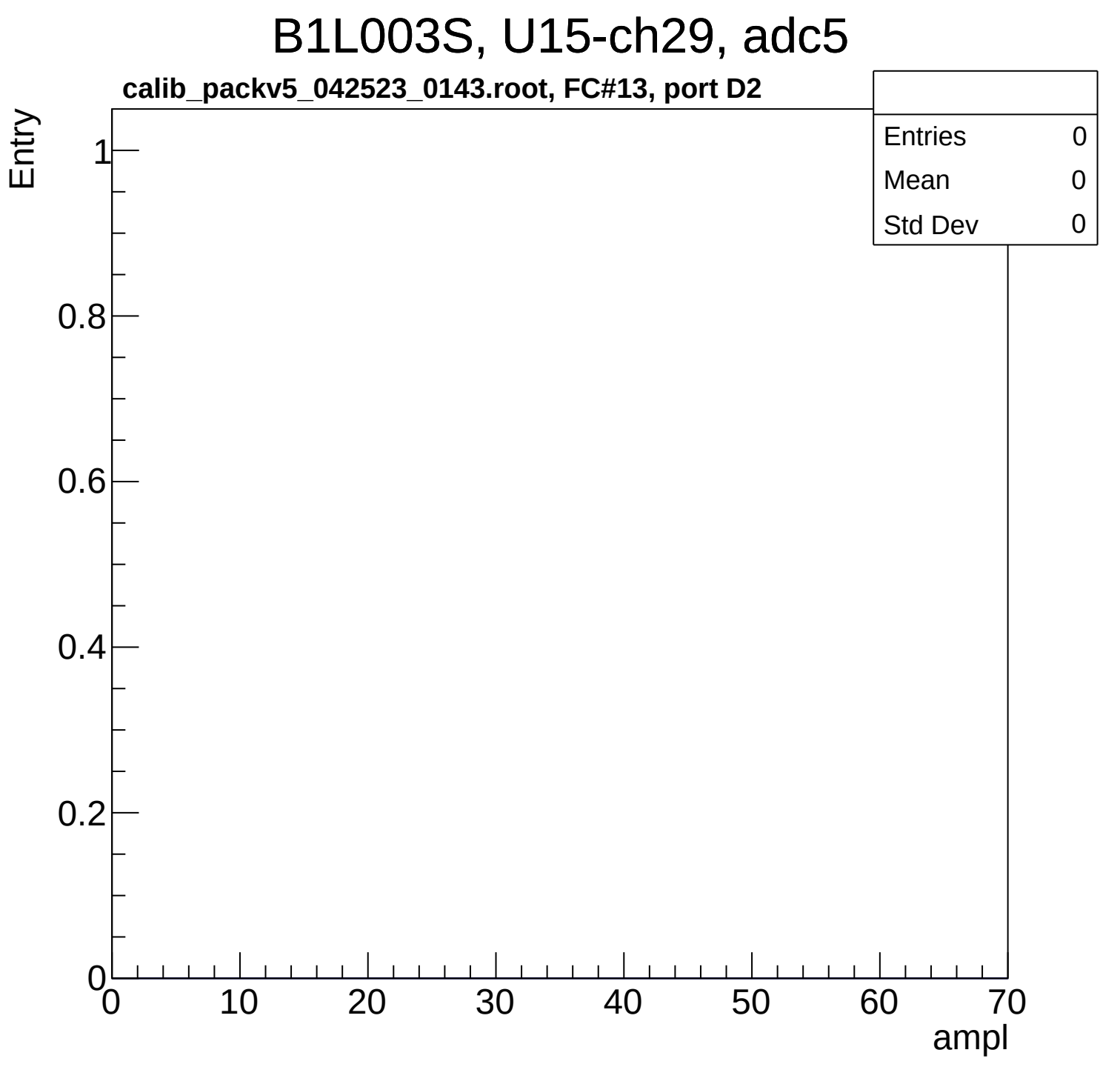
ampl


B1L003S, U15-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
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Std Dev 0
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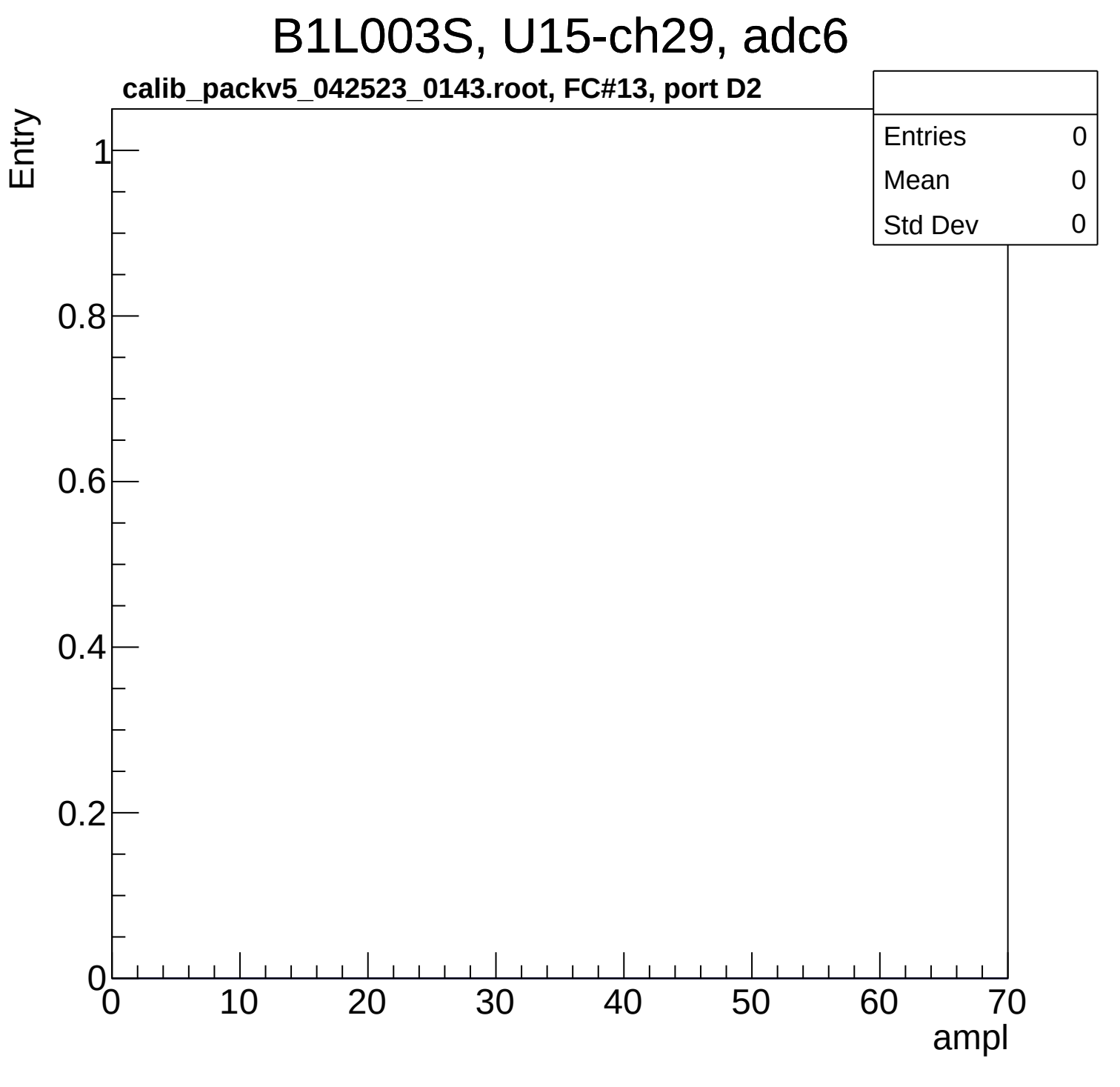
B1L003S, U15-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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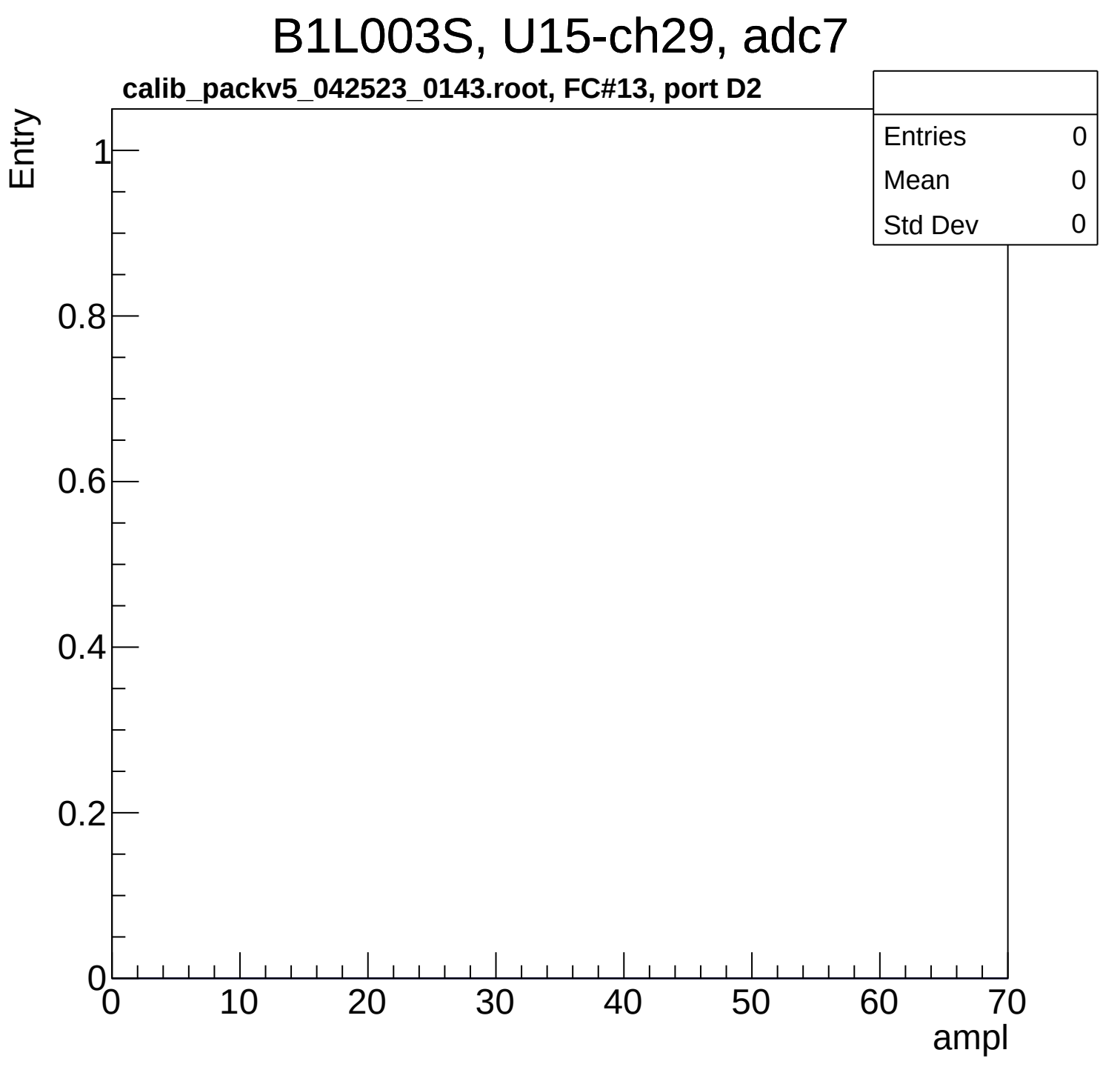
ampl


B1L003S, U15-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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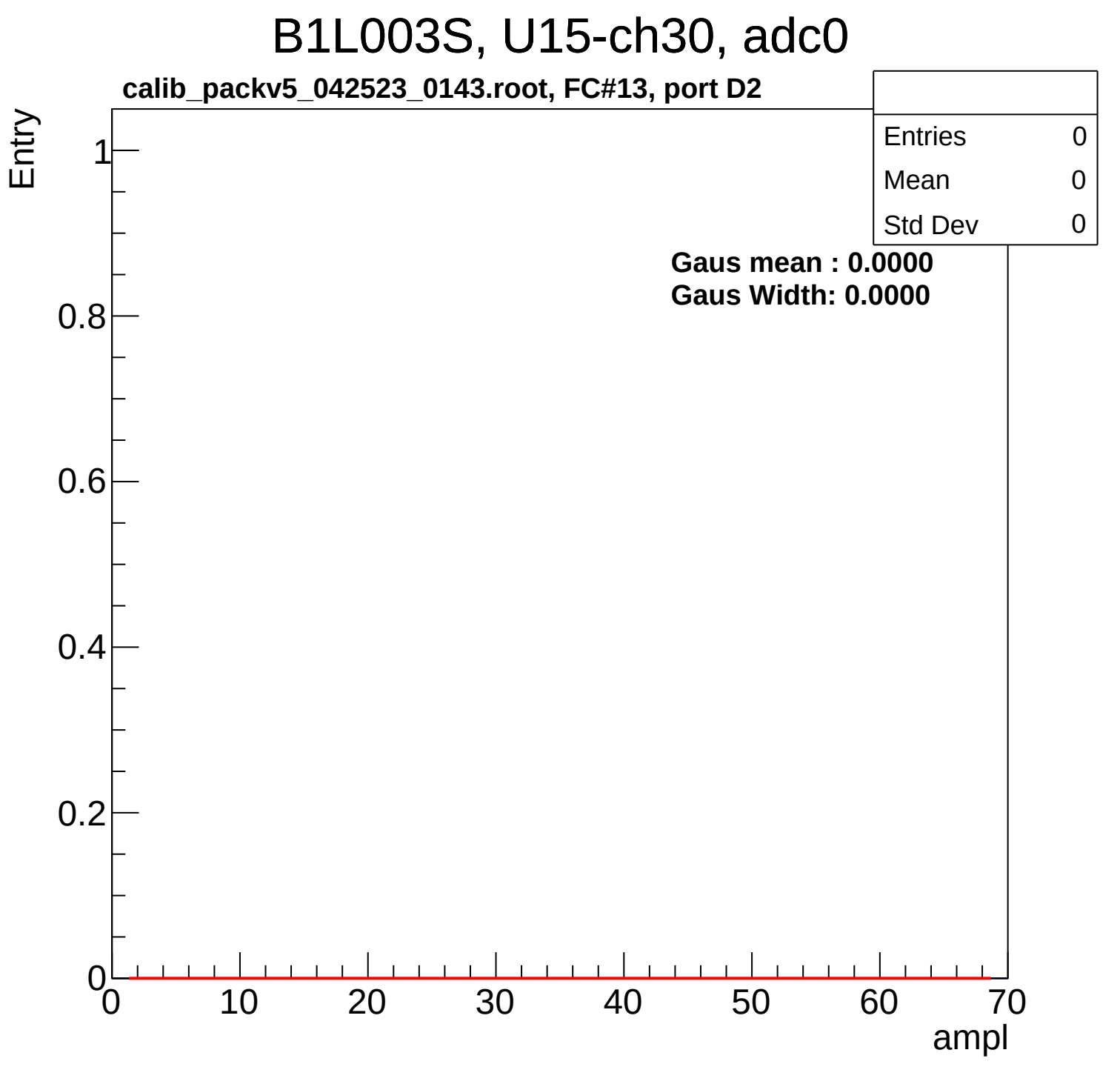
ampl


B1L003S, U15-ch30, adc0
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Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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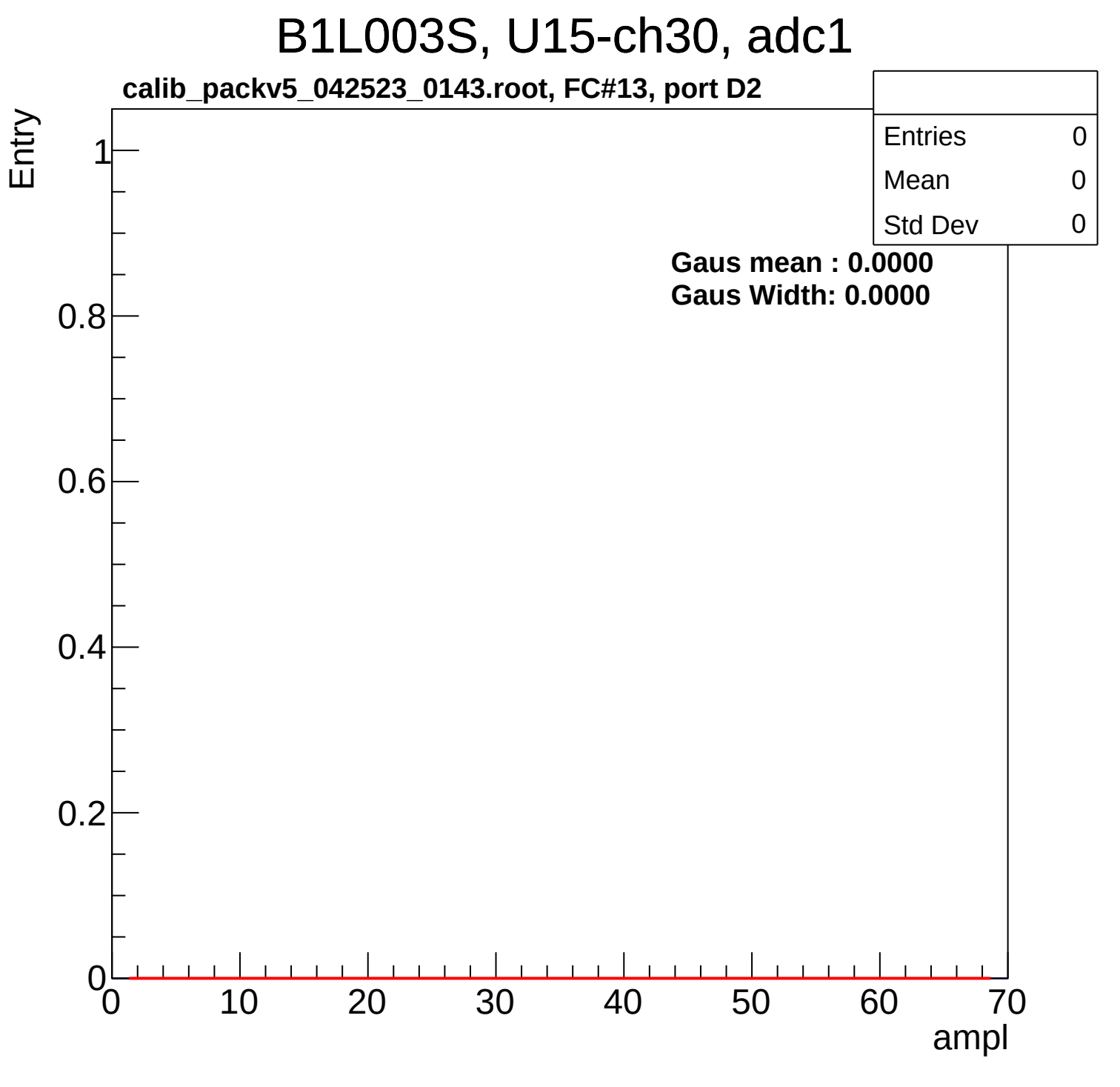
ampl


B1L003S, U15-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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0.6
0.4
0.2
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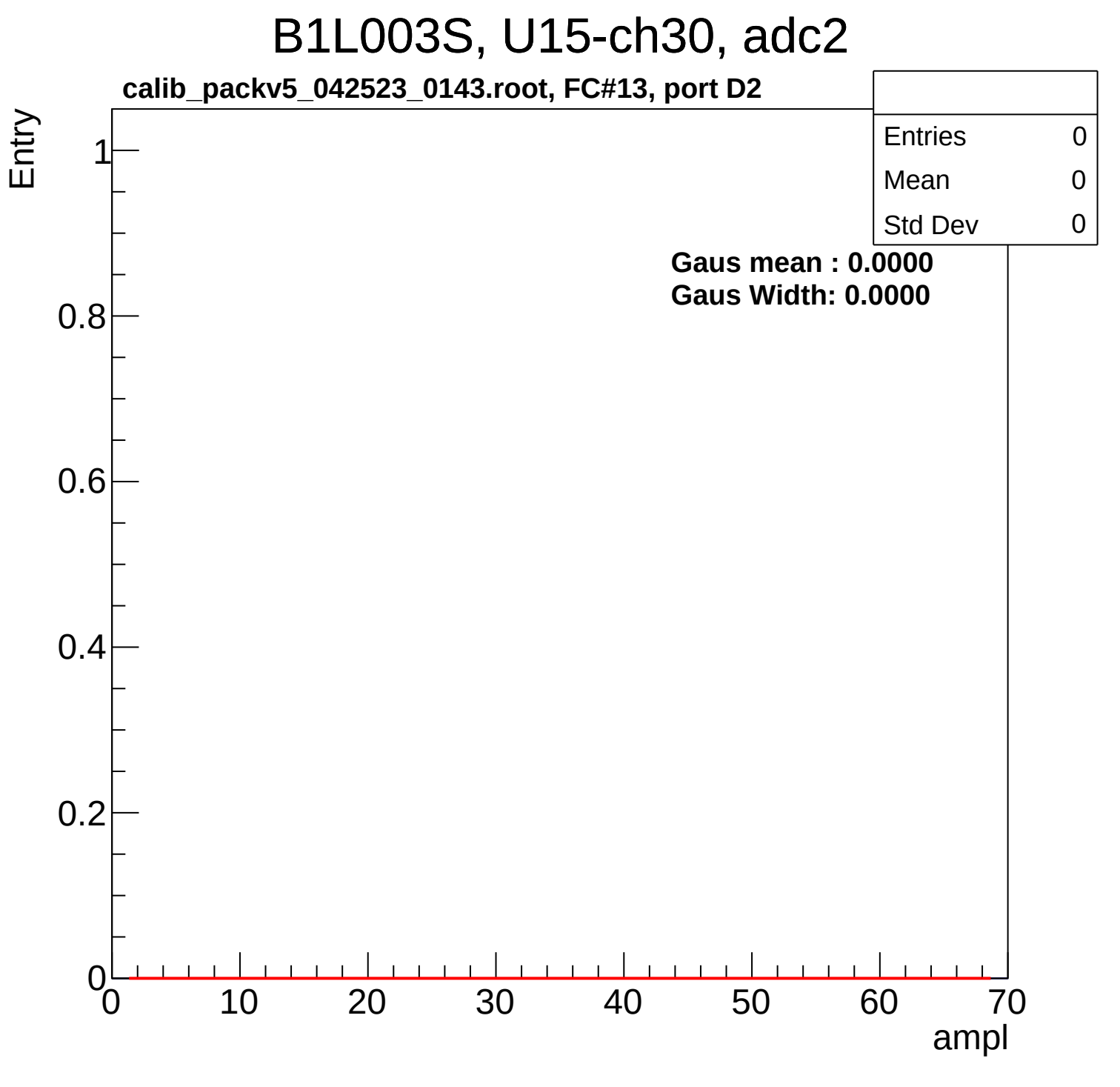
ampl


B1L003S, U15-ch30, adc2
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Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
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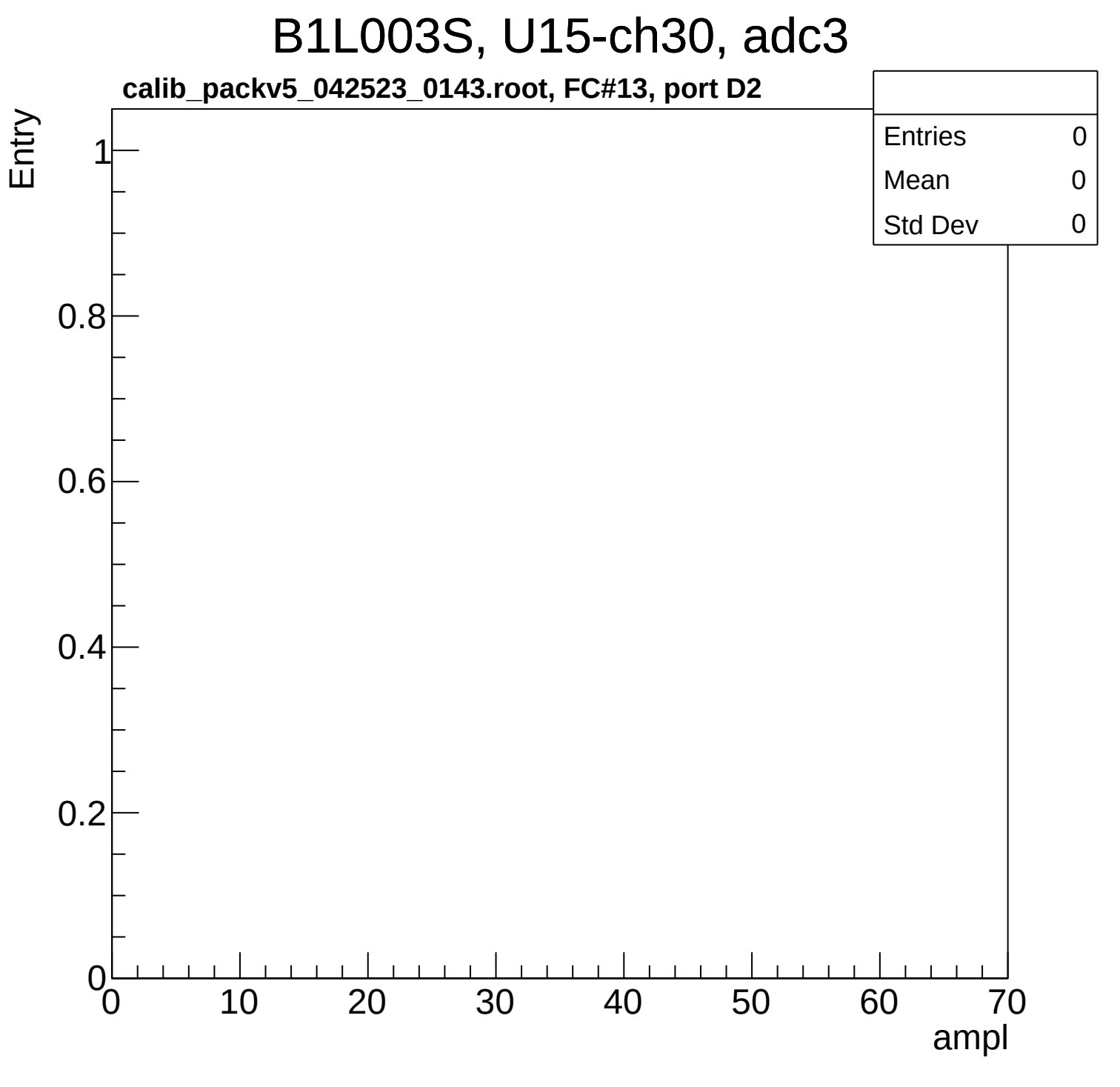
B1L003S, U15-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
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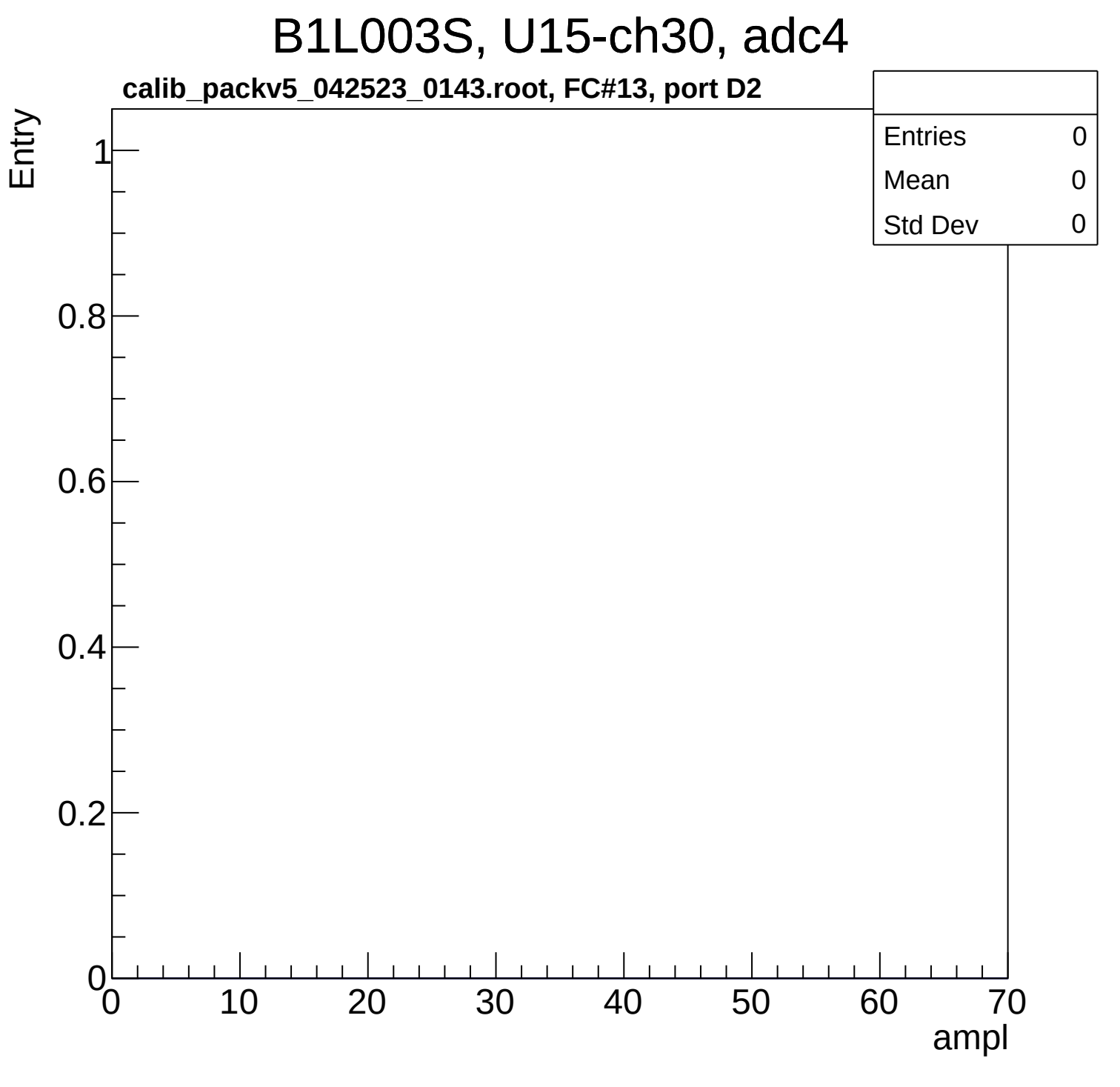
B1L003S, U15-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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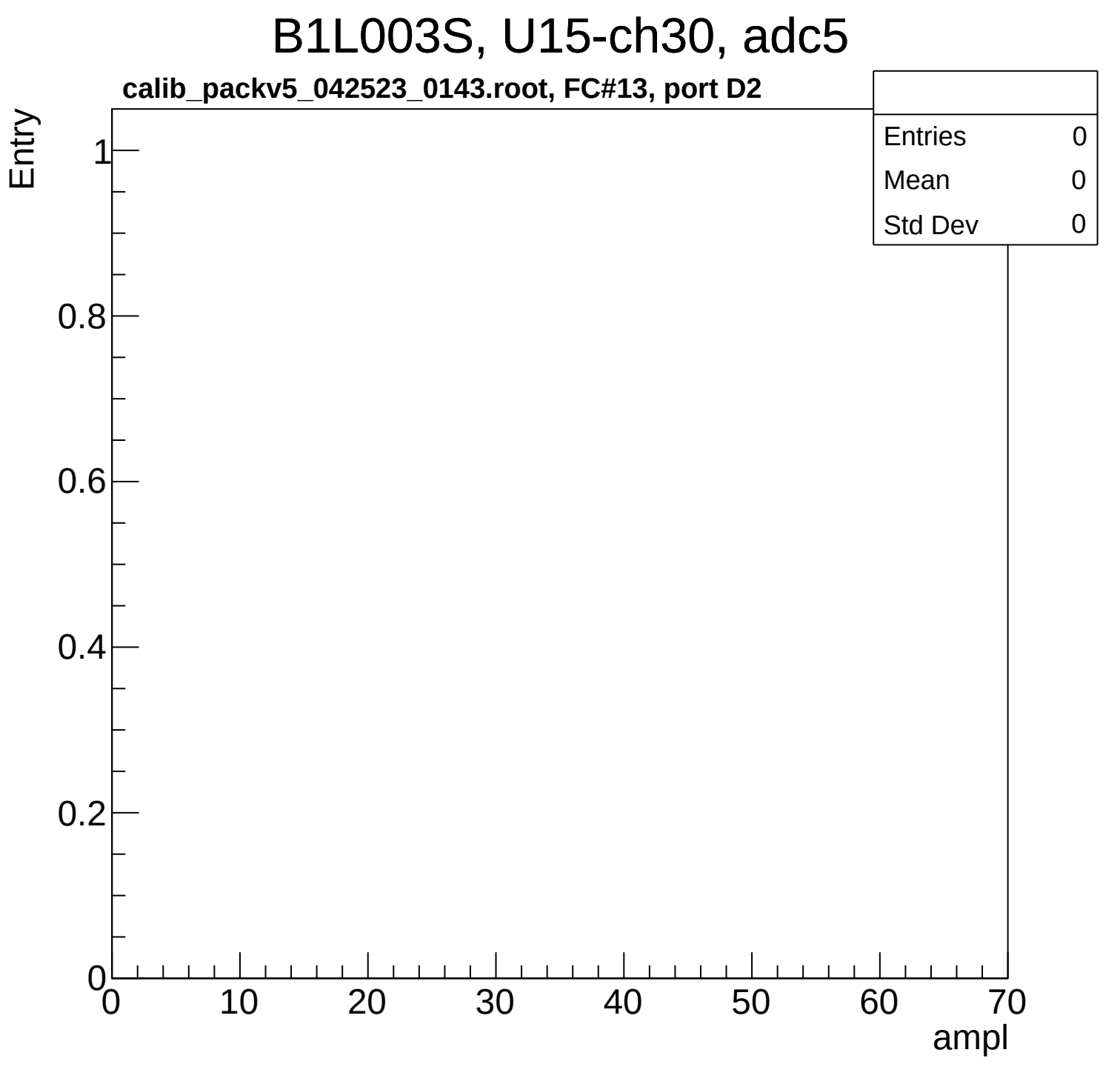
ampl


B1L003S, U15-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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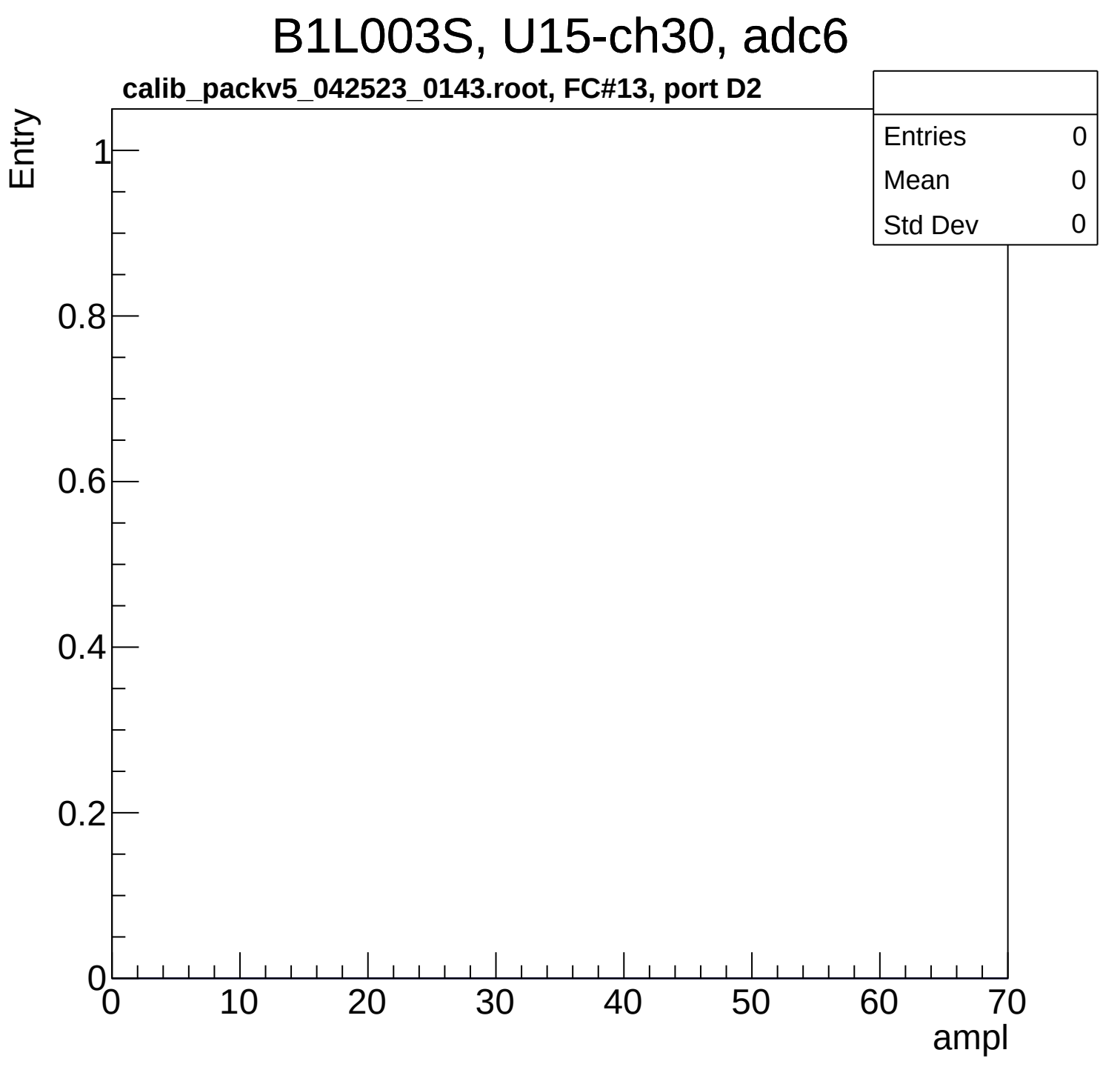
ampl


B1L003S, U15-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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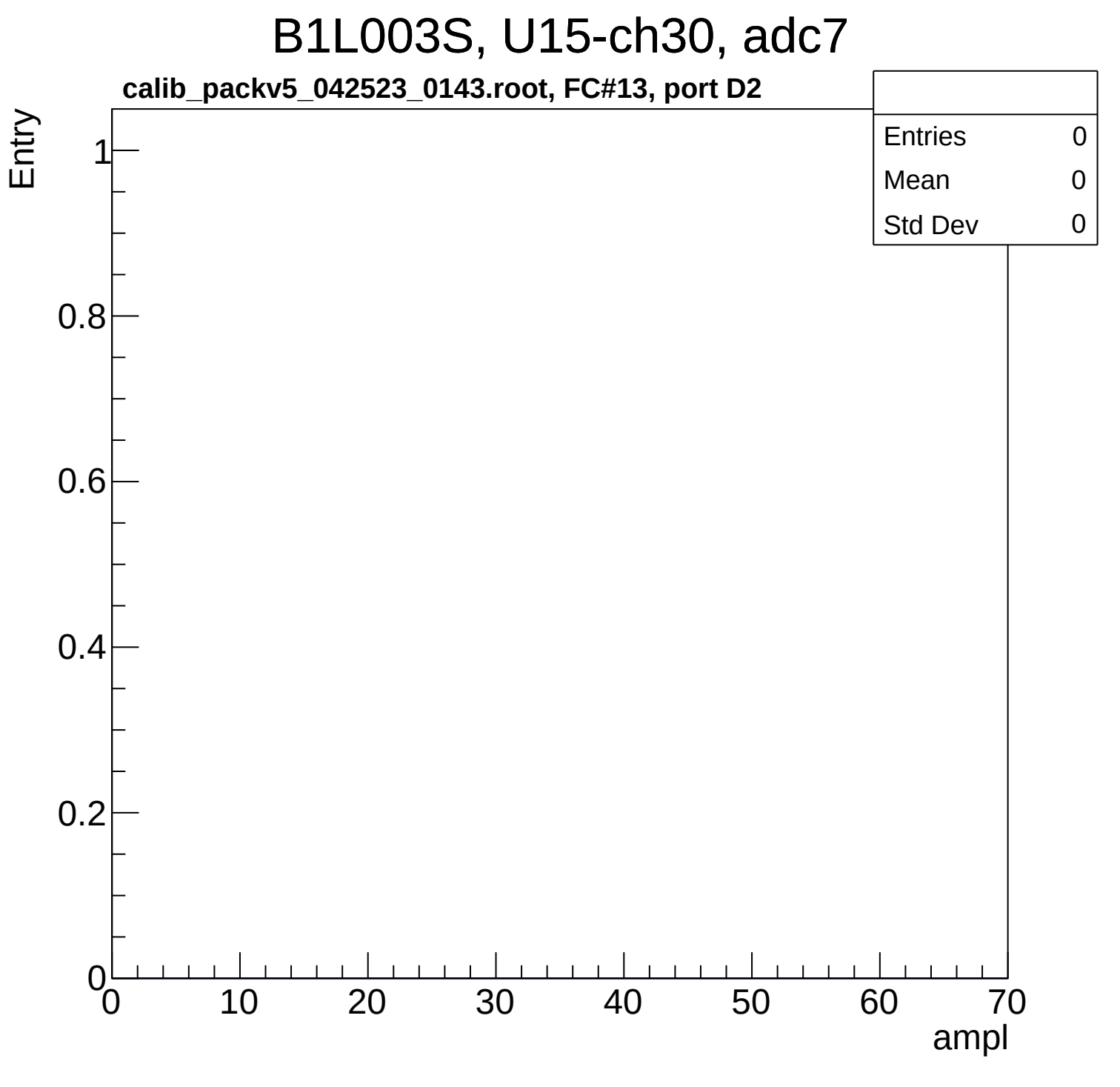
ampl


B1L003S, U15-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
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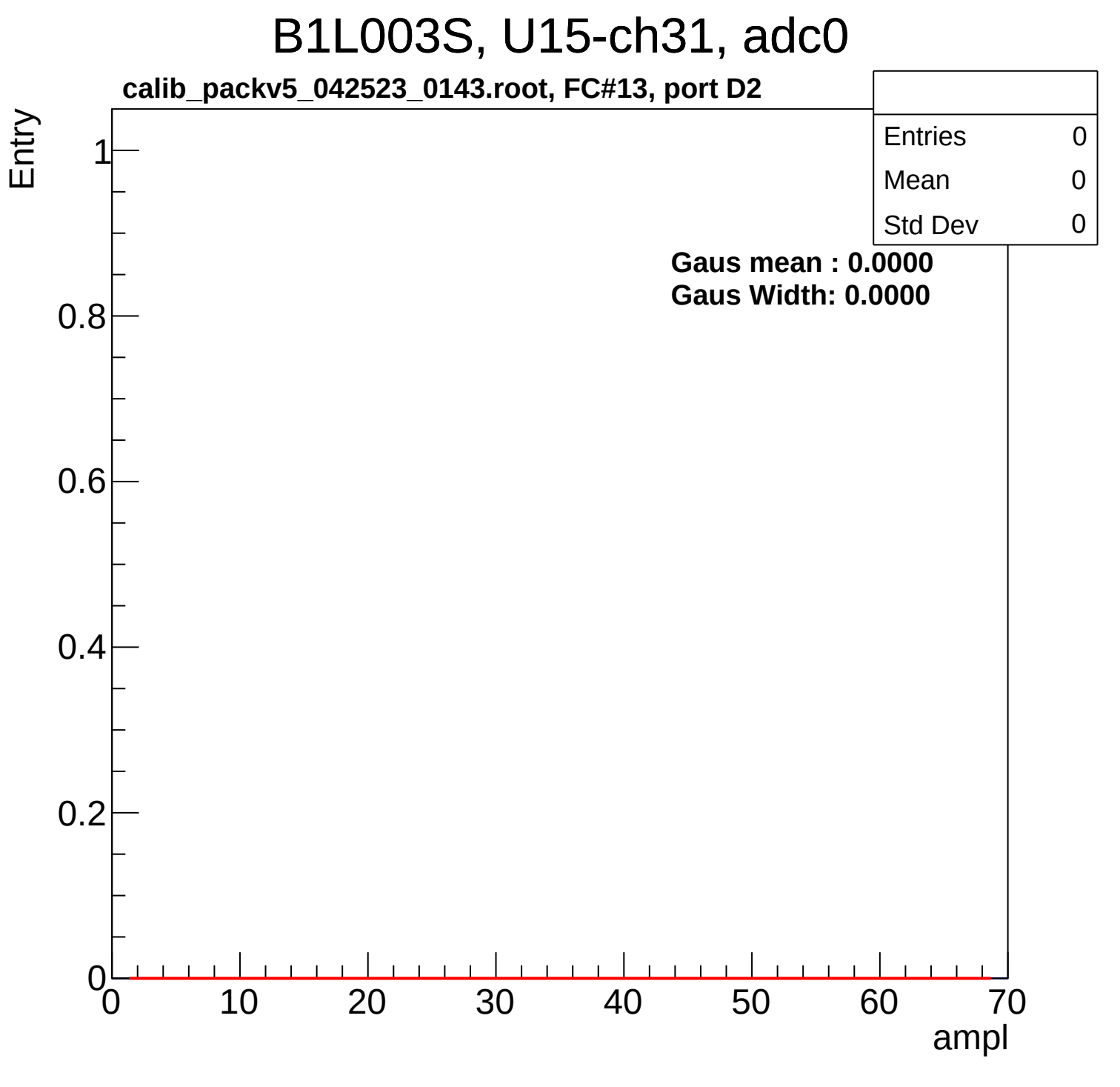
ampl


B1L003S, U15-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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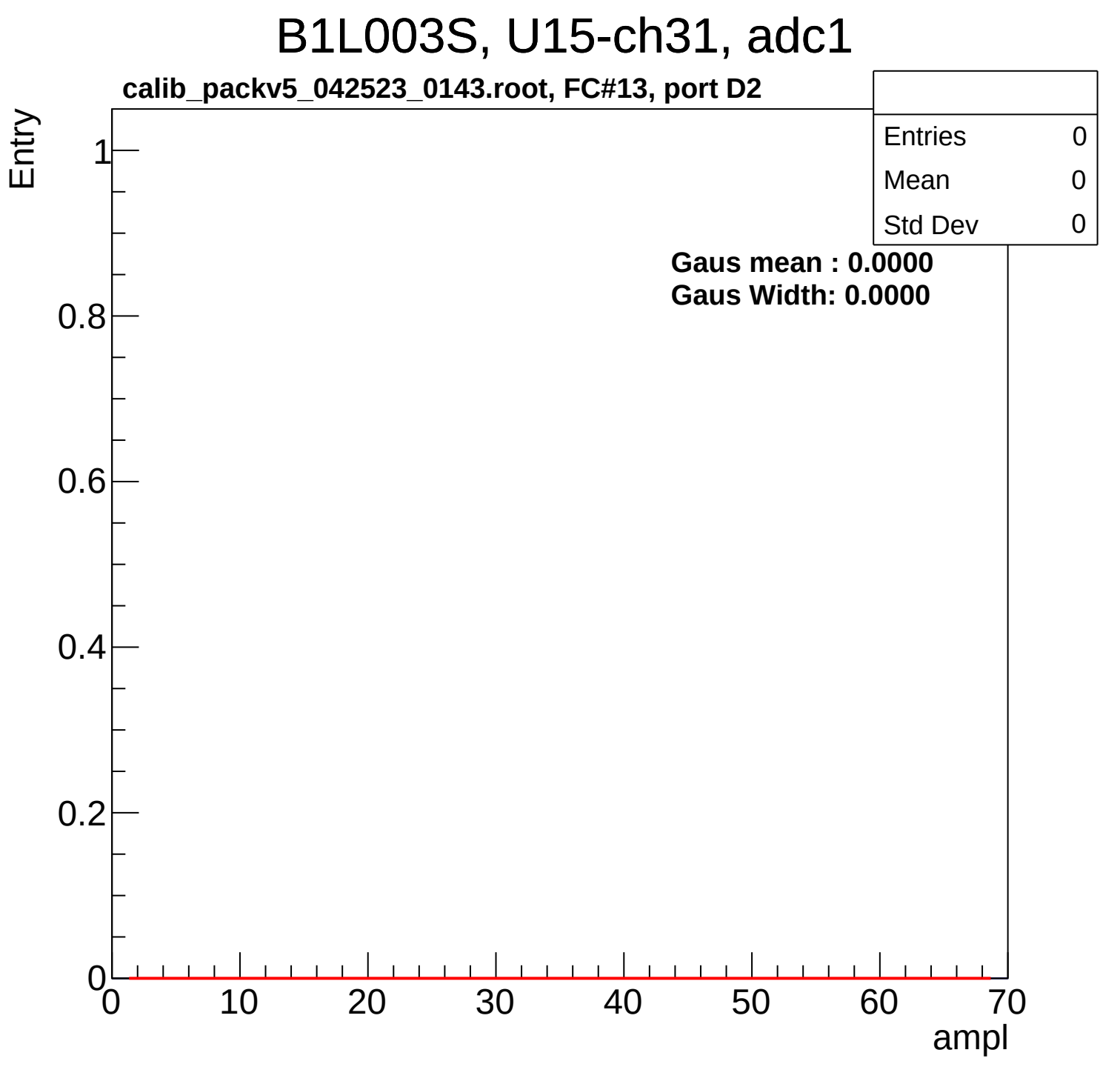
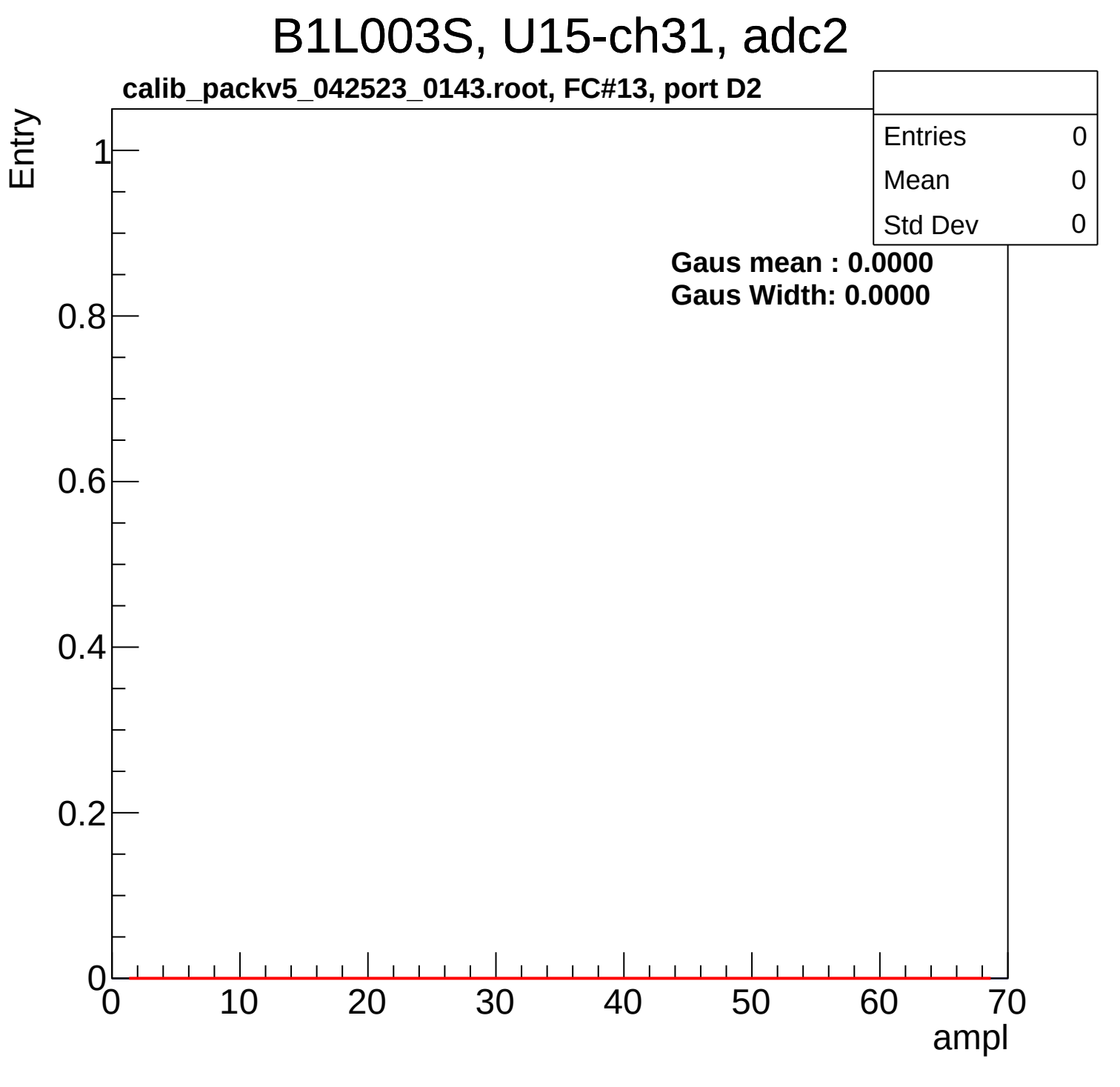
ampl


B1L003S, U15-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Gaus mean : 0.0000
Gaus Width: 0.0000
Entry
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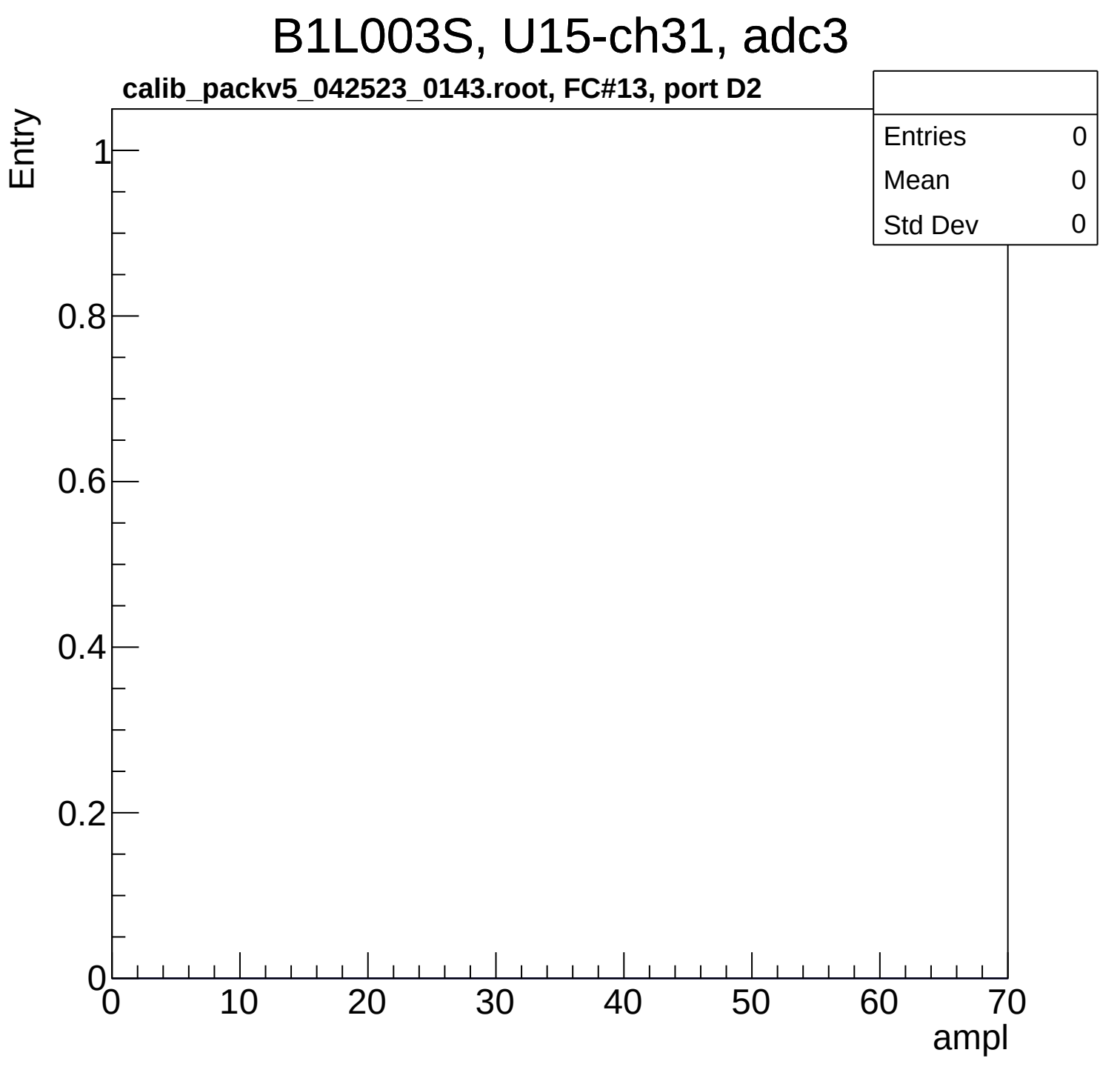
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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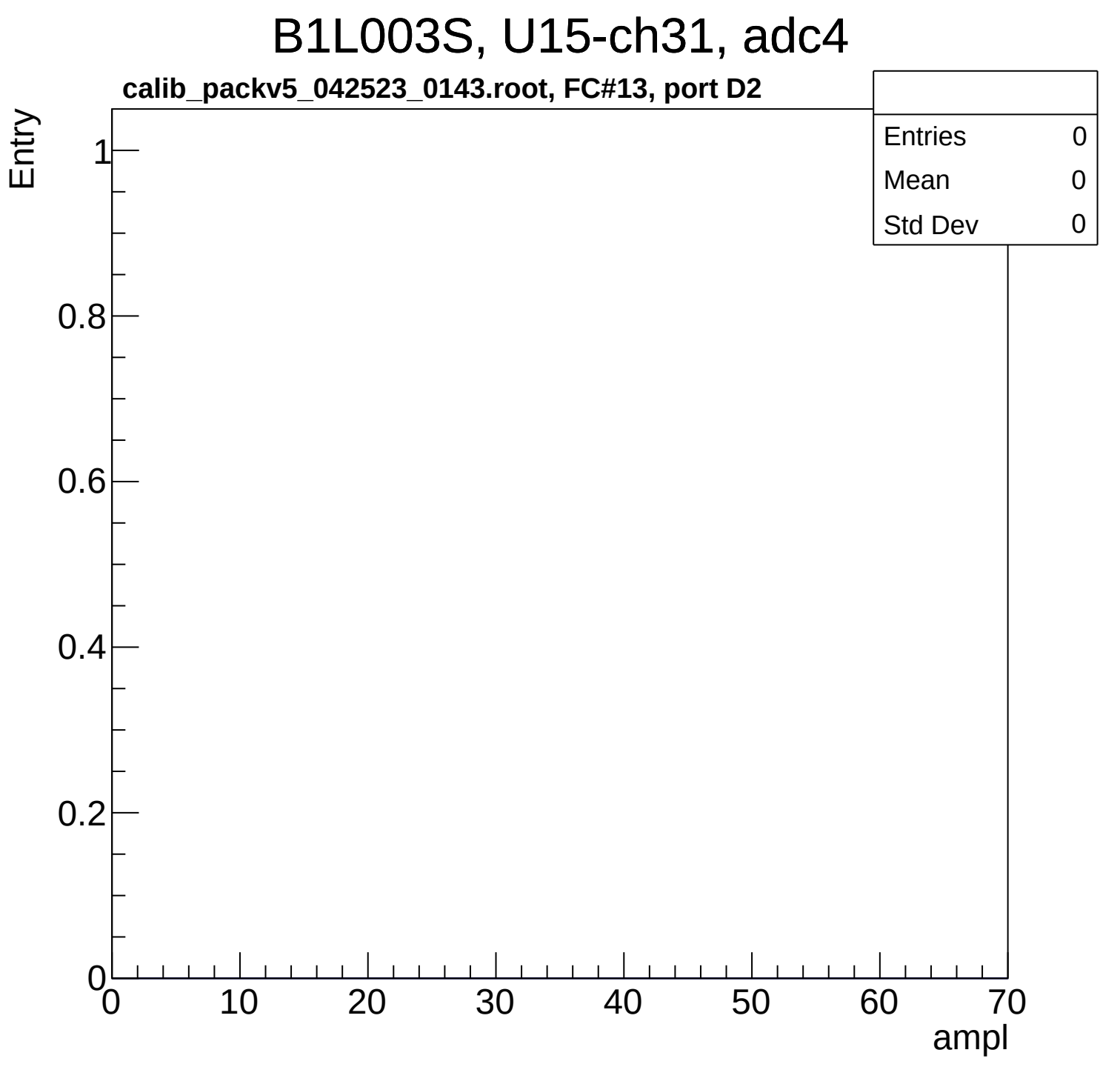
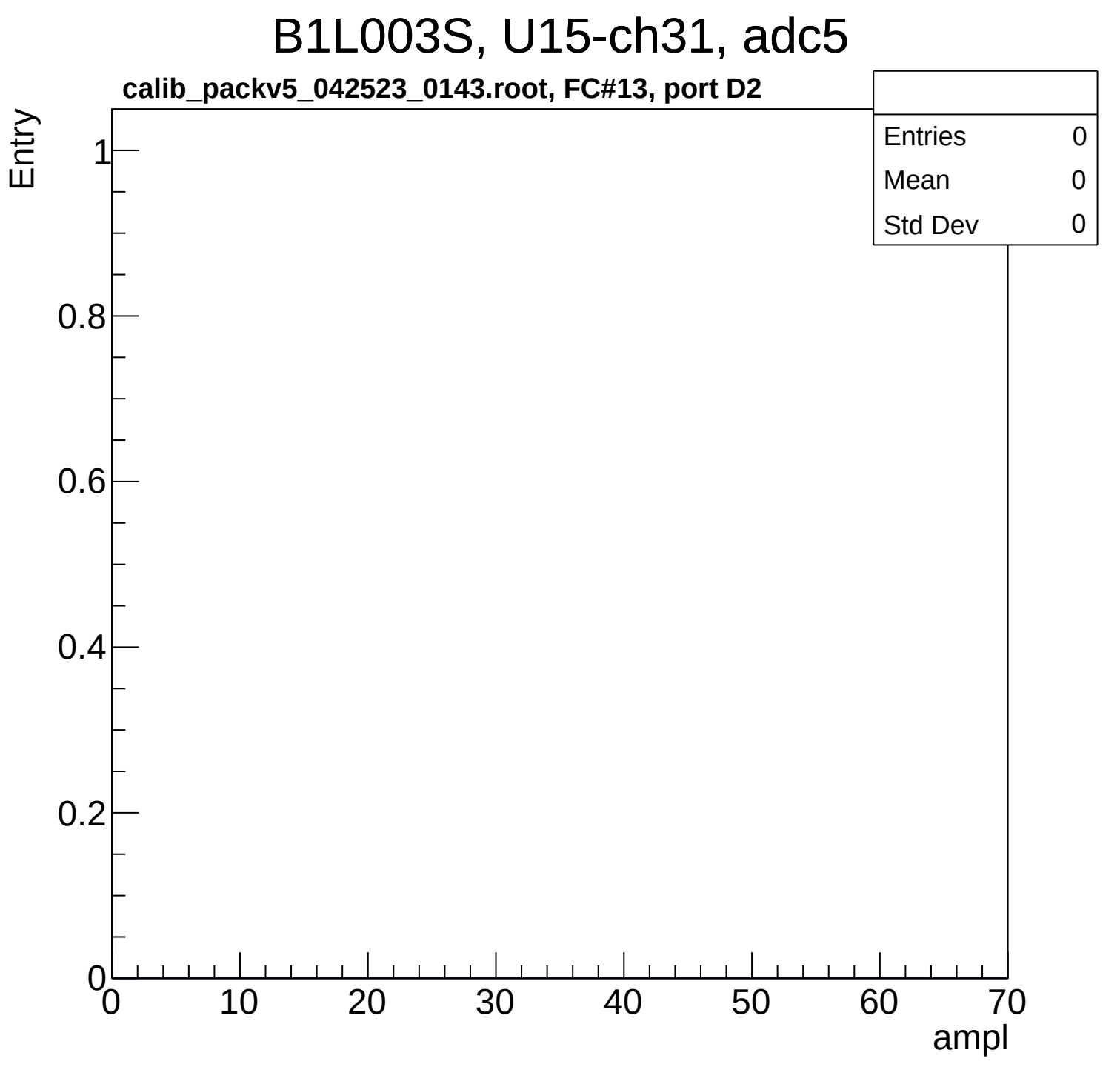
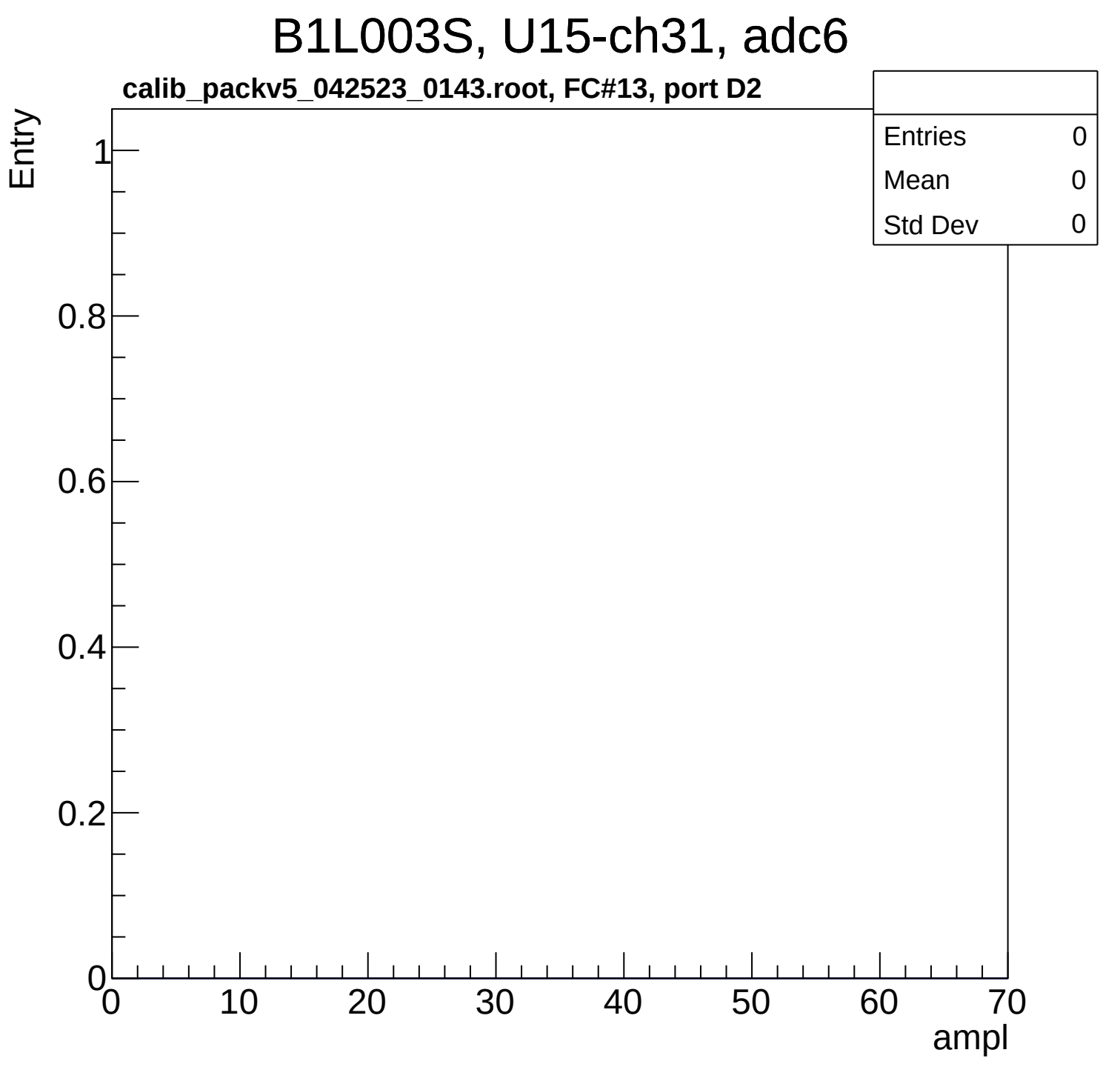
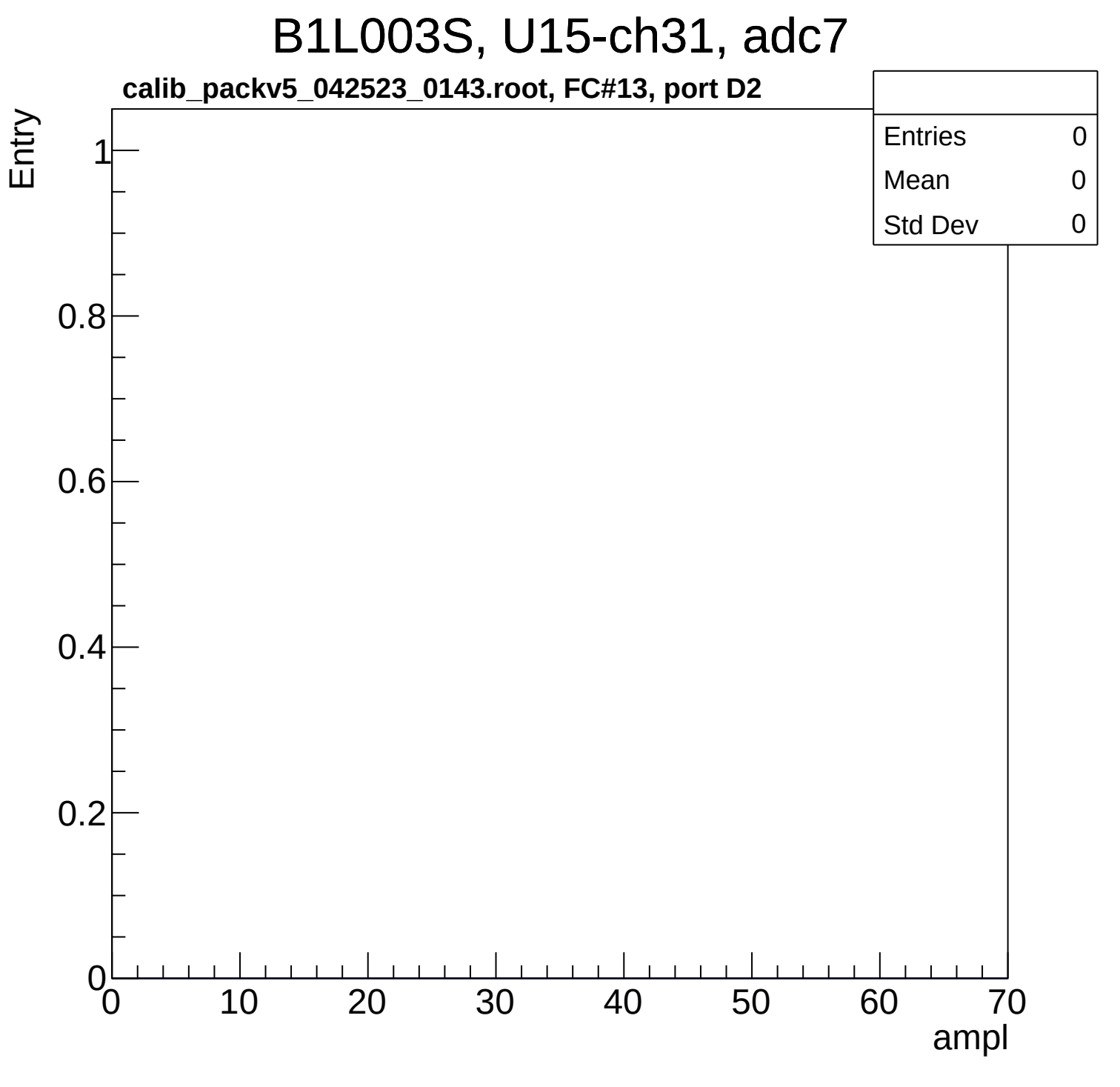
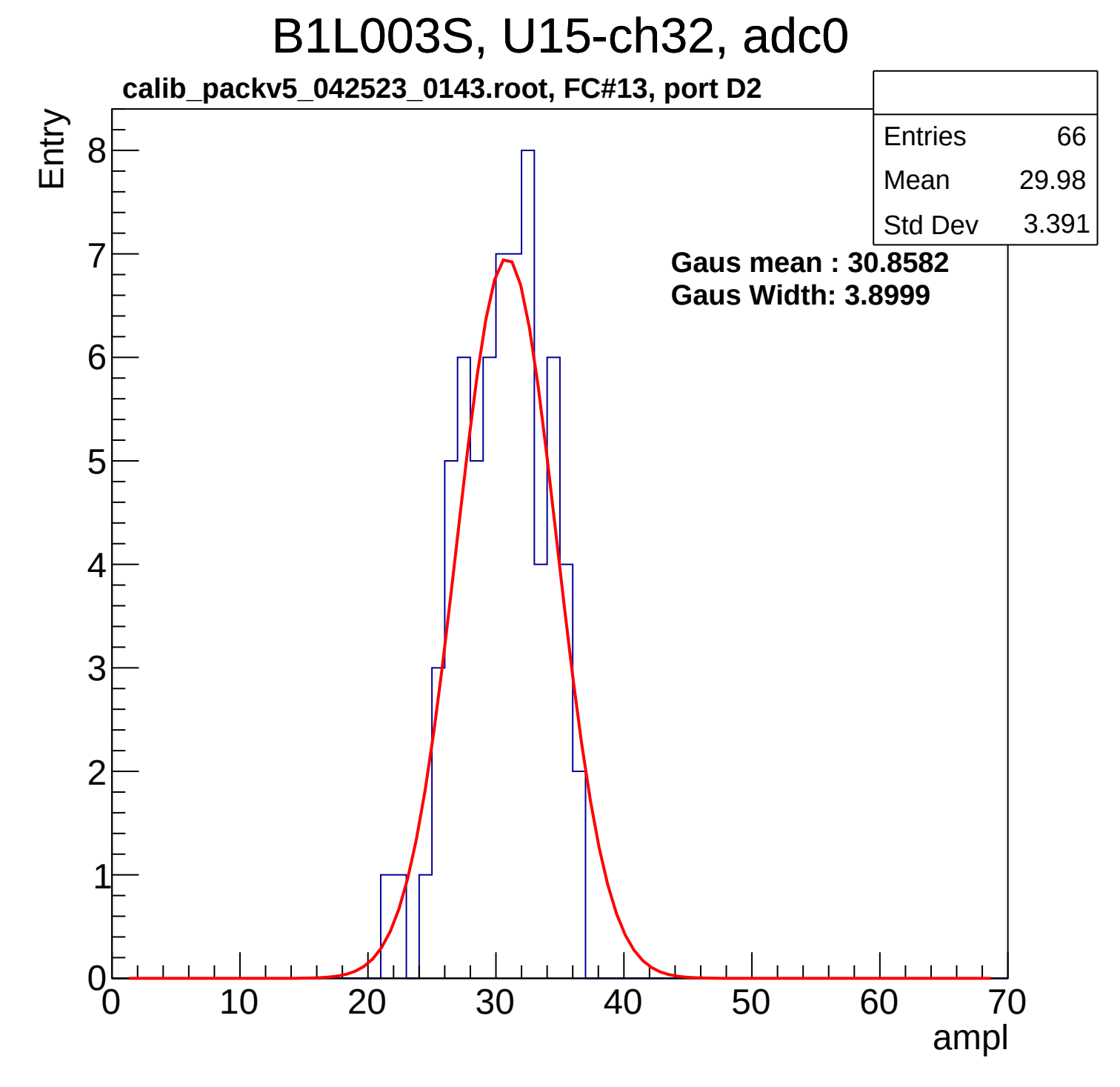
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.98
Std Dev 3.391
Gaus mean : 30.8582
Gaus Width: 3.8999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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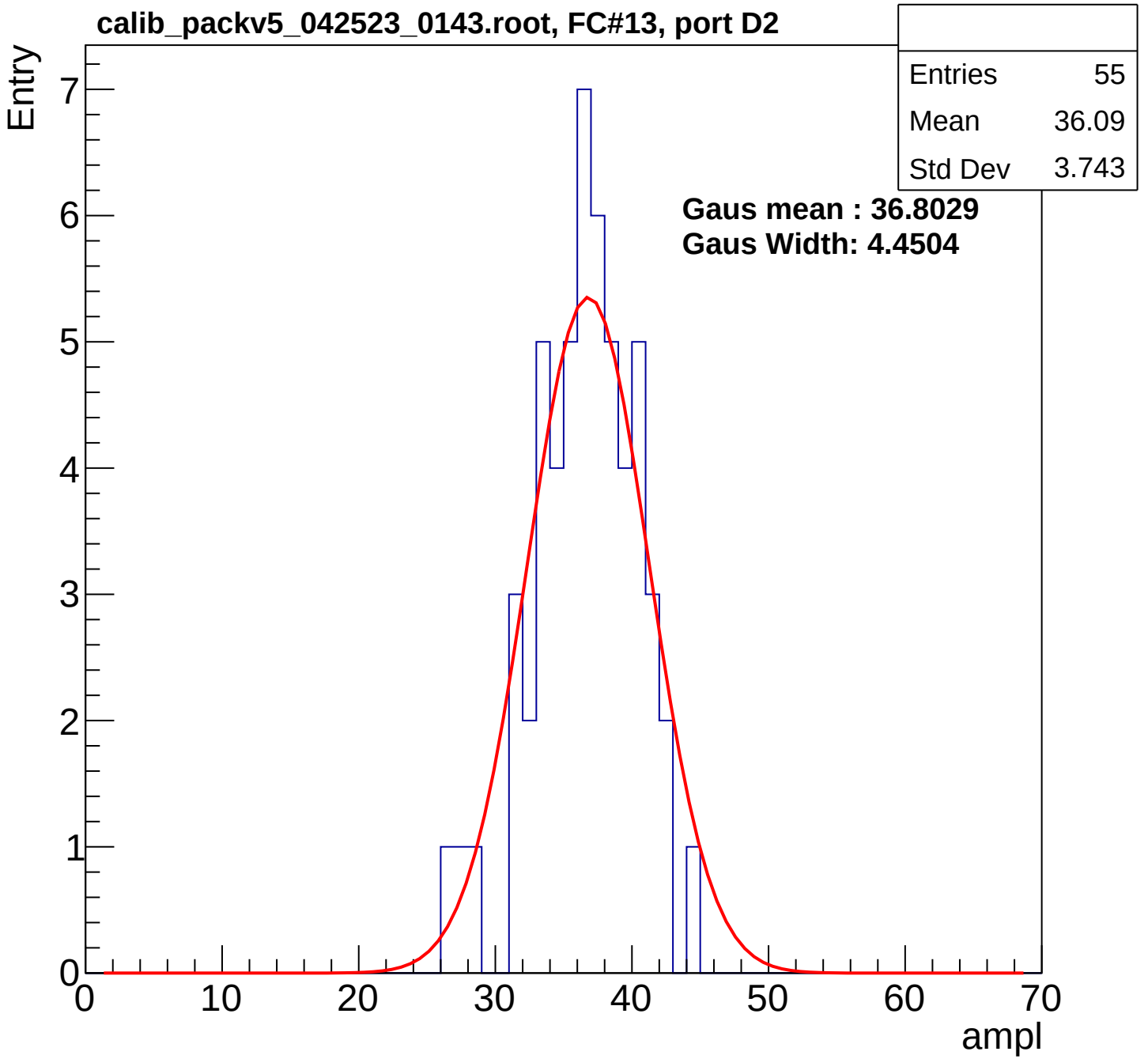
B1L003S, U15-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 36.09
Std Dev 3.743
Gaus mean : 36.8029
Gaus Width: 4.4504
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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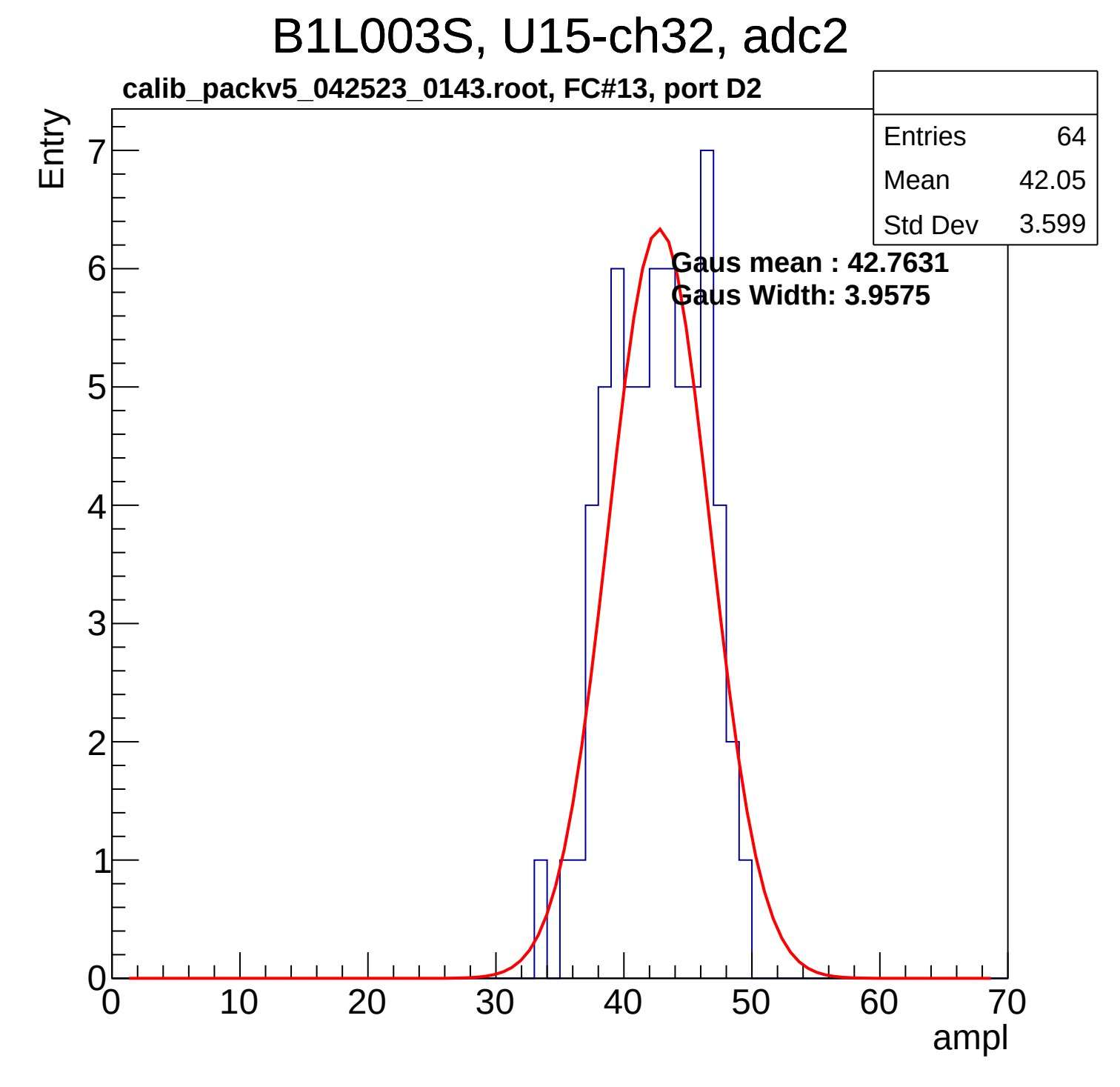
B1L003S, U15-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.05
Std Dev 3.599
Gaus mean : 42.7631
Gaus Width: 3.9575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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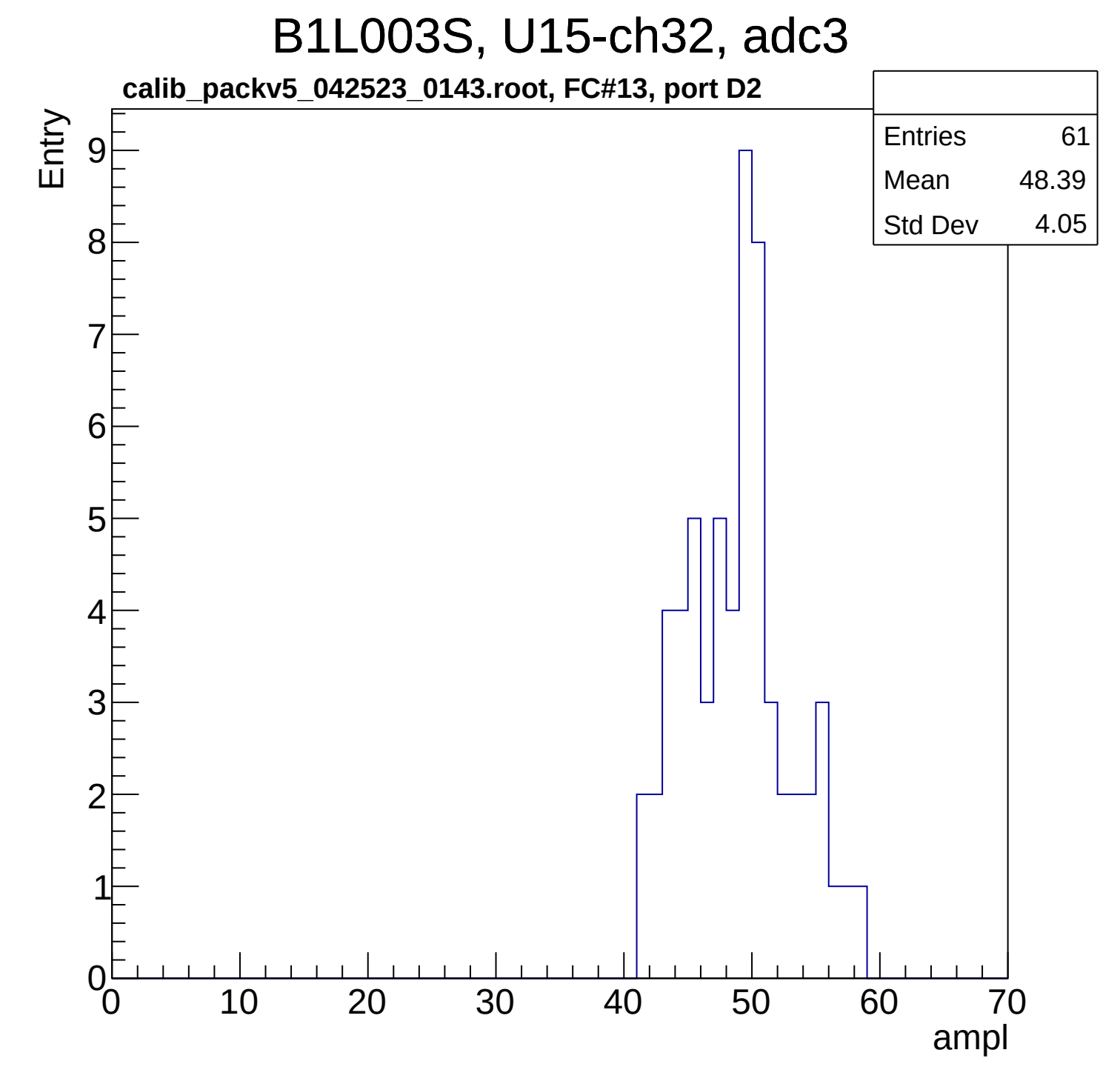
B1L003S, U15-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.39
Std Dev 4.05
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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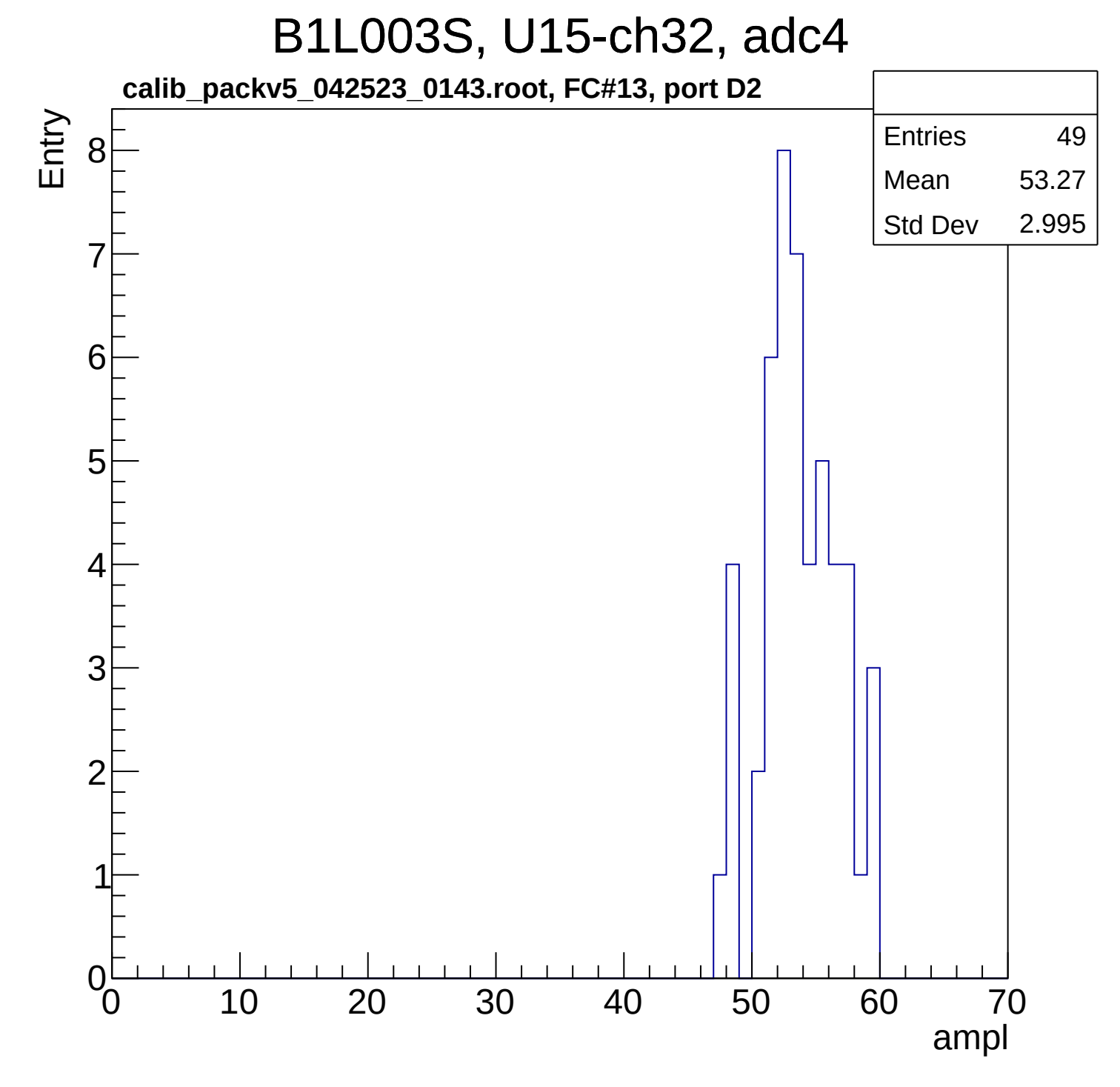
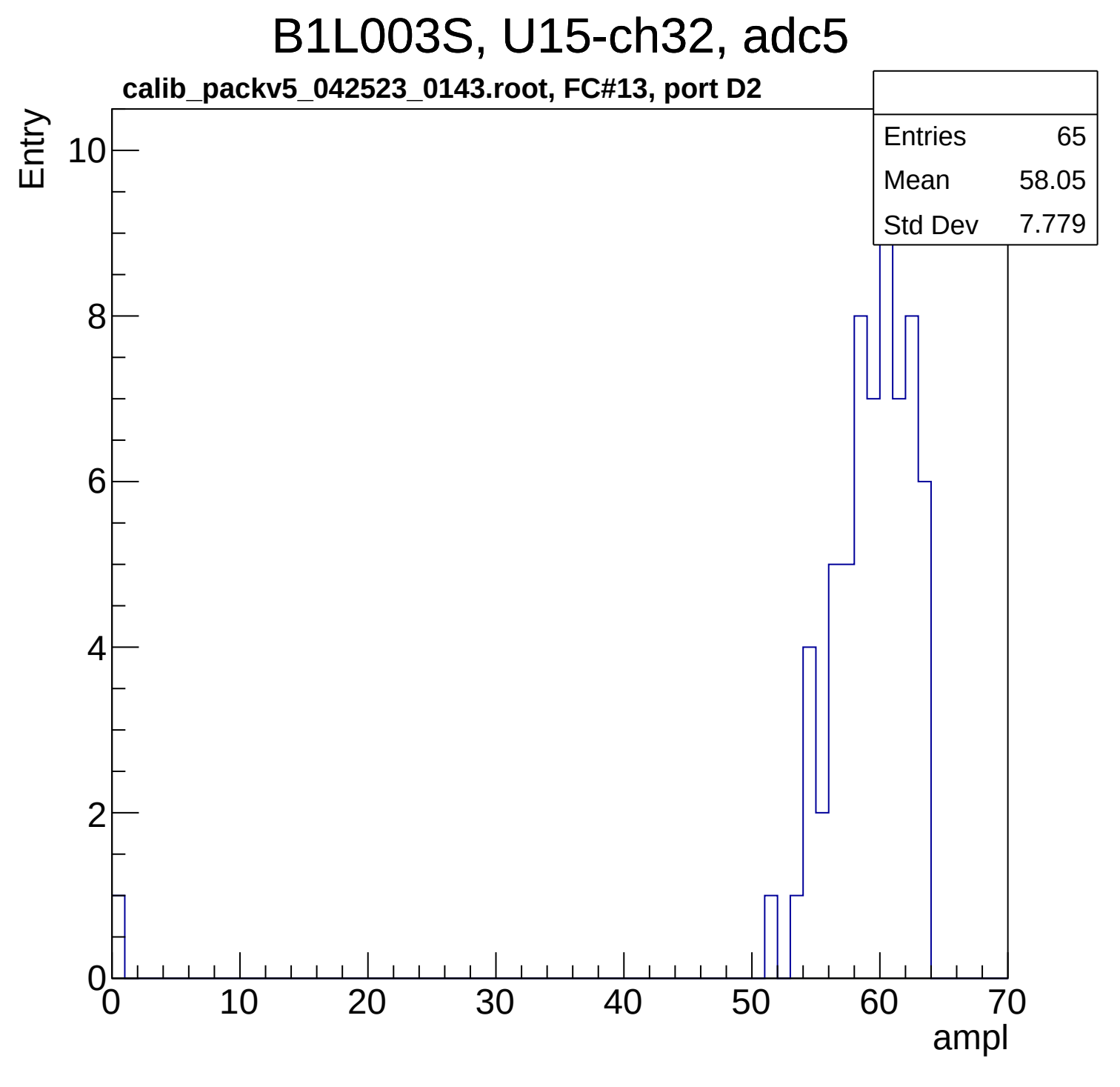
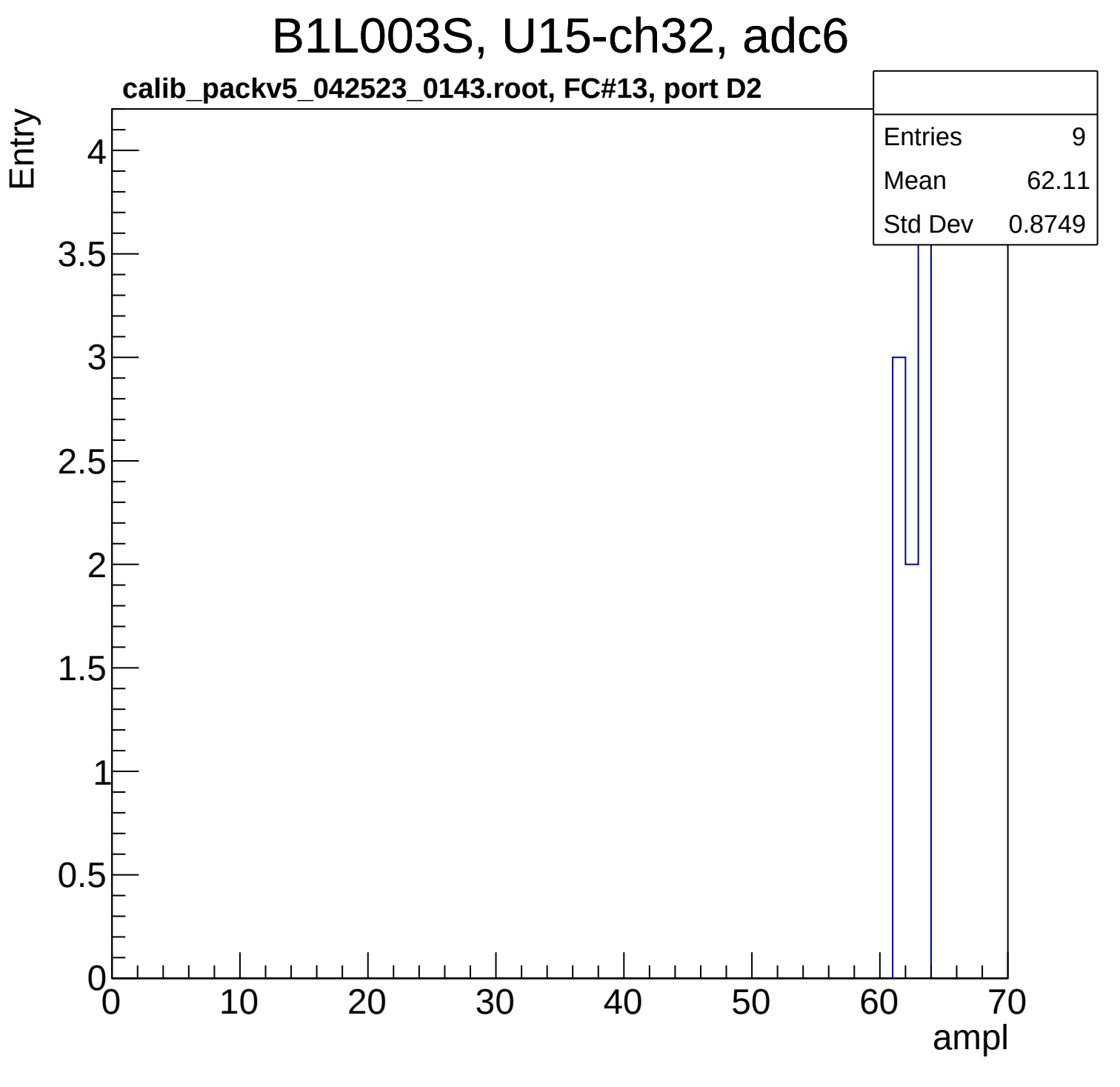
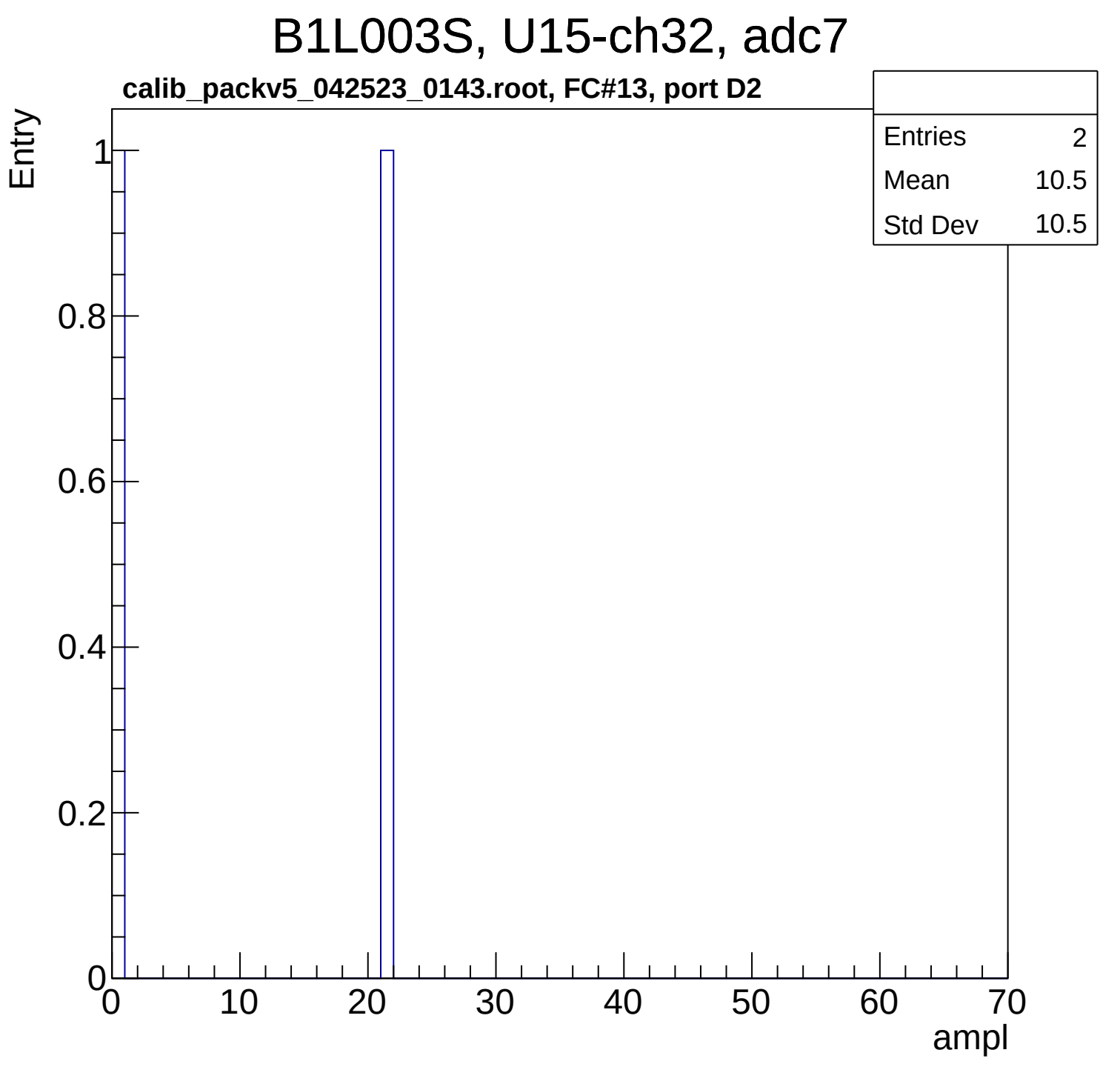
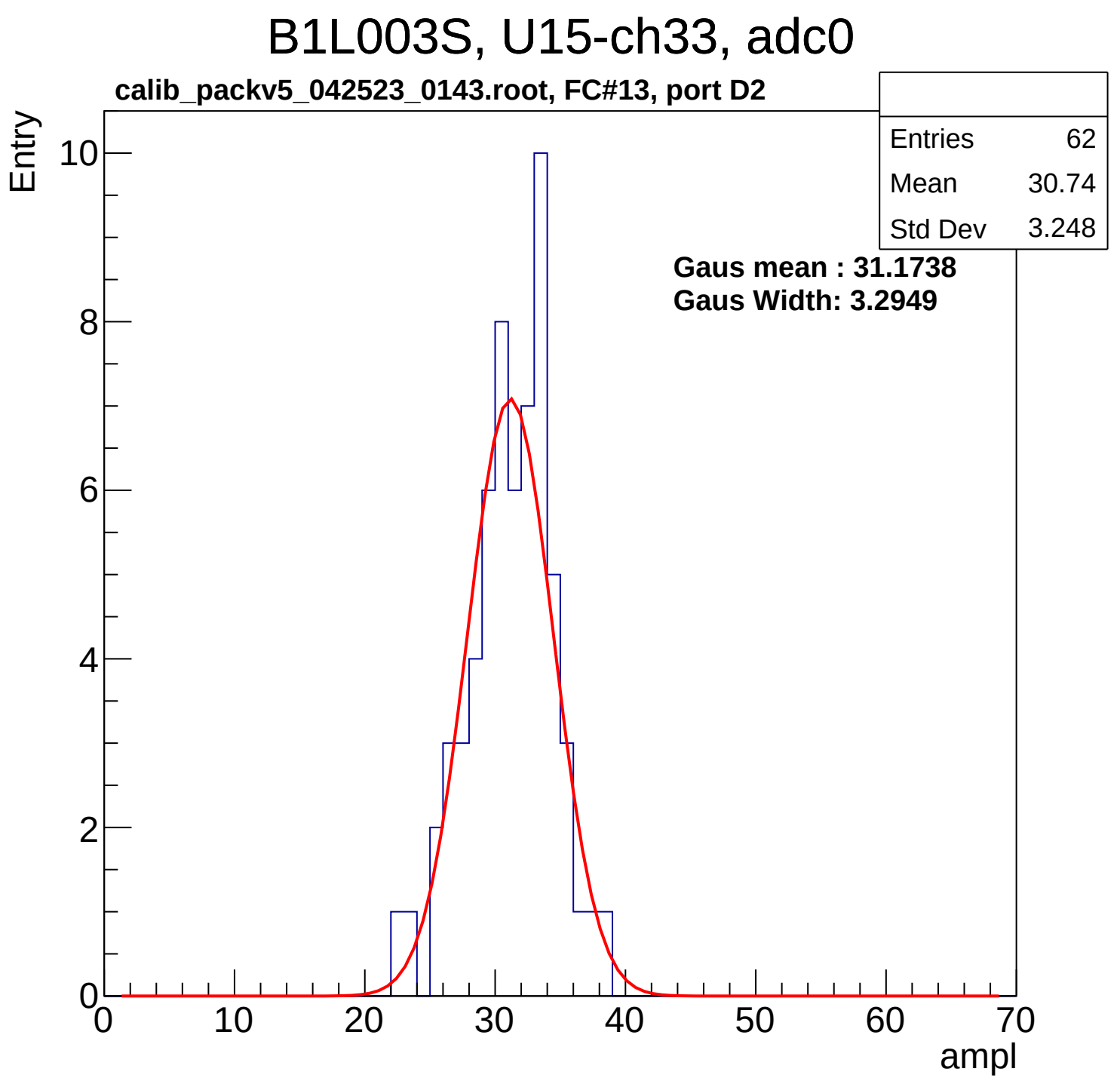
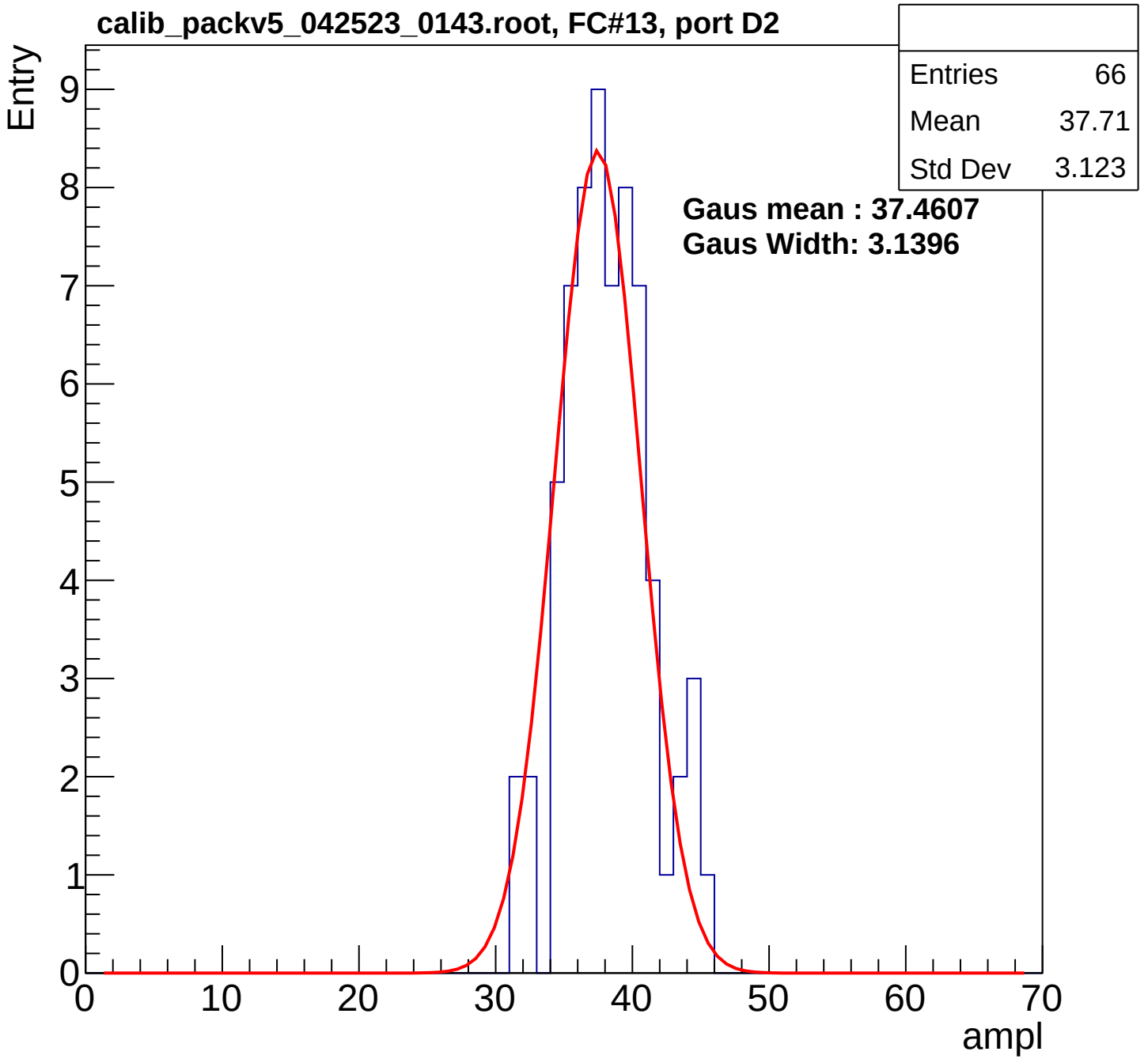

B1L003S, U15-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.27
Std Dev 2.995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

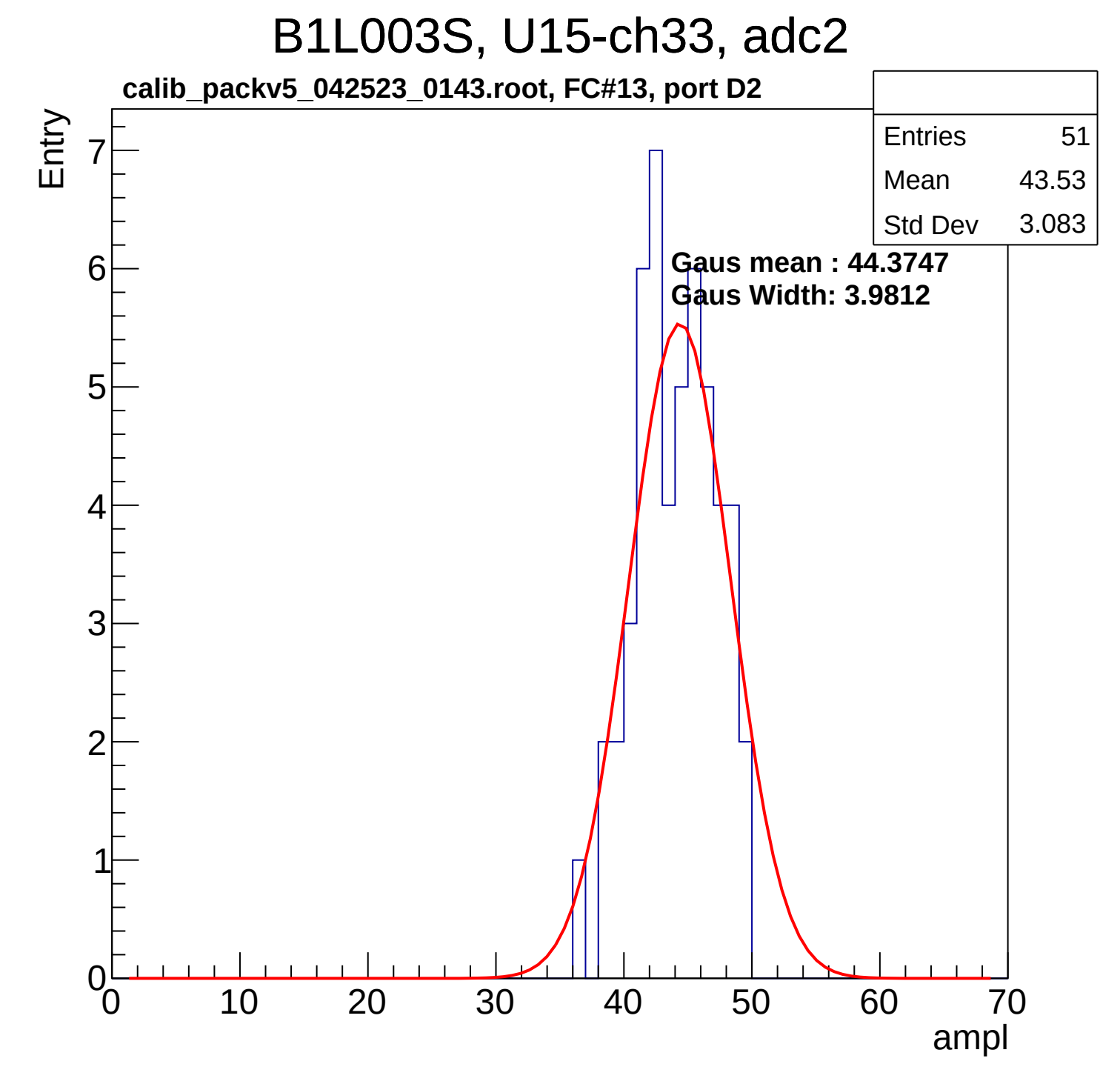

B1L003S, U15-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 58.05
Std Dev 7.779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

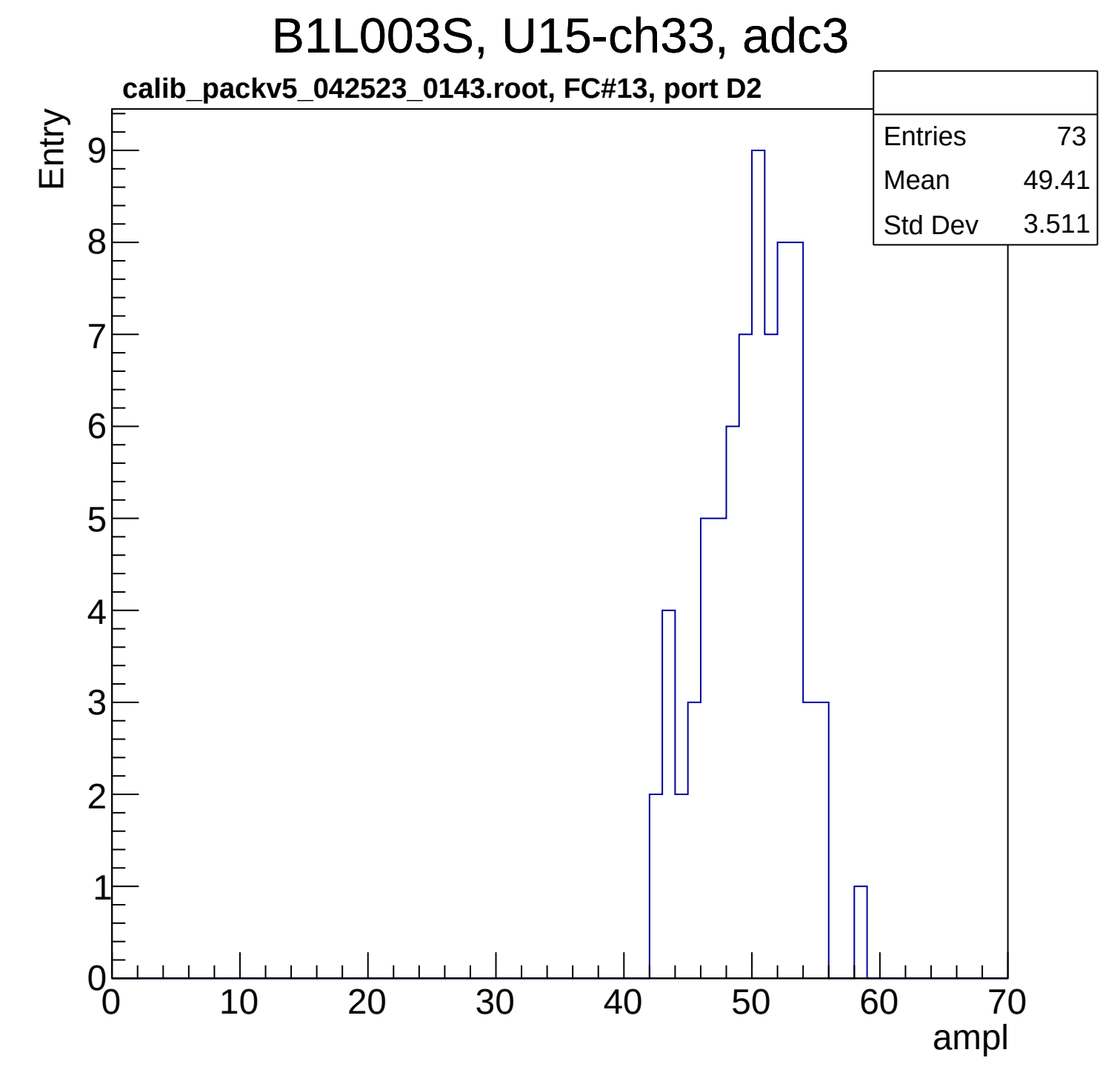

B1L003S, U15-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.8749
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 30.74
Std Dev 3.248
Gaus mean : 31.1738
Gaus Width: 3.2949
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.71
Std Dev 3.123
Gaus mean : 37.4607
Gaus Width: 3.1396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.53
Std Dev 3.083
Gaus mean : 44.3747
Gaus Width: 3.9812
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.41
Std Dev 3.511
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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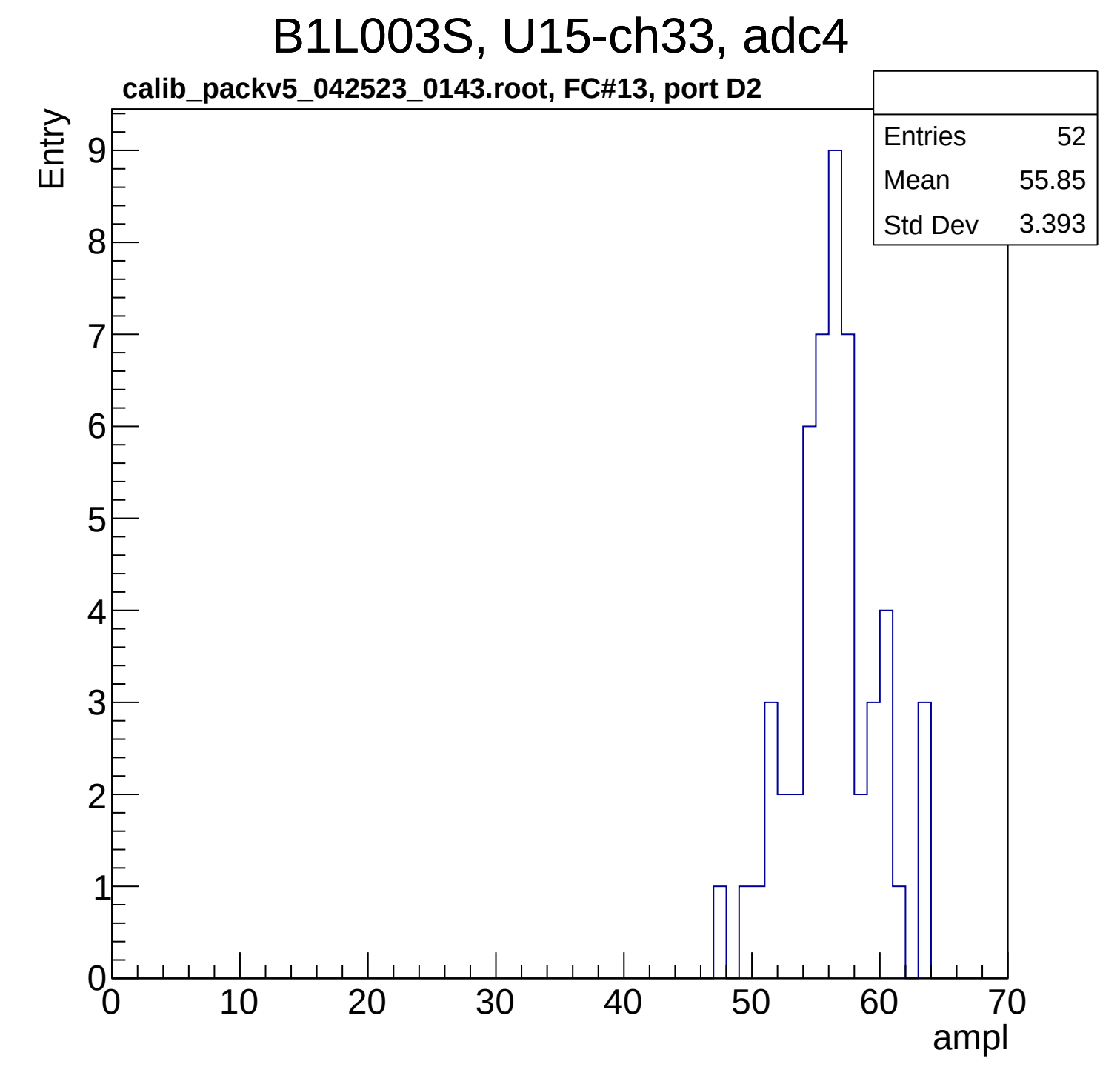
B1L003S, U15-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.85
Std Dev 3.393
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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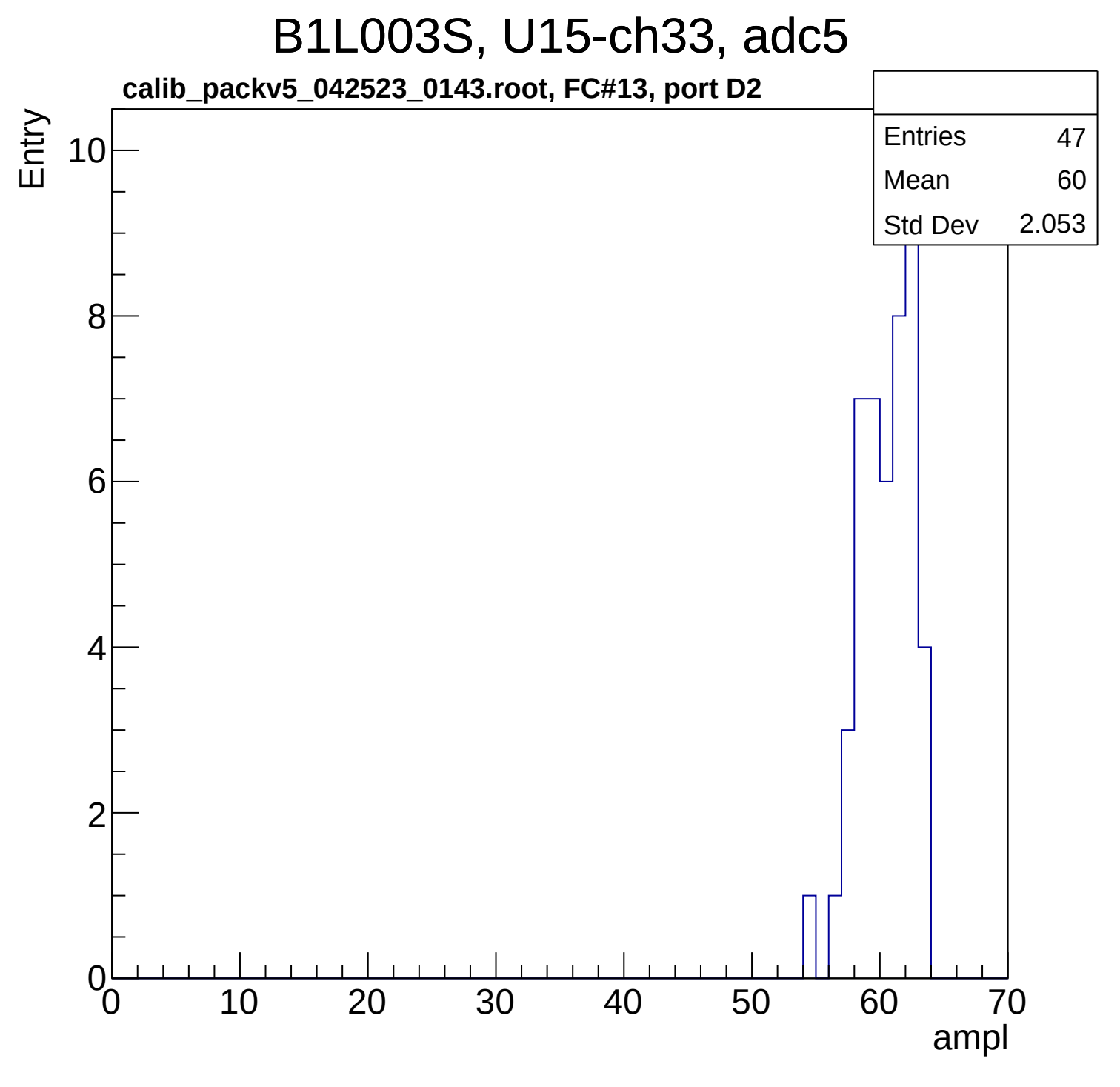
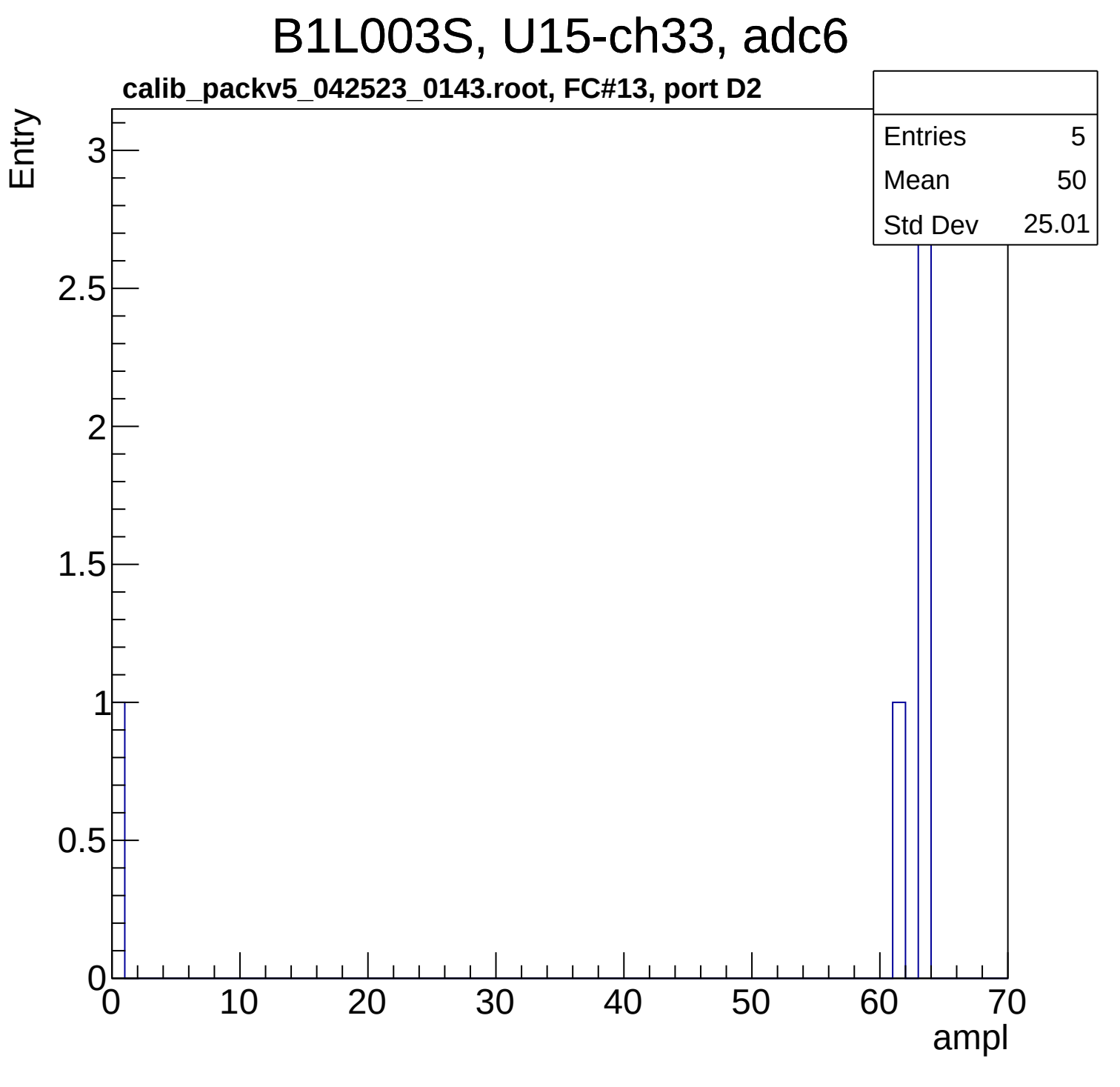

B1L003S, U15-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 60
Std Dev 2.053
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 50
Std Dev 25.01
Entry
ampl
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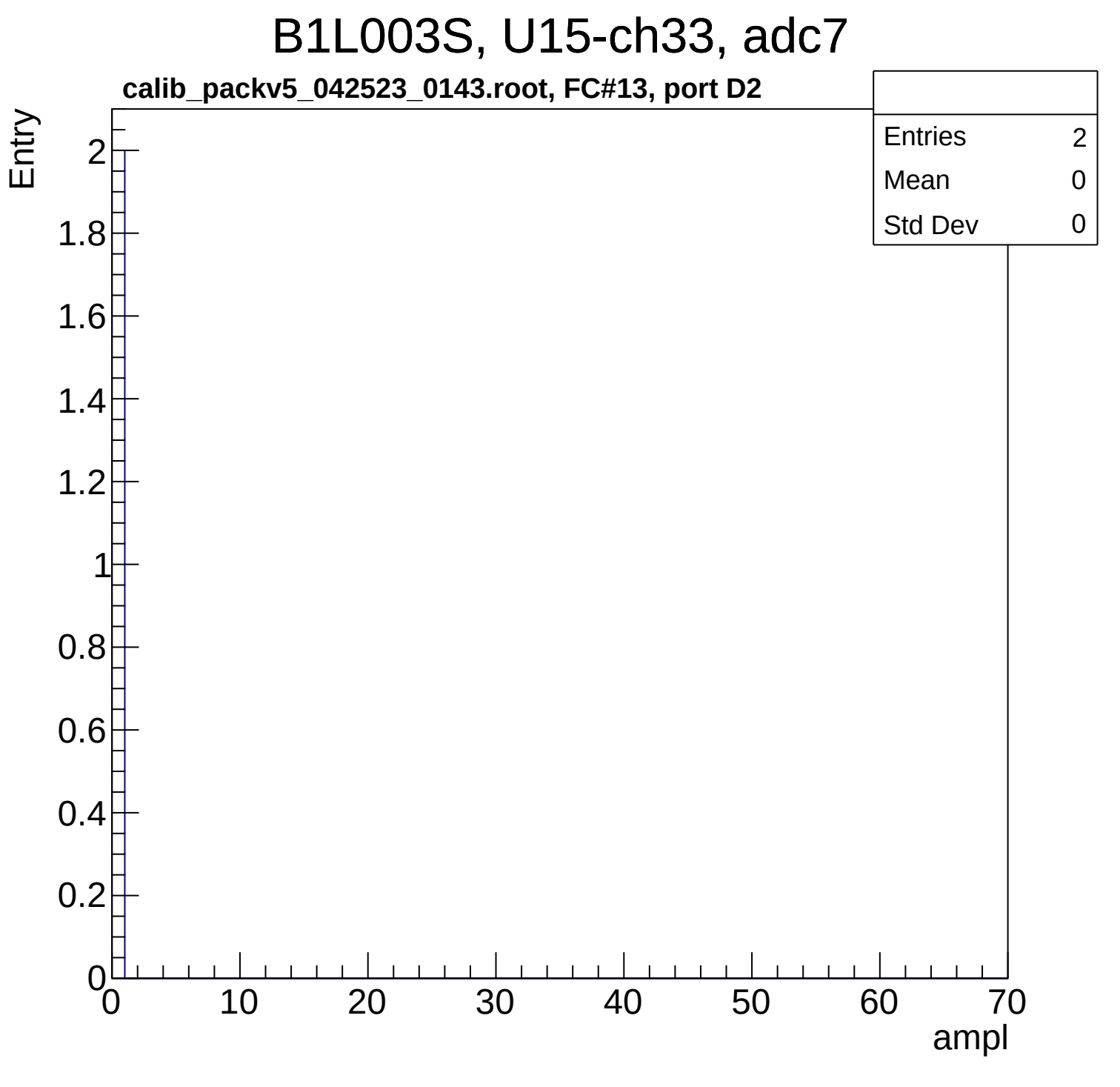
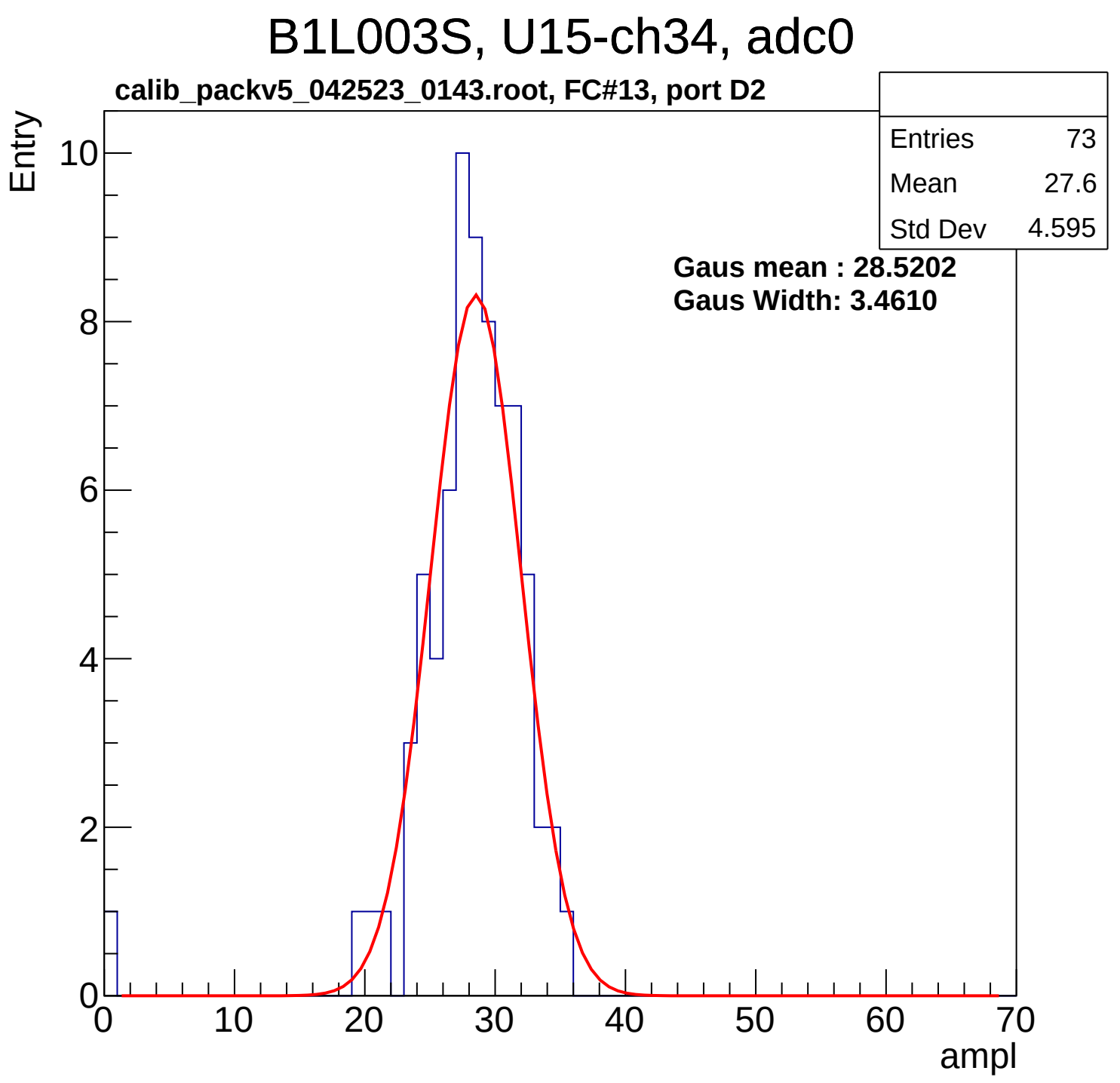
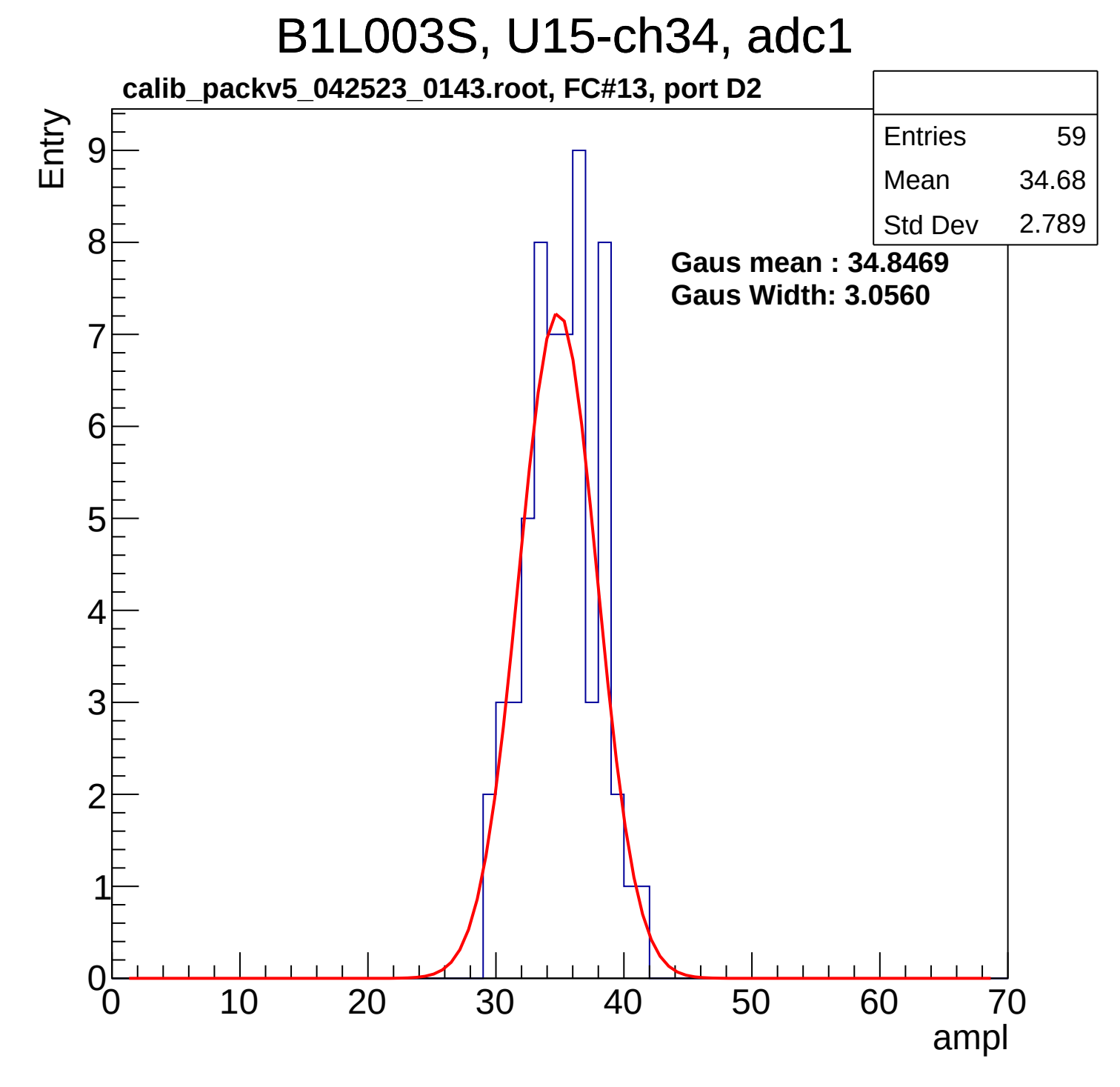
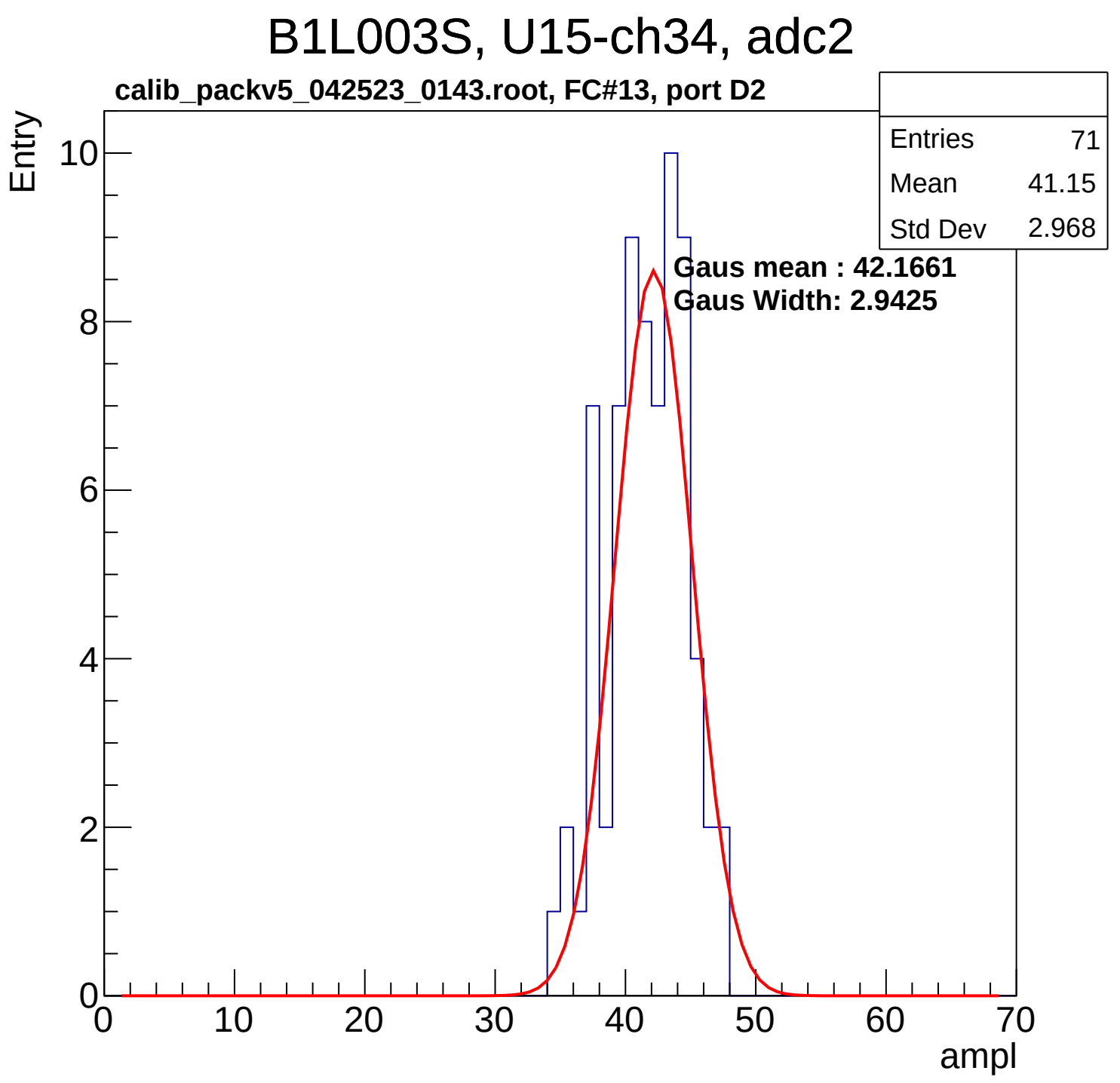
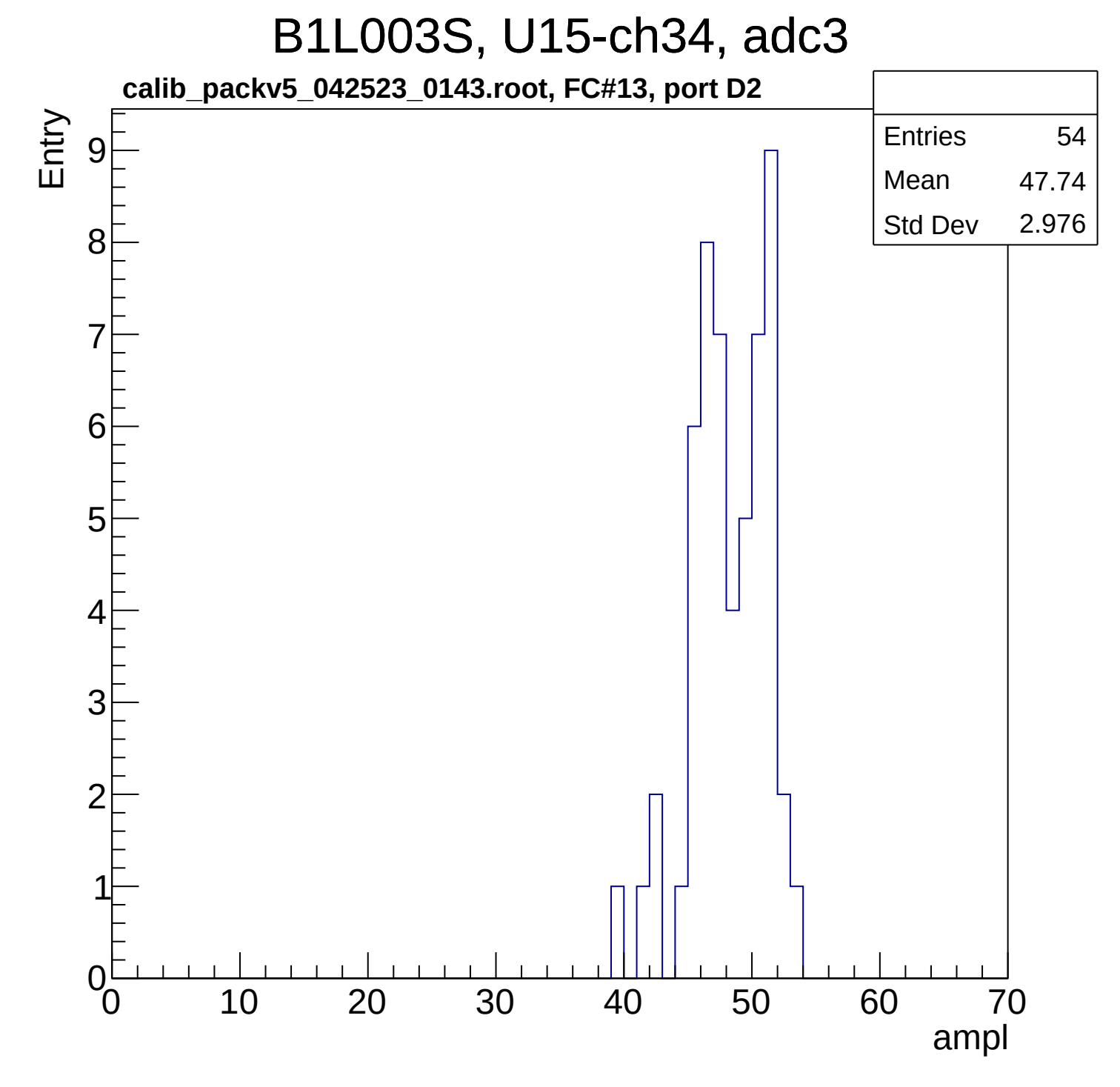
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 27.6
Std Dev 4.595
Gaus mean : 28.5202
Gaus Width: 3.4610
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 34.68
Std Dev 2.789
Gaus mean : 34.8469
Gaus Width: 3.0560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 41.15
Std Dev 2.968
Gaus mean : 42.1661
Gaus Width: 2.9425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 47.74
Std Dev 2.976
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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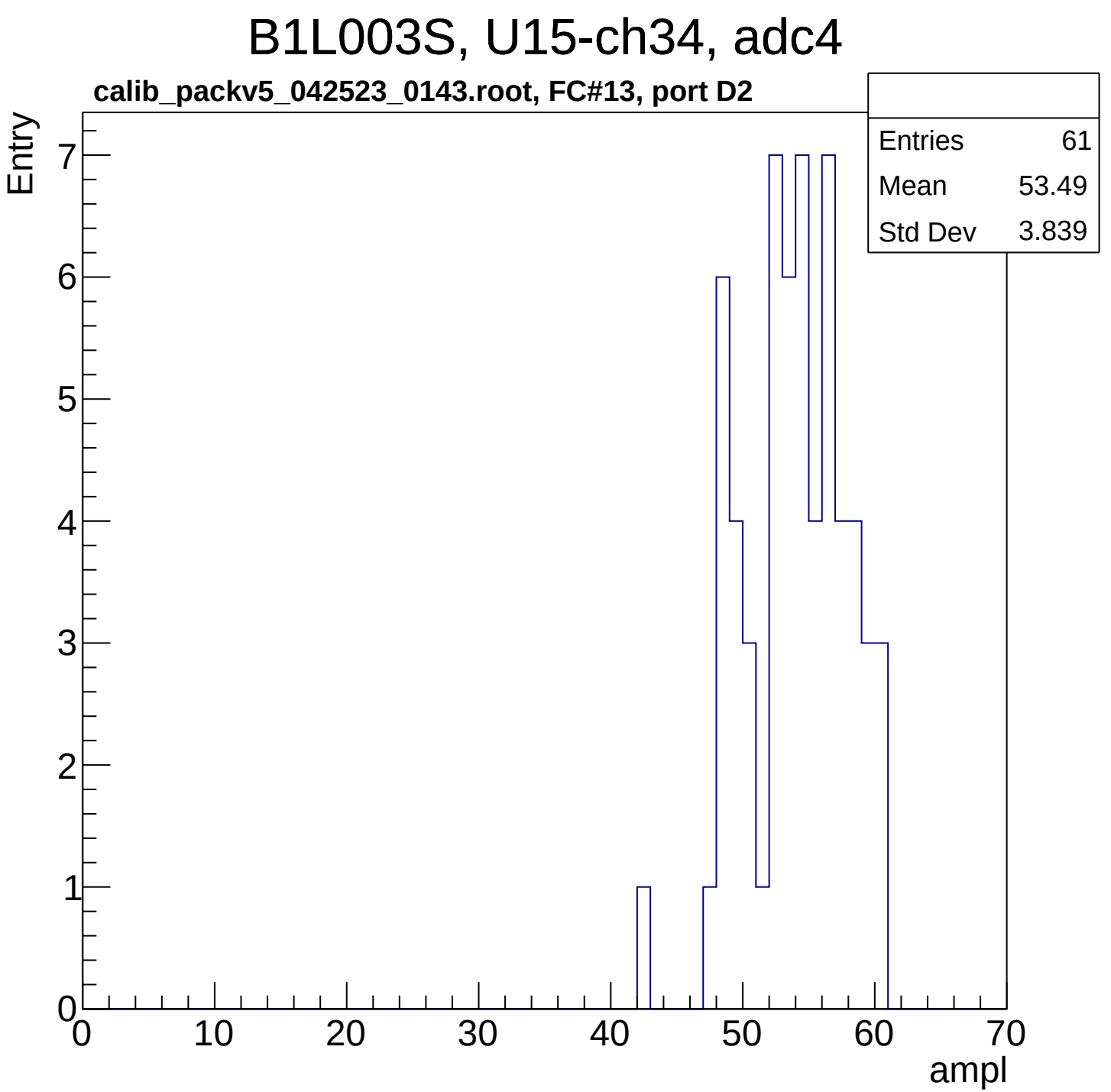
B1L003S, U15-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 53.49
Std Dev 3.839
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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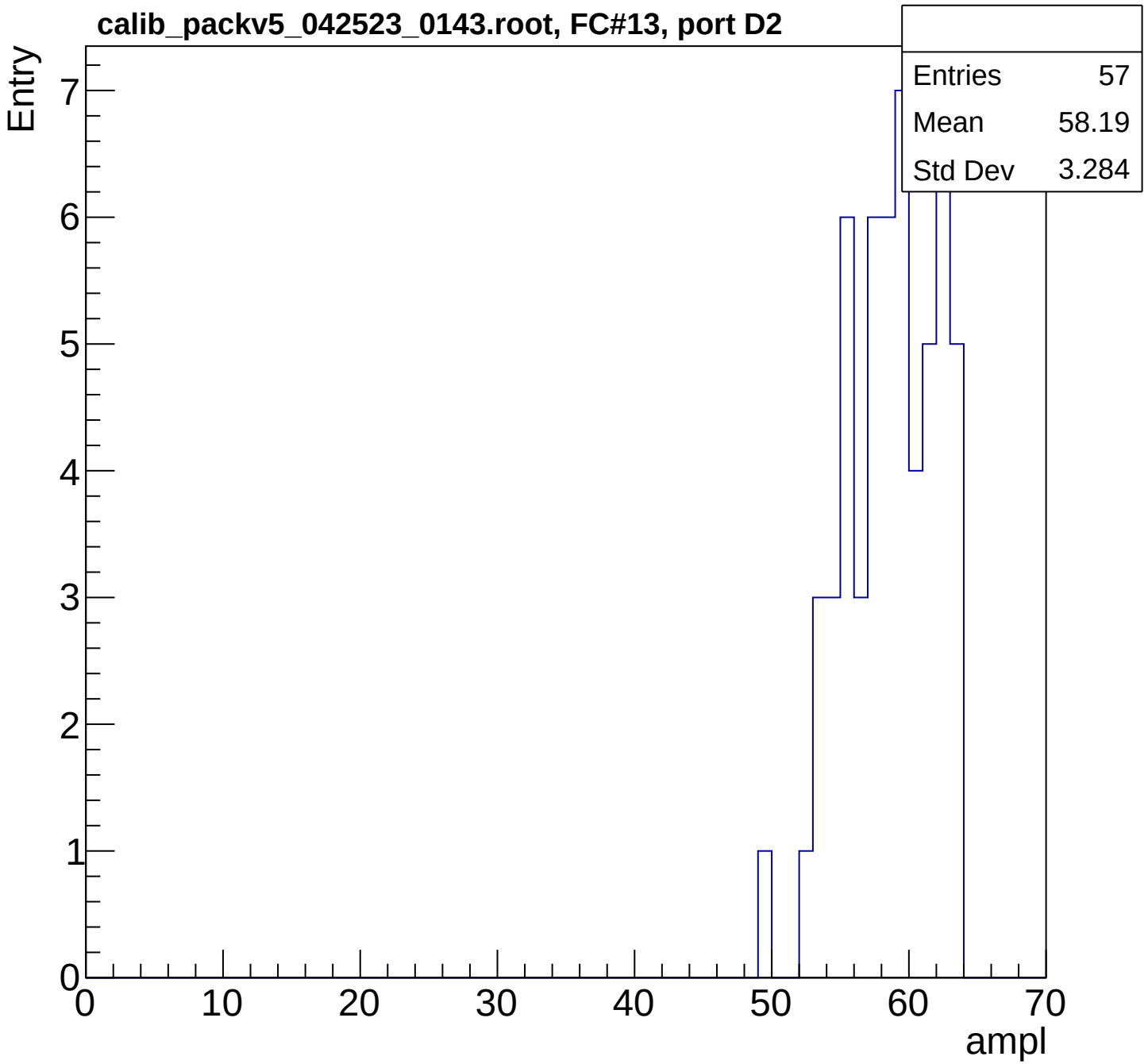
B1L003S, U15-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.19
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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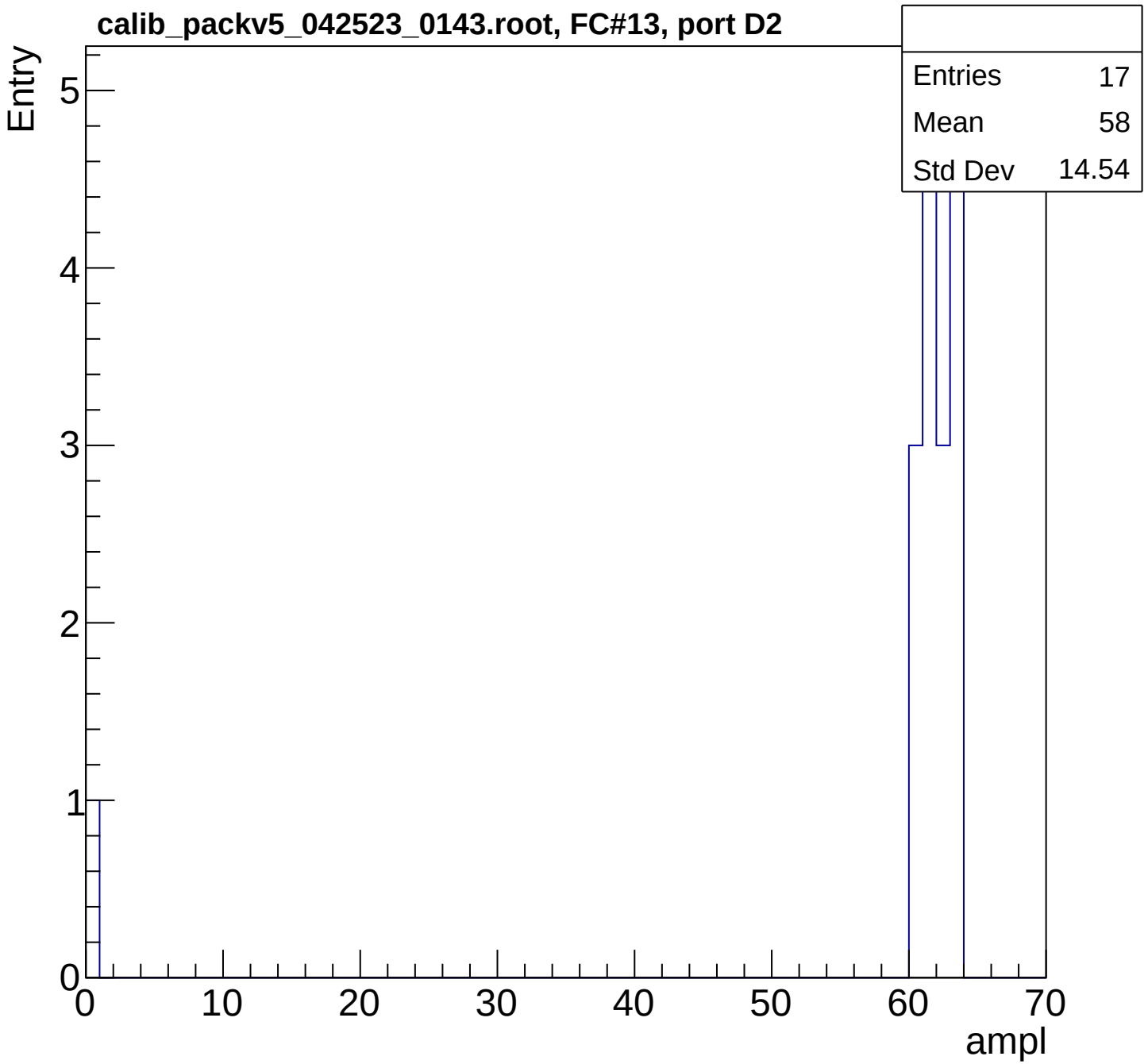
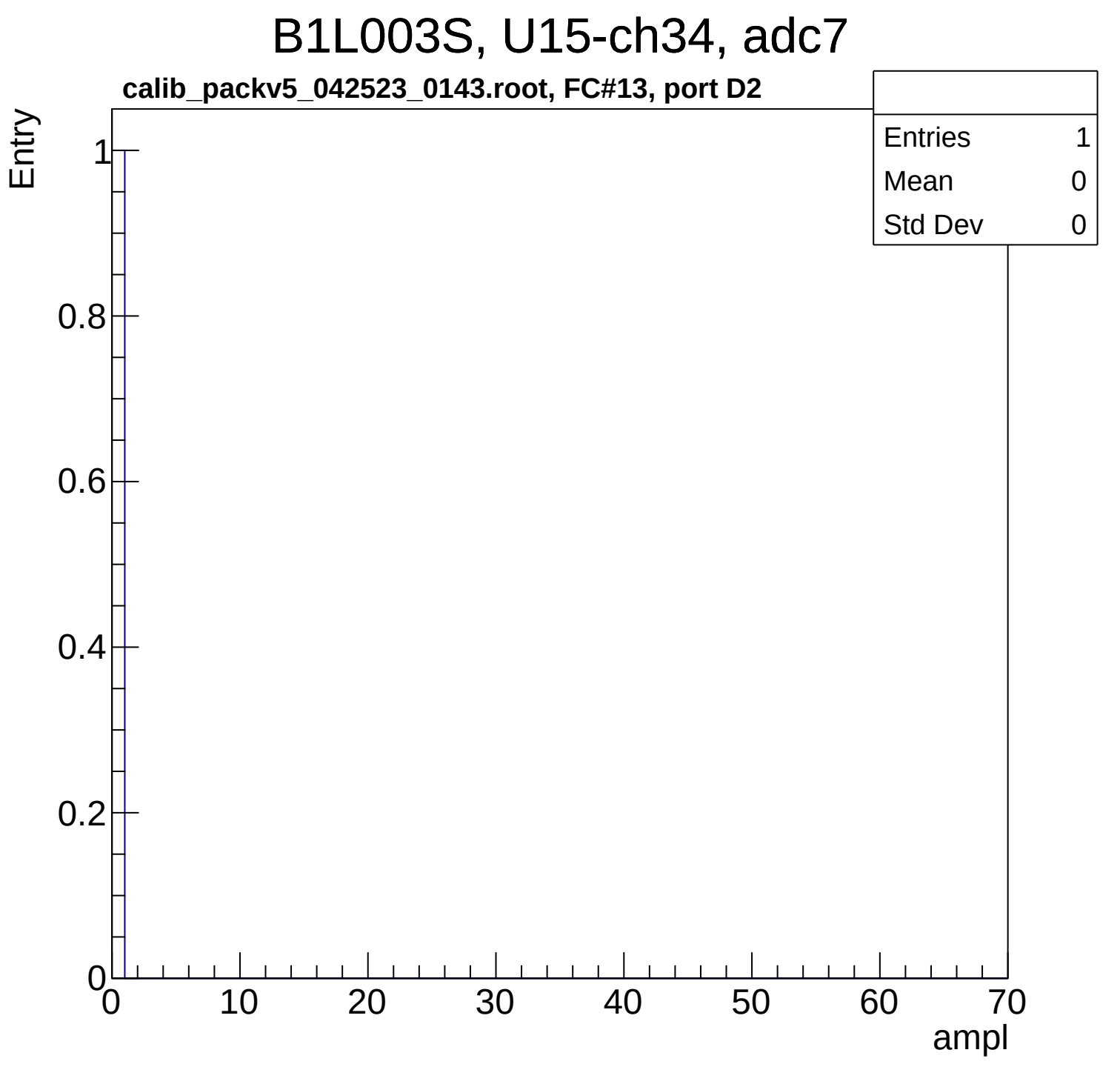
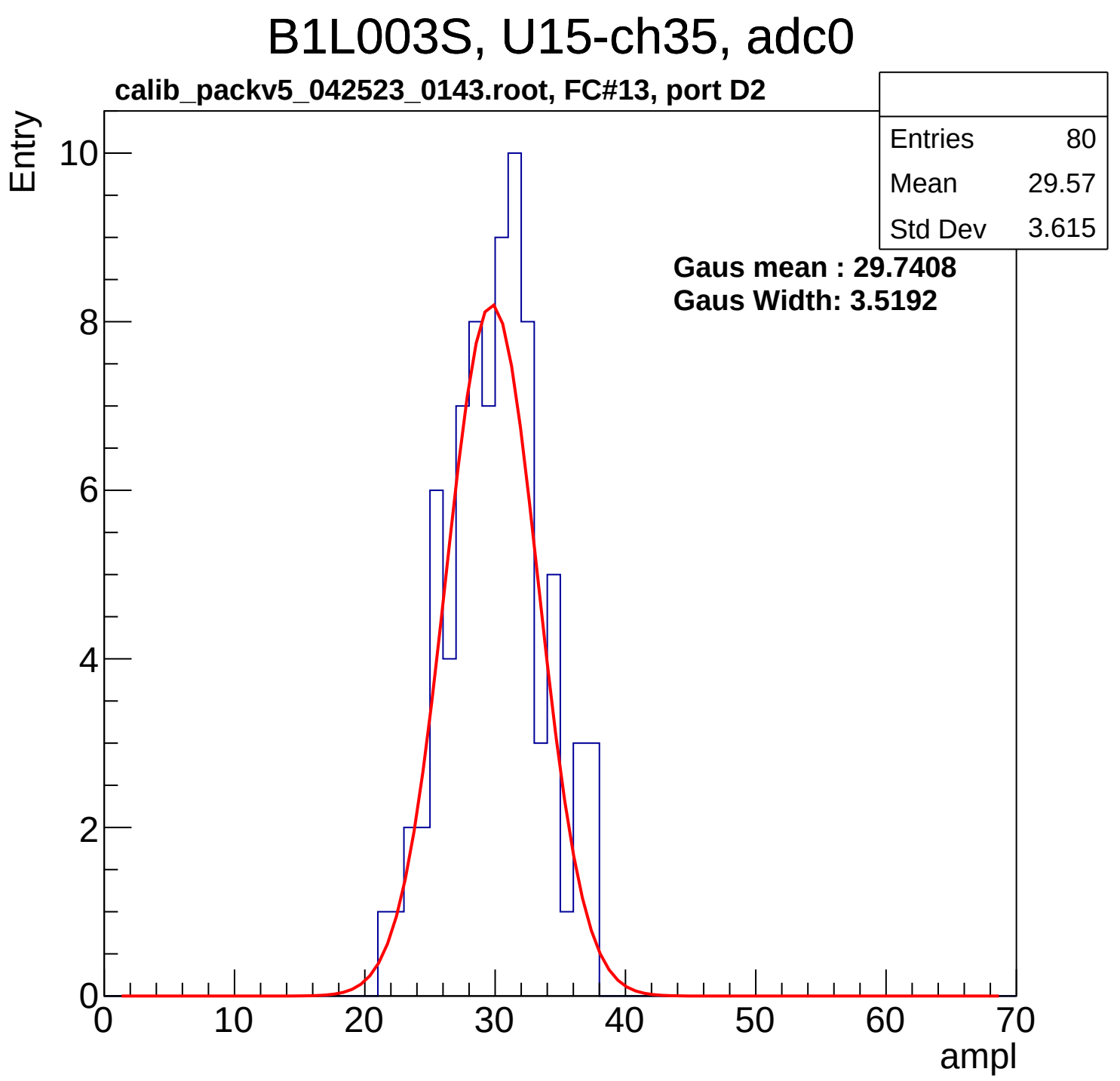

B1L003S, U15-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 58
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 29.57
Std Dev 3.615
Gaus mean : 29.7408
Gaus Width: 3.5192
Entry
ampl
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0
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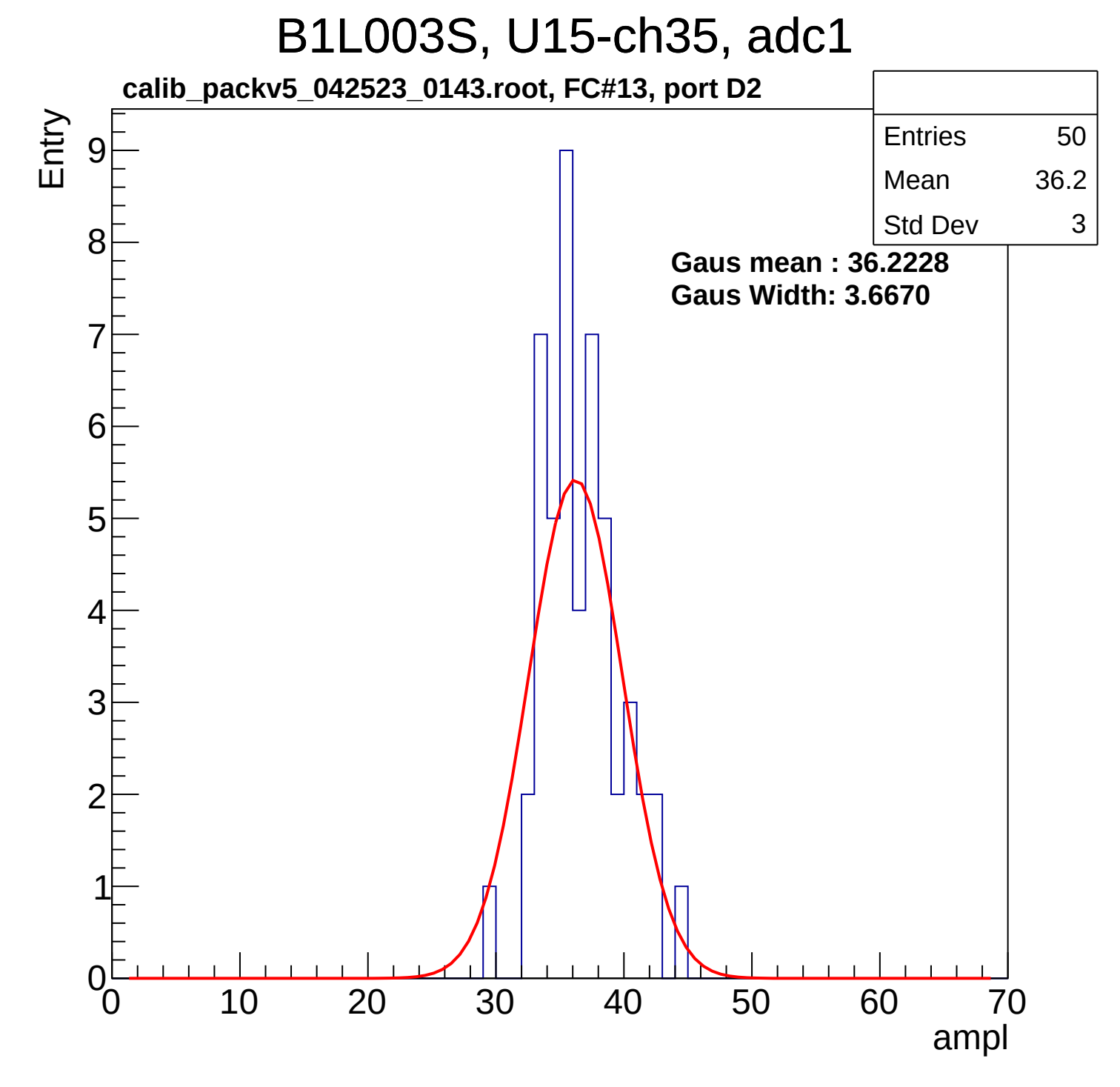
B1L003S, U15-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 36.2
Std Dev 3
Gaus mean : 36.2228
Gaus Width: 3.6670
Entry
ampl
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0
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60
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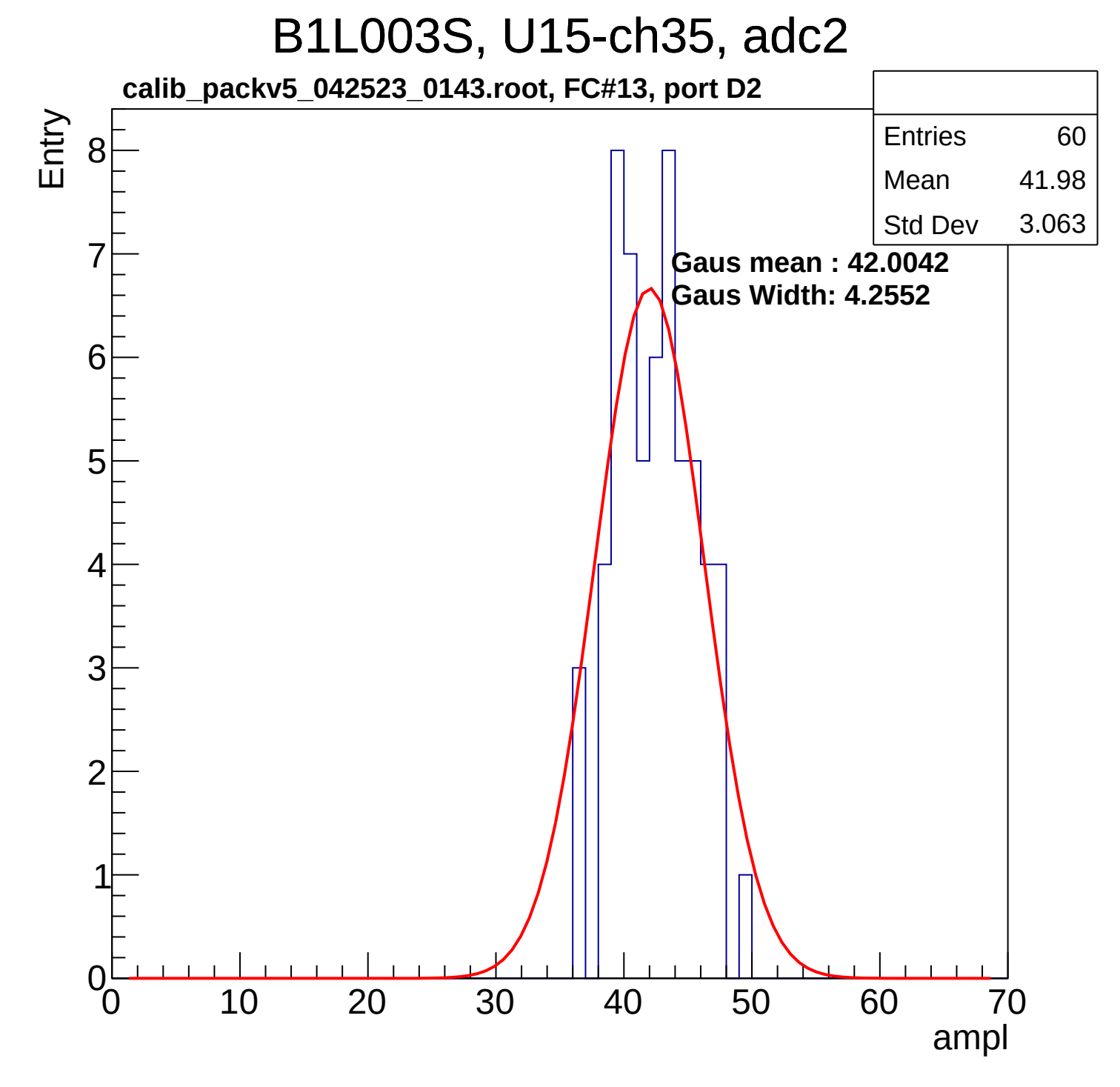
B1L003S, U15-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.98
Std Dev 3.063
Gaus mean : 42.0042
Gaus Width: 4.2552
Entry
ampl
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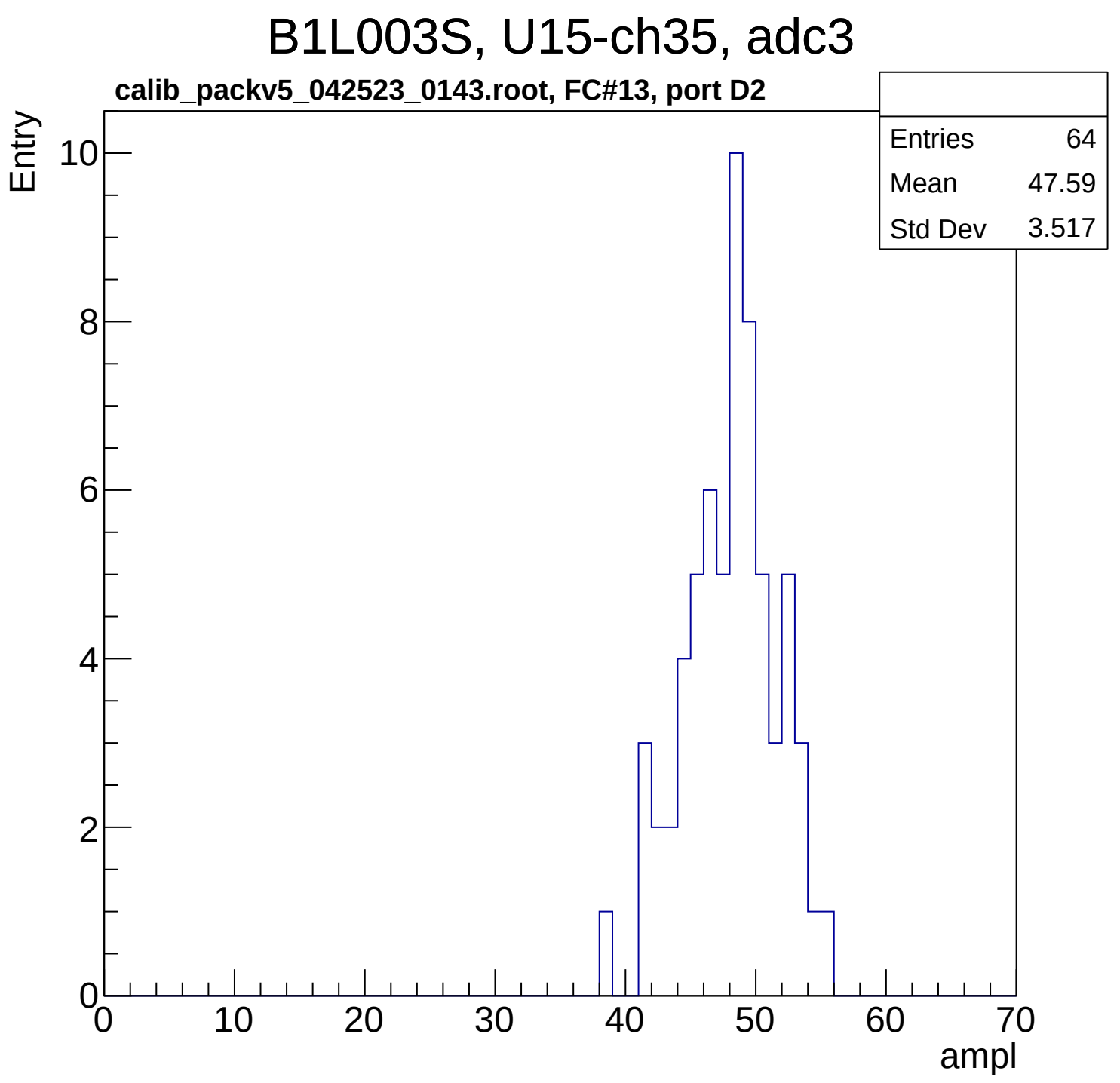
B1L003S, U15-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.59
Std Dev 3.517
Entry
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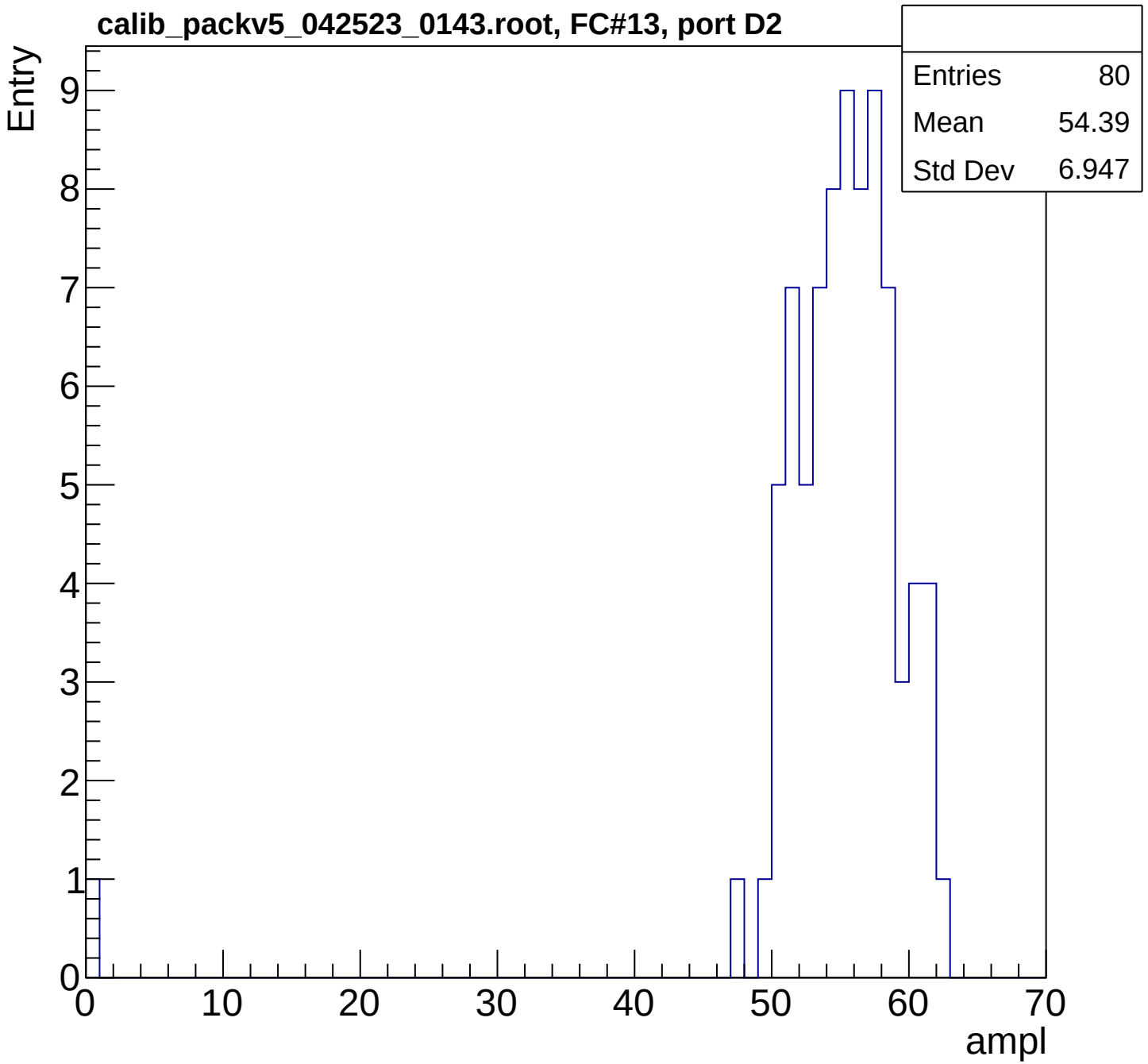
ampl


B1L003S, U15-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 54.39
Std Dev 6.947
Entry
ampl
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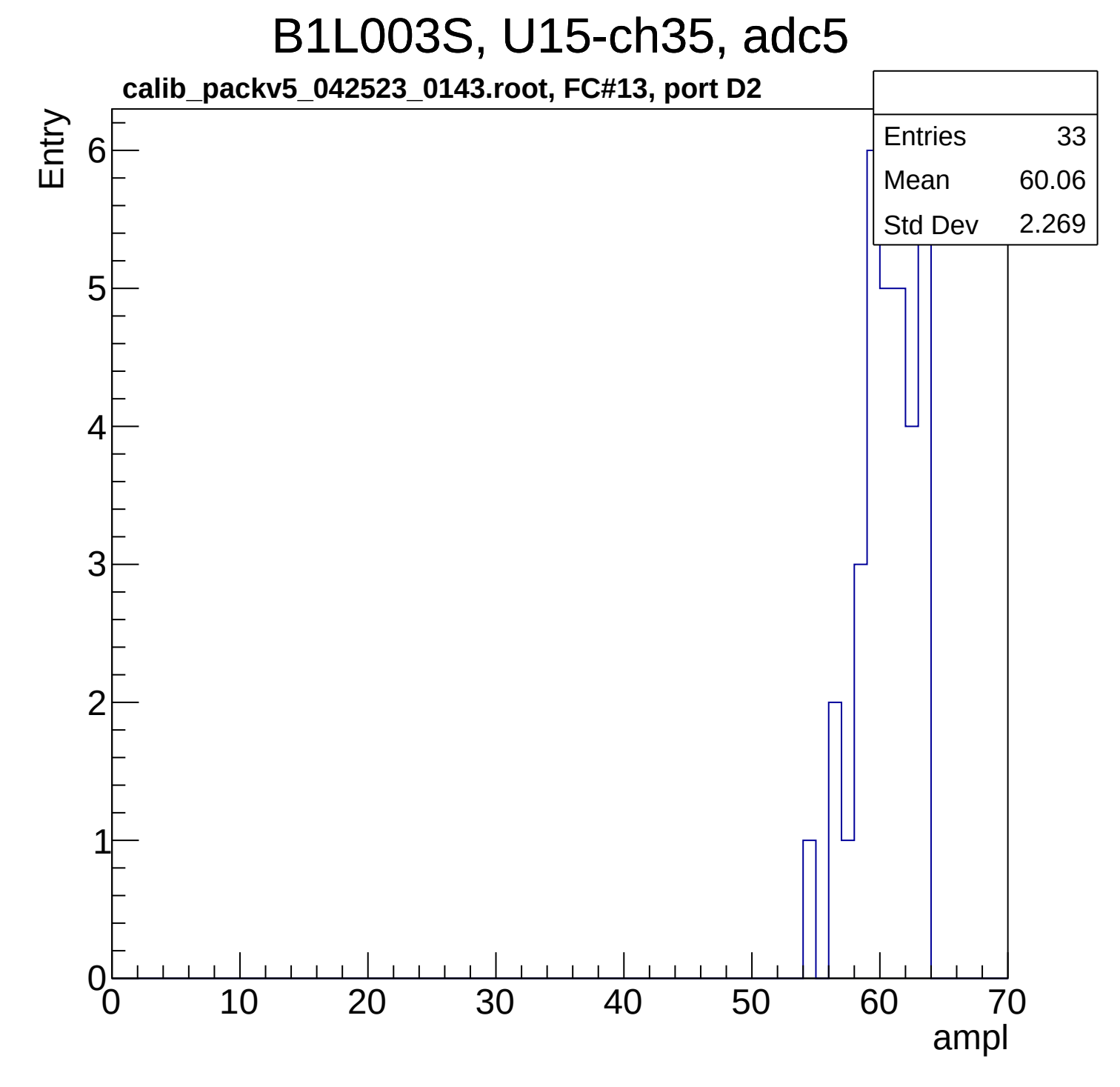
B1L003S, U15-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.06
Std Dev 2.269
Entry
ampl
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3
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0
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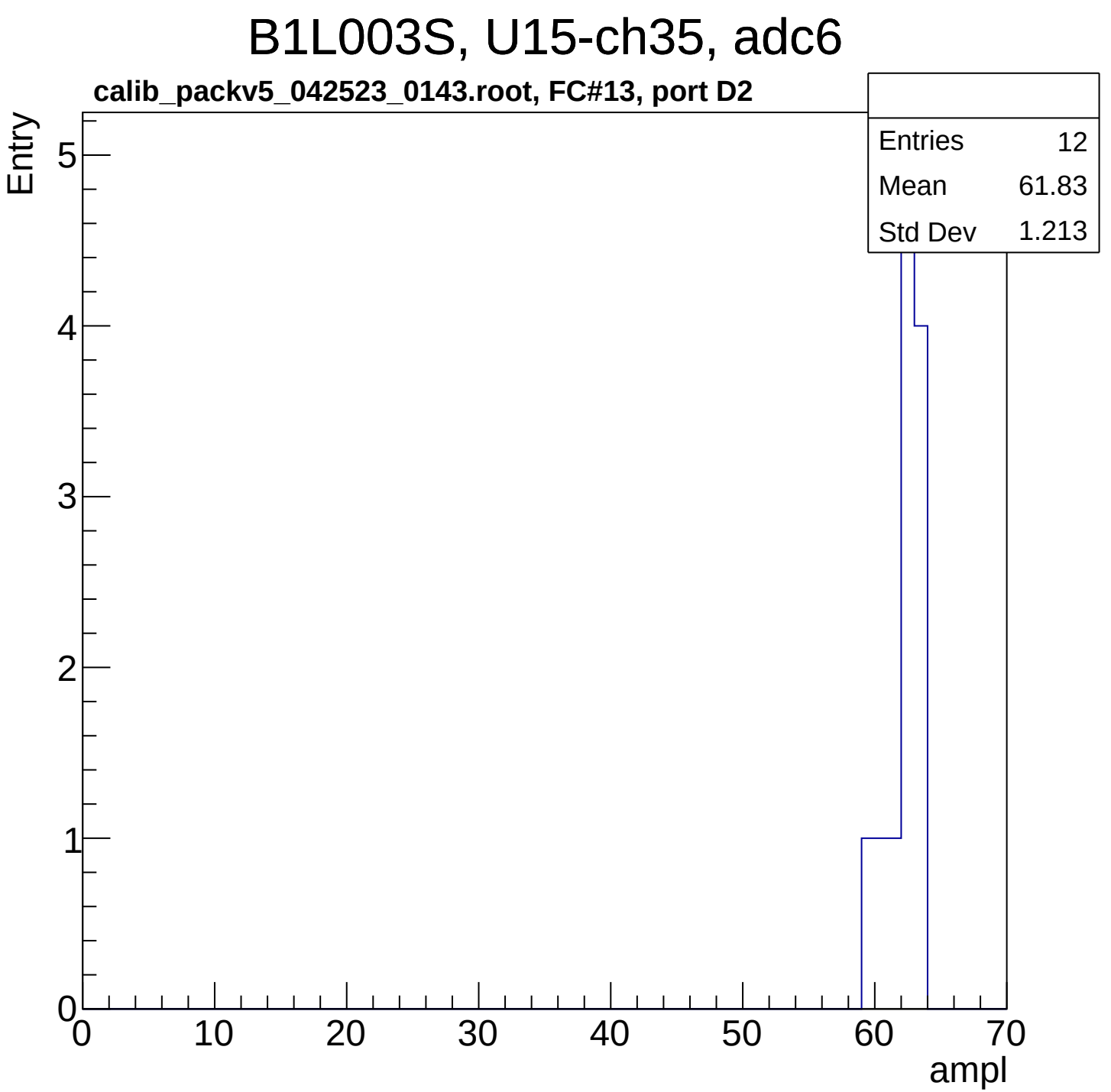
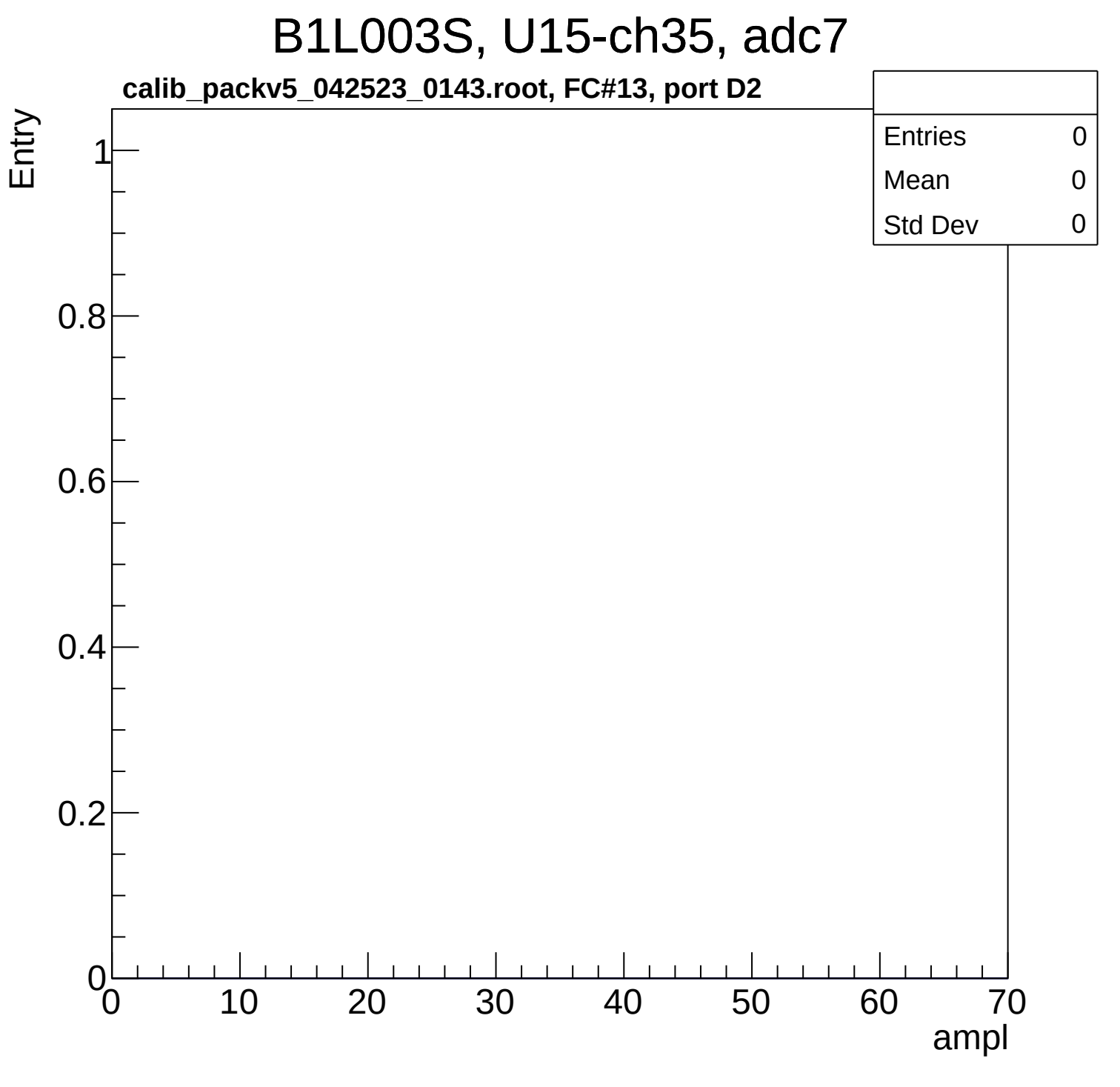

B1L003S, U15-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.83
Std Dev 1.213
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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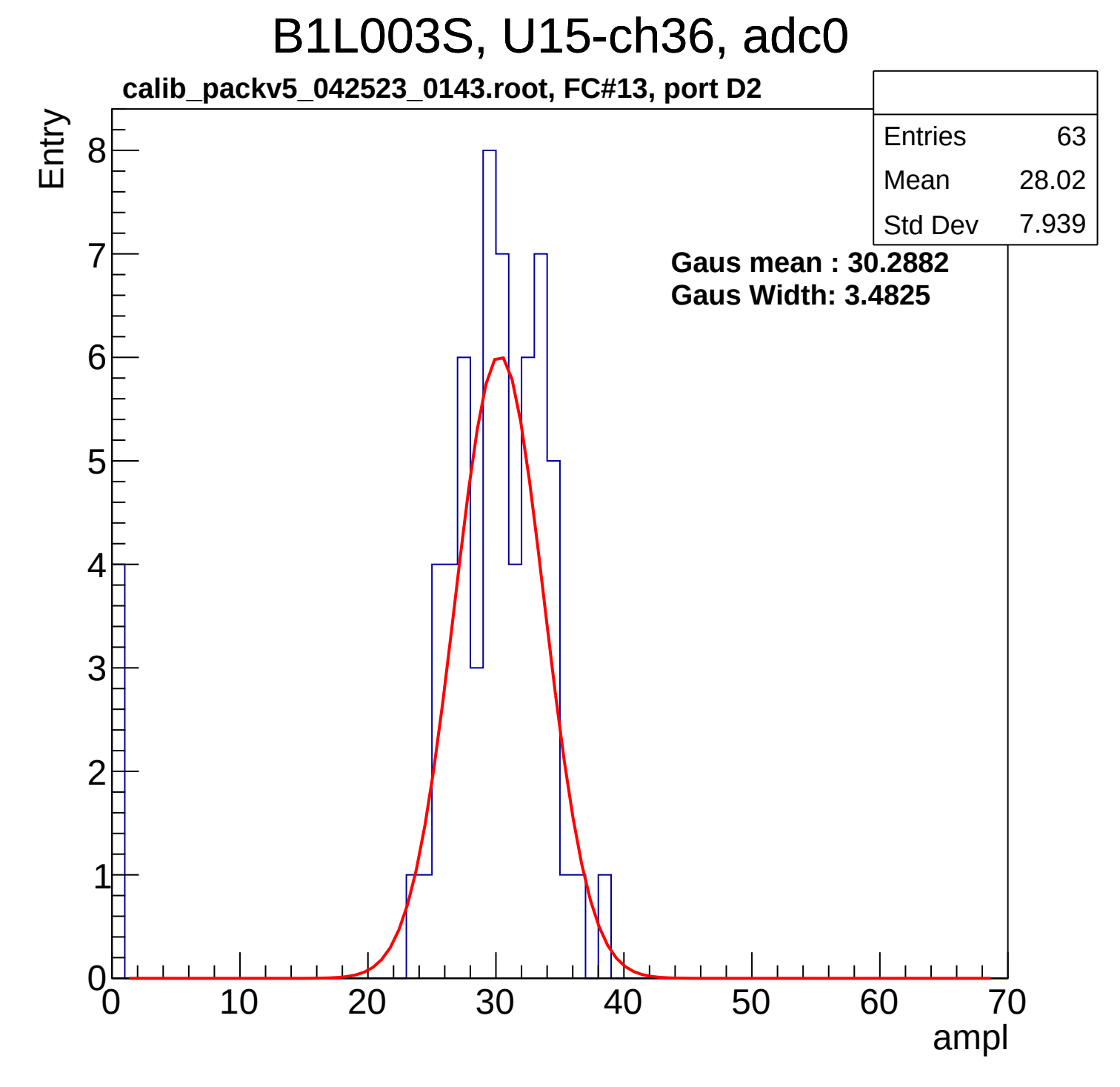
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.02
Std Dev 7.939
Gaus mean : 30.2882
Gaus Width: 3.4825
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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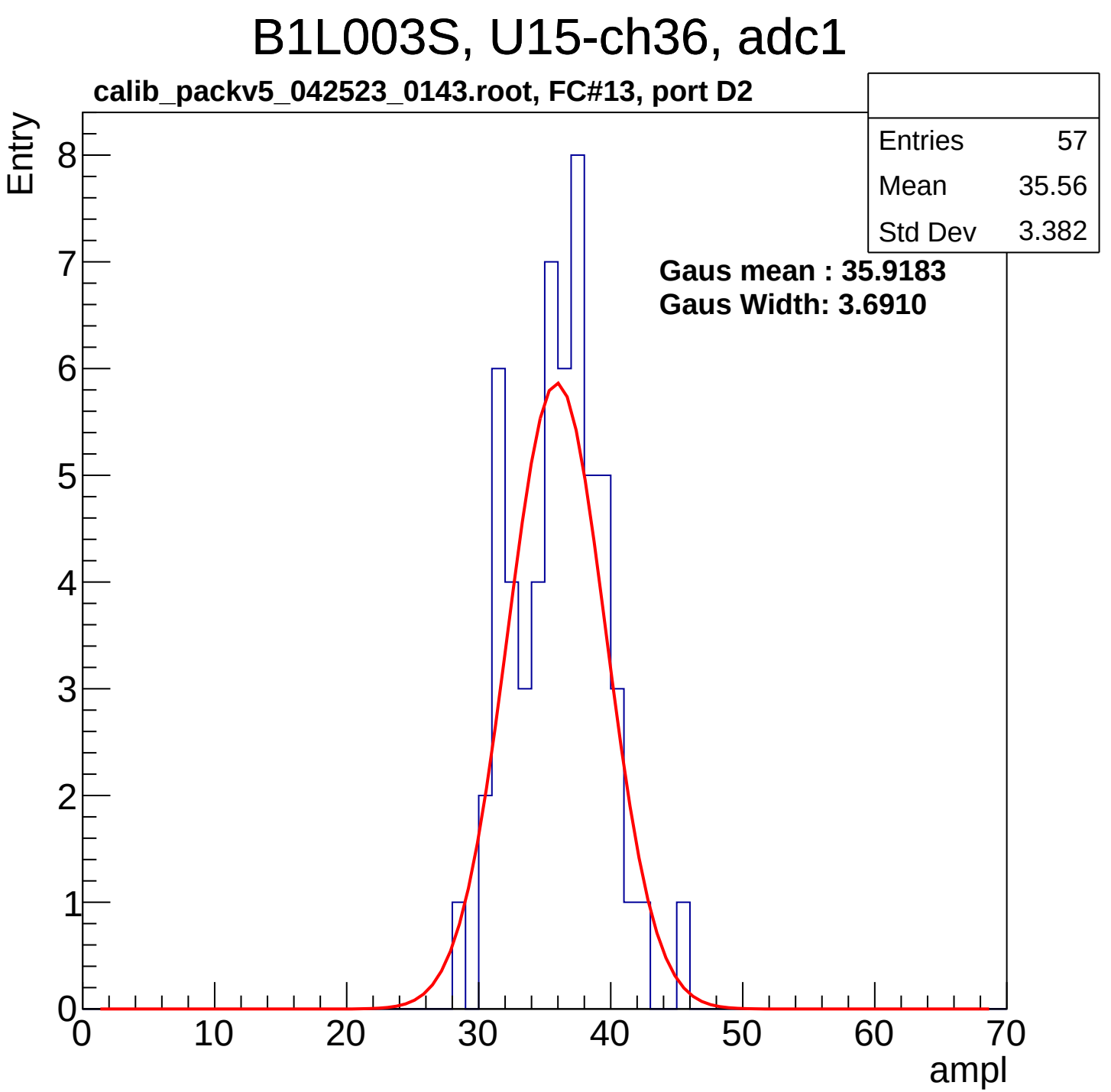
B1L003S, U15-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.56
Std Dev 3.382
Gaus mean : 35.9183
Gaus Width: 3.6910
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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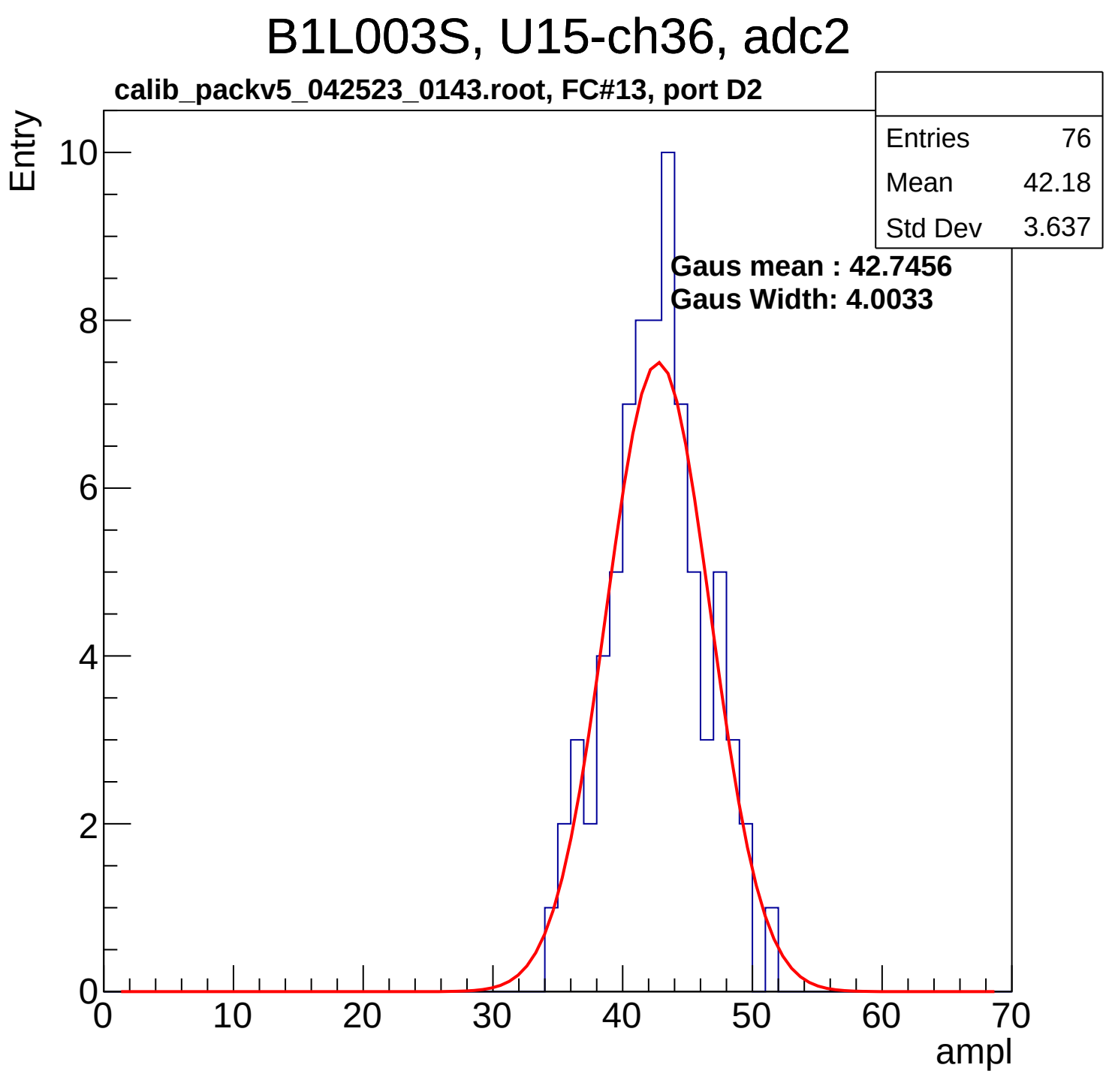
B1L003S, U15-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 42.18
Std Dev 3.637
Gaus mean : 42.7456
Gaus Width: 4.0033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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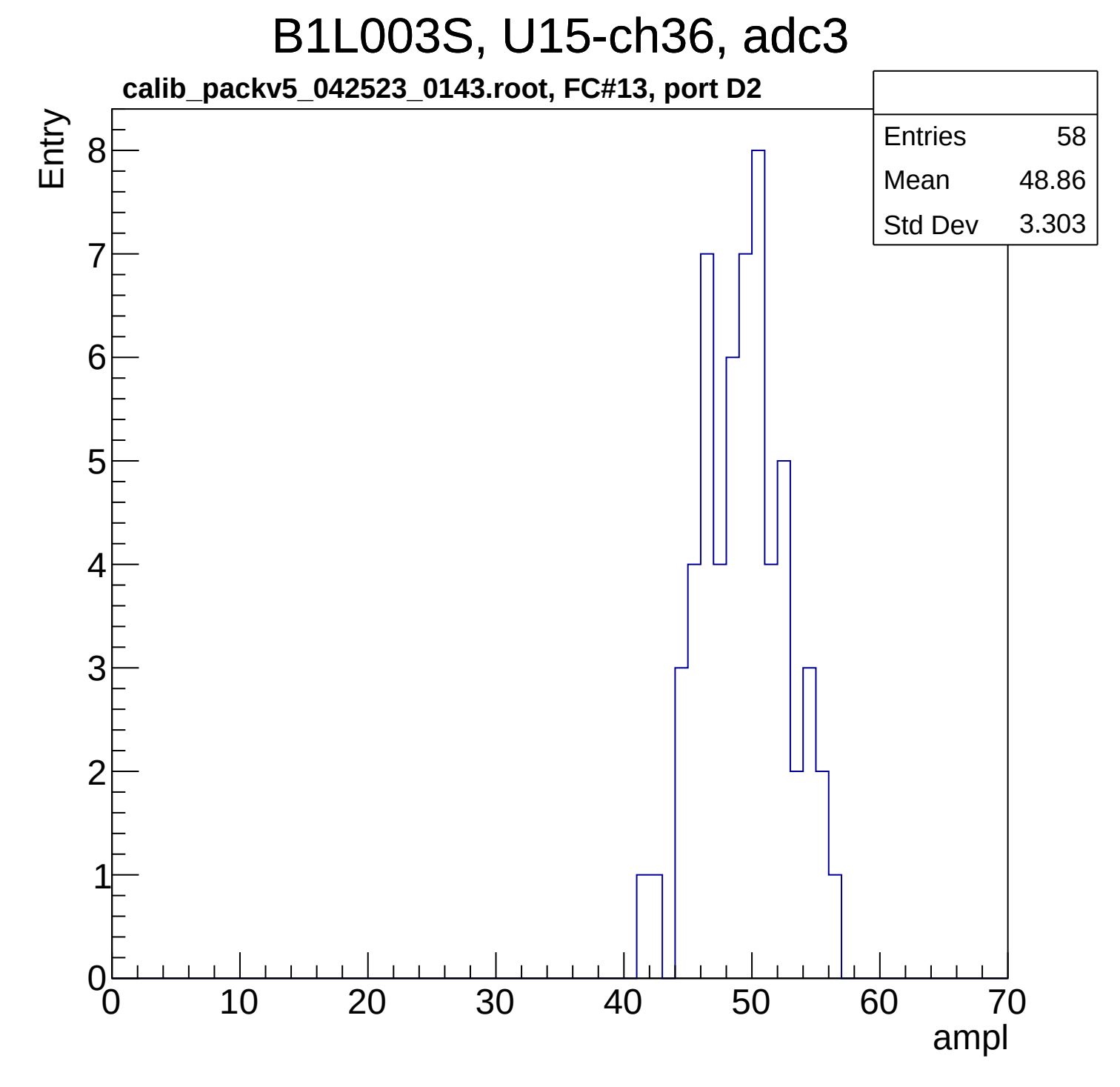
B1L003S, U15-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.86
Std Dev 3.303
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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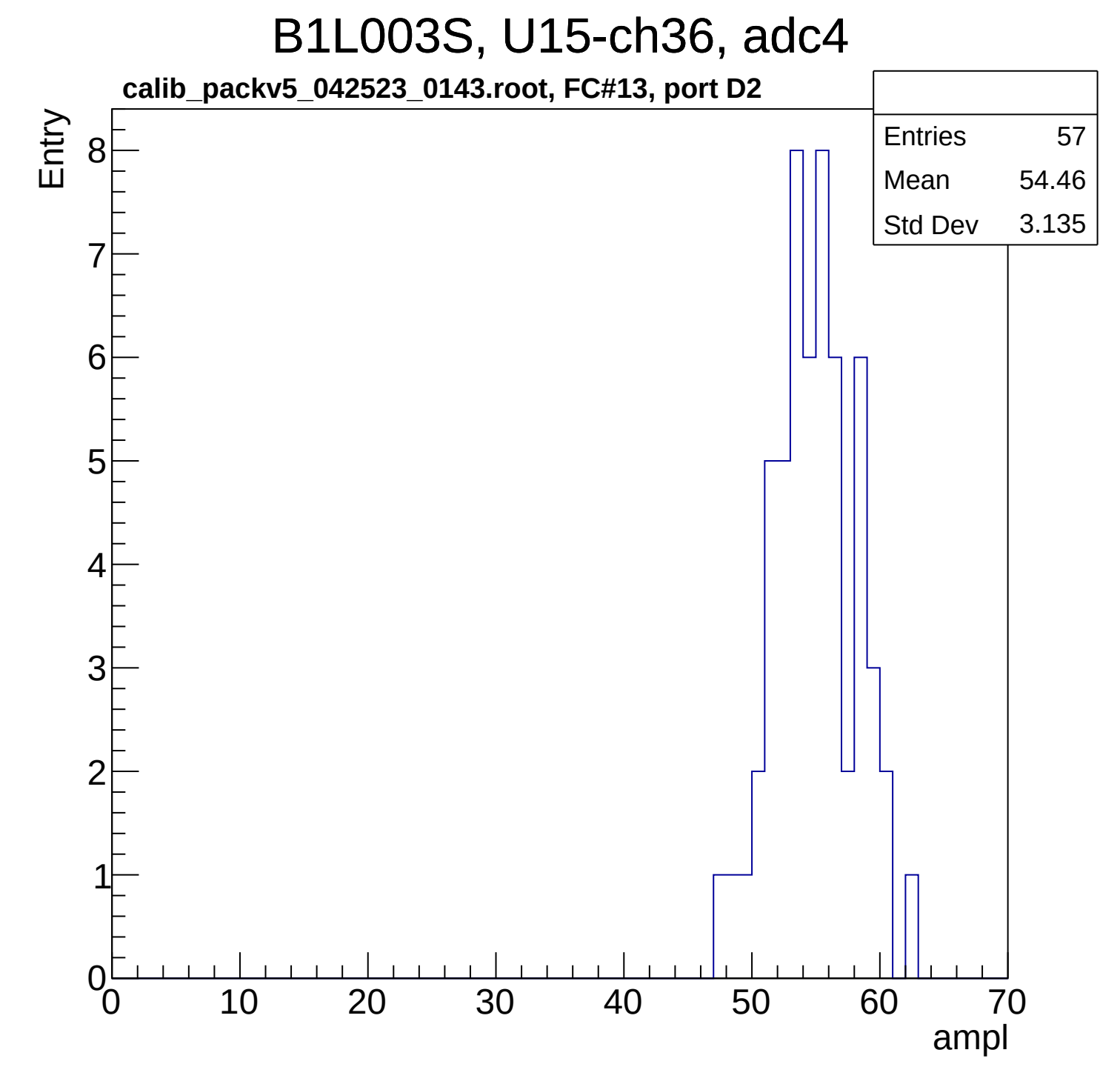
B1L003S, U15-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.46
Std Dev 3.135
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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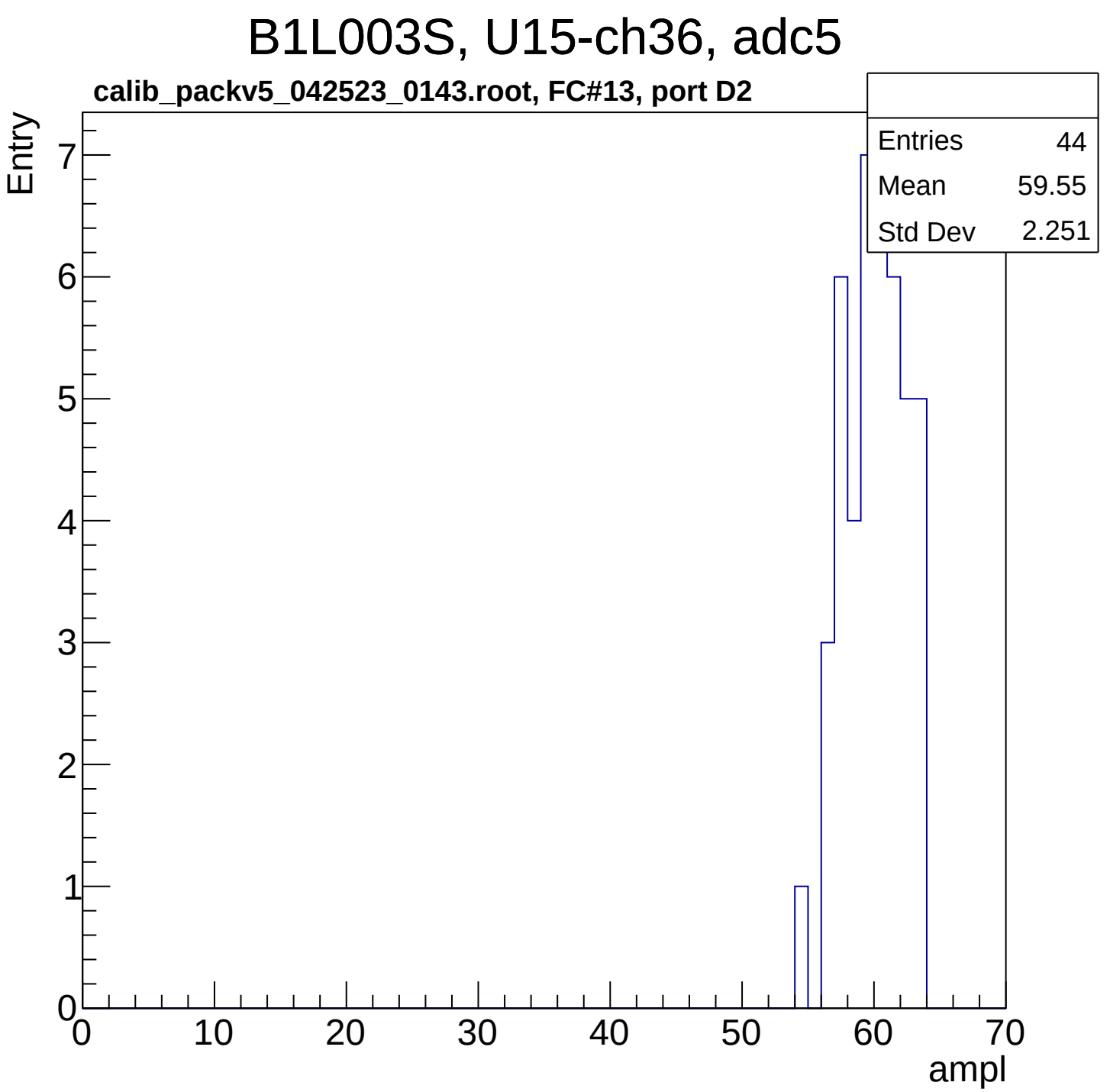
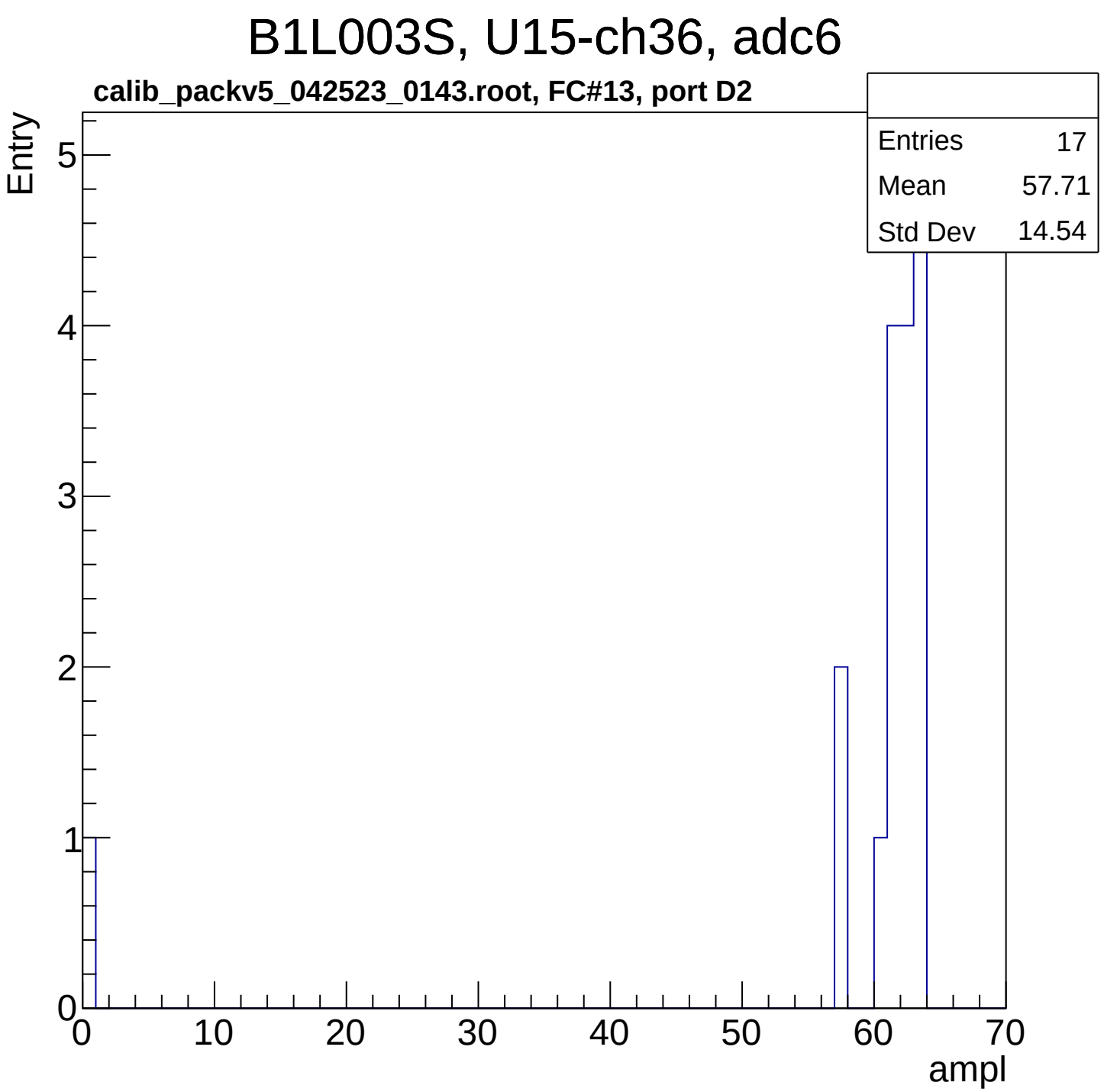
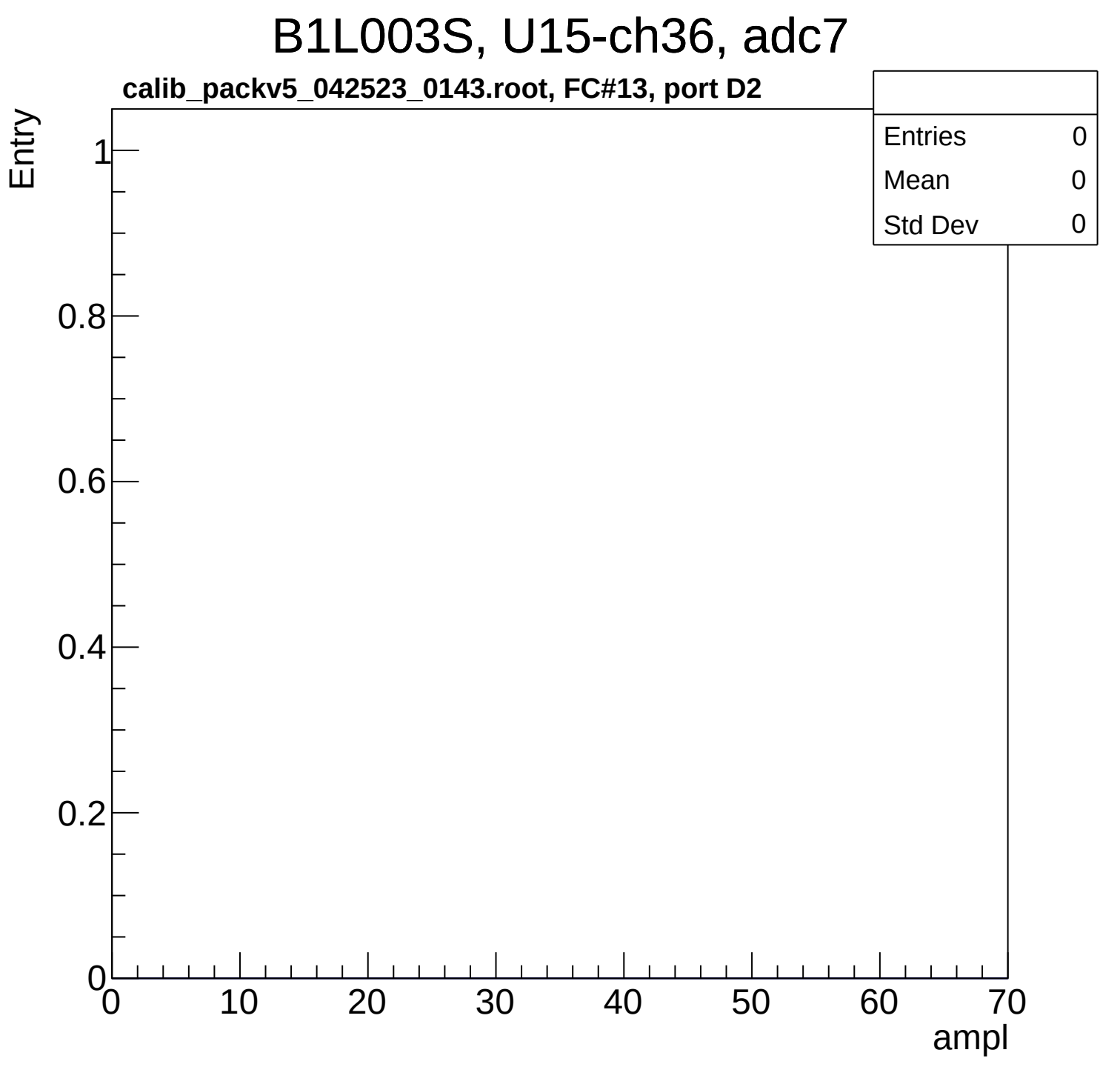

B1L003S, U15-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.55
Std Dev 2.251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.71
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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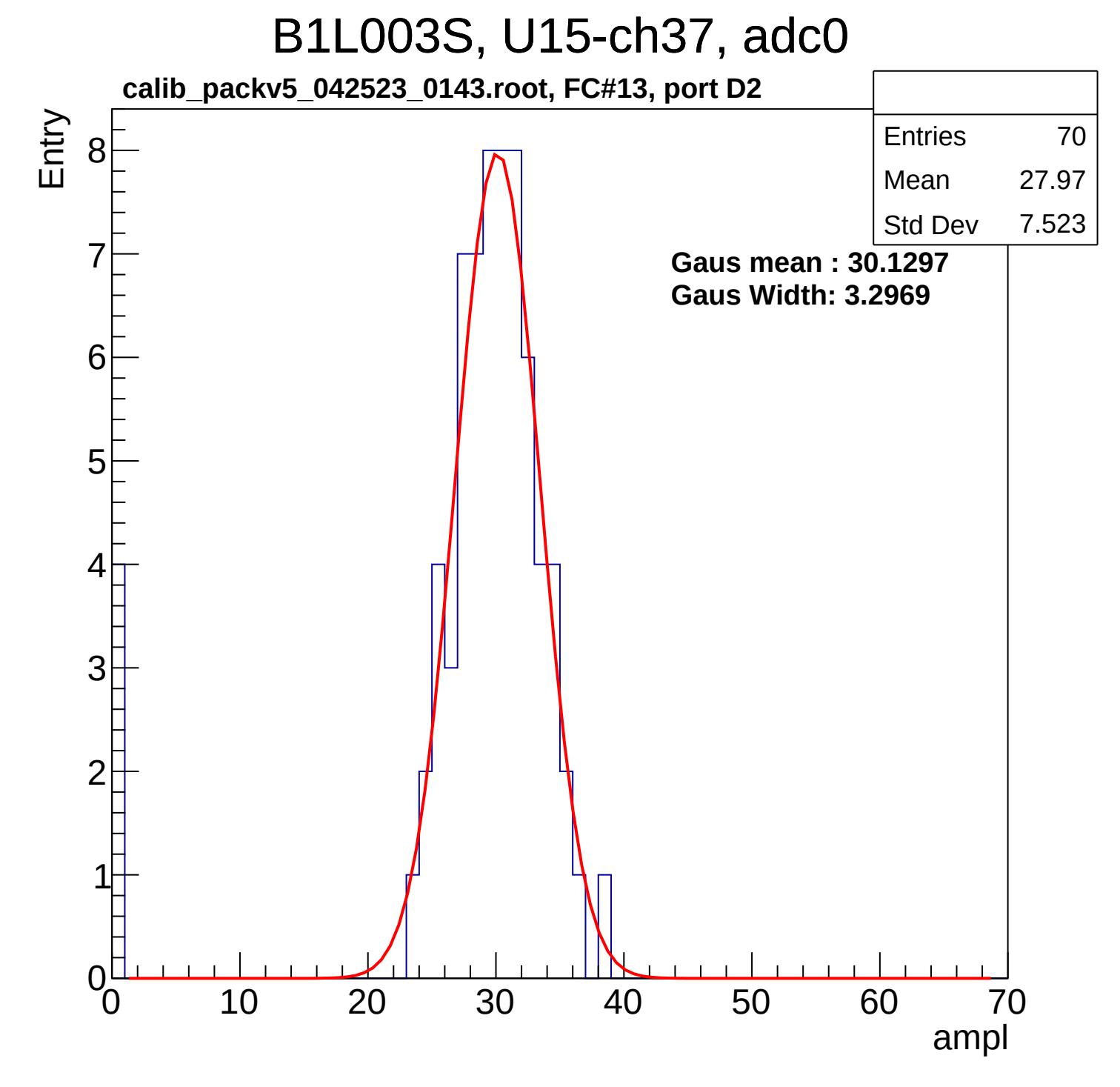
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.97
Std Dev 7.523
Gaus mean : 30.1297
Gaus Width: 3.2969
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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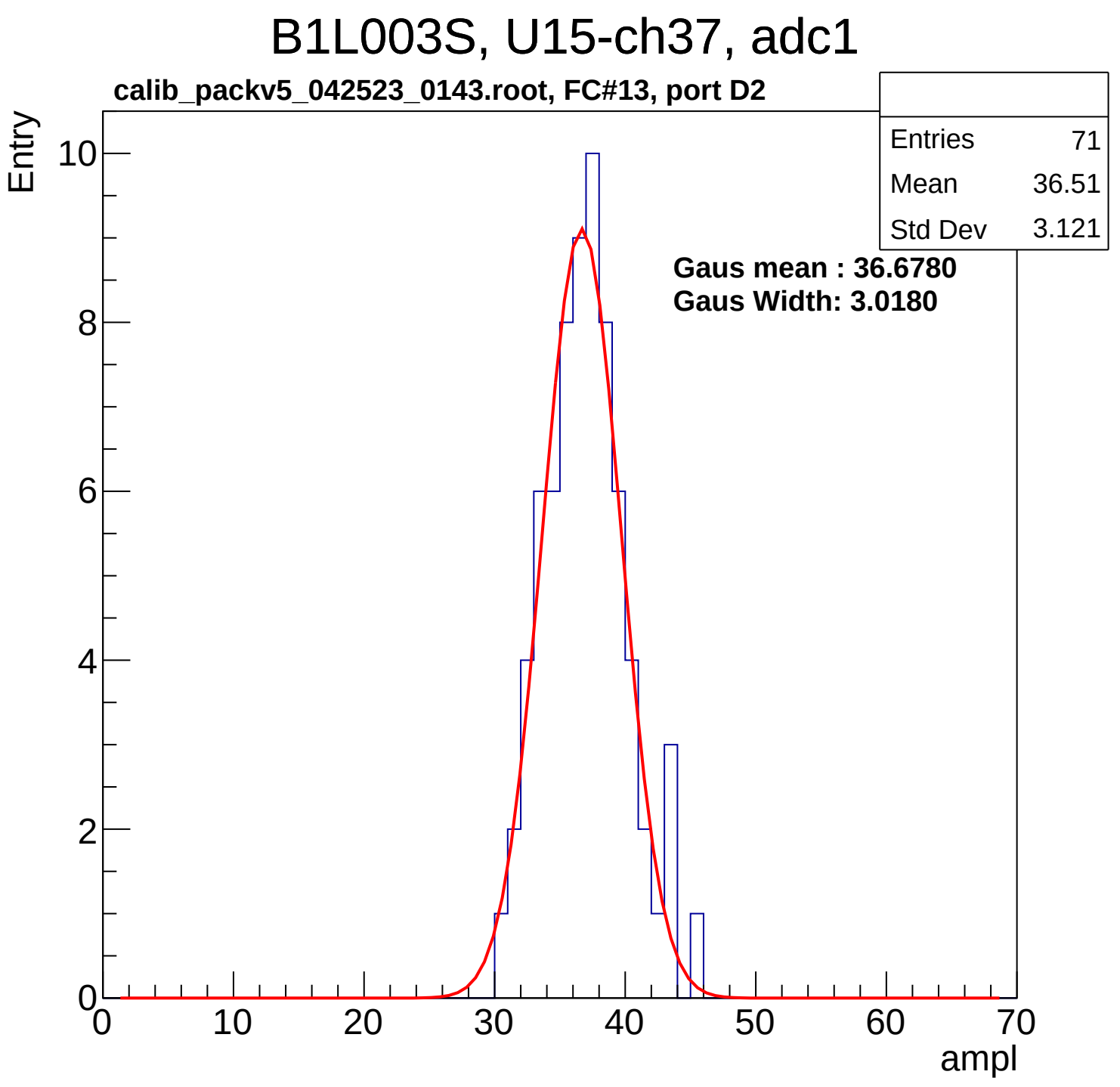
B1L003S, U15-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.51
Std Dev 3.121
Gaus mean : 36.6780
Gaus Width: 3.0180
Entry
ampl
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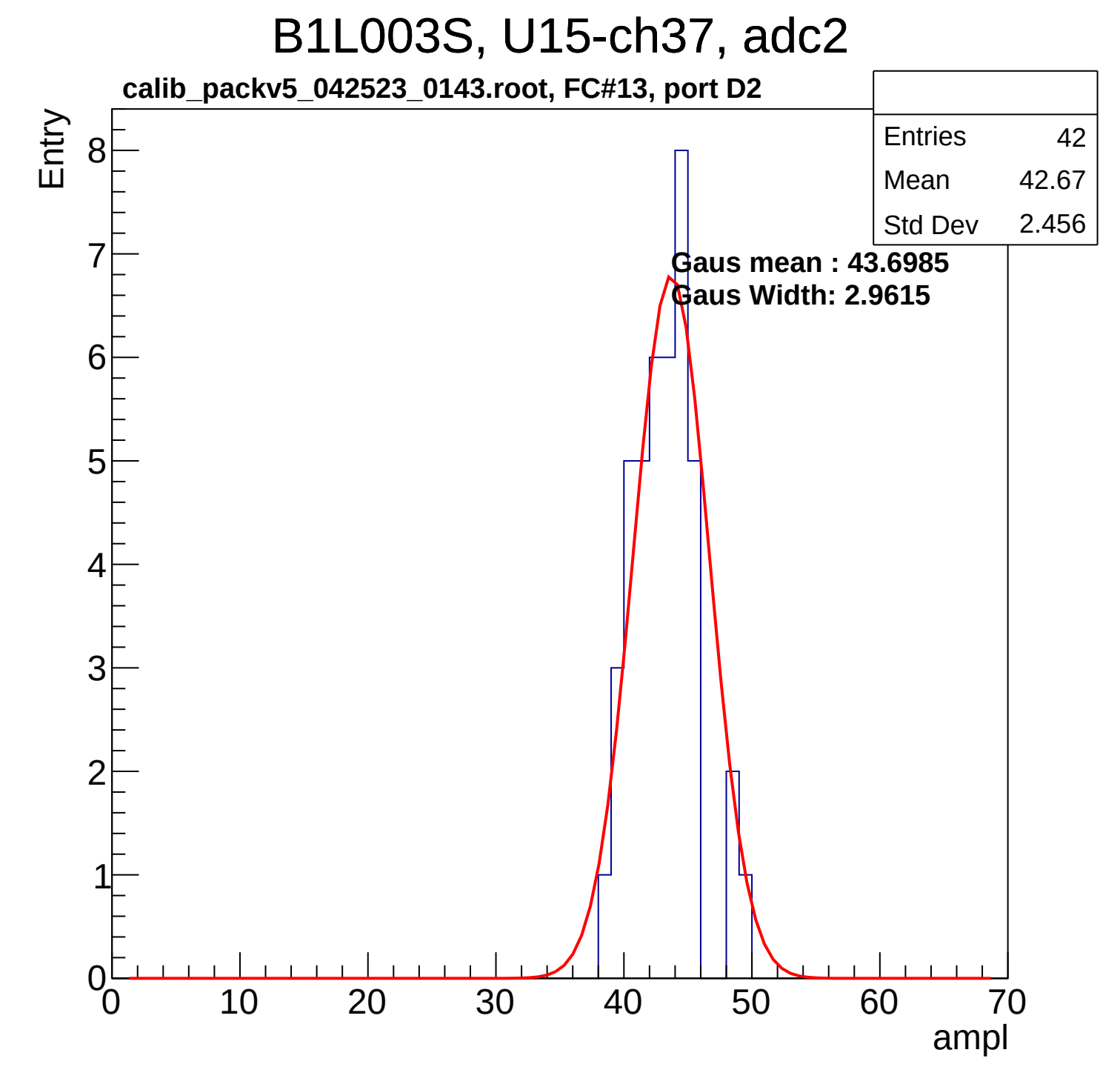
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 42.67
Std Dev 2.456
Gaus mean : 43.6985
Gaus Width: 2.9615
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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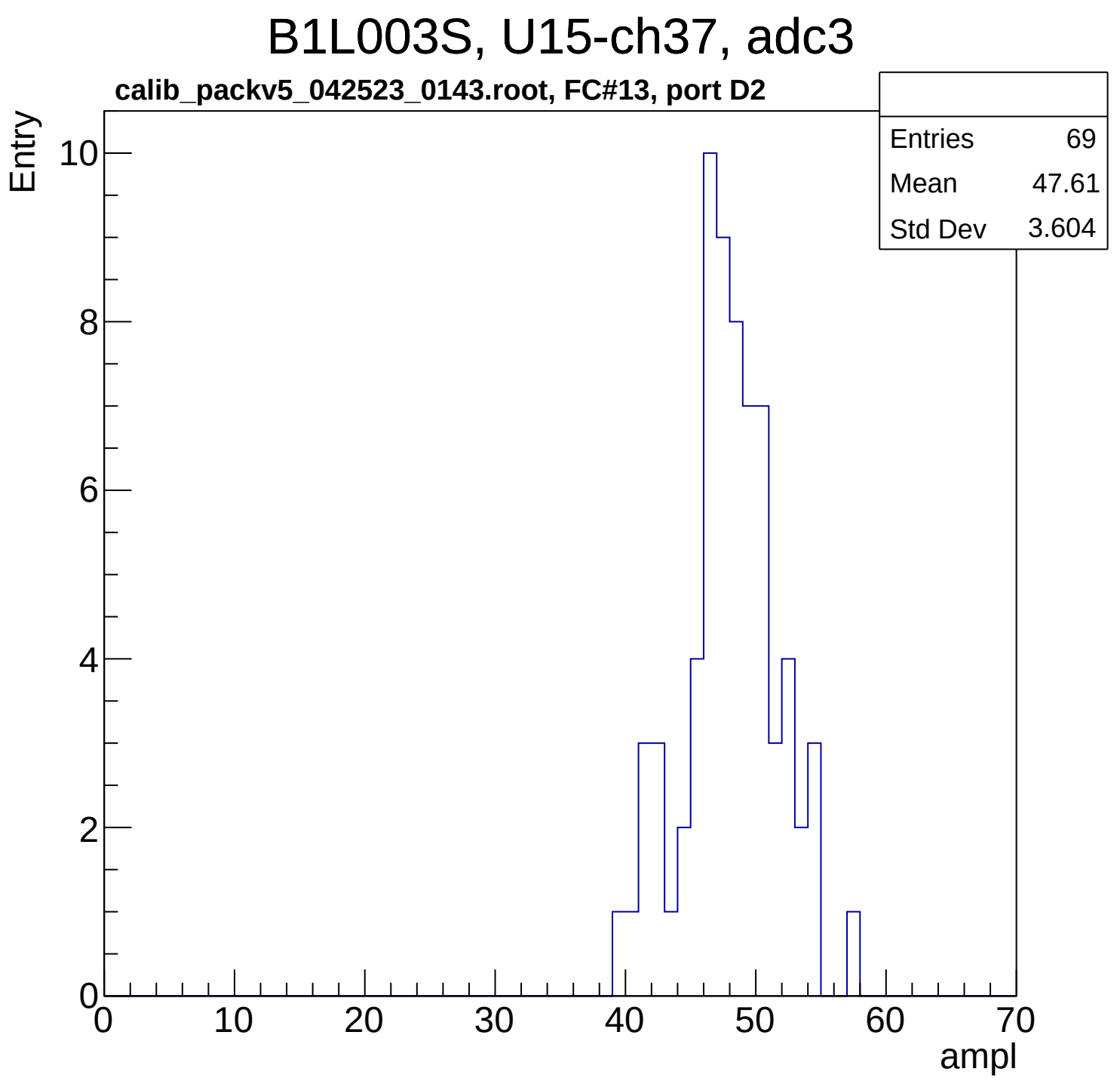
B1L003S, U15-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 47.61
Std Dev 3.604
Entry
ampl
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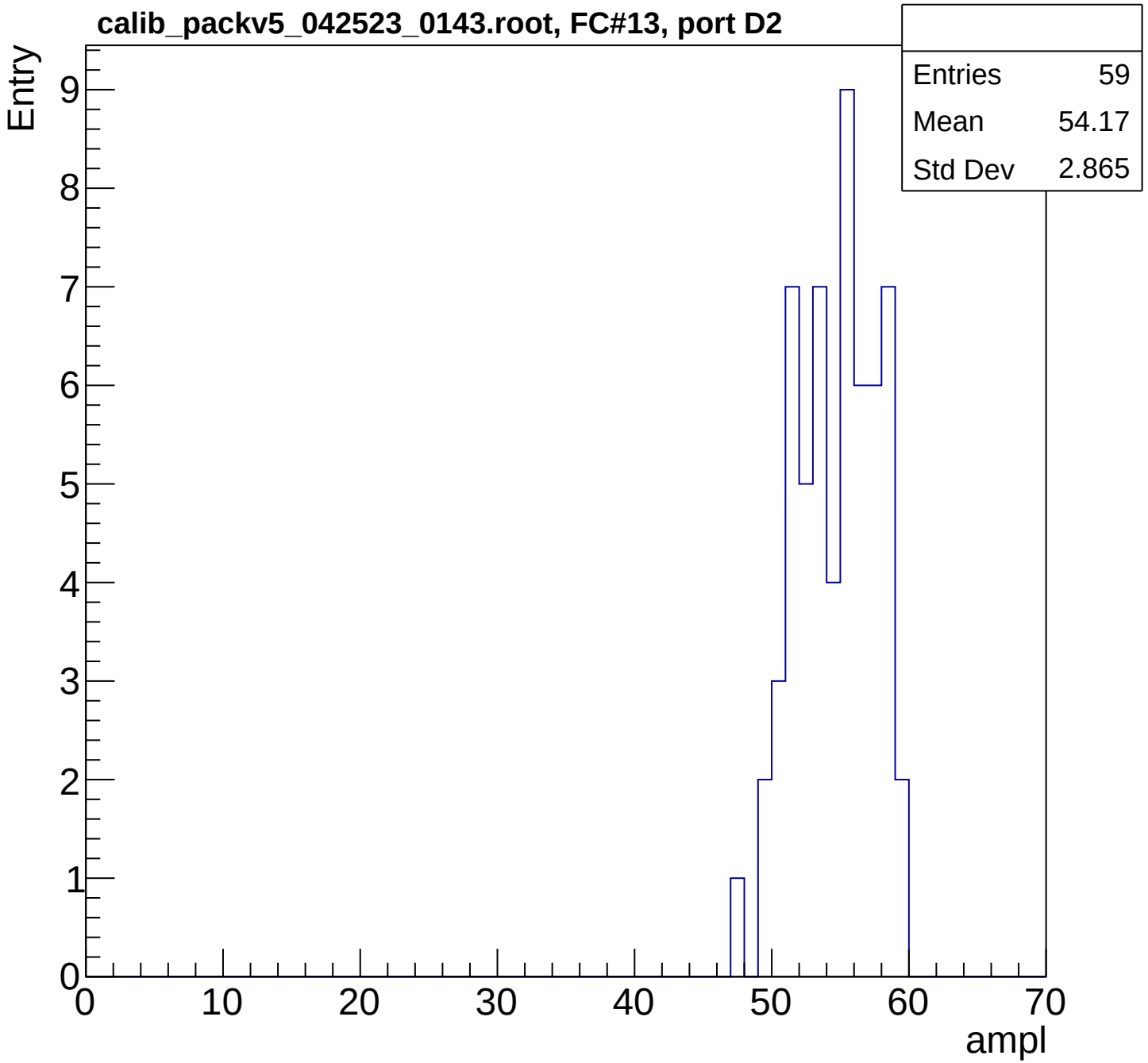
B1L003S, U15-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.17
Std Dev 2.865
Entry
ampl
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3
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6
7
8
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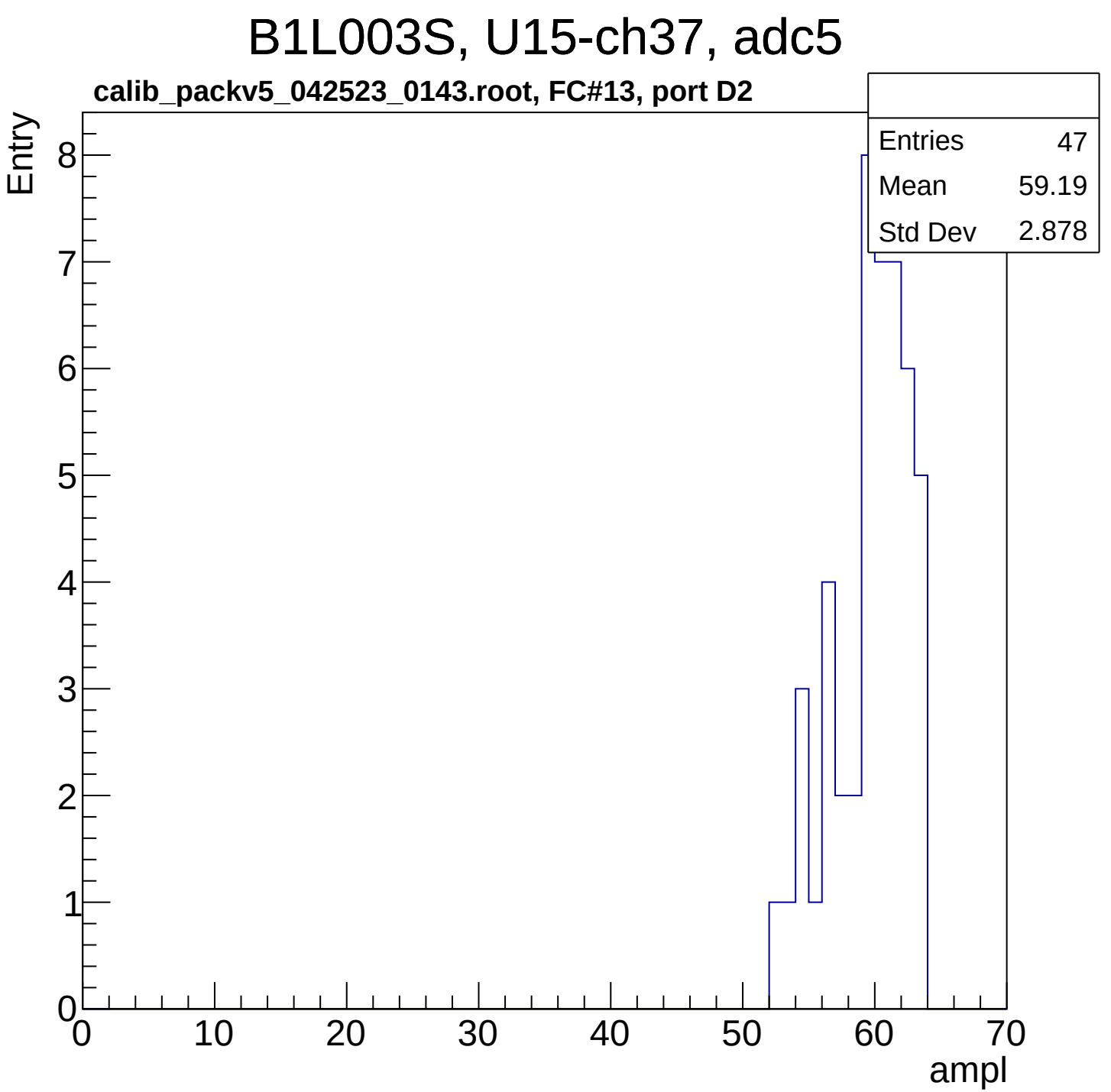
B1L003S, U15-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.19
Std Dev 2.878
Entry
ampl
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4
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0
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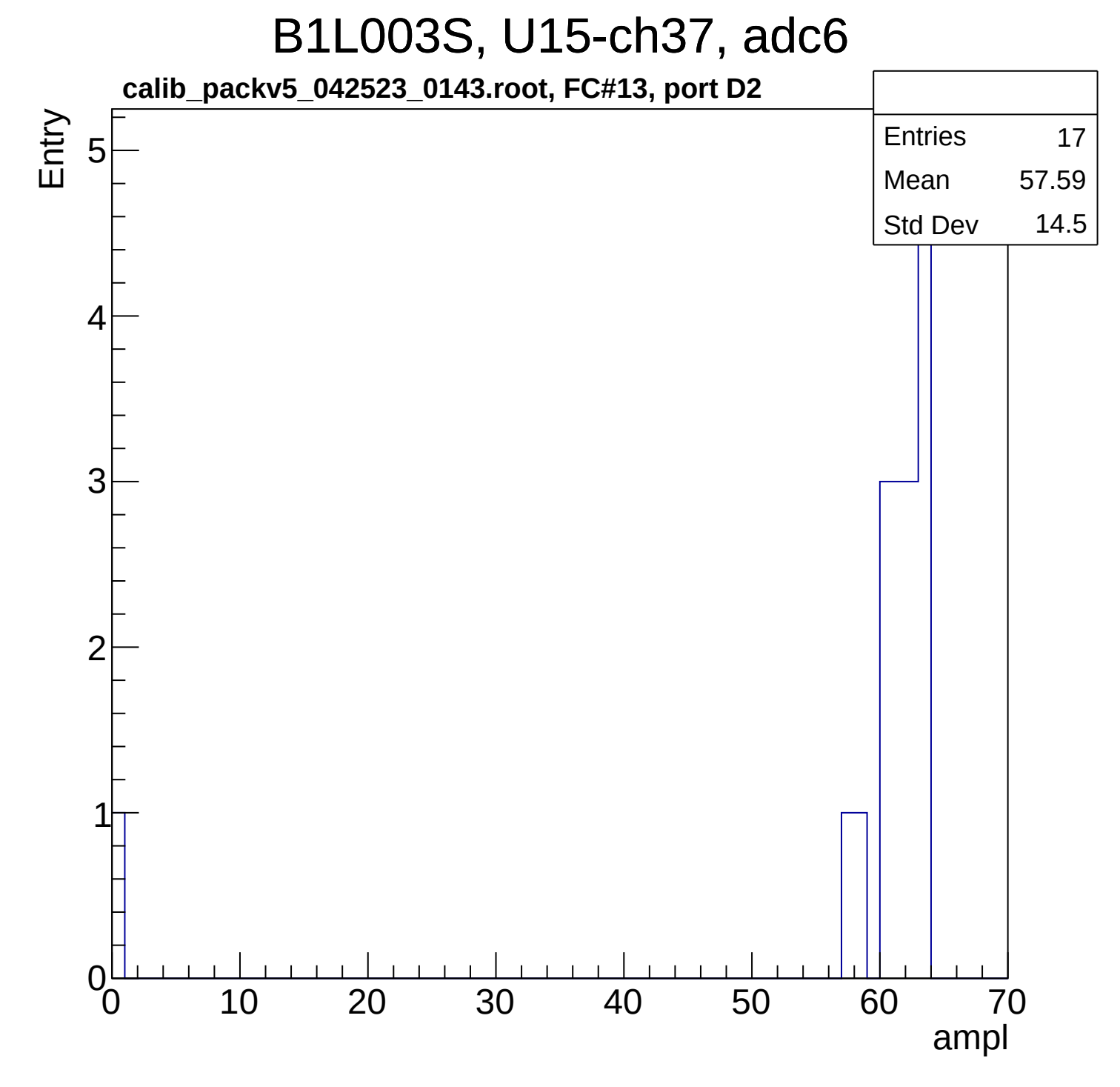
B1L003S, U15-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.59
Std Dev 14.5
Entry
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3
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5
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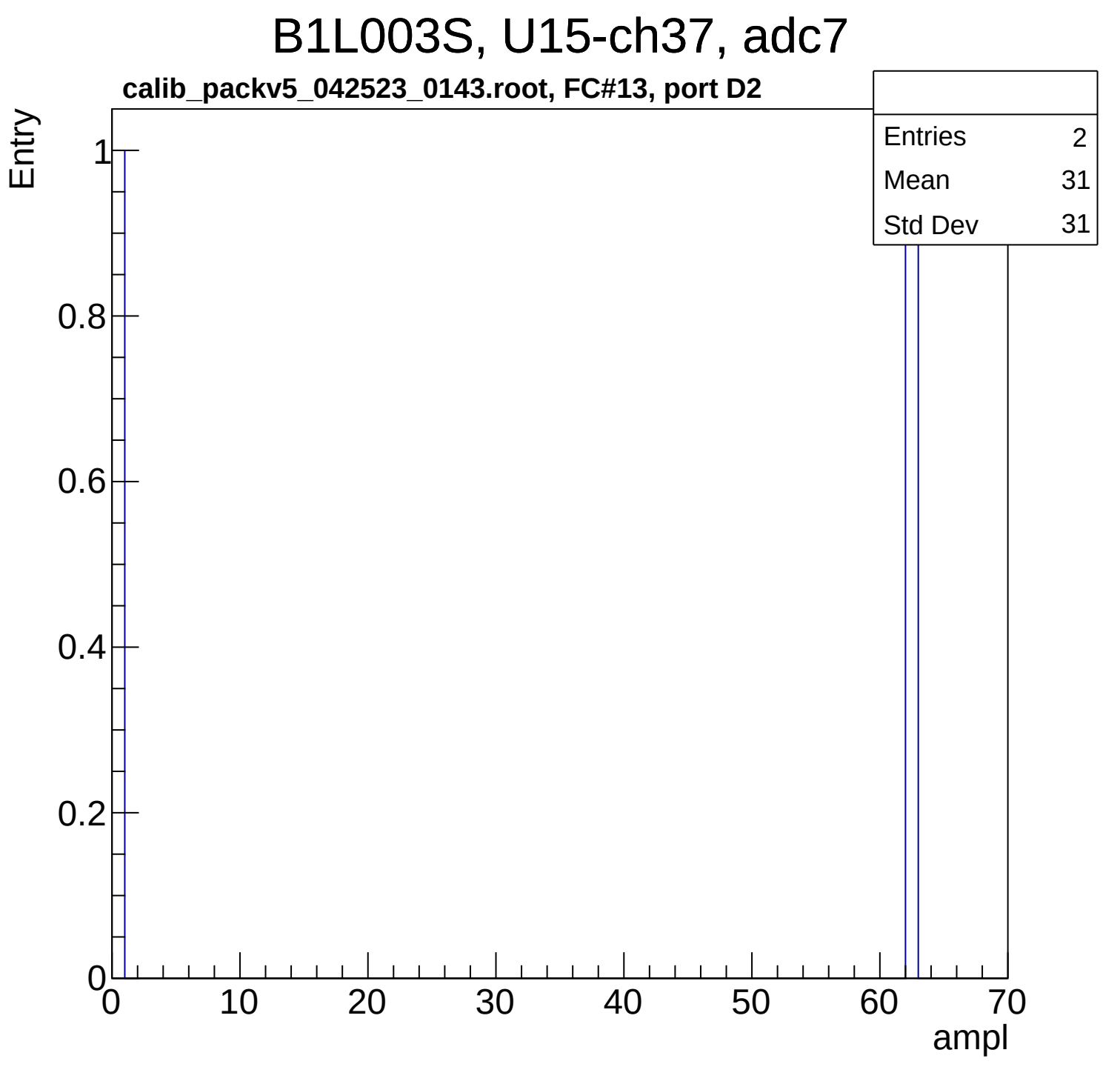
50
60
70
ampl


B1L003S, U15-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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0.6
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50
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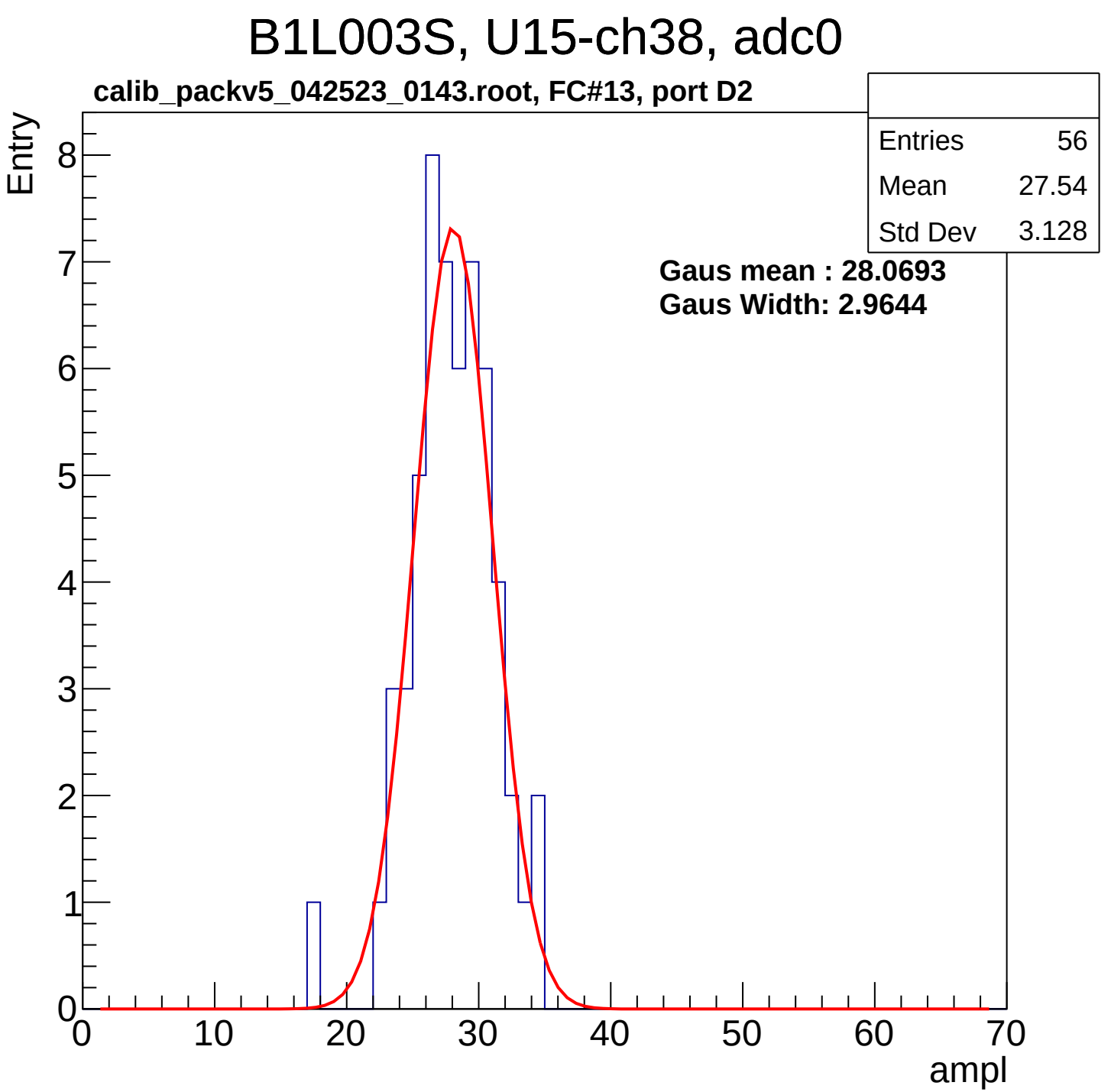
70
ampl


B1L003S, U15-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 27.54
Std Dev 3.128
Gaus mean : 28.0693
Gaus Width: 2.9644
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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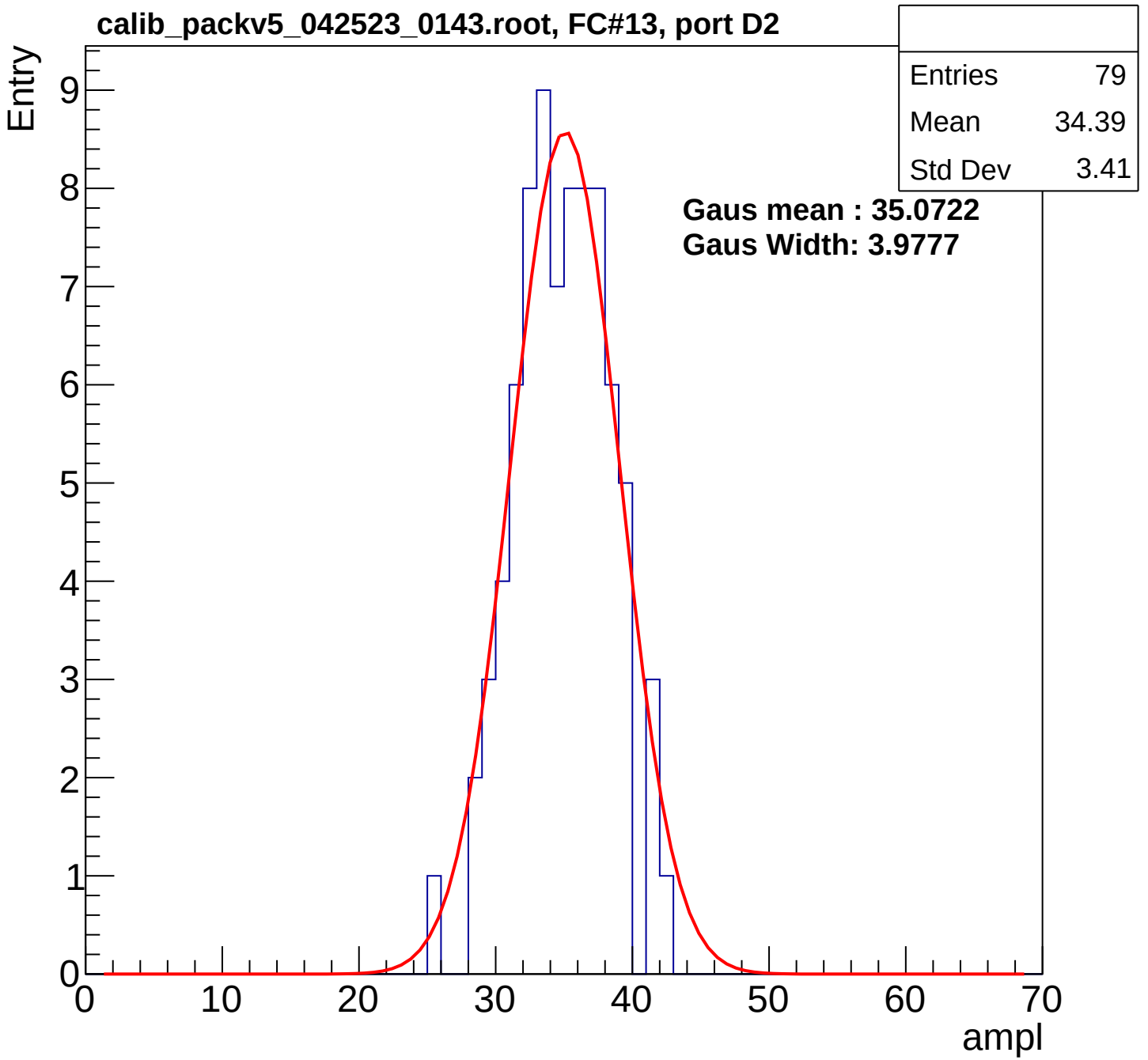
B1L003S, U15-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 34.39
Std Dev 3.41
Gaus mean : 35.0722
Gaus Width: 3.9777
Entry
ampl
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20
30
40
50
60
70
1
2
3
4
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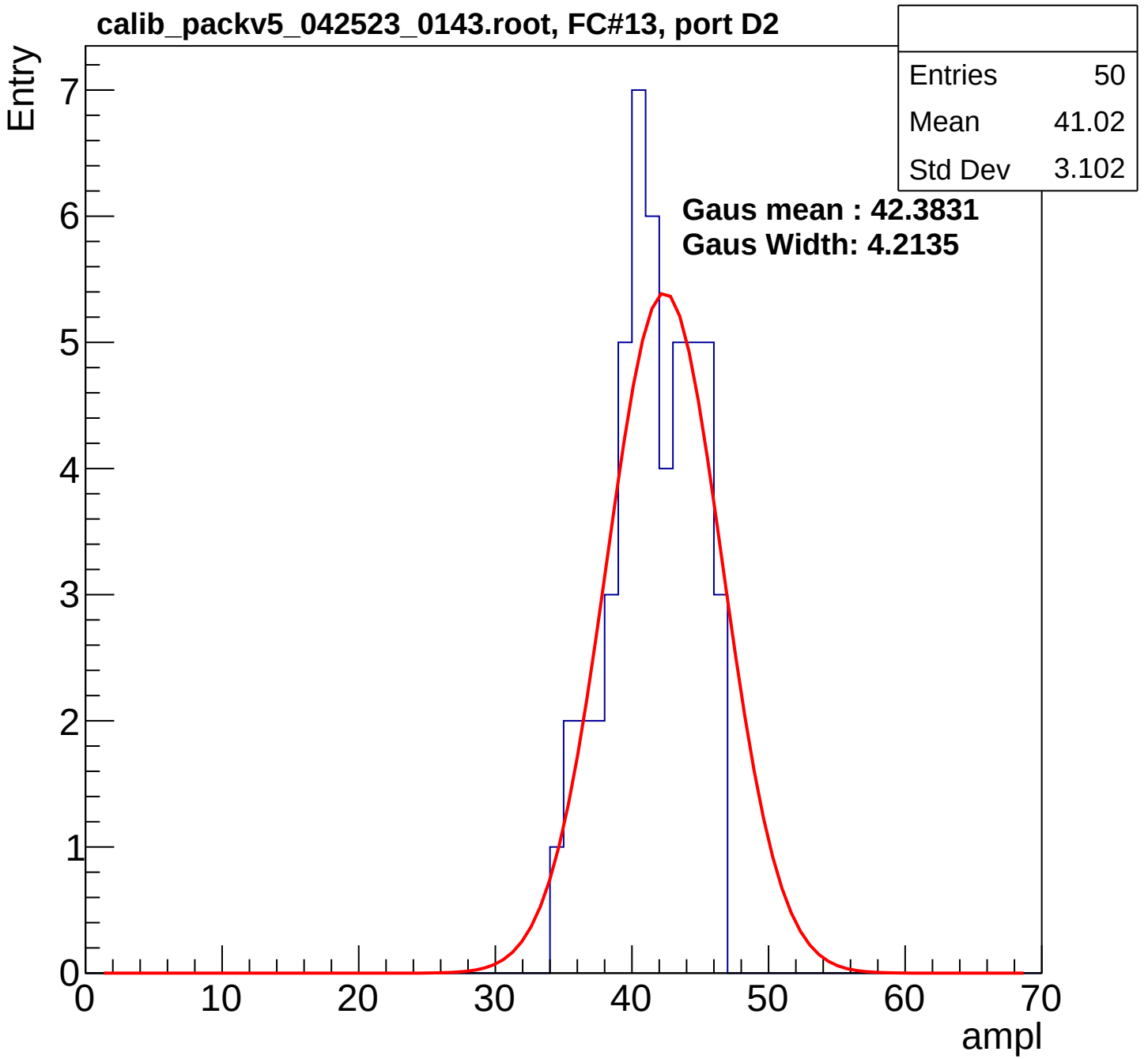
6
7
8
9


B1L003S, U15-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.02
Std Dev 3.102
Gaus mean : 42.3831
Gaus Width: 4.2135
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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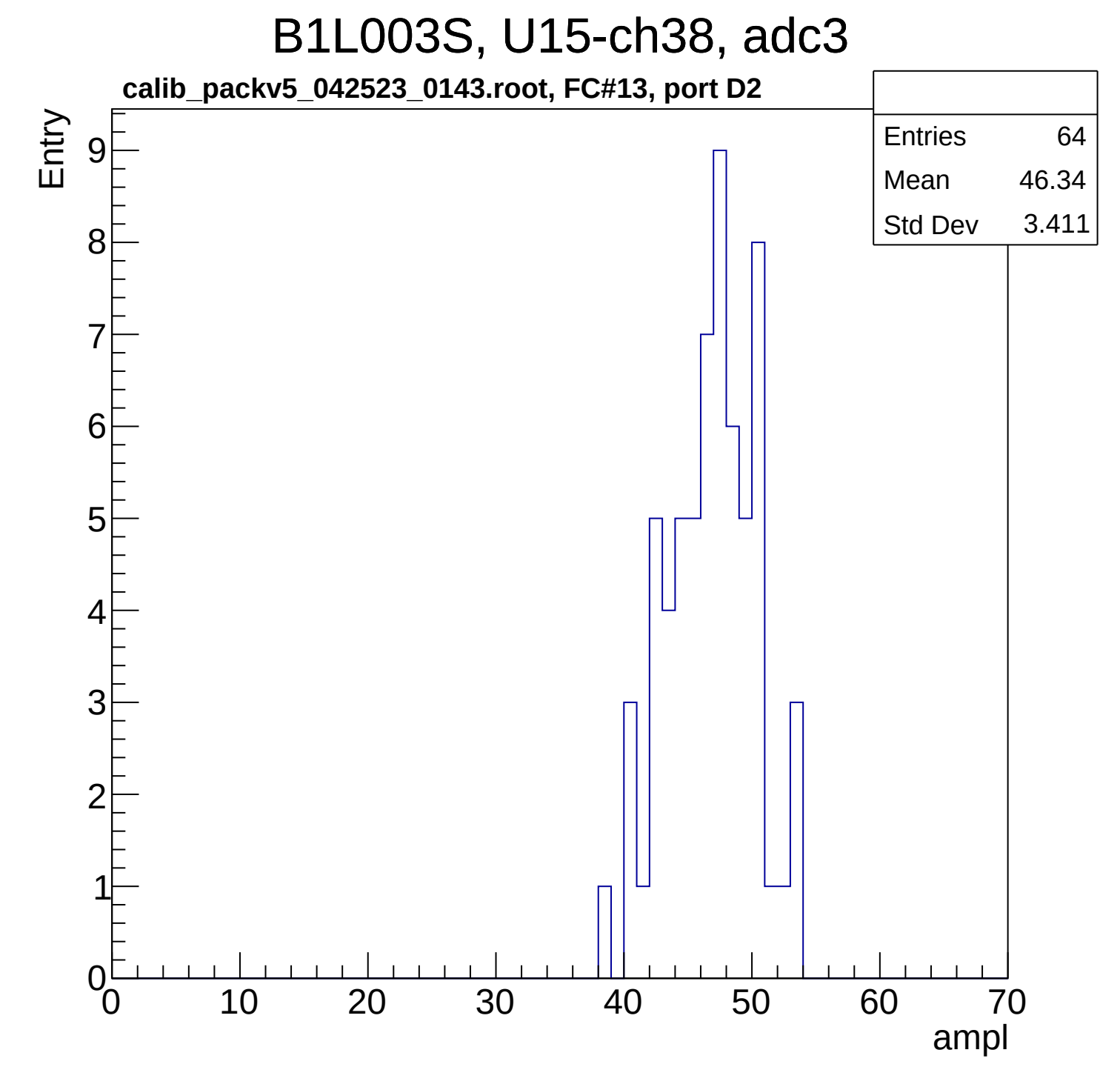
B1L003S, U15-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 46.34
Std Dev 3.411
Entry
ampl
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3
4
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6
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8
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0
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60
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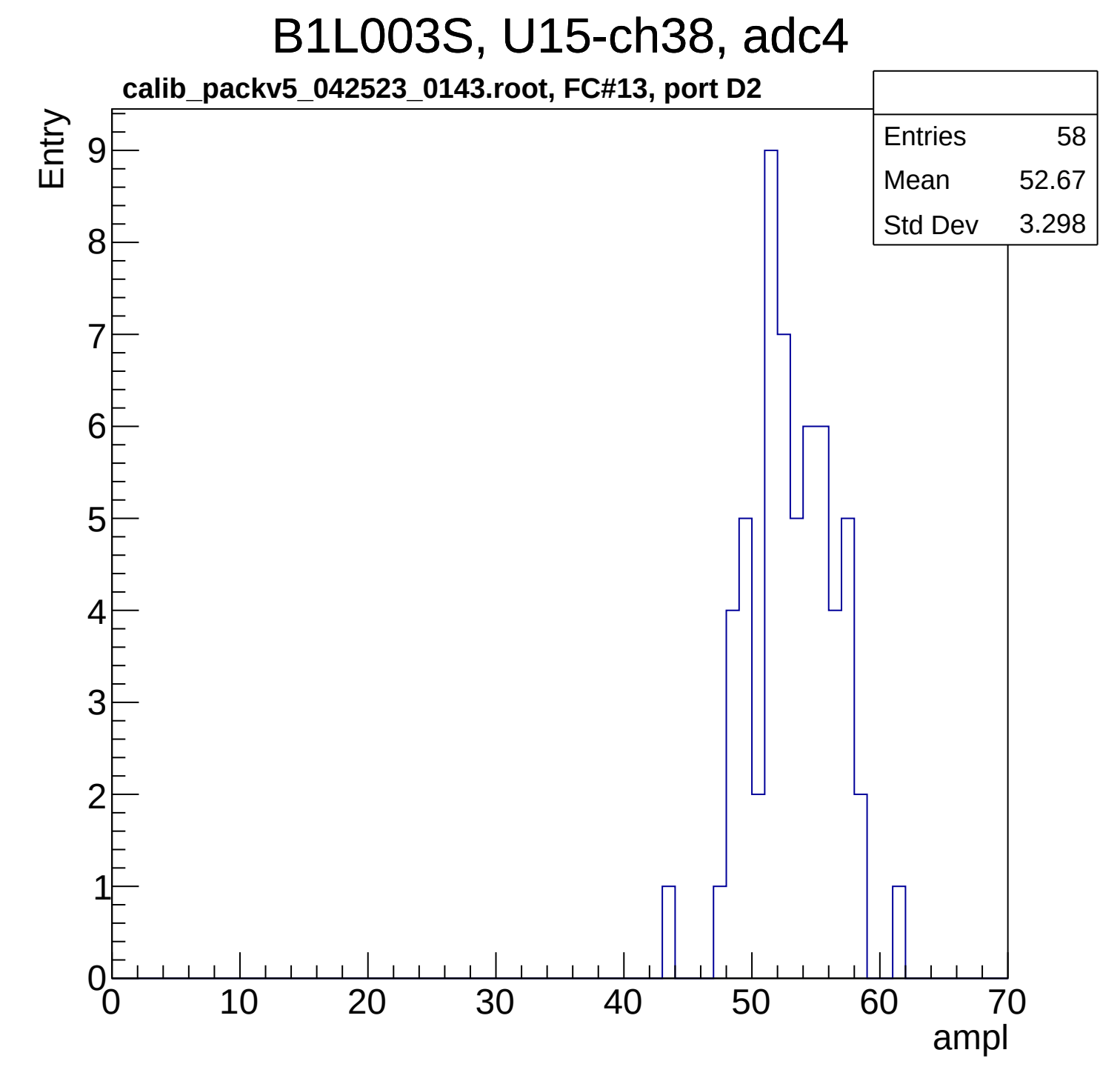
B1L003S, U15-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 52.67
Std Dev 3.298
Entry
ampl
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7
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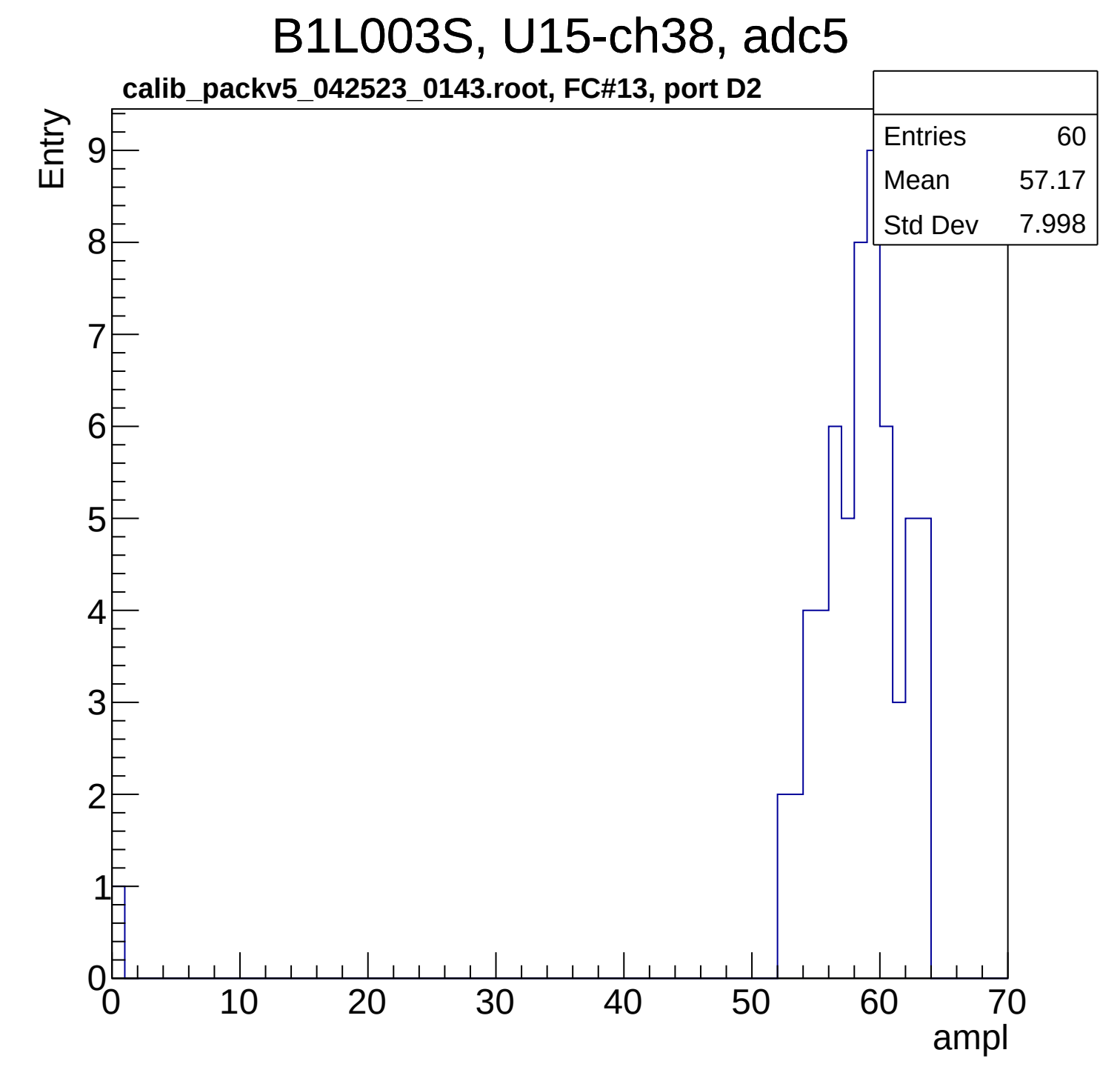
B1L003S, U15-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.17
Std Dev 7.998
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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60
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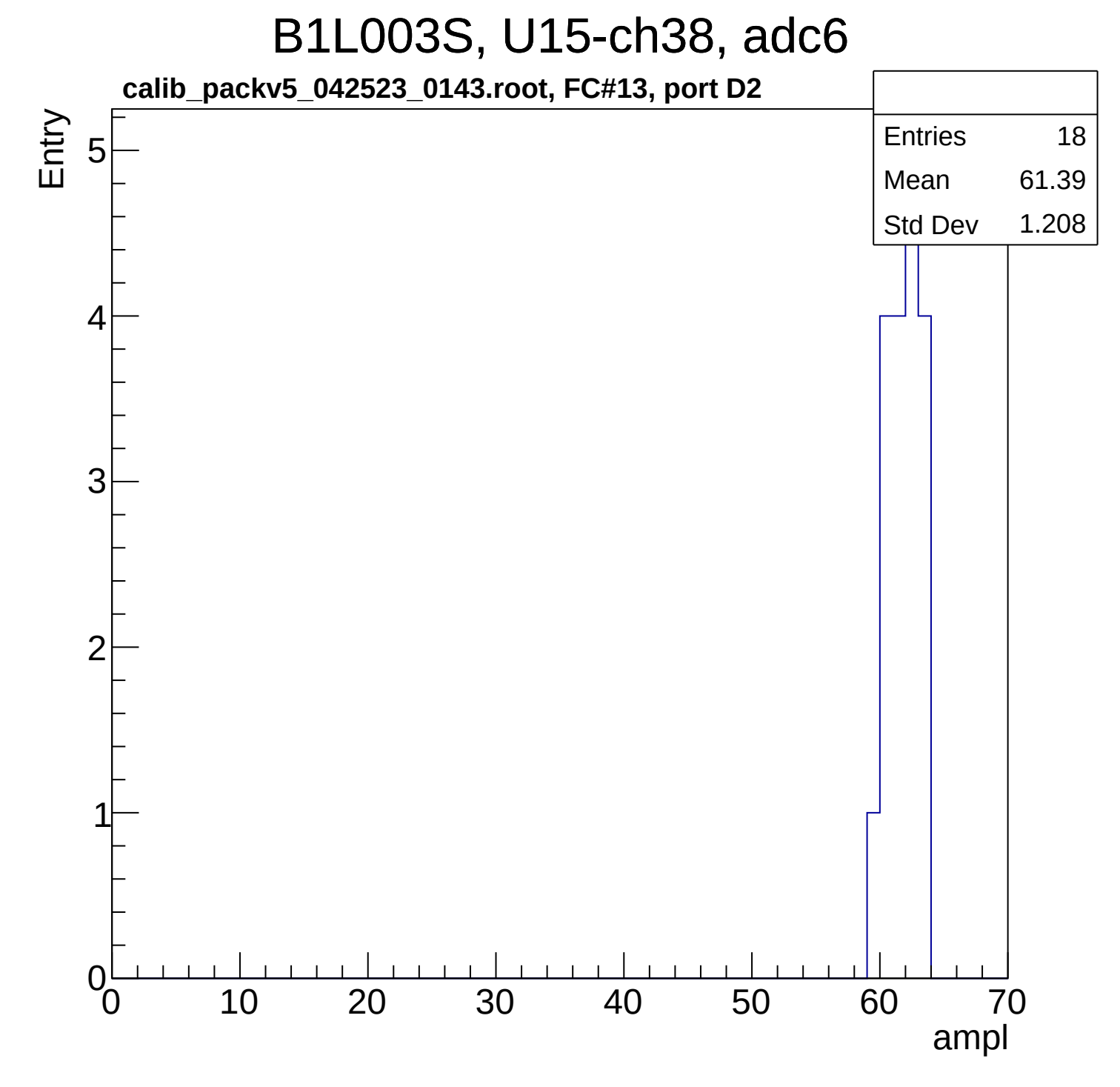
B1L003S, U15-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.39
Std Dev 1.208
Entry
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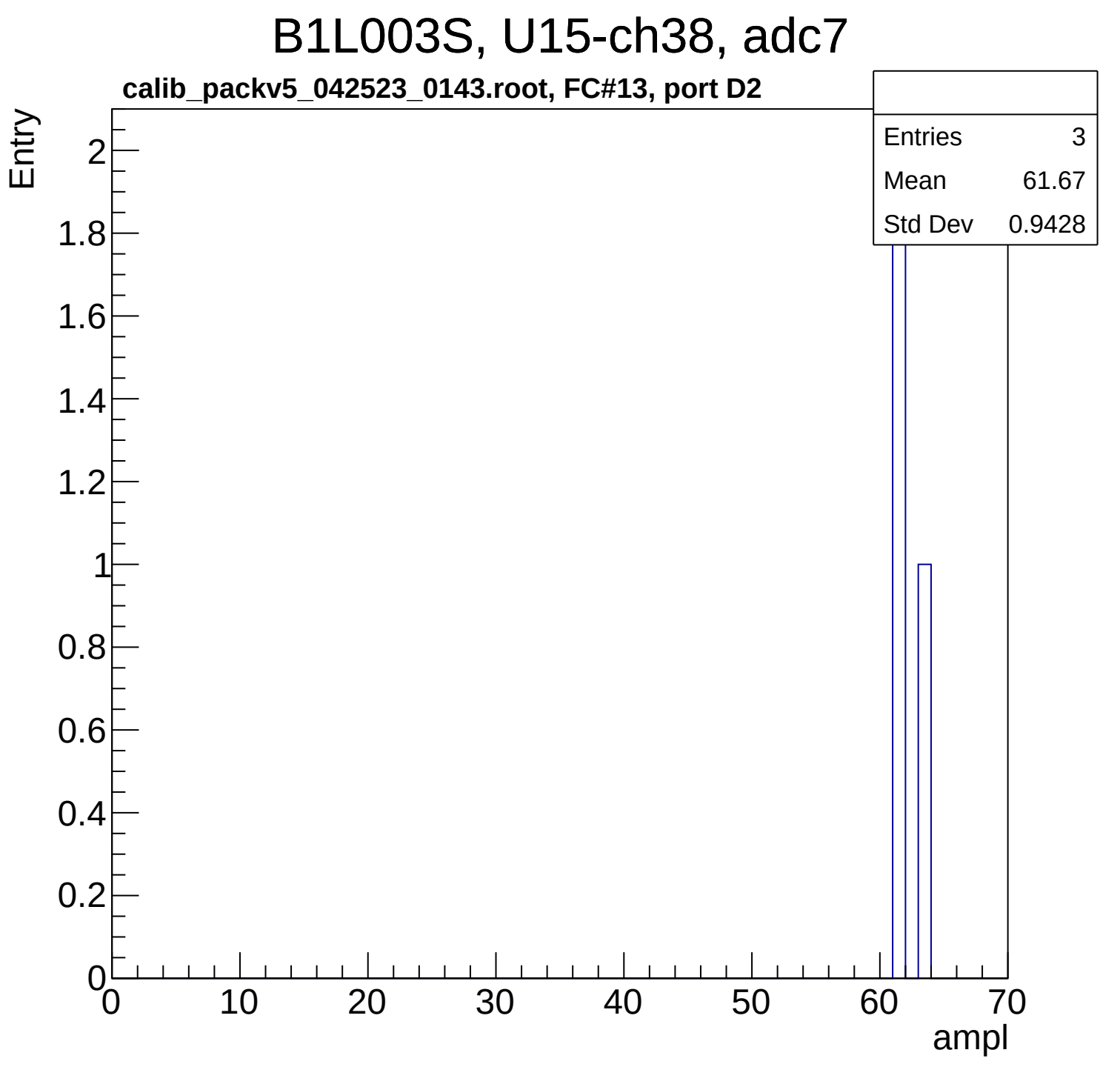
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
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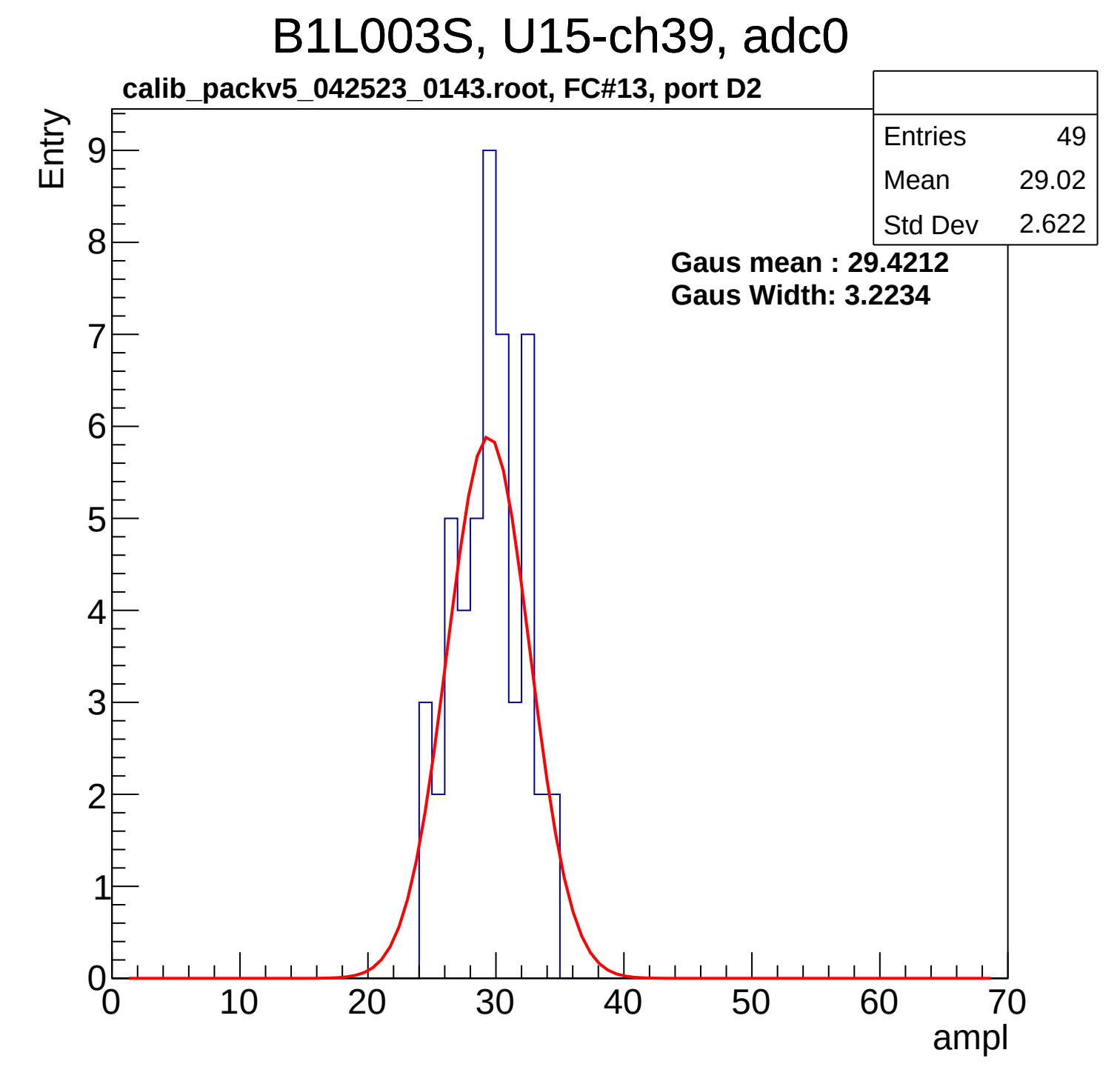
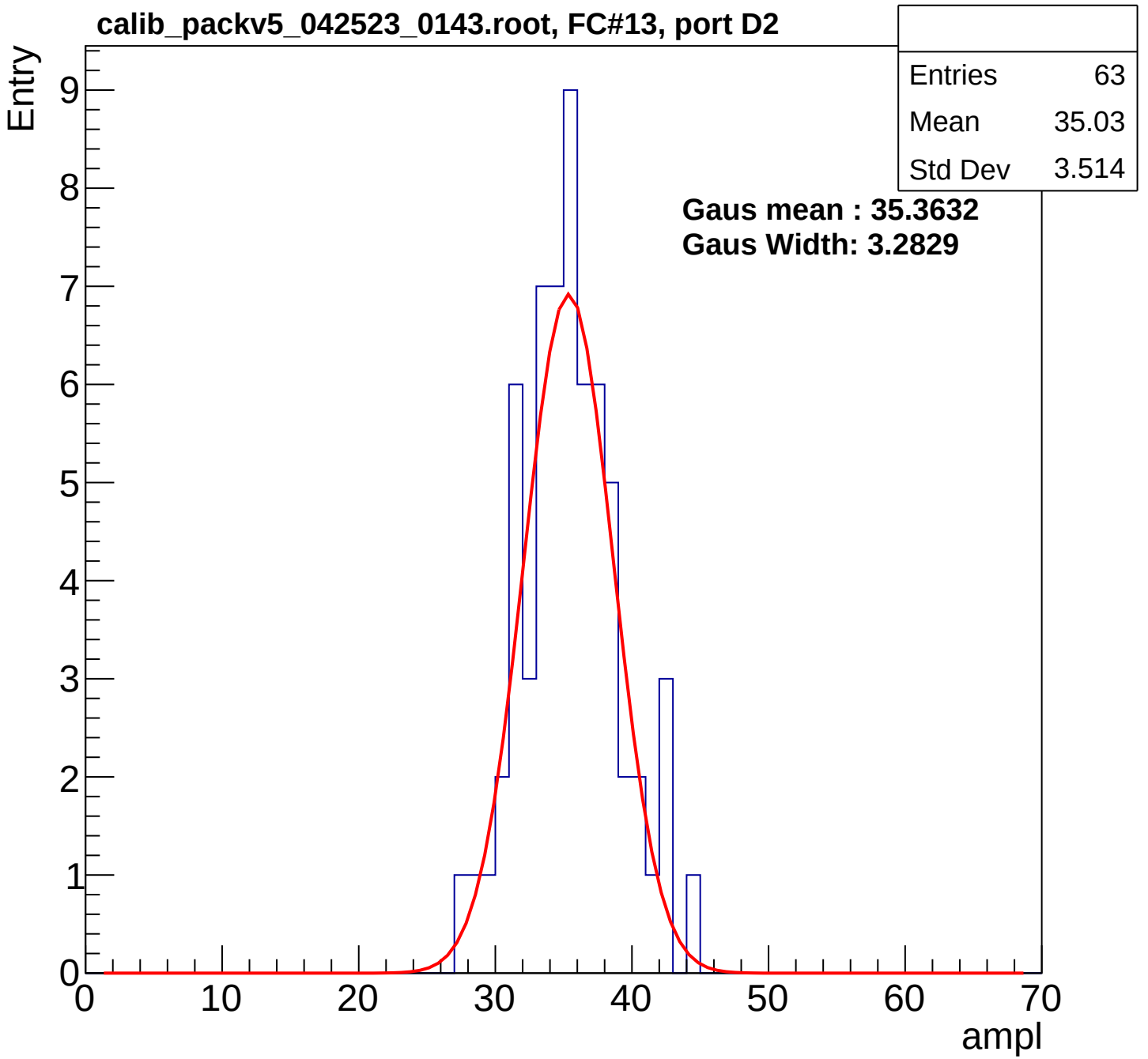

B1L003S, U15-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.02
Std Dev 2.622
Gaus mean : 29.4212
Gaus Width: 3.2234
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 35.03
Std Dev 3.514
Gaus mean : 35.3632
Gaus Width: 3.2829
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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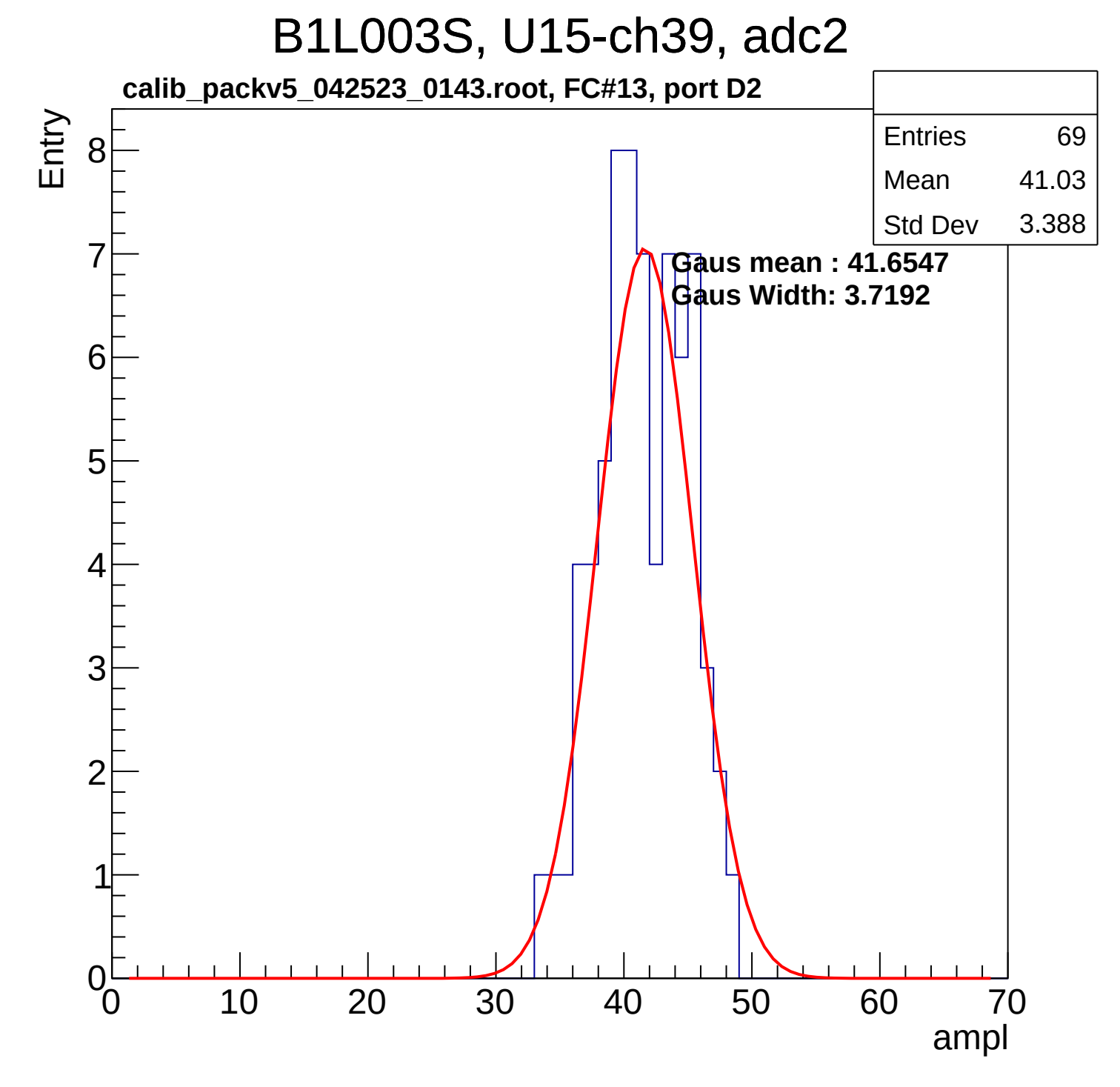
B1L003S, U15-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.03
Std Dev 3.388
Gaus mean : 41.6547
Gaus Width: 3.7192
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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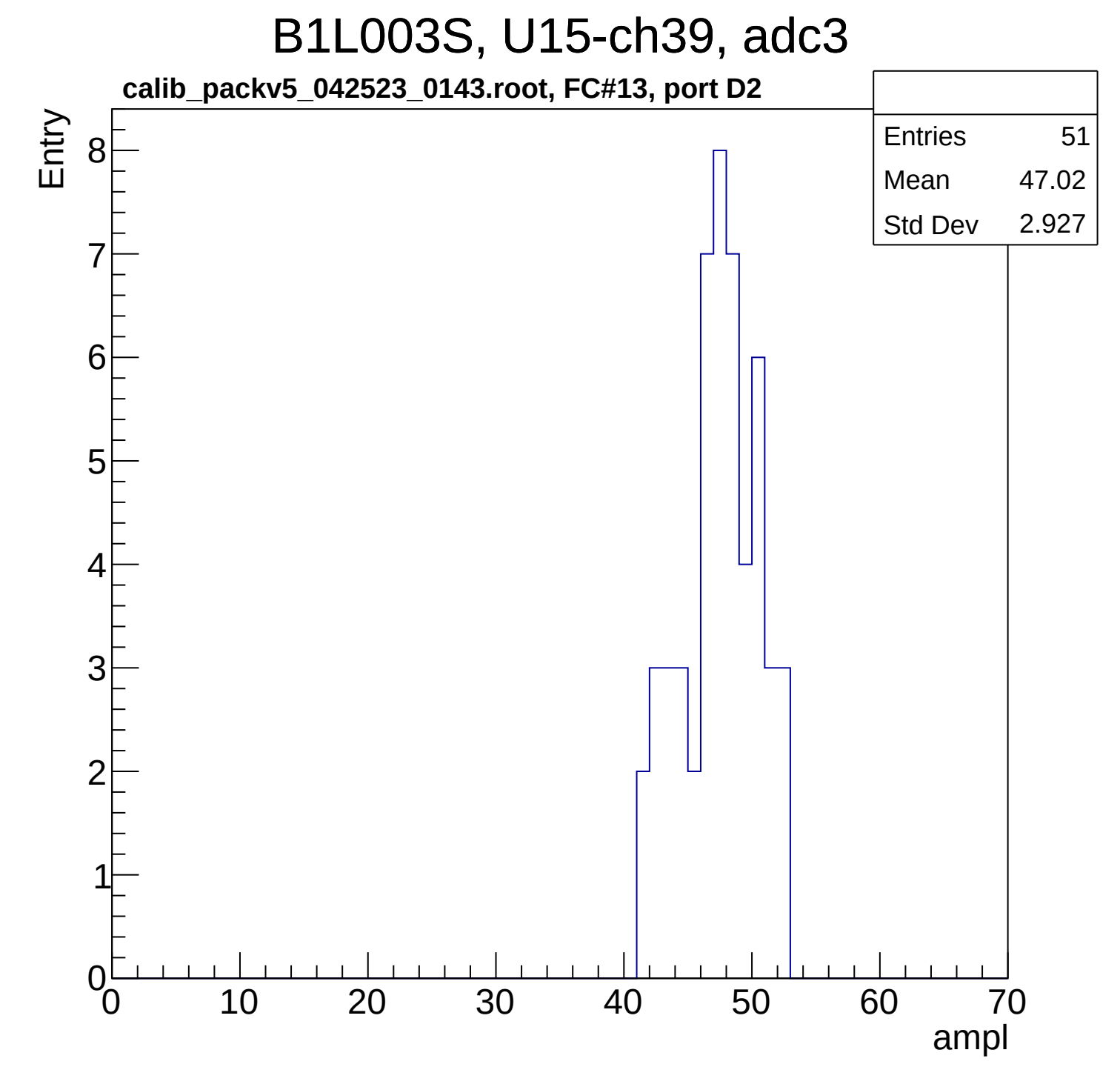
B1L003S, U15-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 47.02
Std Dev 2.927
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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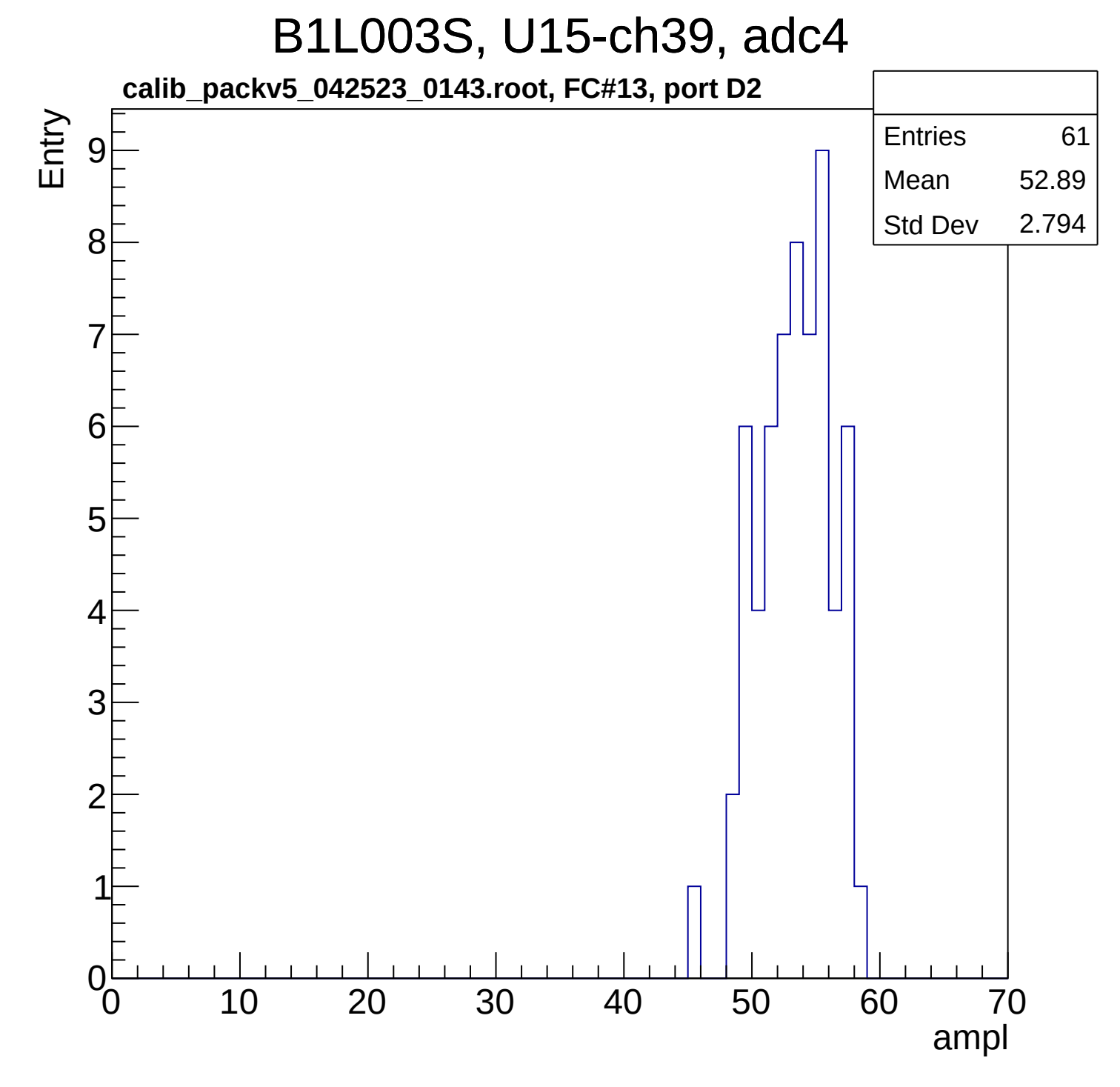
B1L003S, U15-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 52.89
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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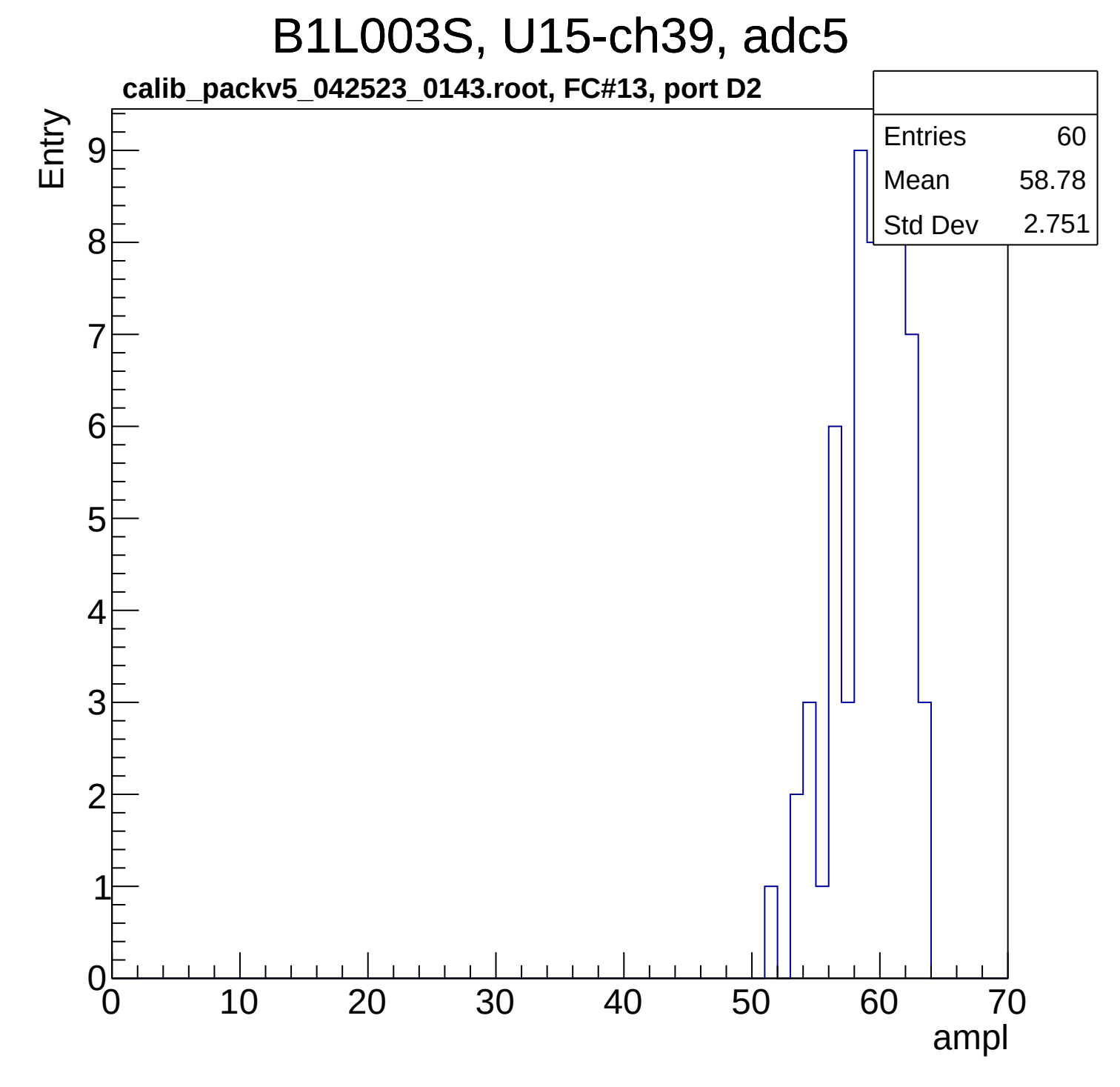
B1L003S, U15-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.78
Std Dev 2.751
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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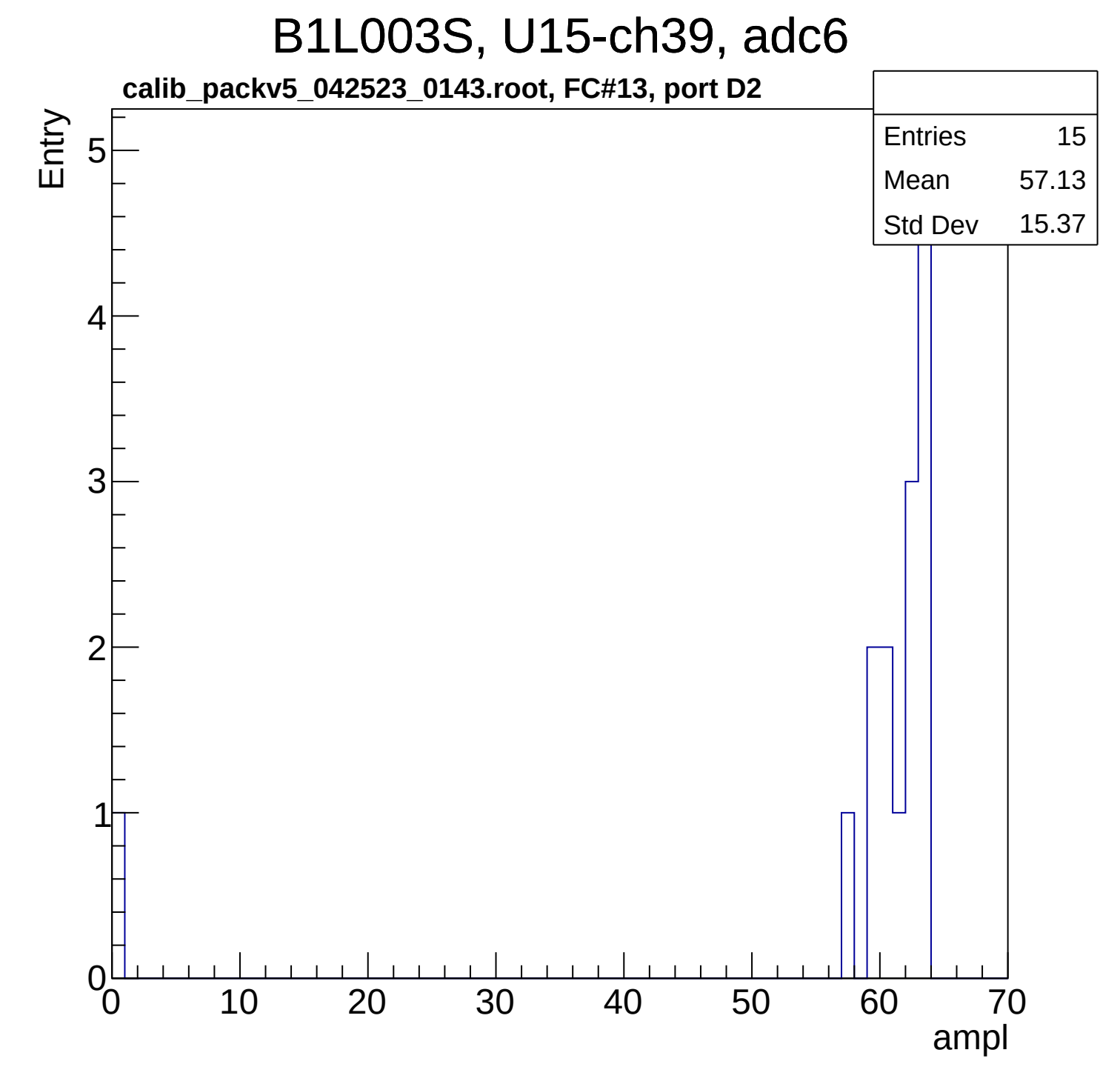
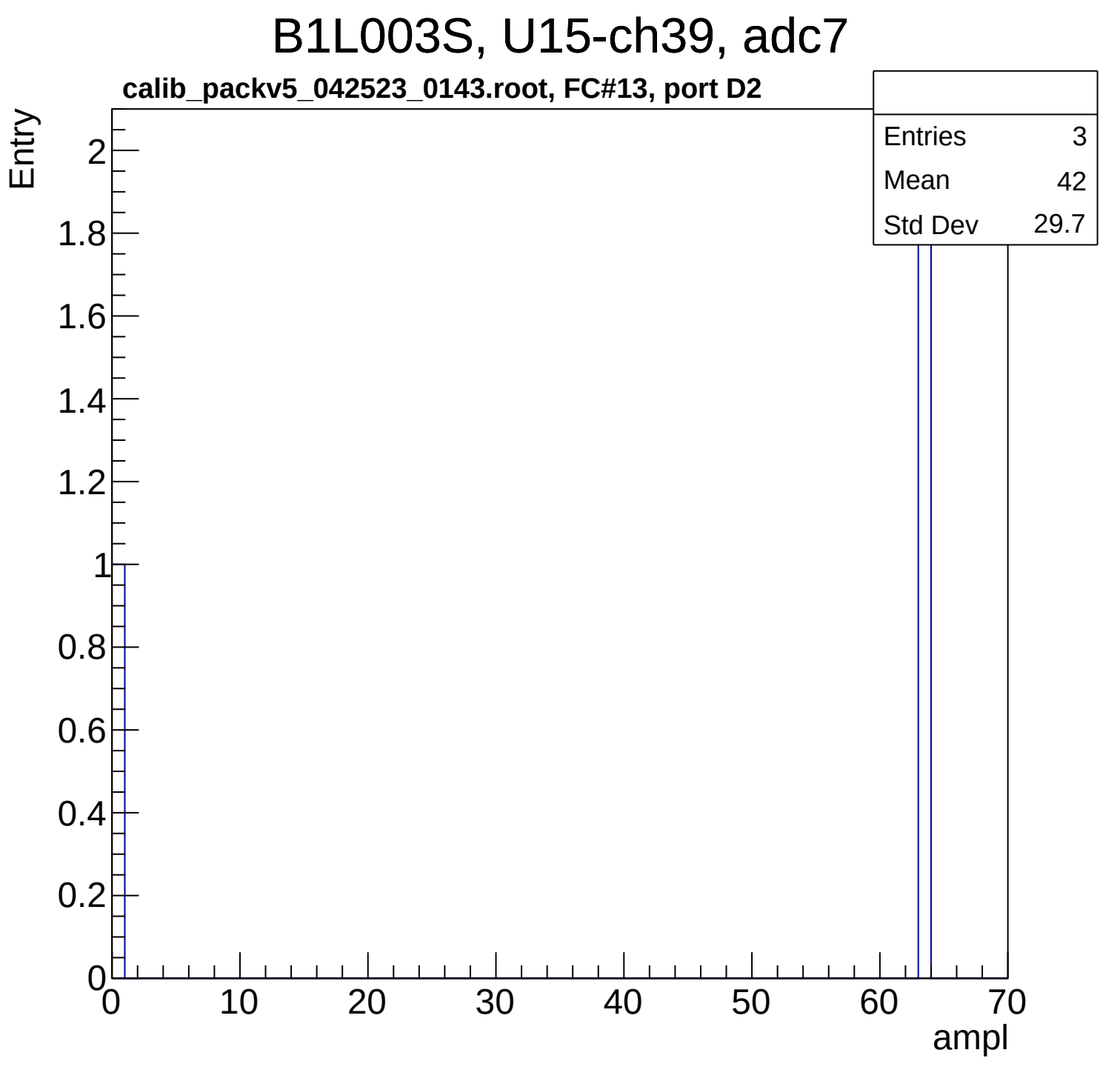
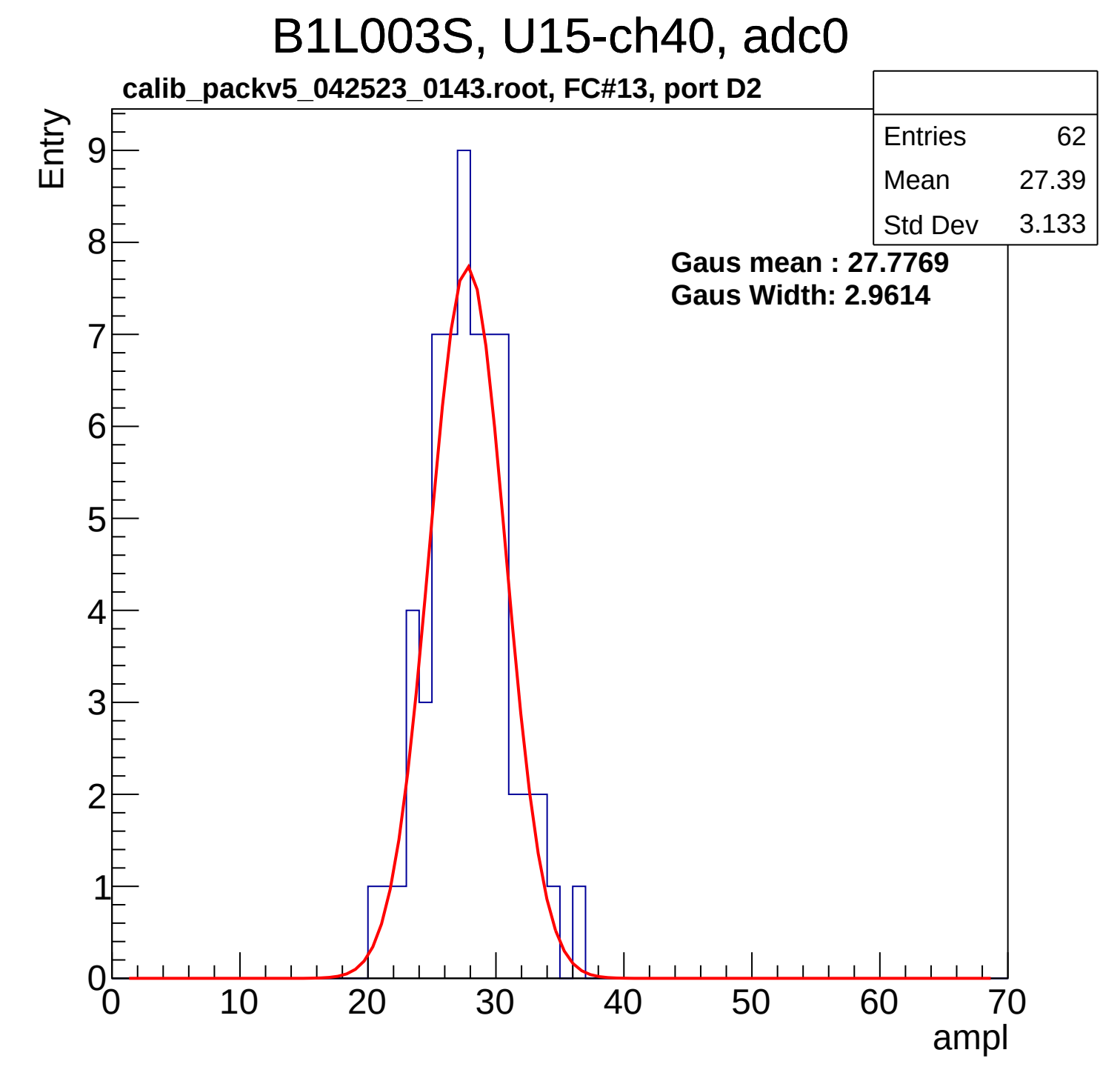
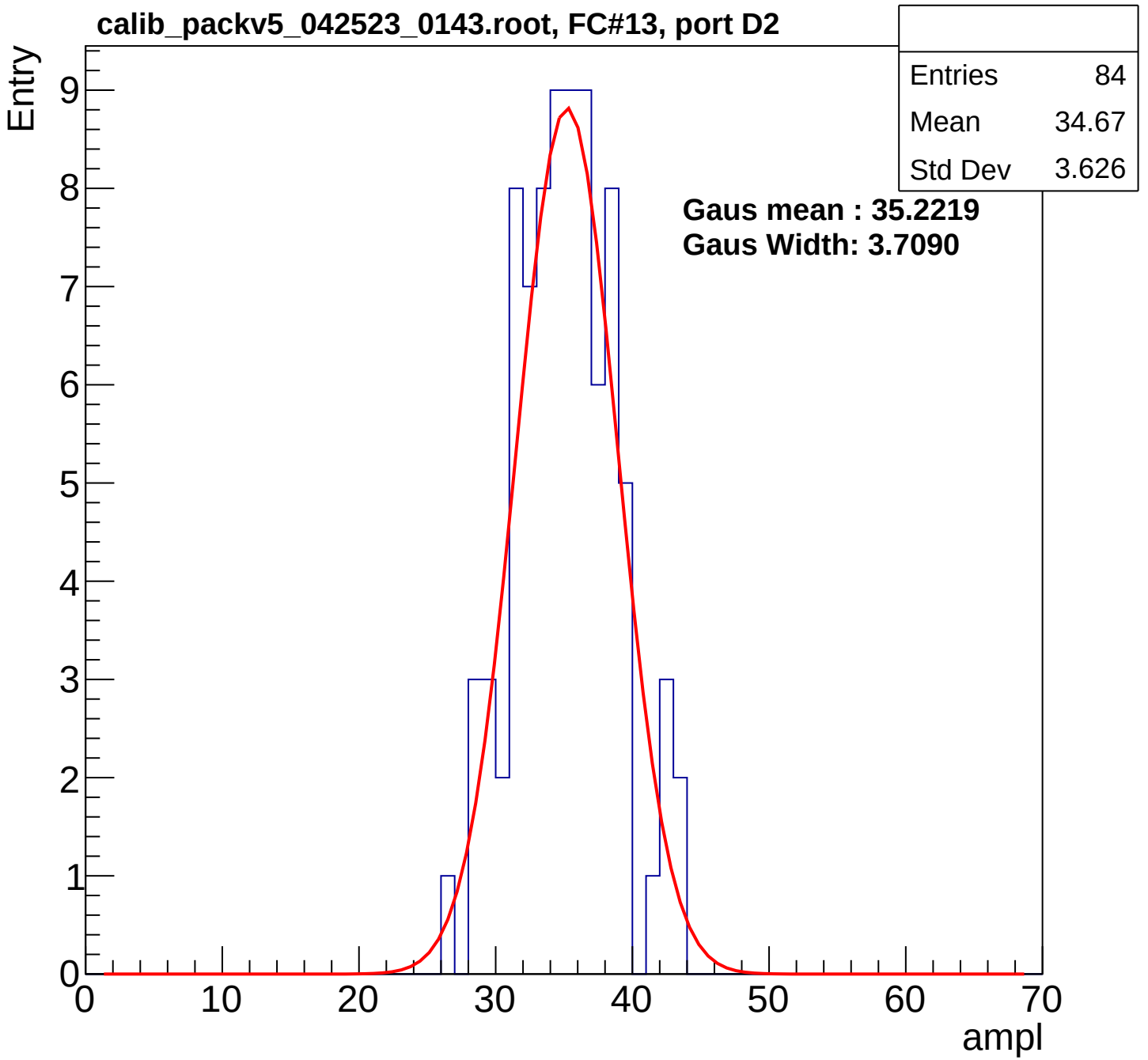

B1L003S, U15-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.13
Std Dev 15.37
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 27.39
Std Dev 3.133
Gaus mean : 27.7769
Gaus Width: 2.9614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 34.67
Std Dev 3.626
Gaus mean : 35.2219
Gaus Width: 3.7090
Entry
ampl
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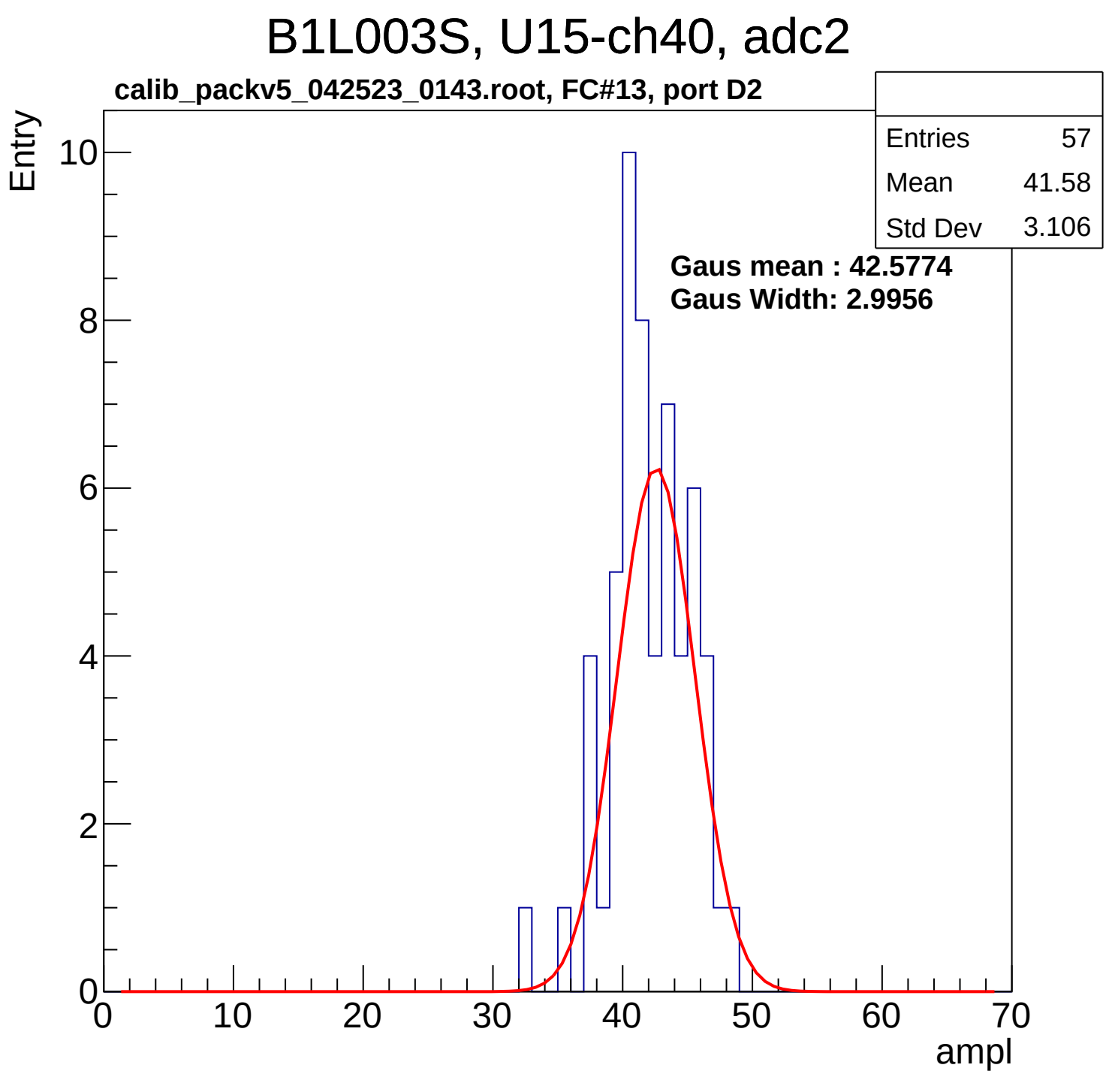
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 41.58
Std Dev 3.106
Gaus mean : 42.5774
Gaus Width: 2.9956
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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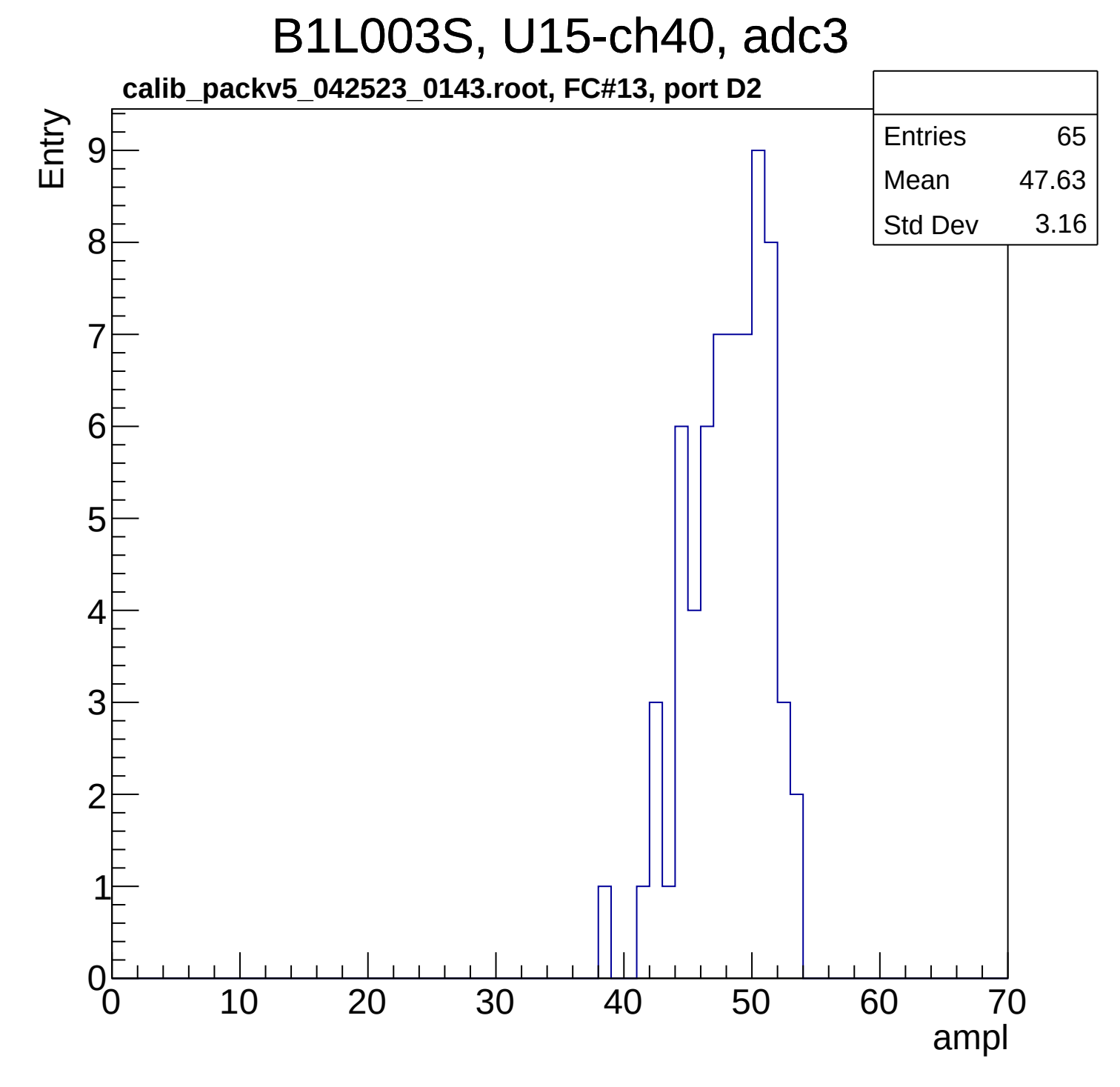
B1L003S, U15-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.63
Std Dev 3.16
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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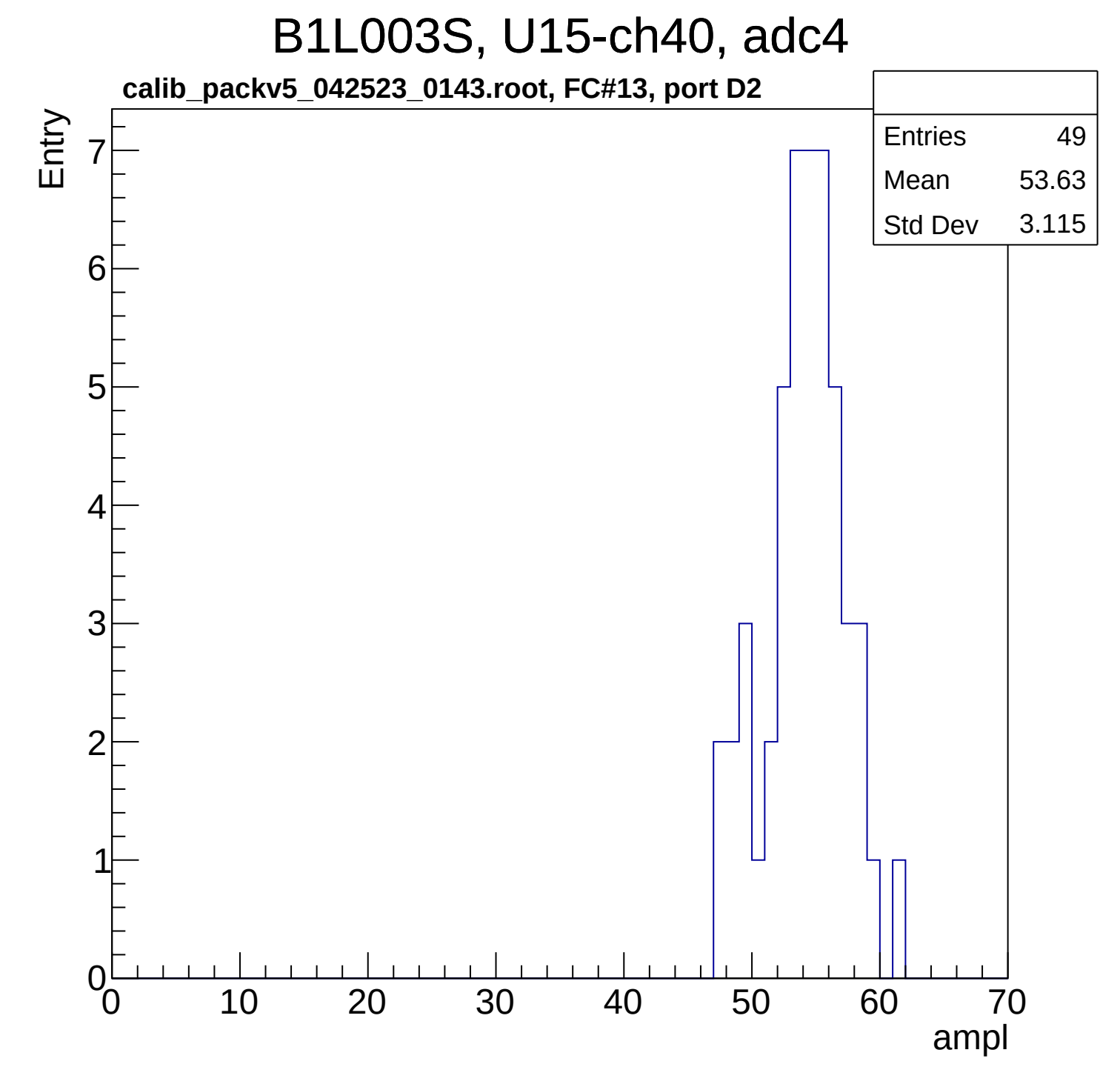
B1L003S, U15-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.63
Std Dev 3.115
Entry
ampl
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1
2
3
4
5
6
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0
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20
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40
50
60
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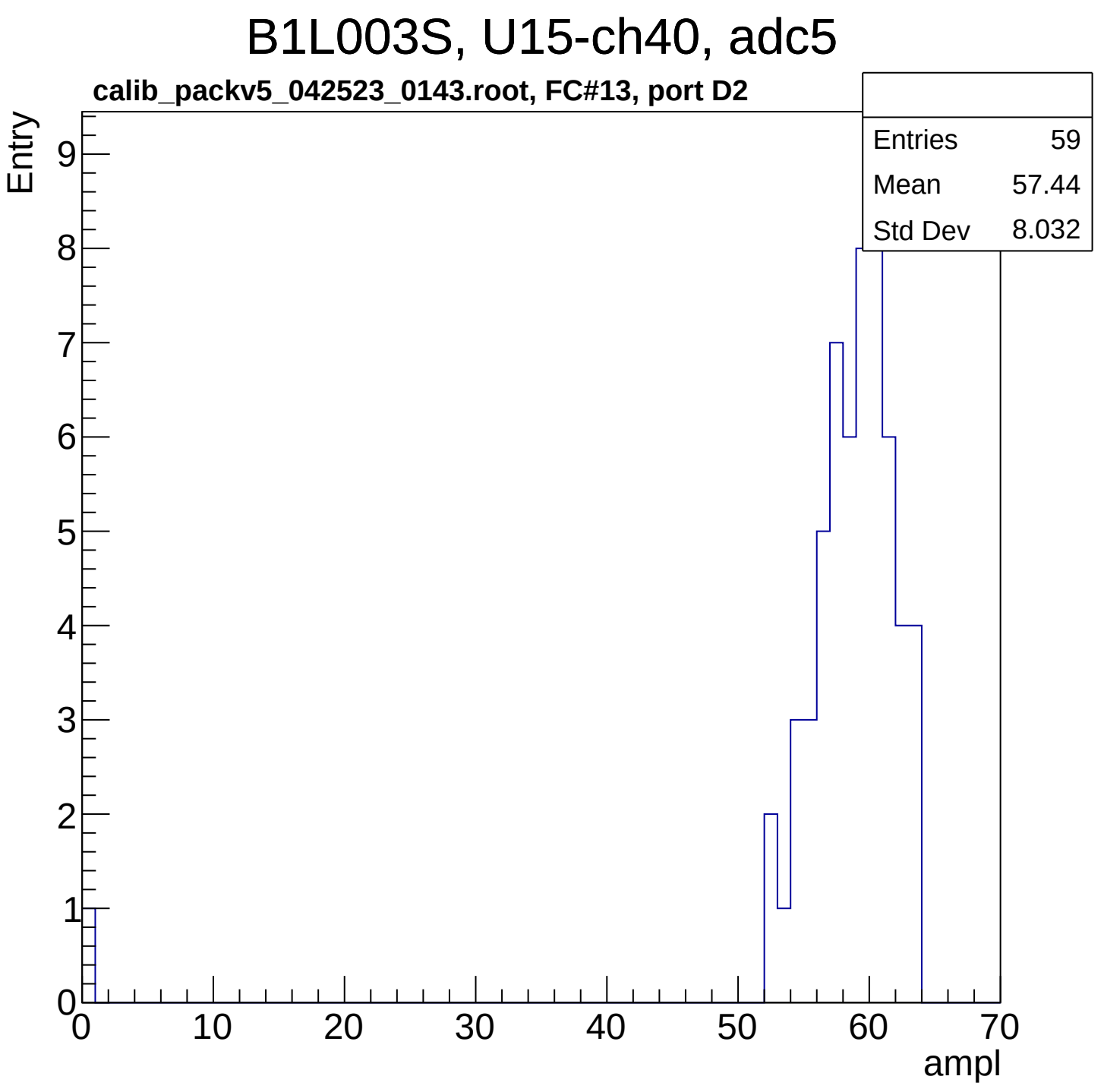
B1L003S, U15-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.44
Std Dev 8.032
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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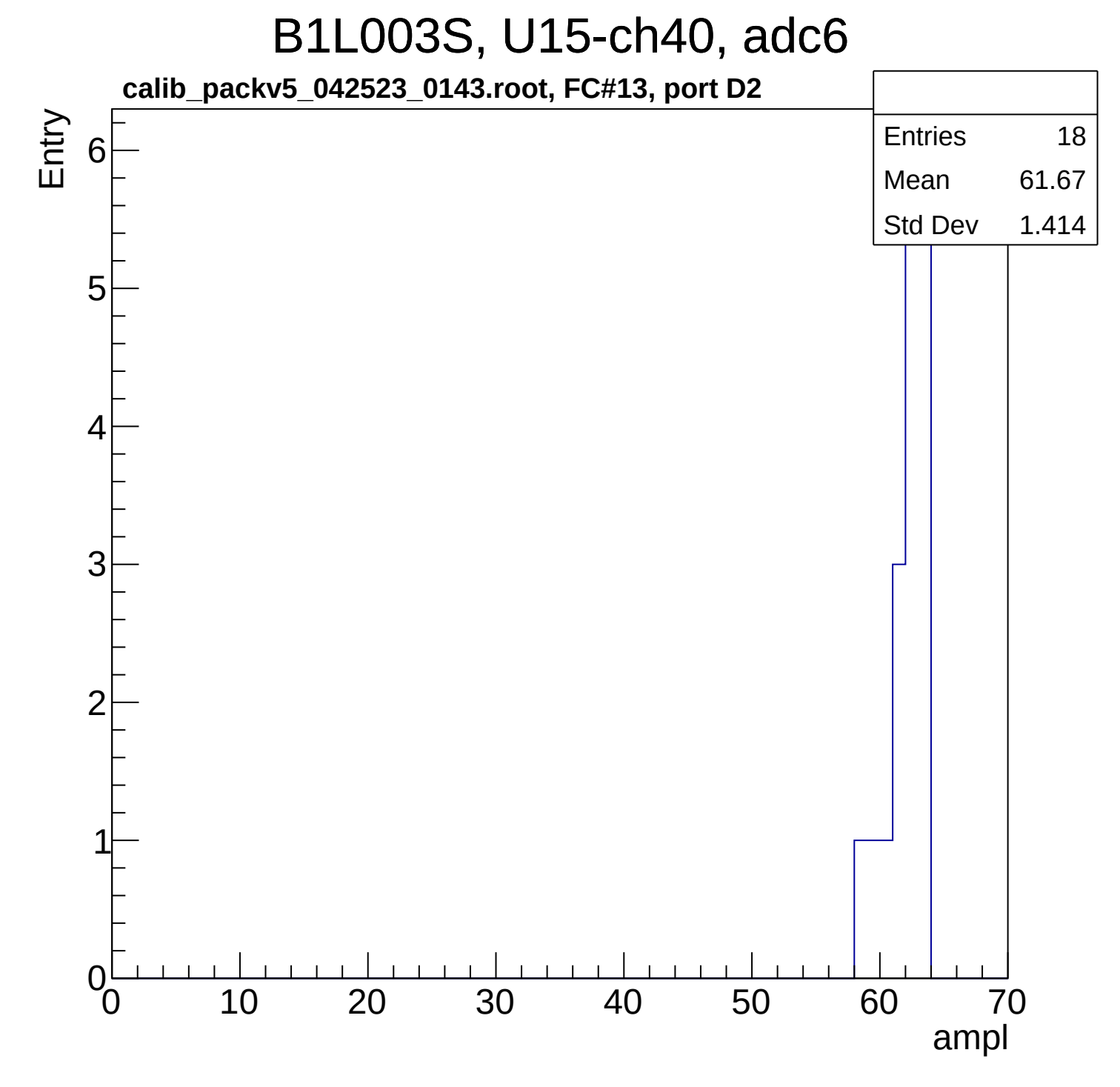
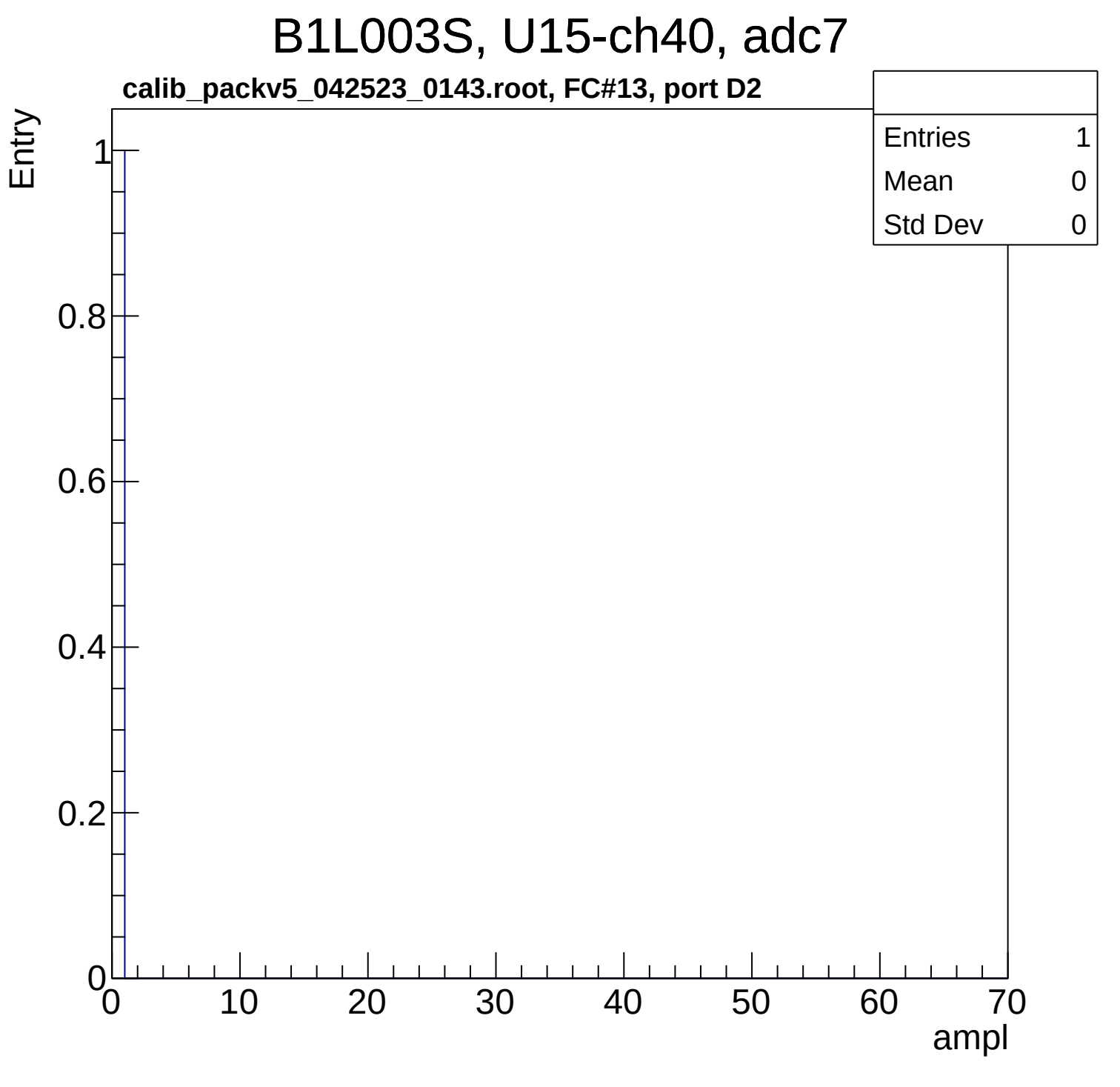
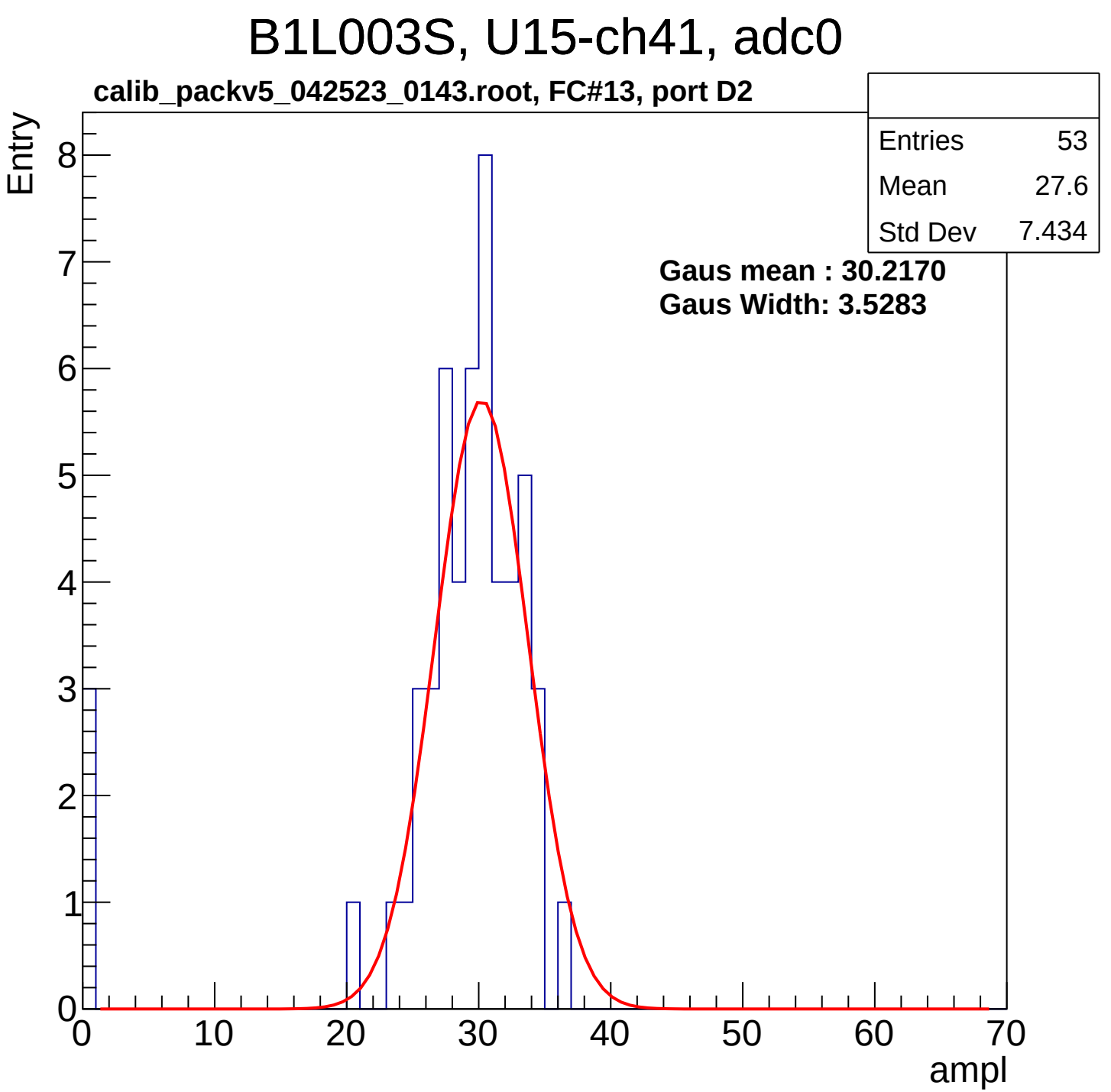

B1L003S, U15-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.67
Std Dev 1.414
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 27.6
Std Dev 7.434
Gaus mean : 30.2170
Gaus Width: 3.5283
Entry
ampl
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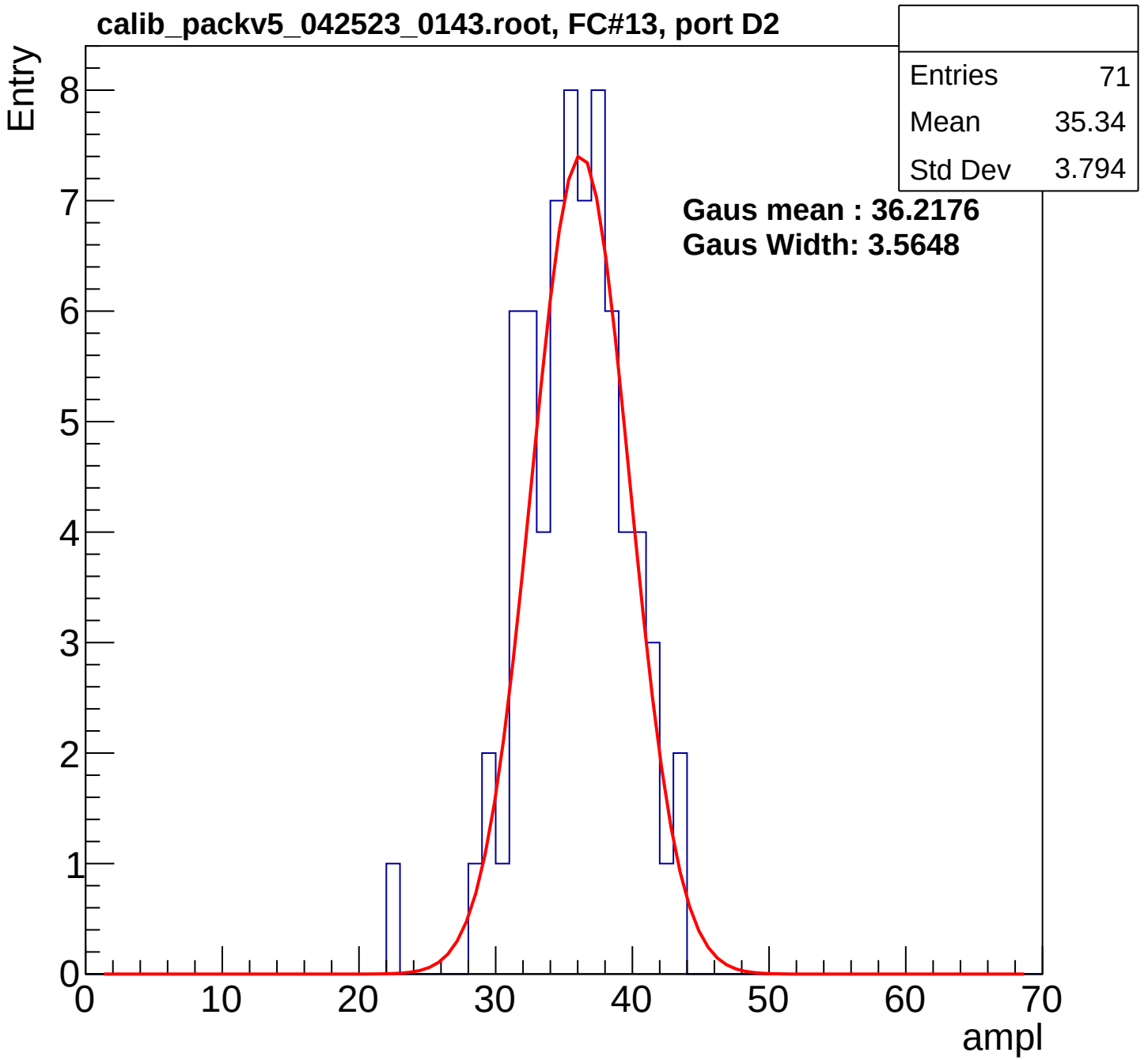
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.34
Std Dev 3.794
Gaus mean : 36.2176
Gaus Width: 3.5648
Entry
ampl
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1
2
3
4
5
6
7
8
0
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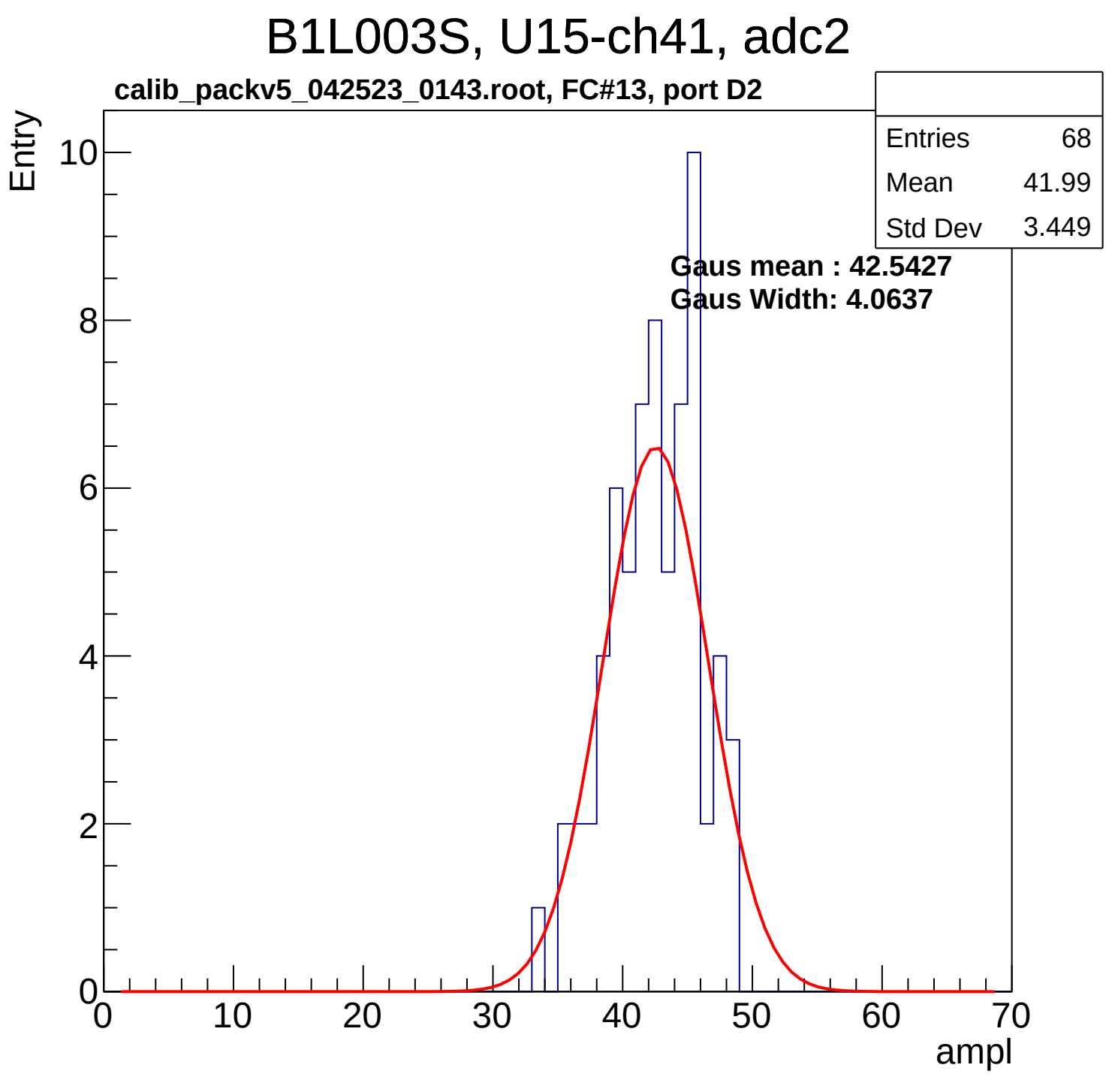
B1L003S, U15-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 41.99
Std Dev 3.449
Gaus mean : 42.5427
Gaus Width: 4.0637
Entry
ampl
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4
6
8
10
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60
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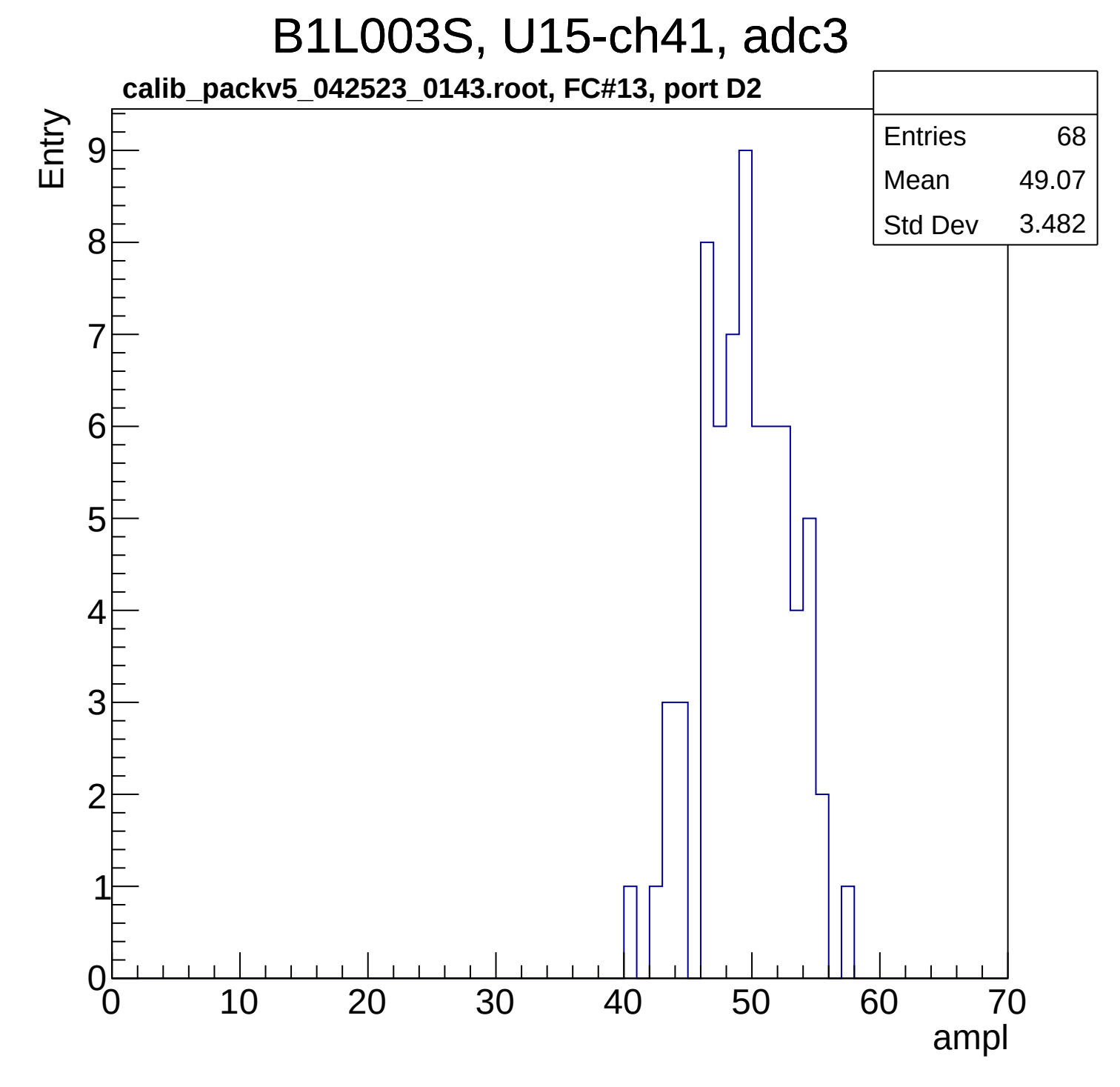
B1L003S, U15-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.07
Std Dev 3.482
Entry
ampl
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5
6
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60
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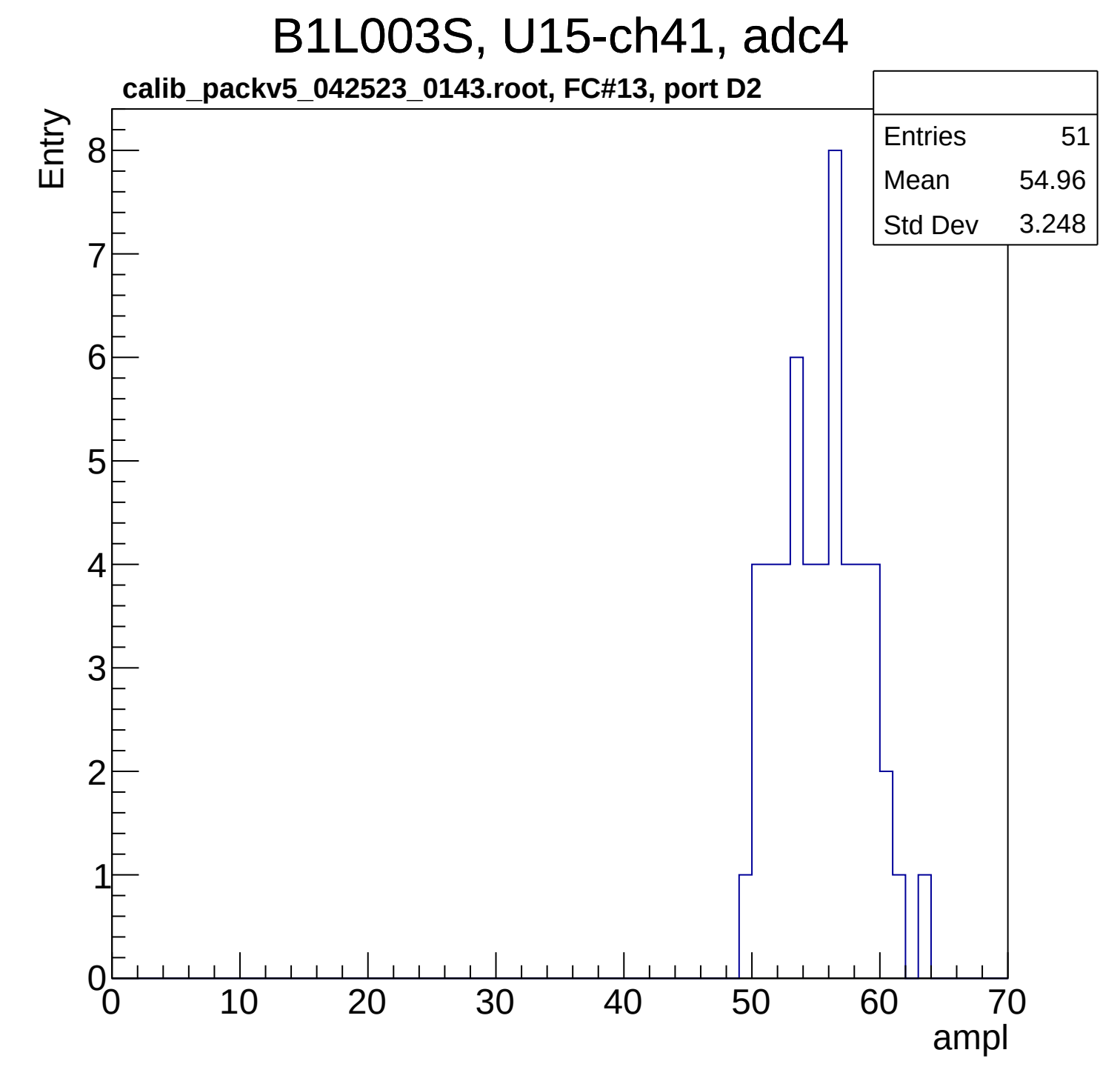
B1L003S, U15-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.96
Std Dev 3.248
Entry
ampl
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4
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40
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60
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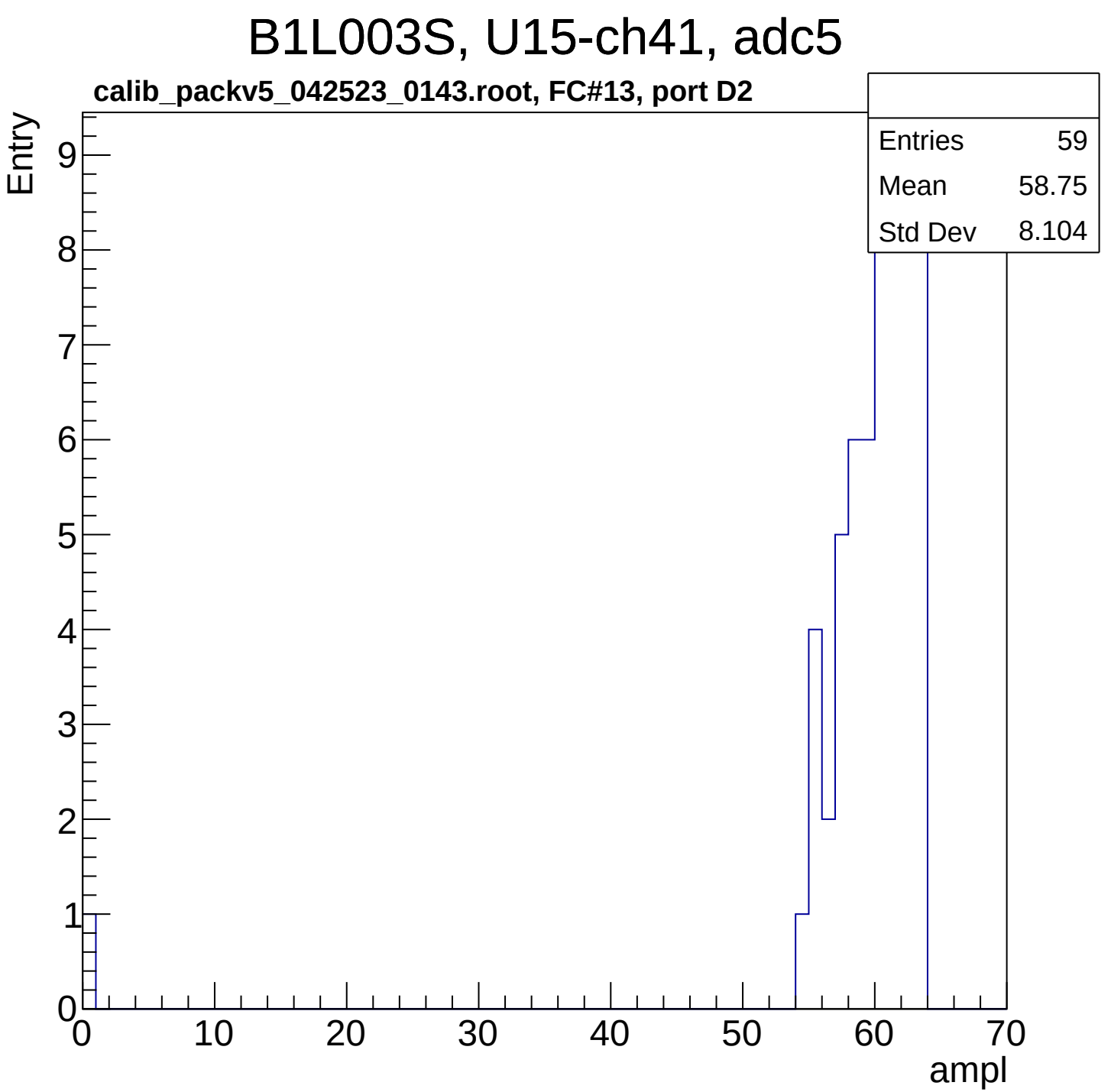
B1L003S, U15-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.75
Std Dev 8.104
Entry
ampl
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2
3
4
5
6
7
8
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0
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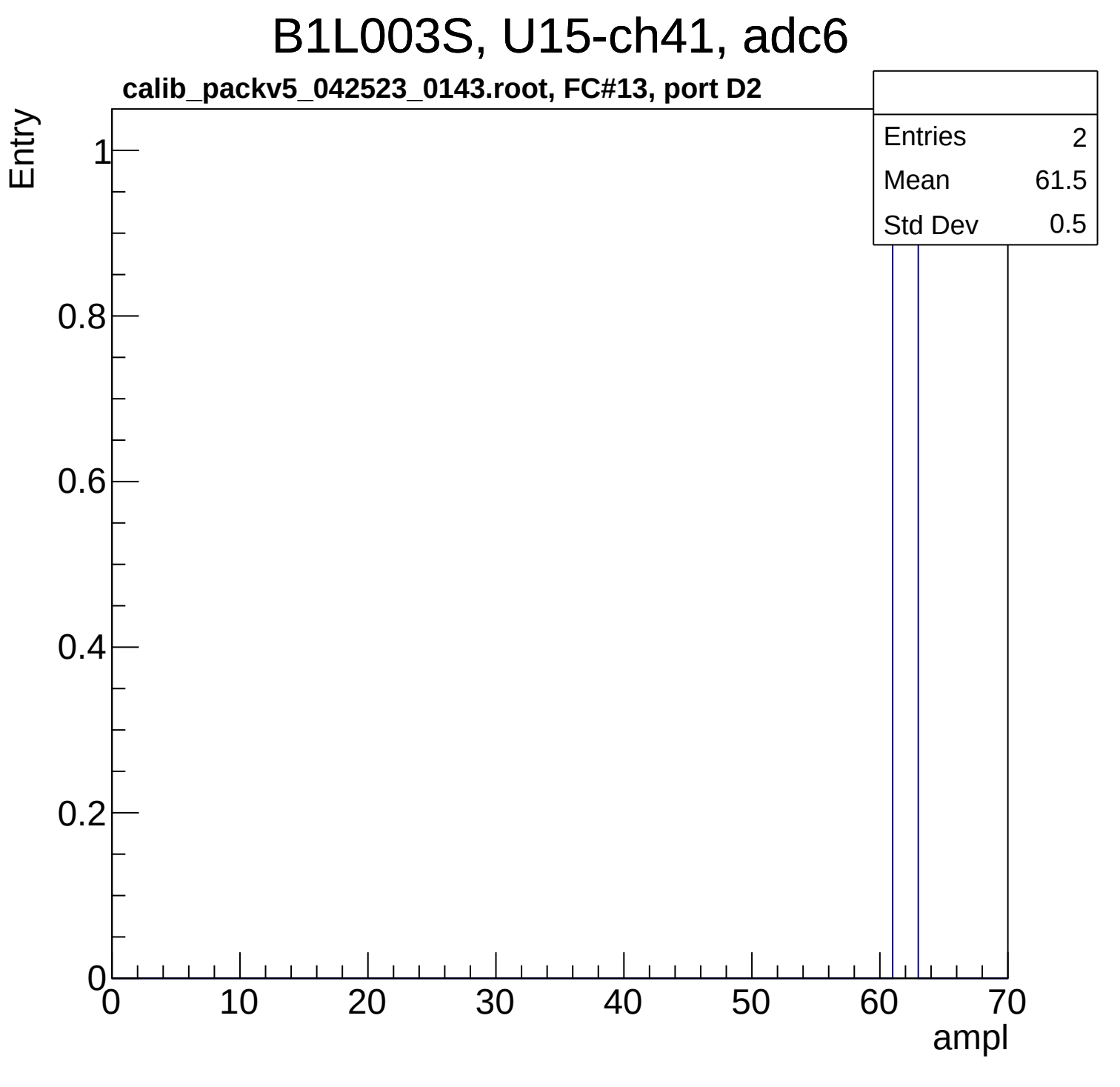
B1L003S, U15-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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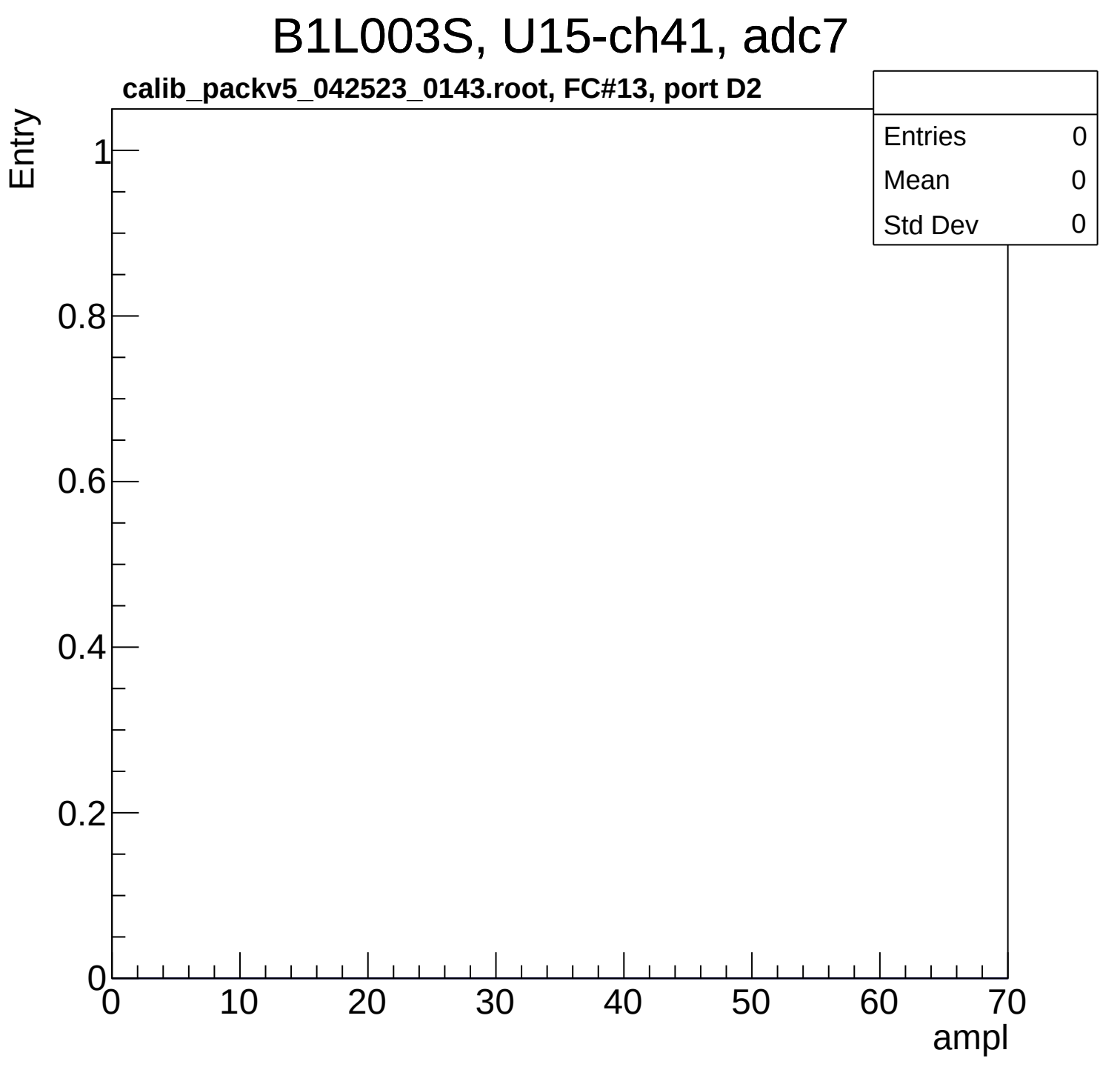
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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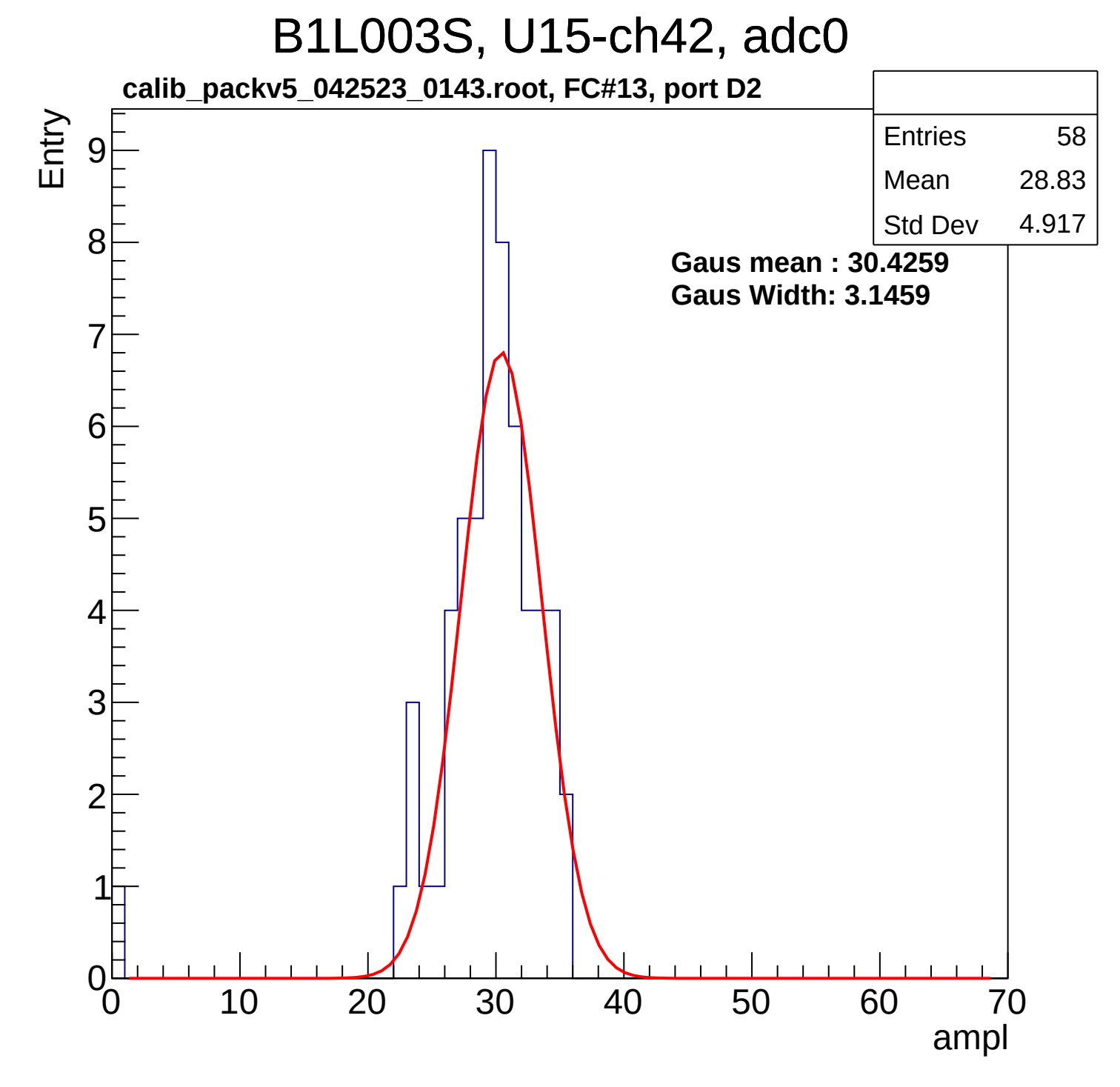
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.83
Std Dev 4.917
Gaus mean : 30.4259
Gaus Width: 3.1459
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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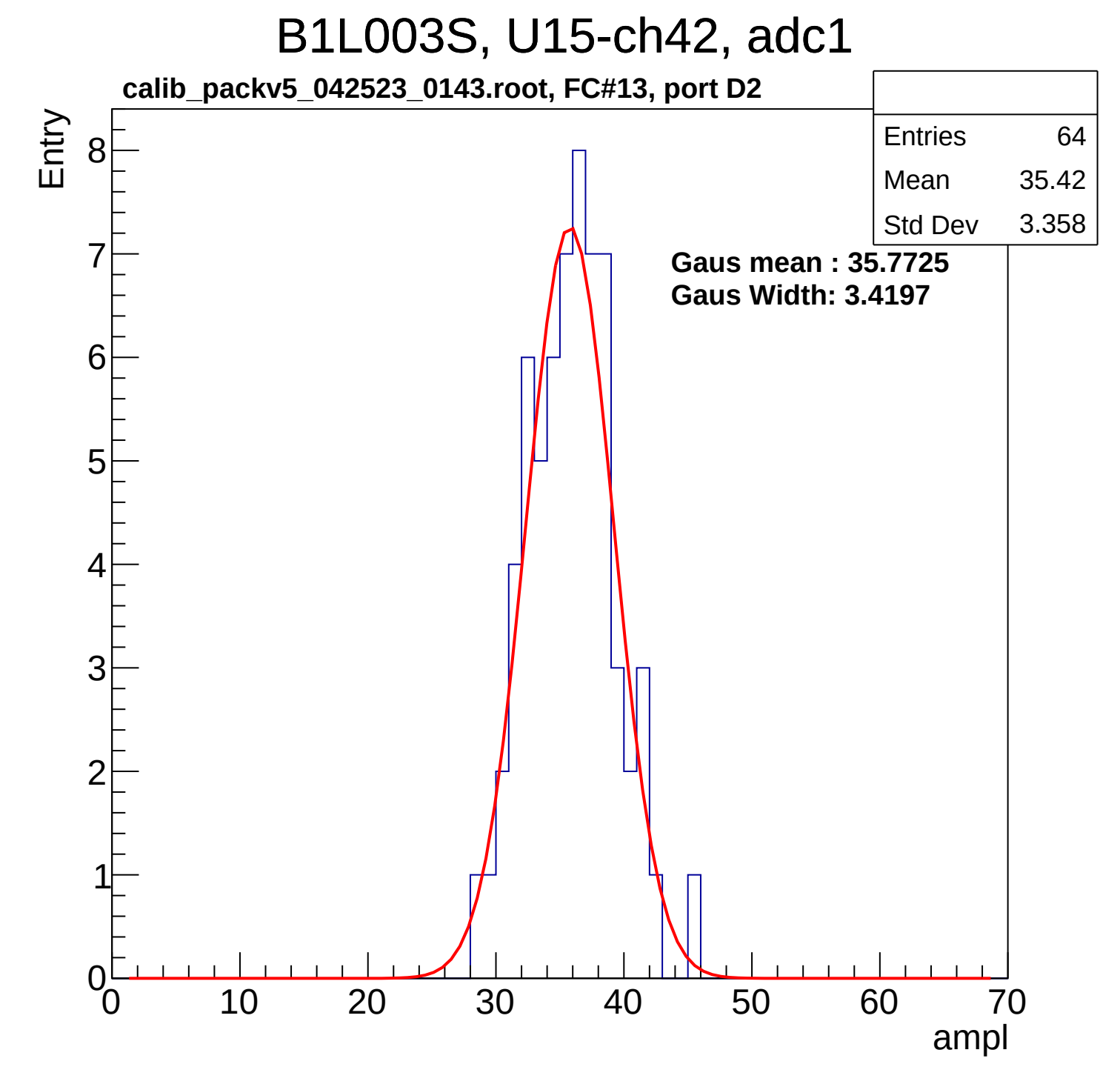
B1L003S, U15-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.42
Std Dev 3.358
Gaus mean : 35.7725
Gaus Width: 3.4197
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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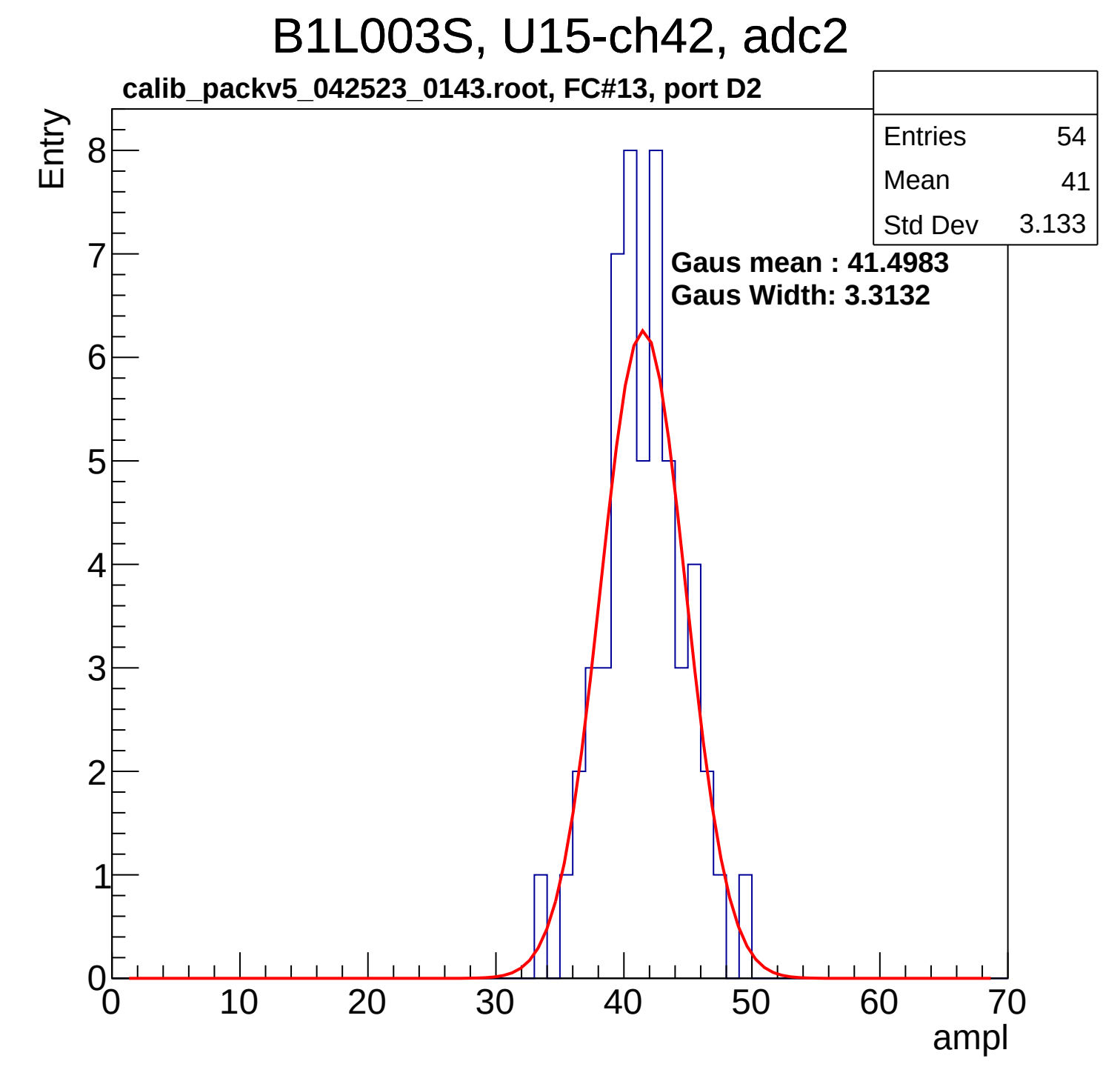
B1L003S, U15-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41
Std Dev 3.133
Gaus mean : 41.4983
Gaus Width: 3.3132
Entry
ampl
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1
2
3
4
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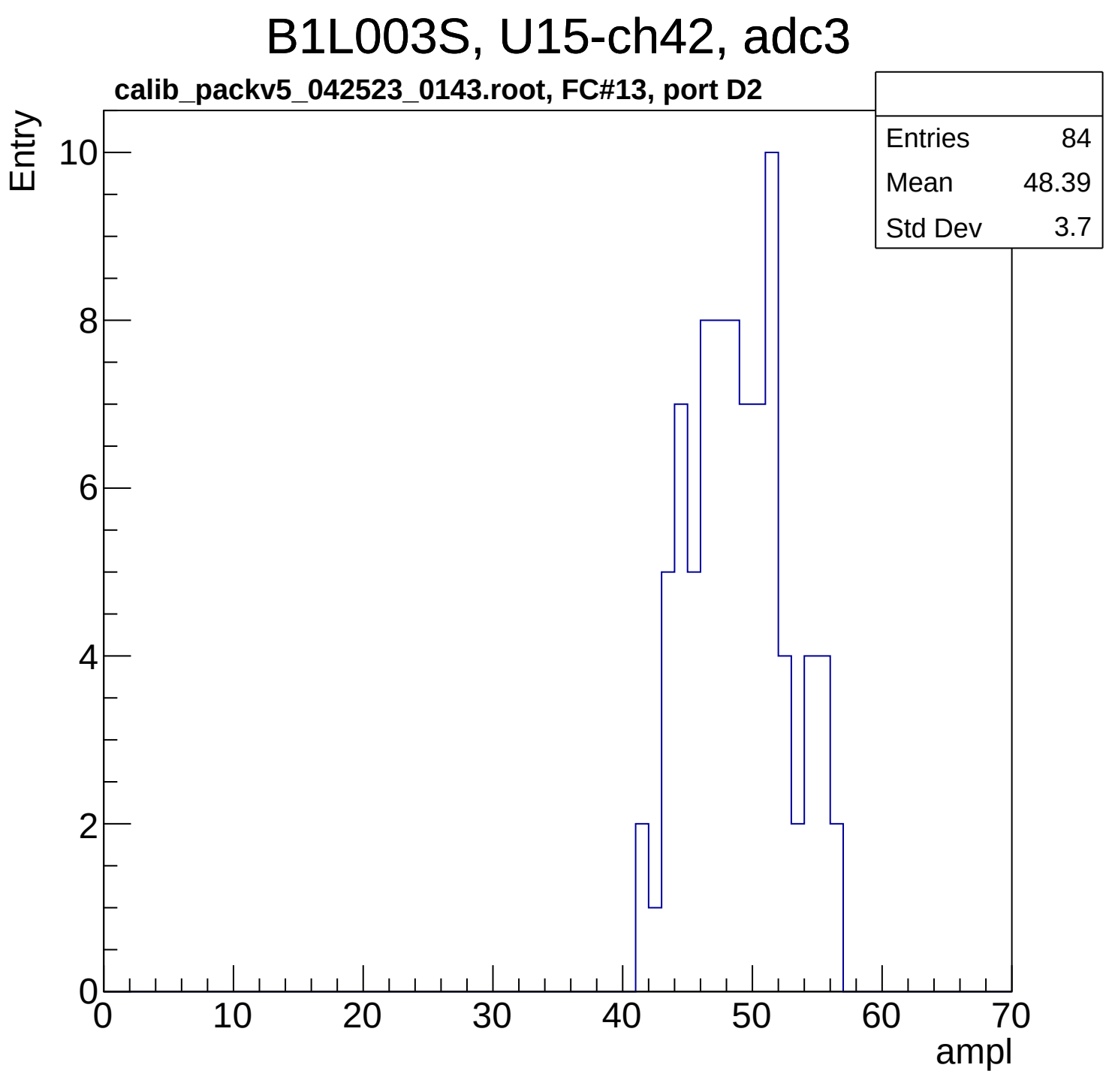
B1L003S, U15-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 48.39
Std Dev 3.7
Entry
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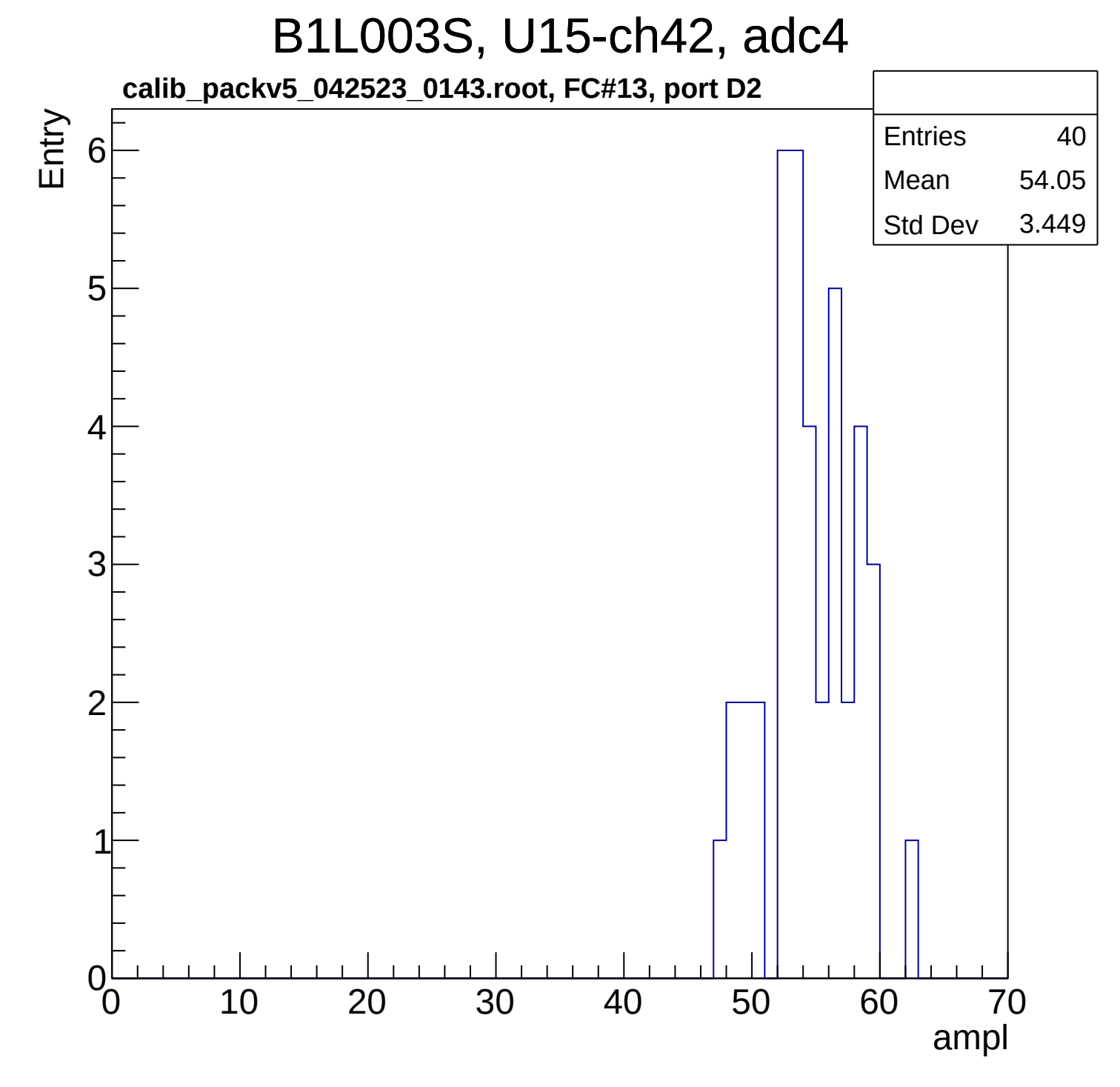
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 54.05
Std Dev 3.449
Entry
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2
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4
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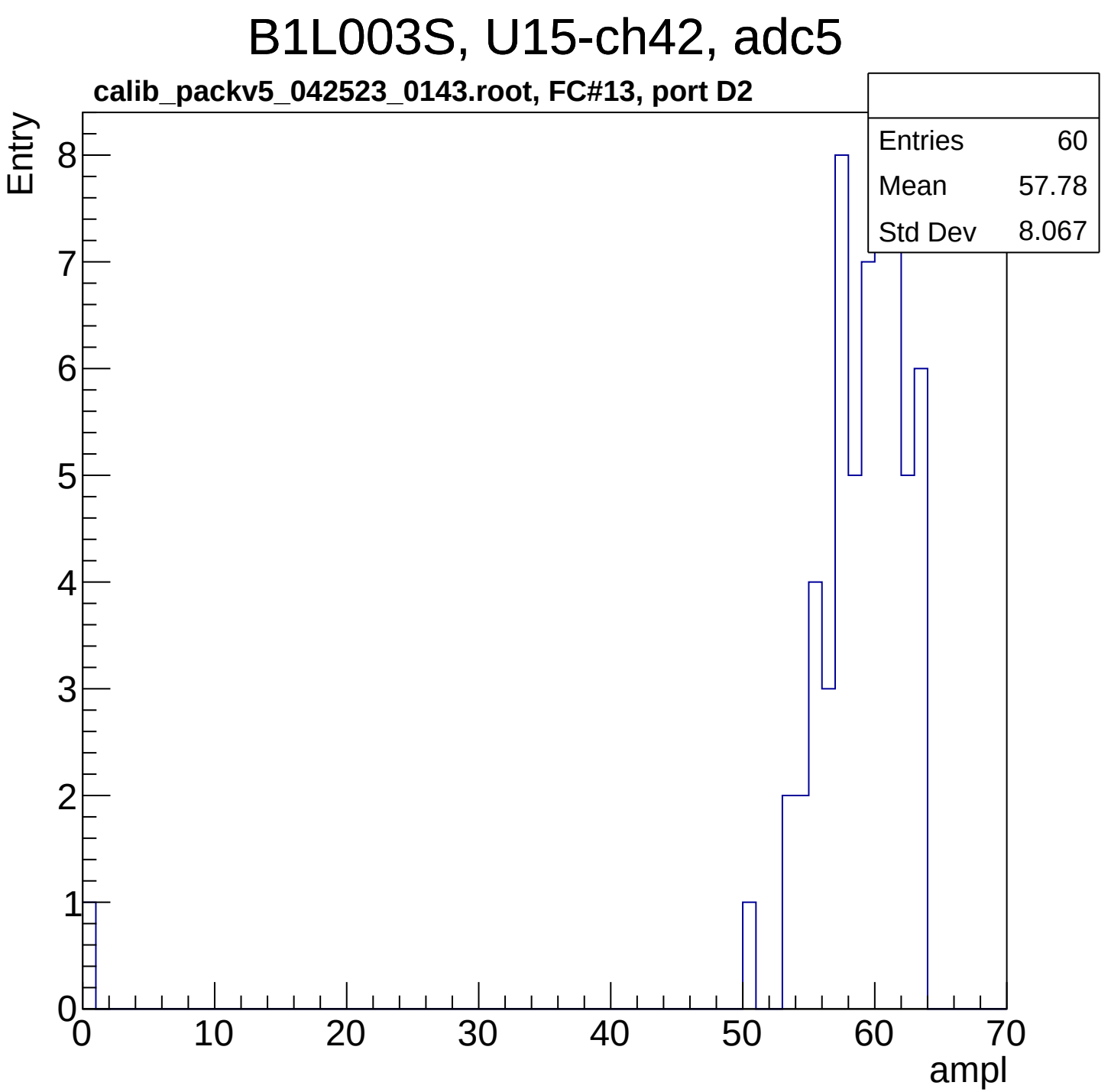
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.78
Std Dev 8.067
Entry
ampl
0
1
2
3
4
5
6
7
8
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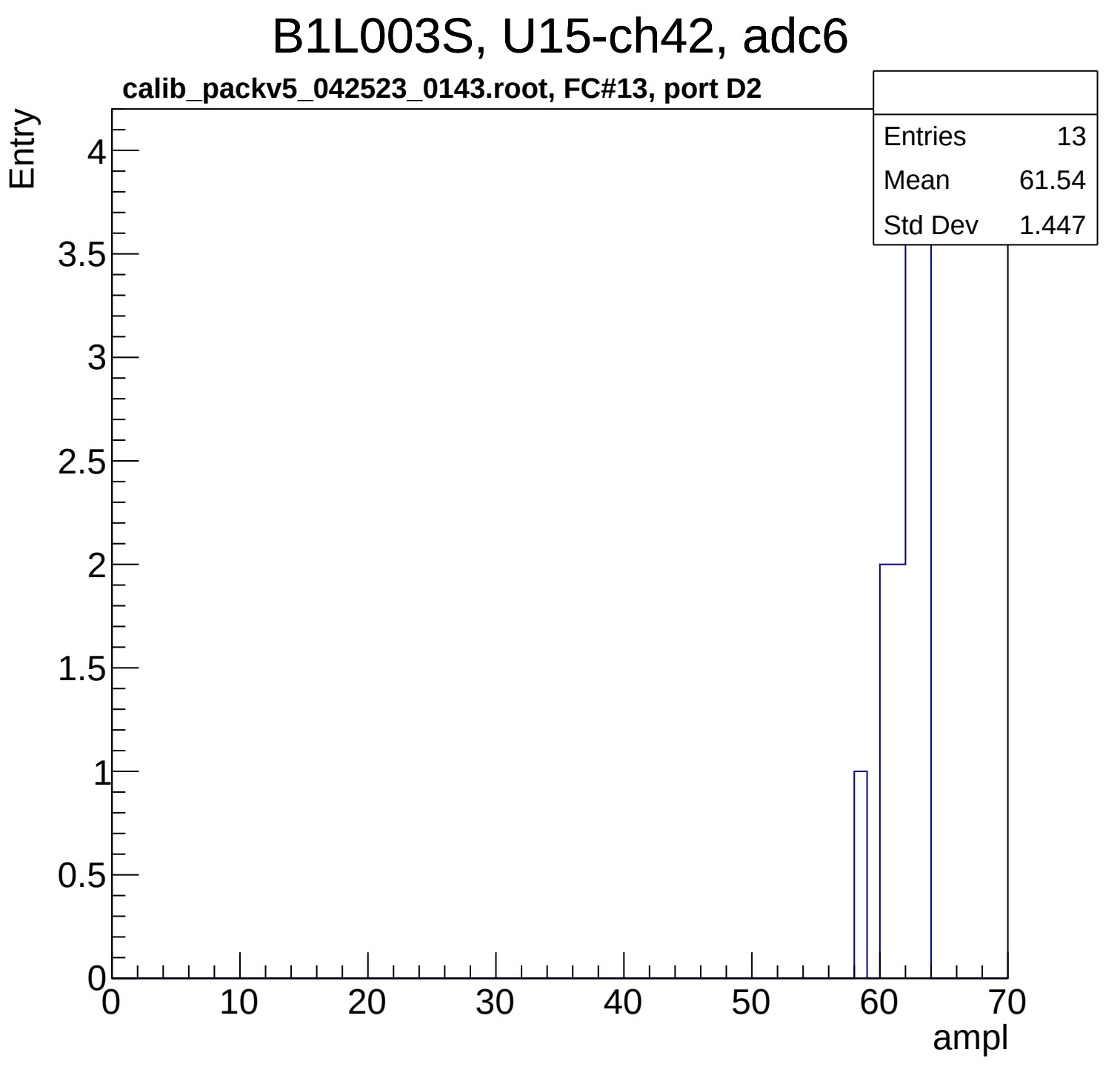
B1L003S, U15-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.54
Std Dev 1.447
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
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0
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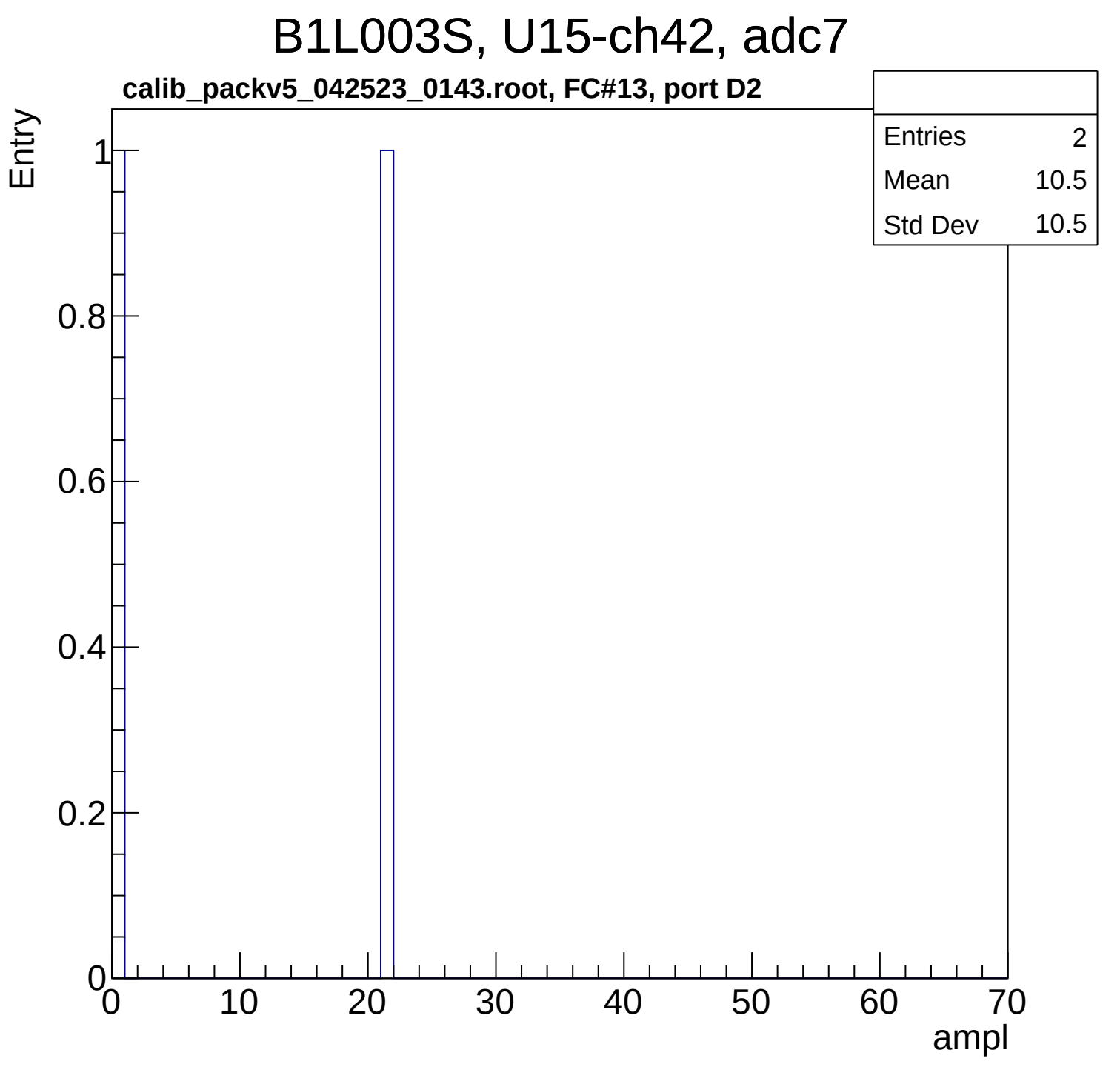
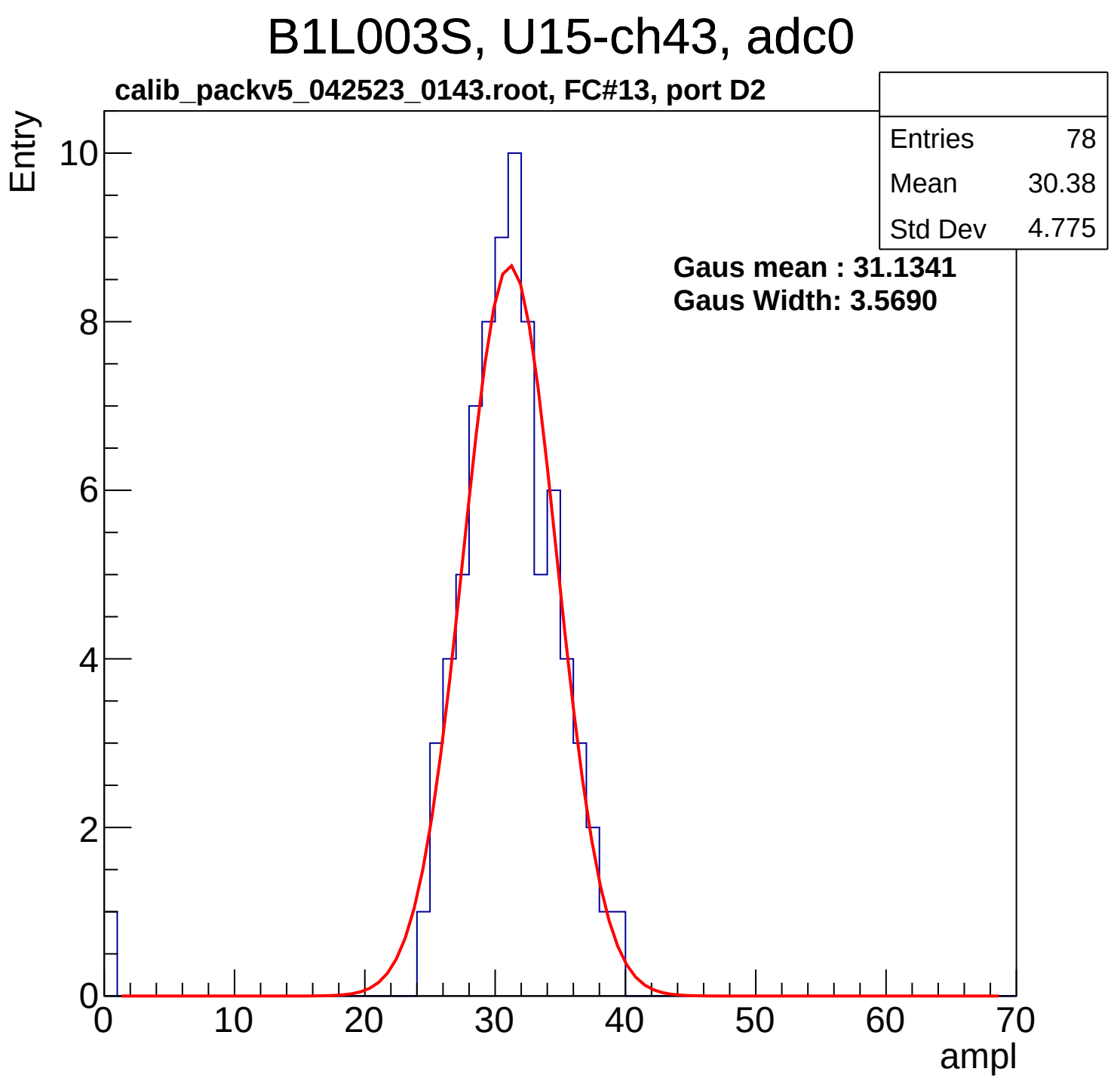
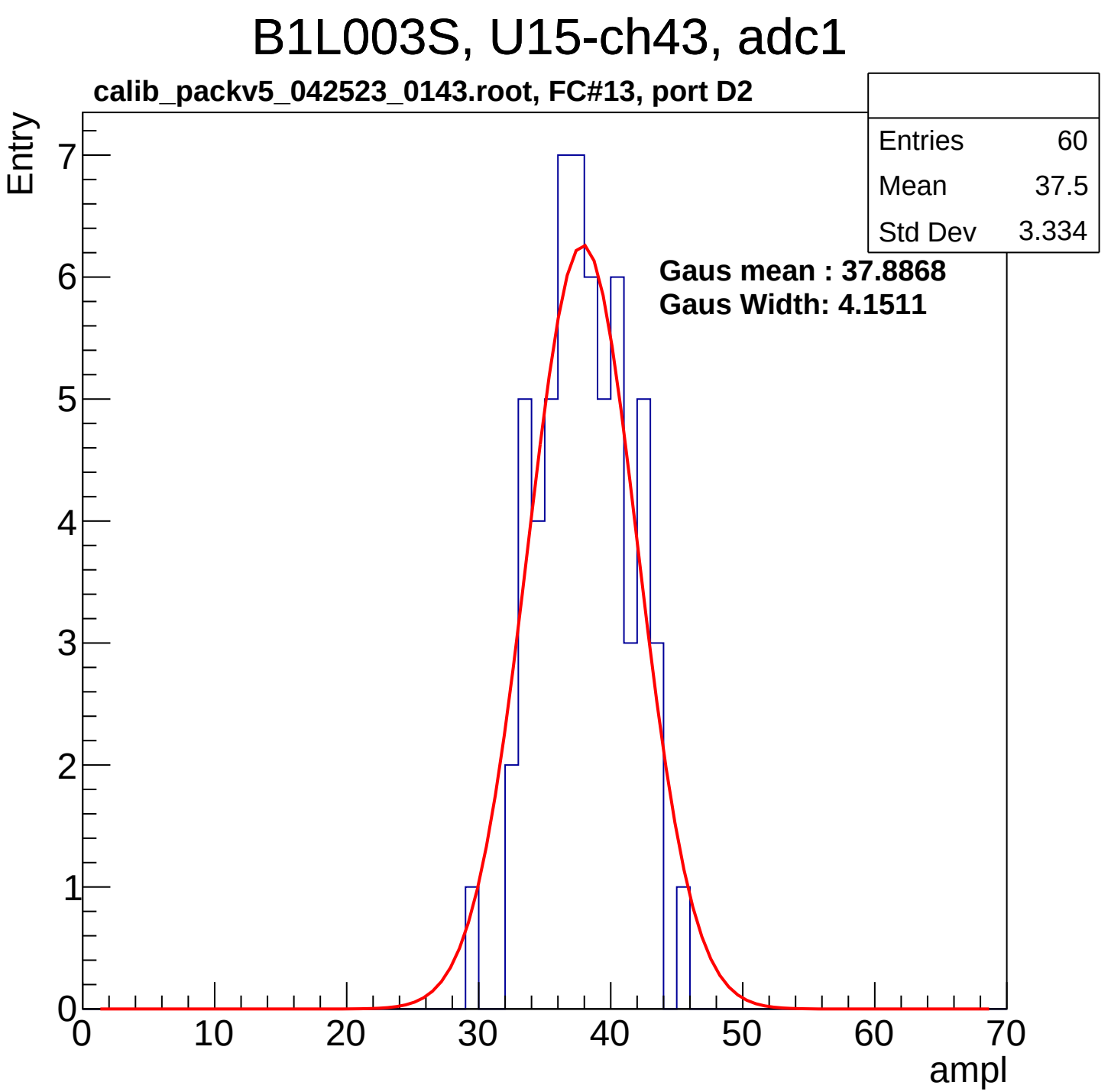

B1L003S, U15-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 30.38
Std Dev 4.775
Gaus mean : 31.1341
Gaus Width: 3.5690
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 37.5
Std Dev 3.334
Gaus mean : 37.8868
Gaus Width: 4.1511
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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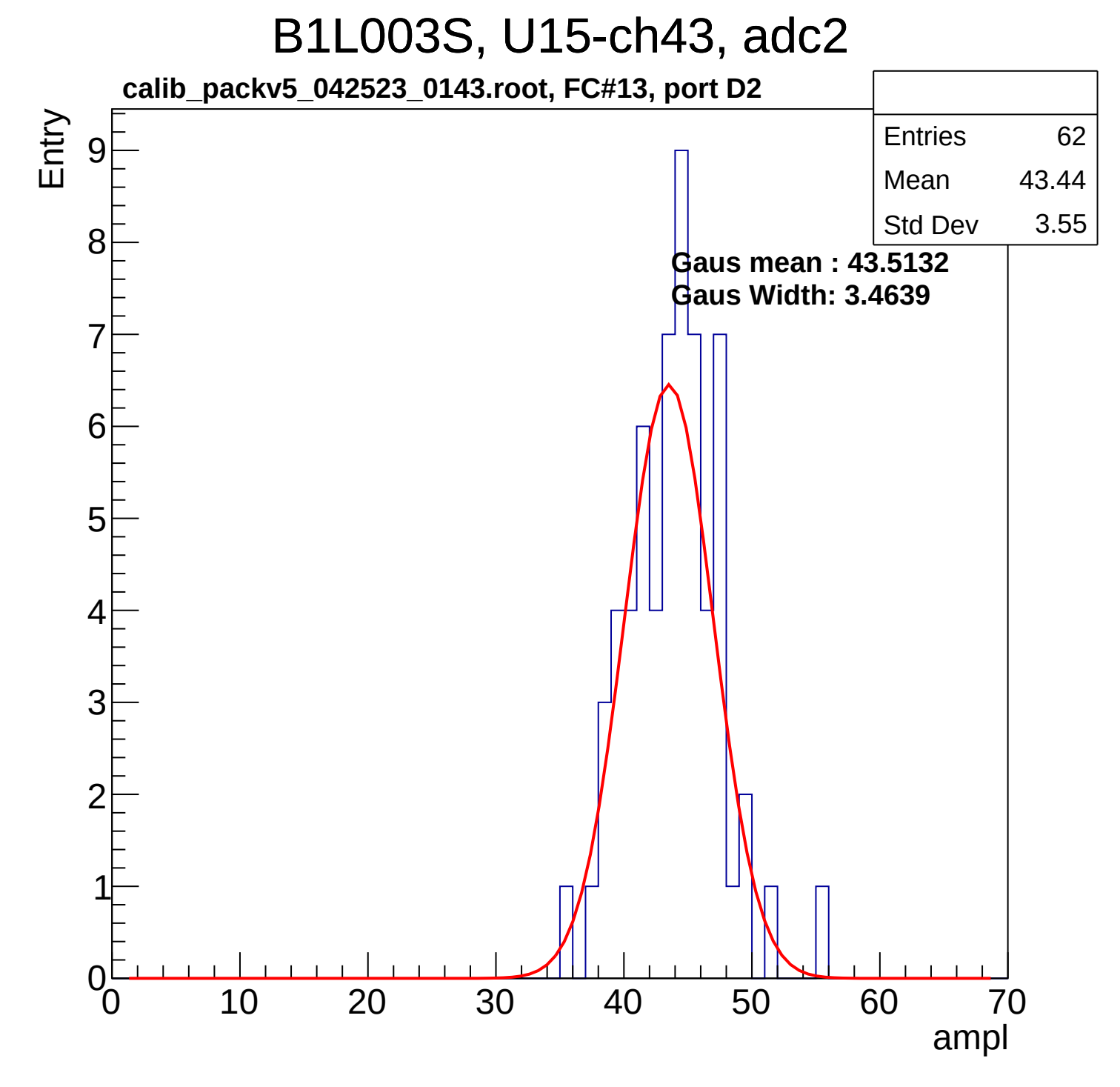
B1L003S, U15-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.44
Std Dev 3.55
Gaus mean : 43.5132
Gaus Width: 3.4639
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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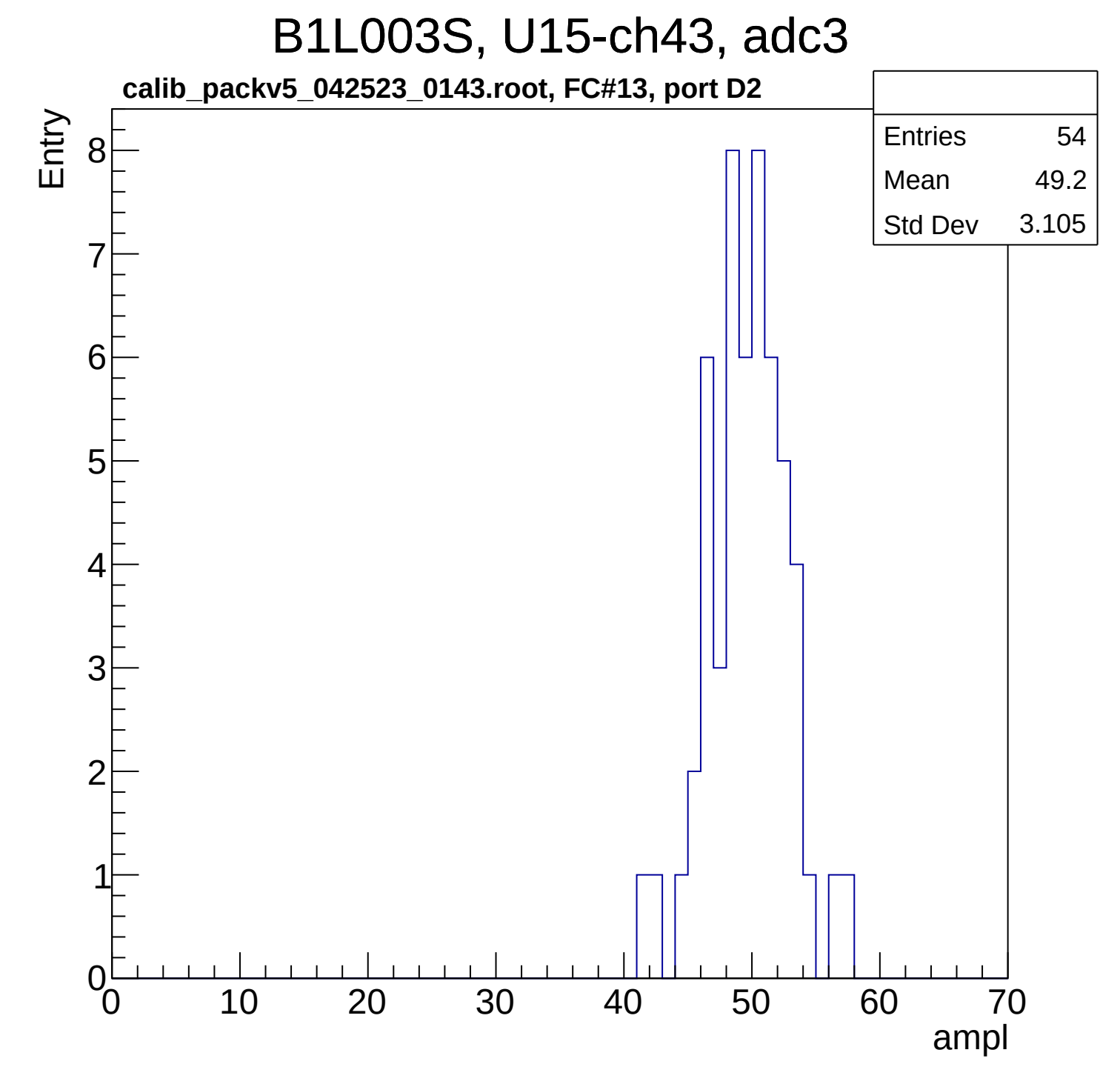
B1L003S, U15-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.2
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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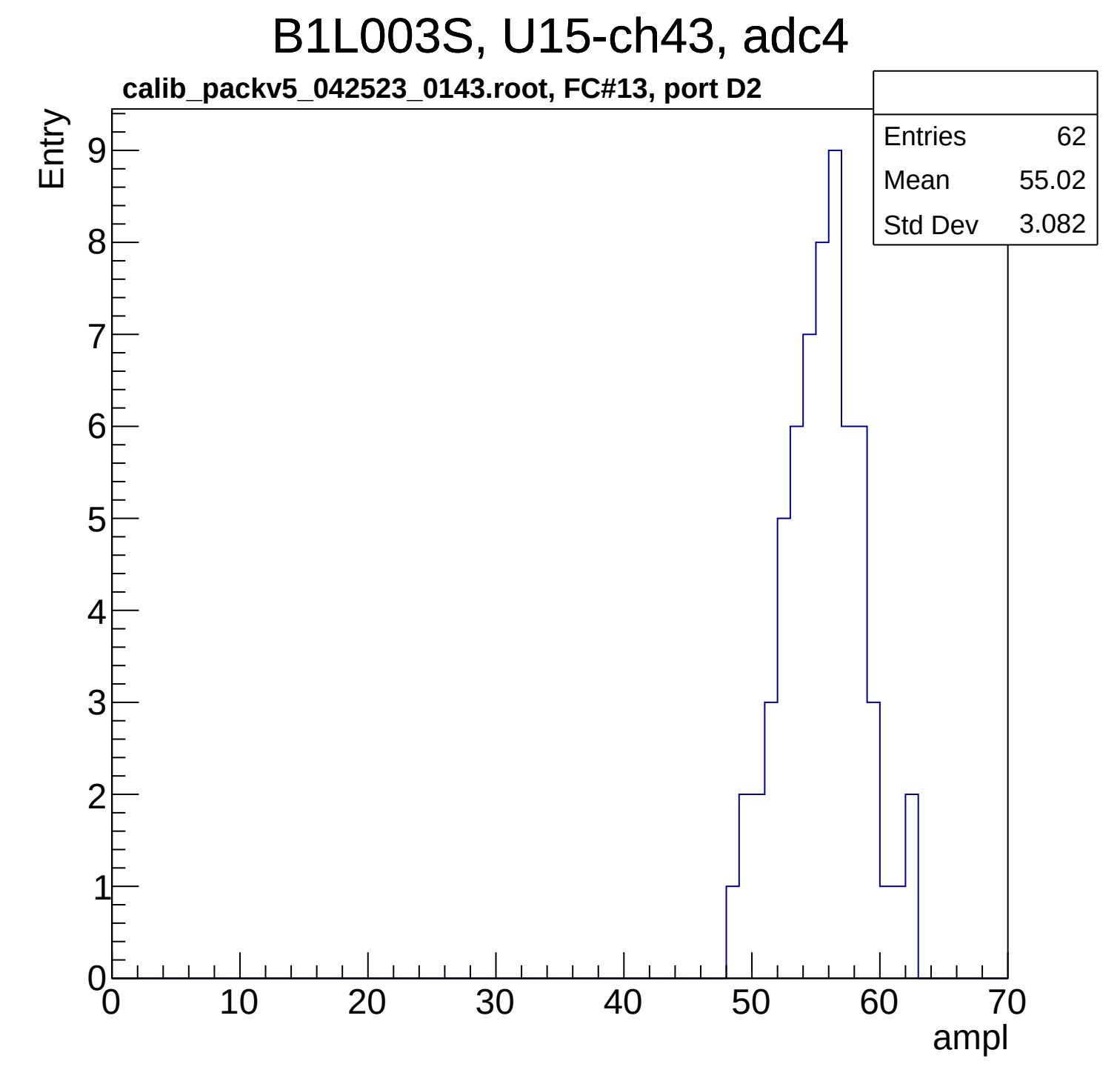
B1L003S, U15-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 55.02
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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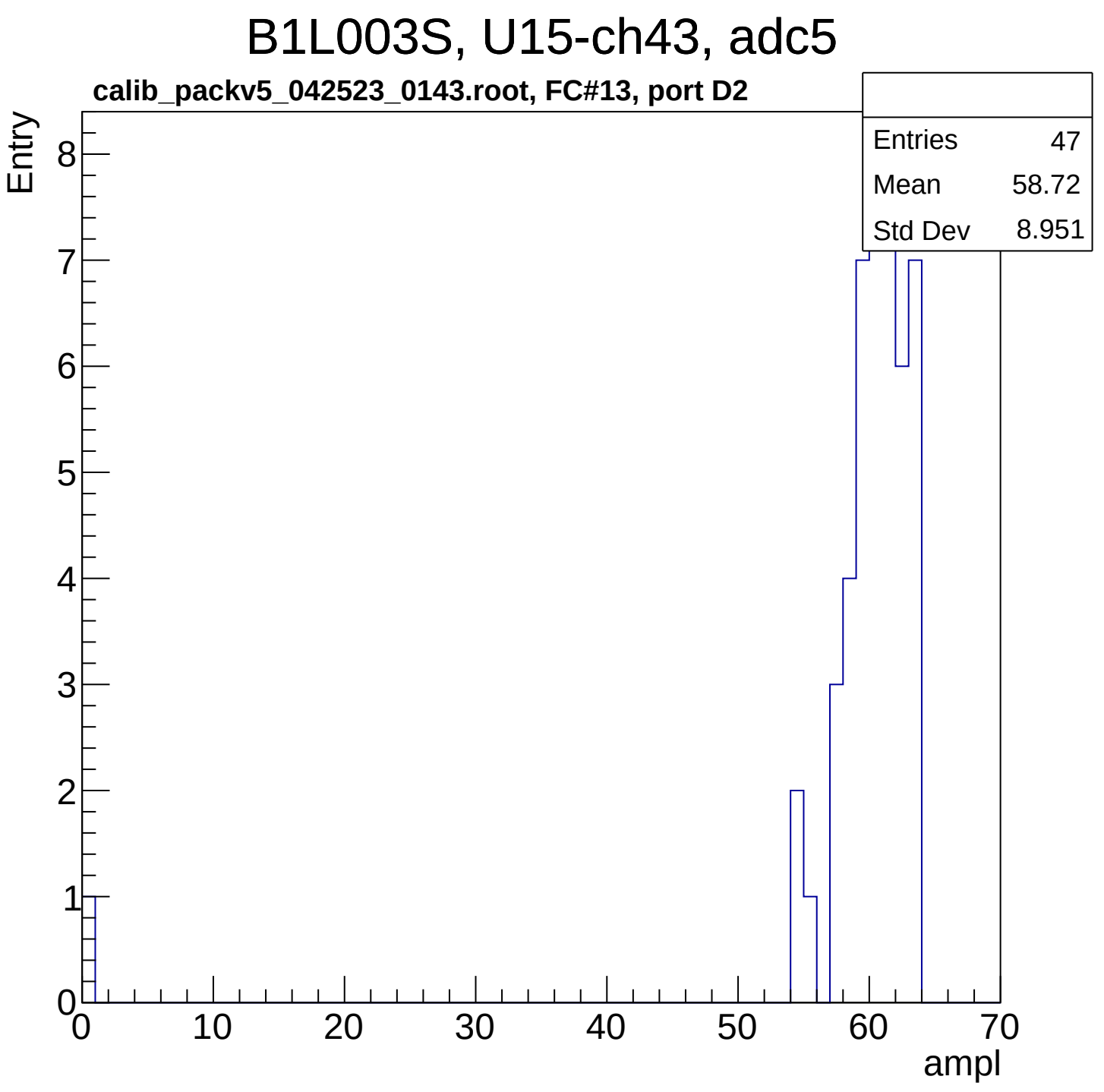
B1L003S, U15-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.72
Std Dev 8.951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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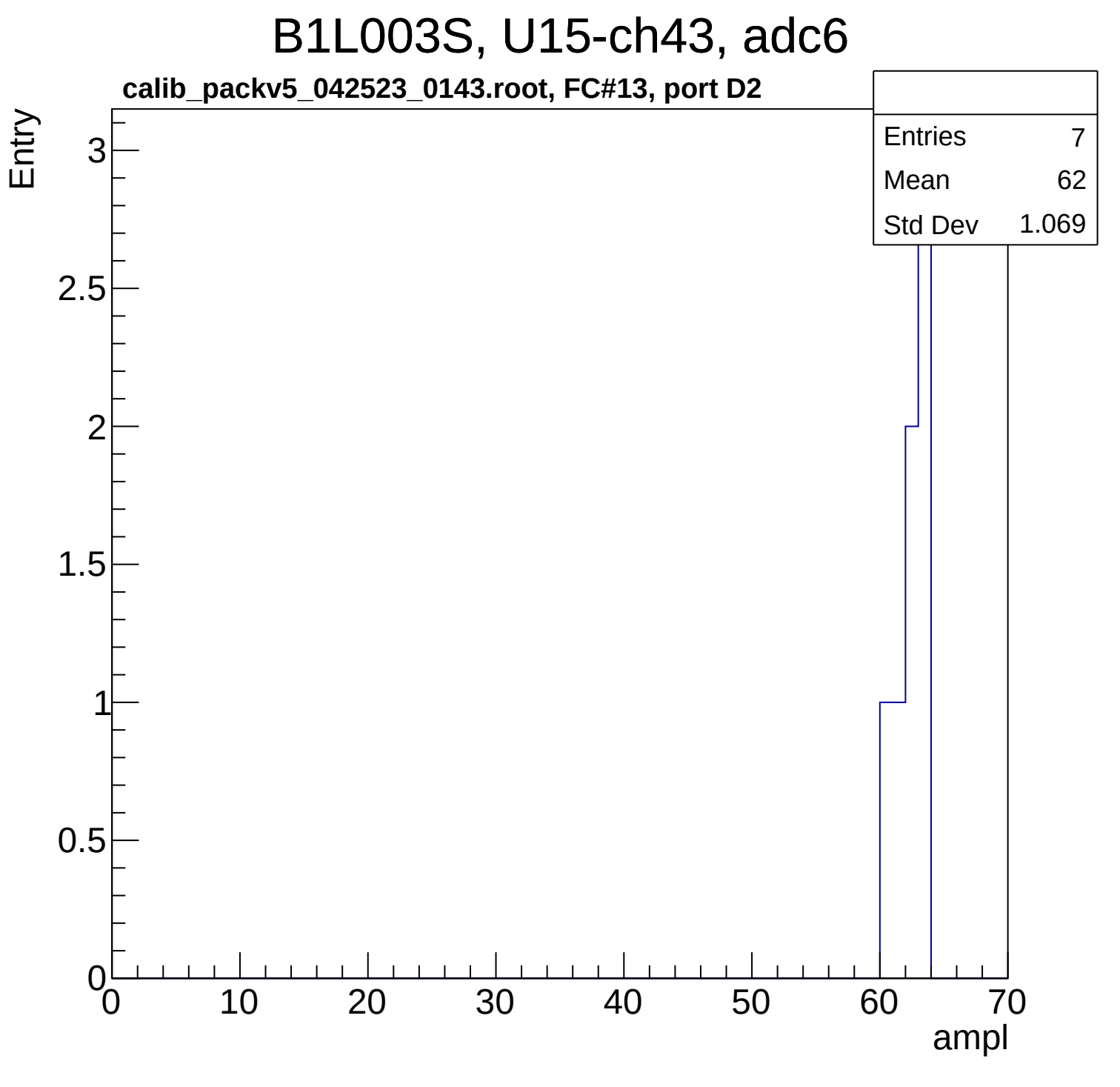
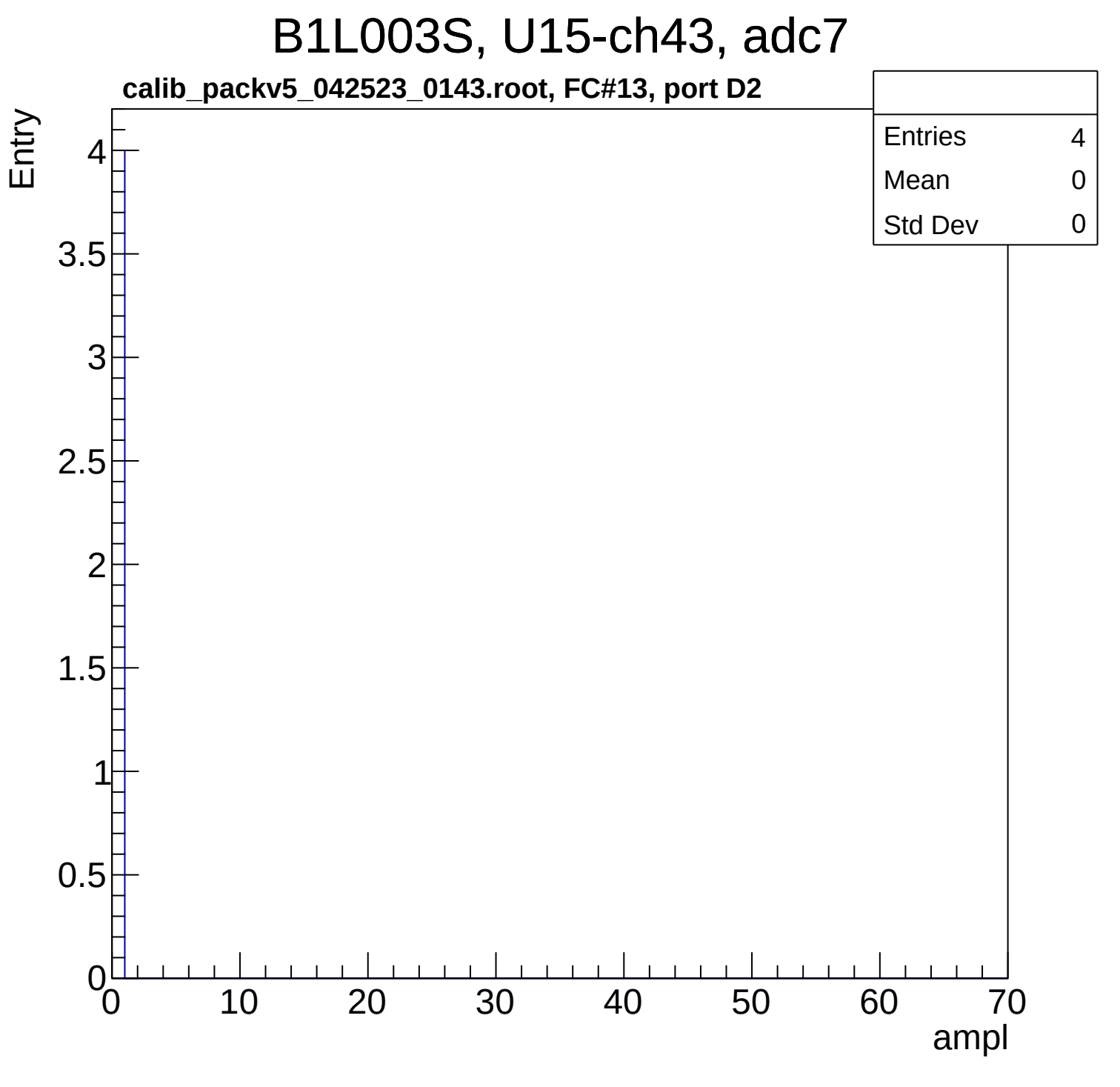
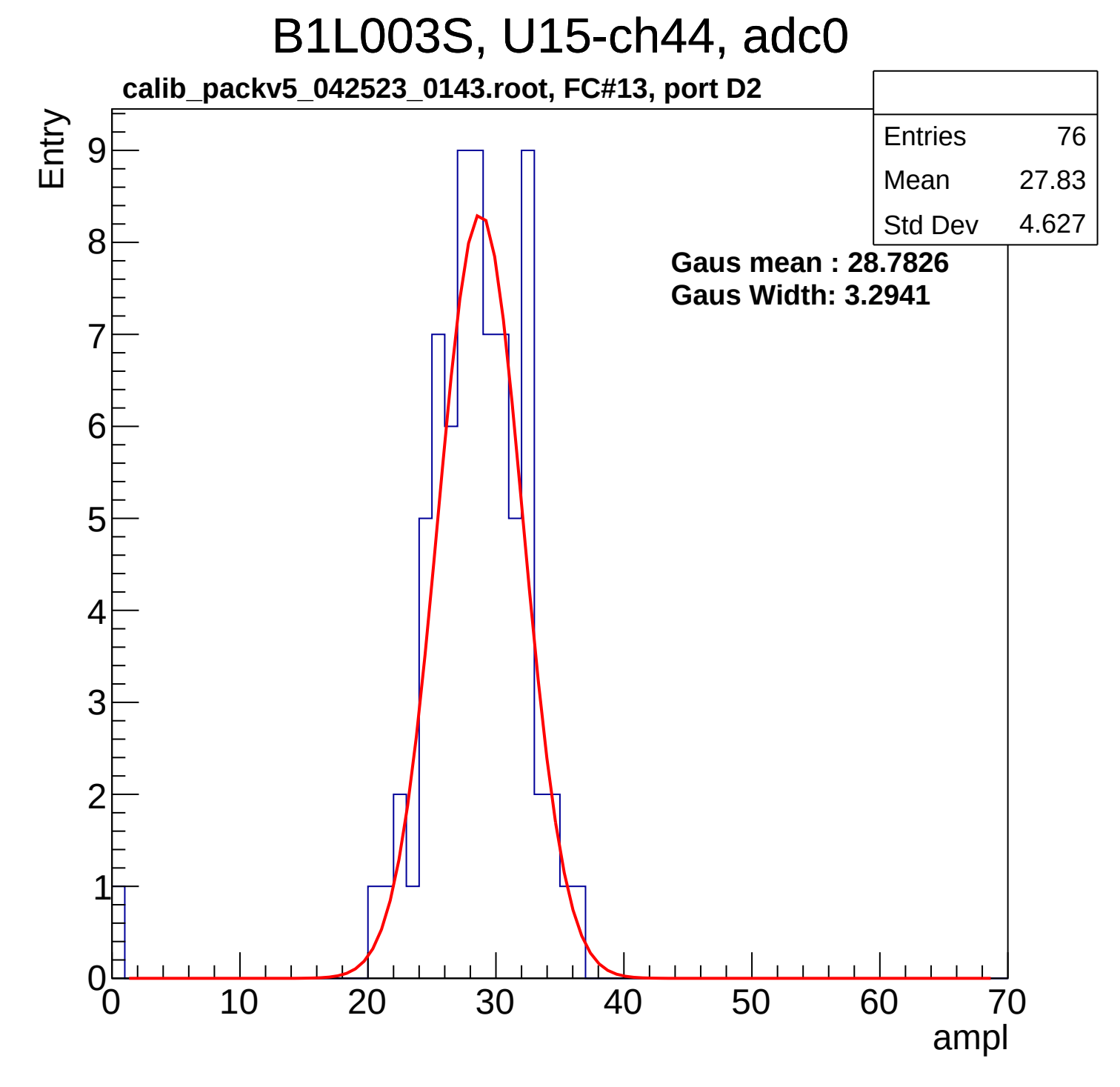
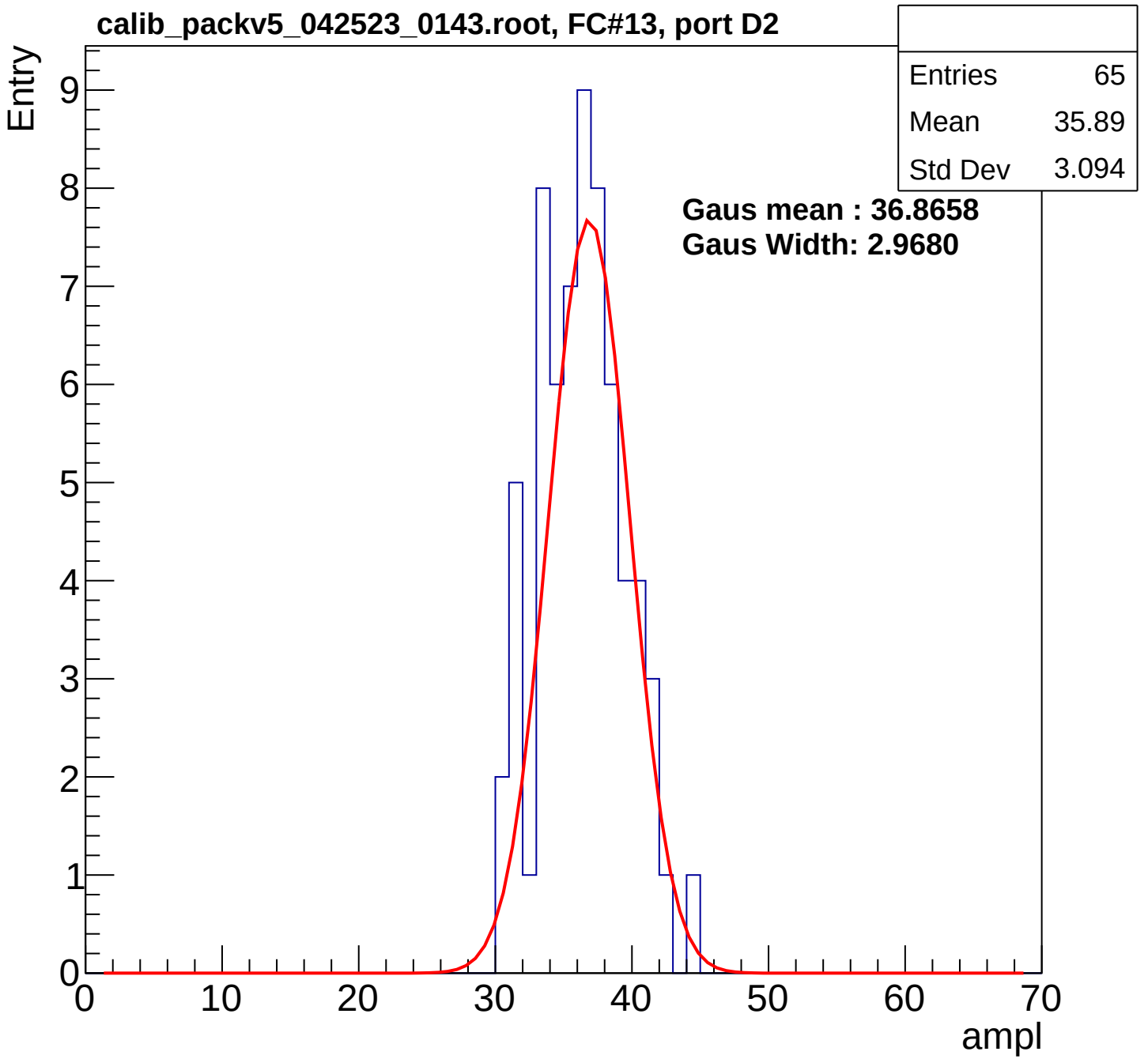

B1L003S, U15-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 1.069
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 27.83
Std Dev 4.627
Gaus mean : 28.7826
Gaus Width: 3.2941
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.89
Std Dev 3.094
Gaus mean : 36.8658
Gaus Width: 2.9680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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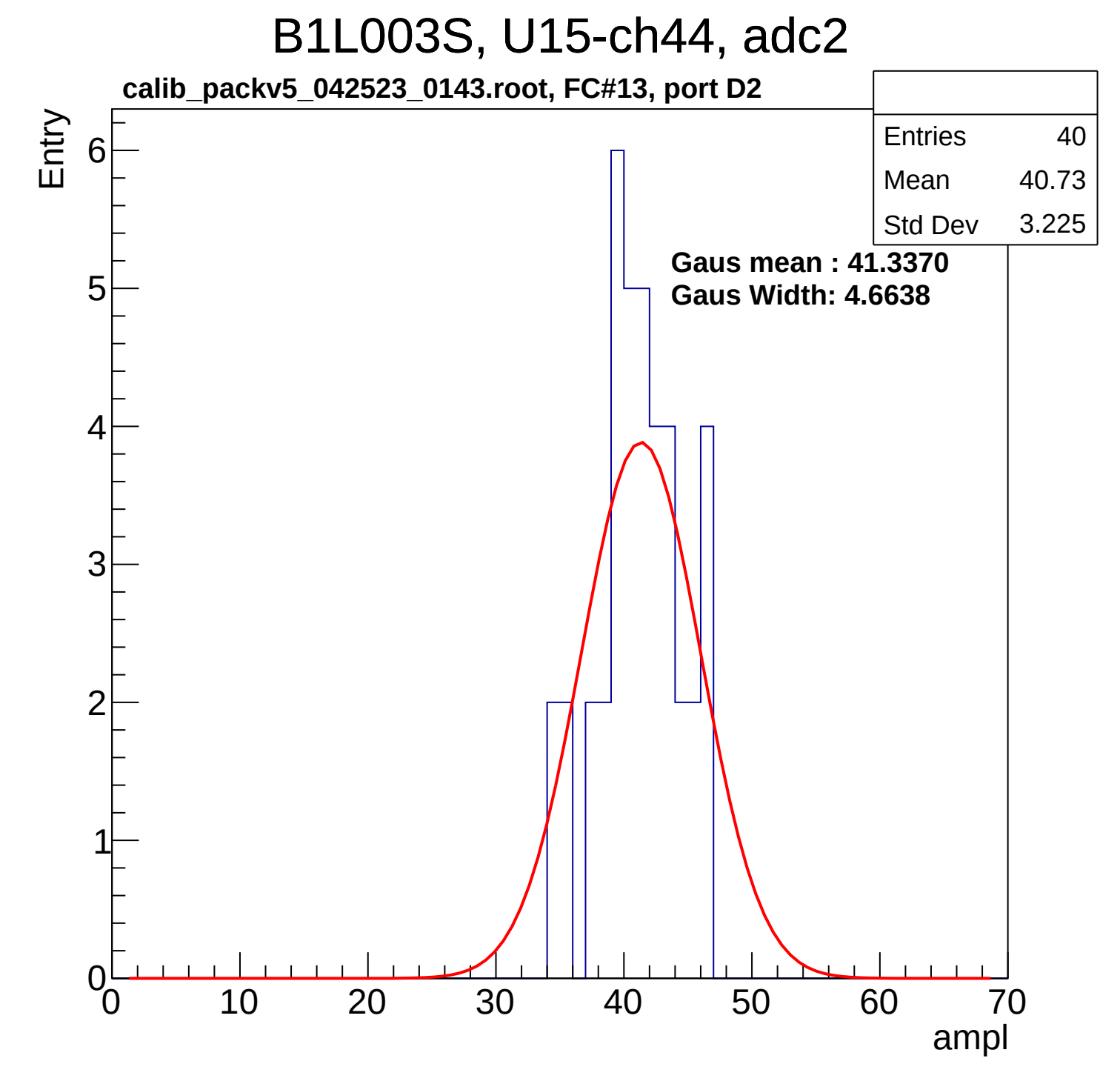
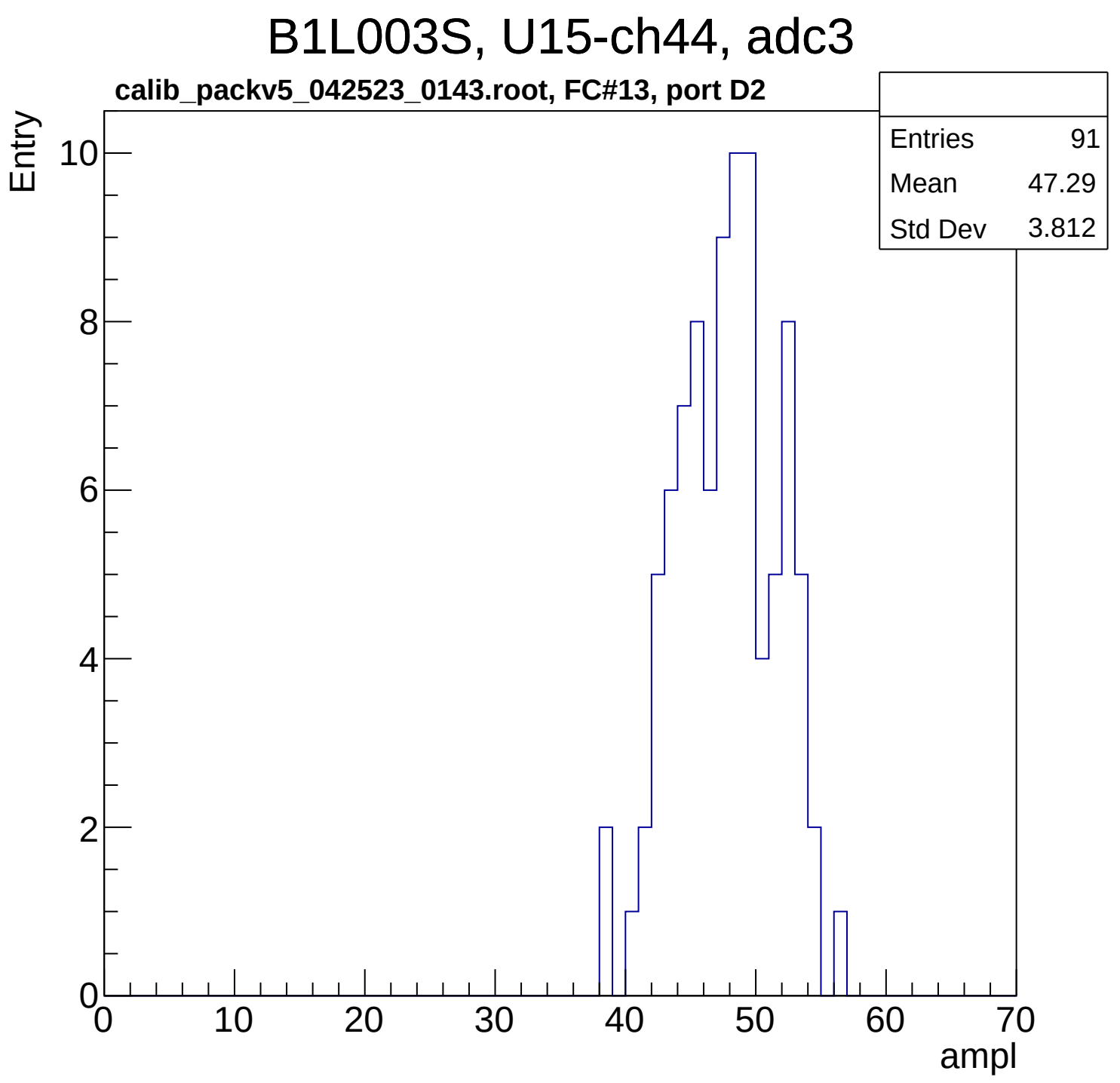
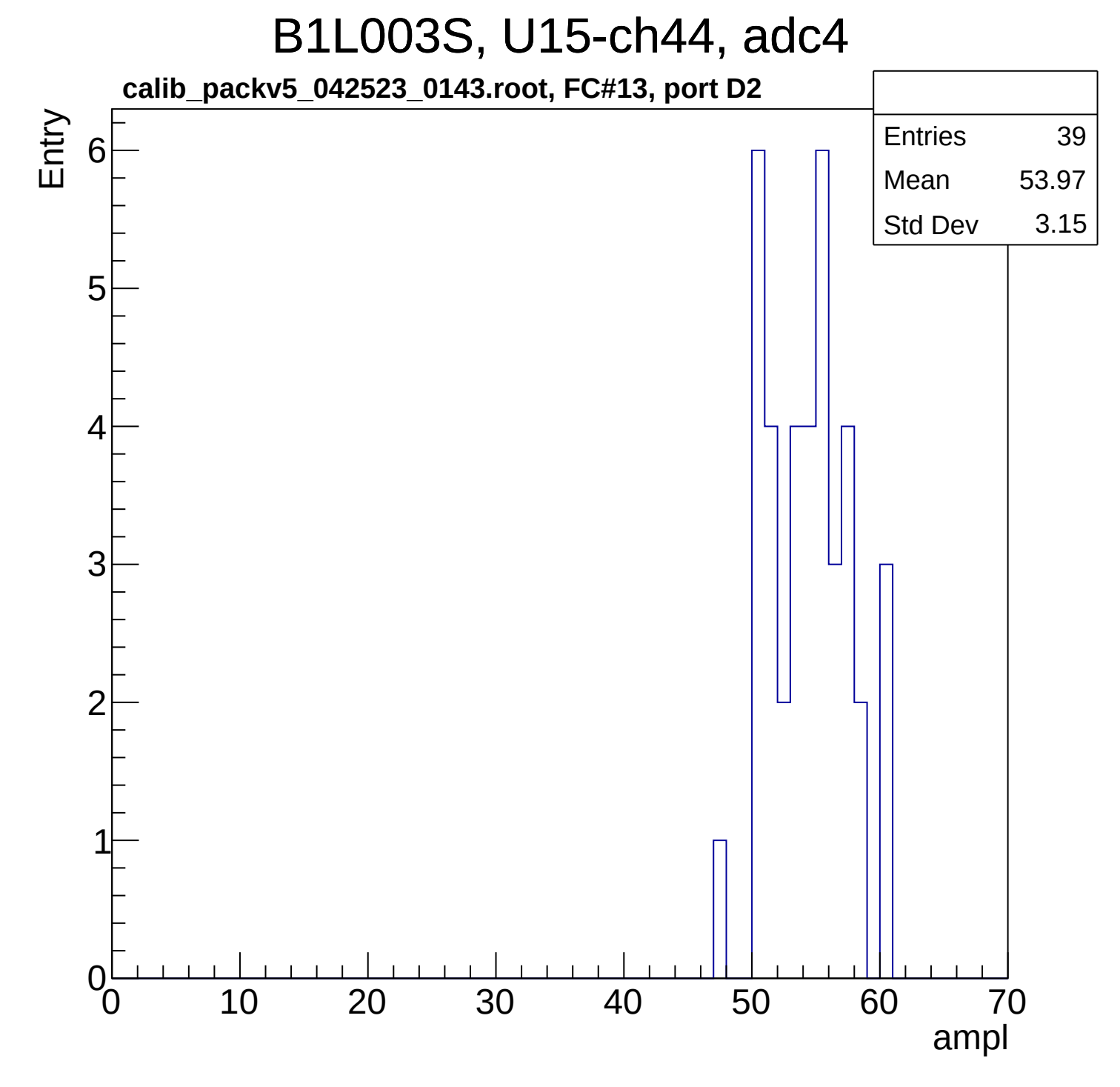
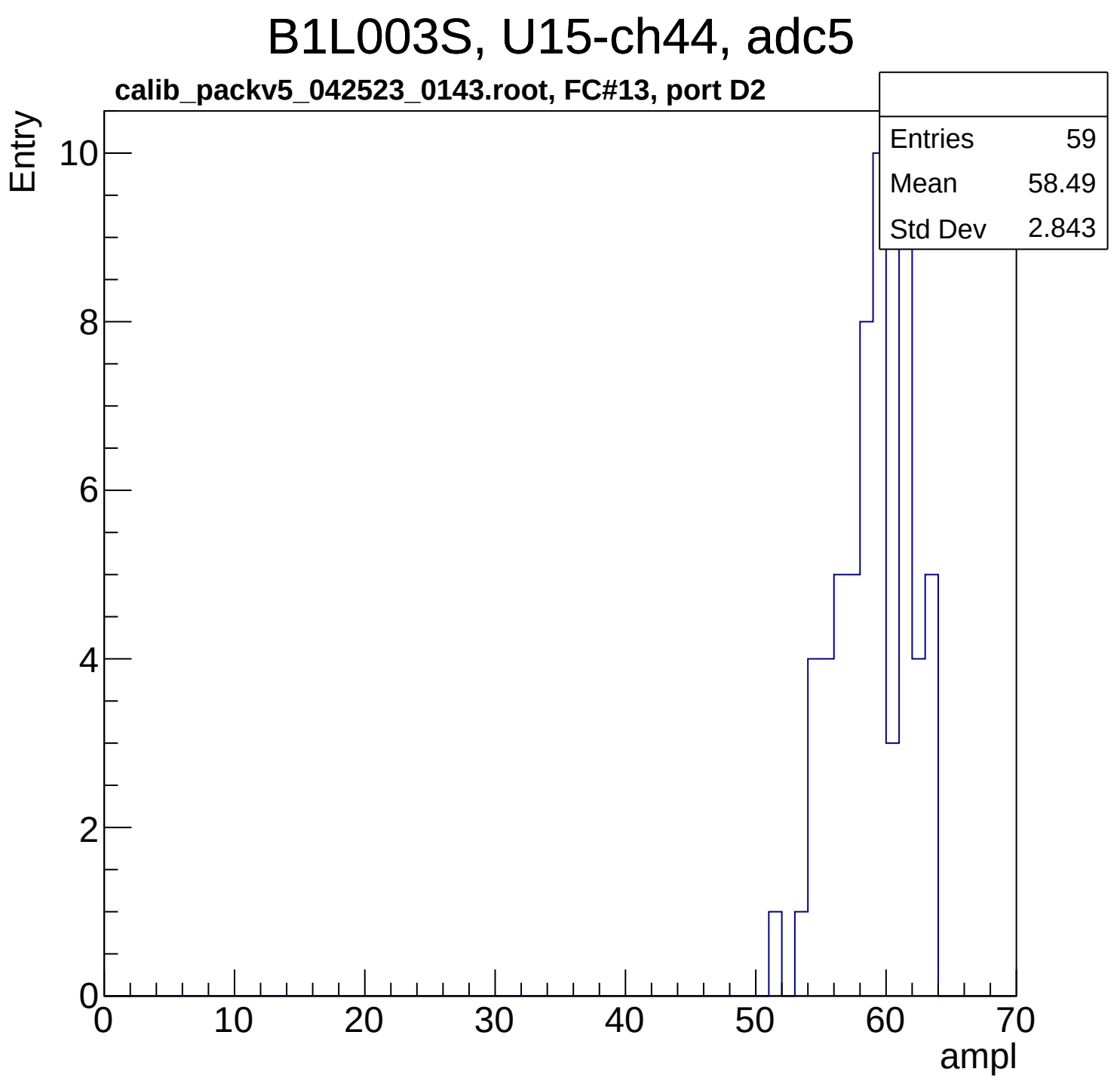
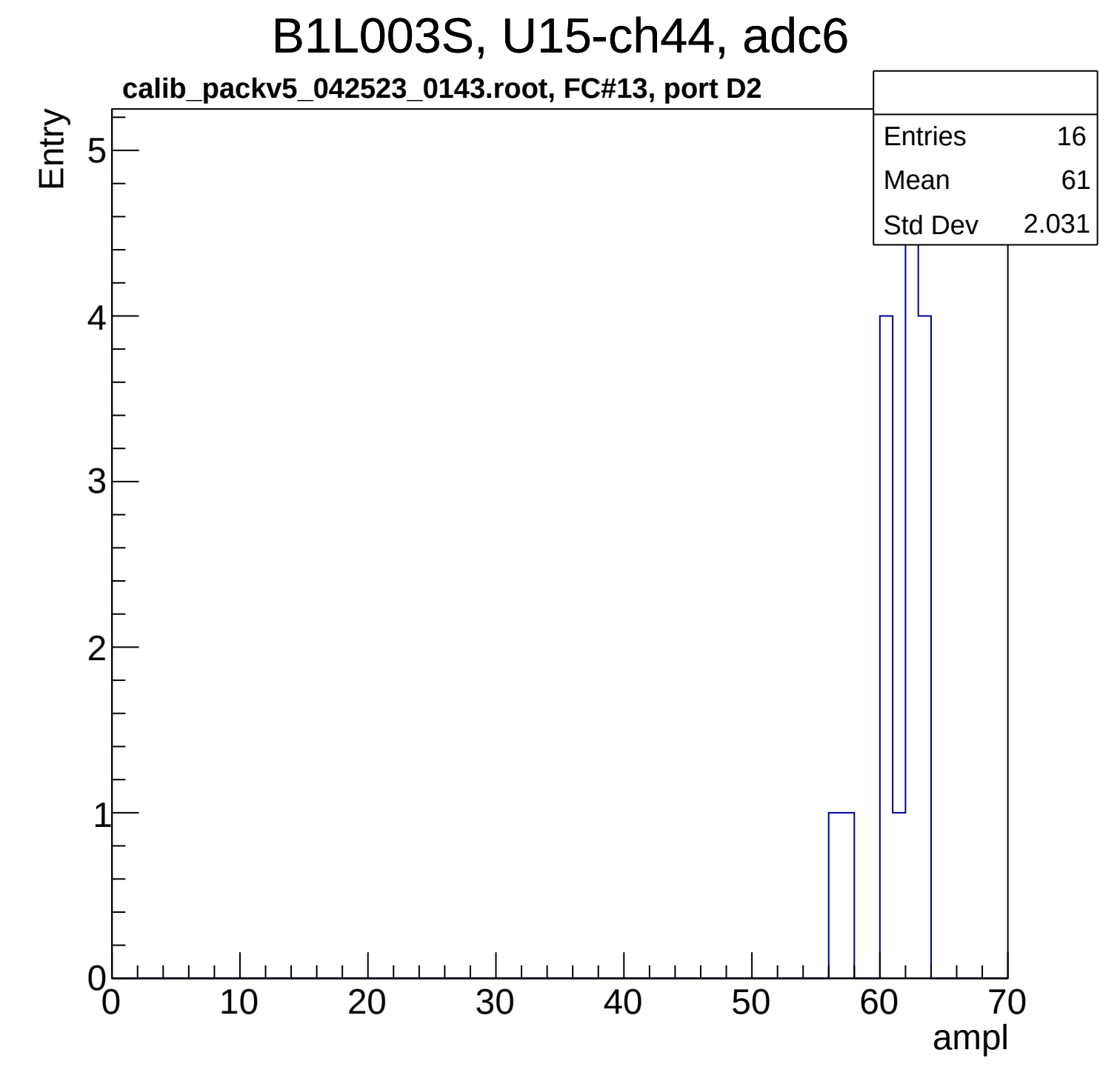
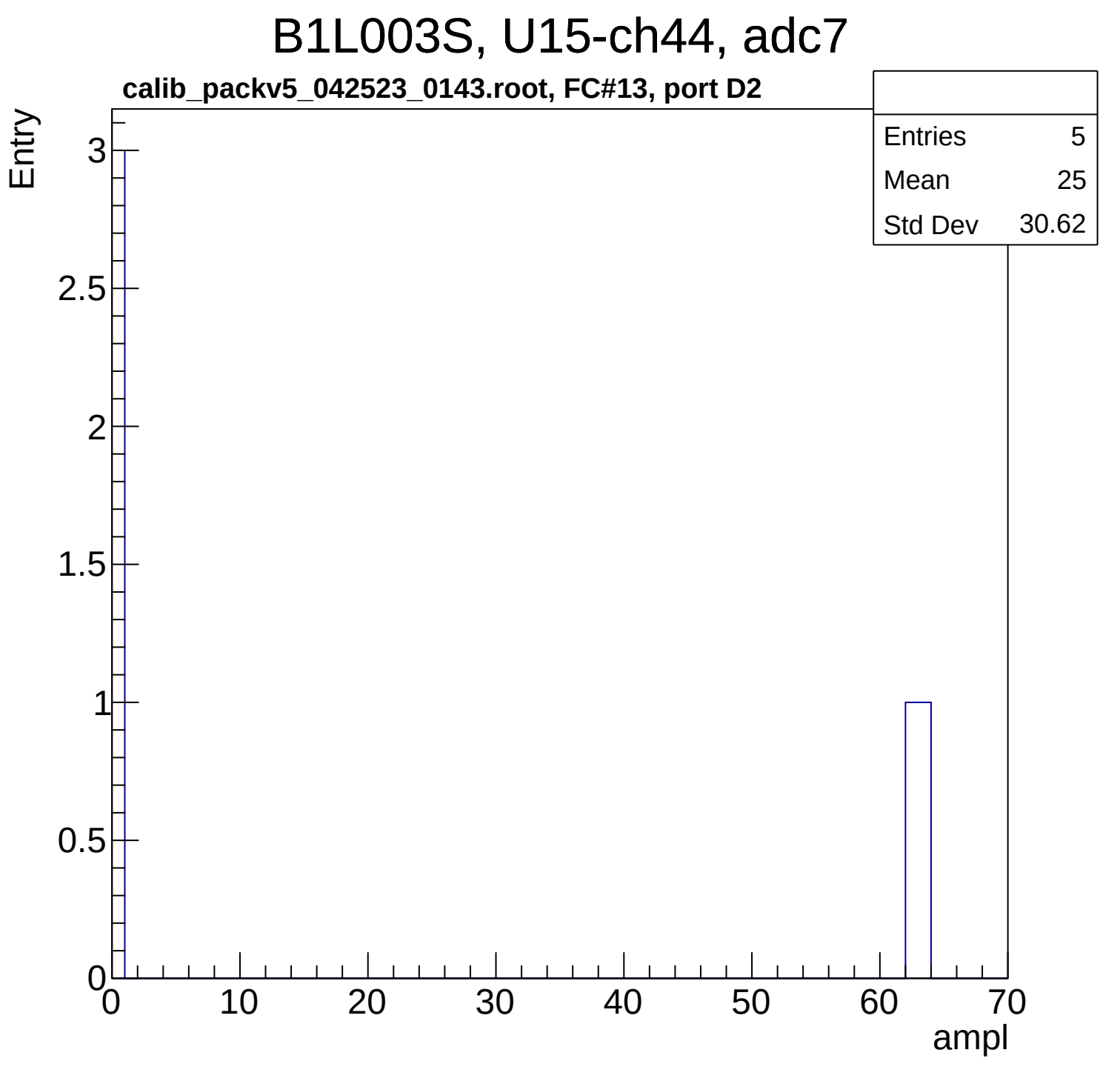

B1L003S, U15-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 40.73
Std Dev 3.225
Gaus mean : 41.3370
Gaus Width: 4.6638
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

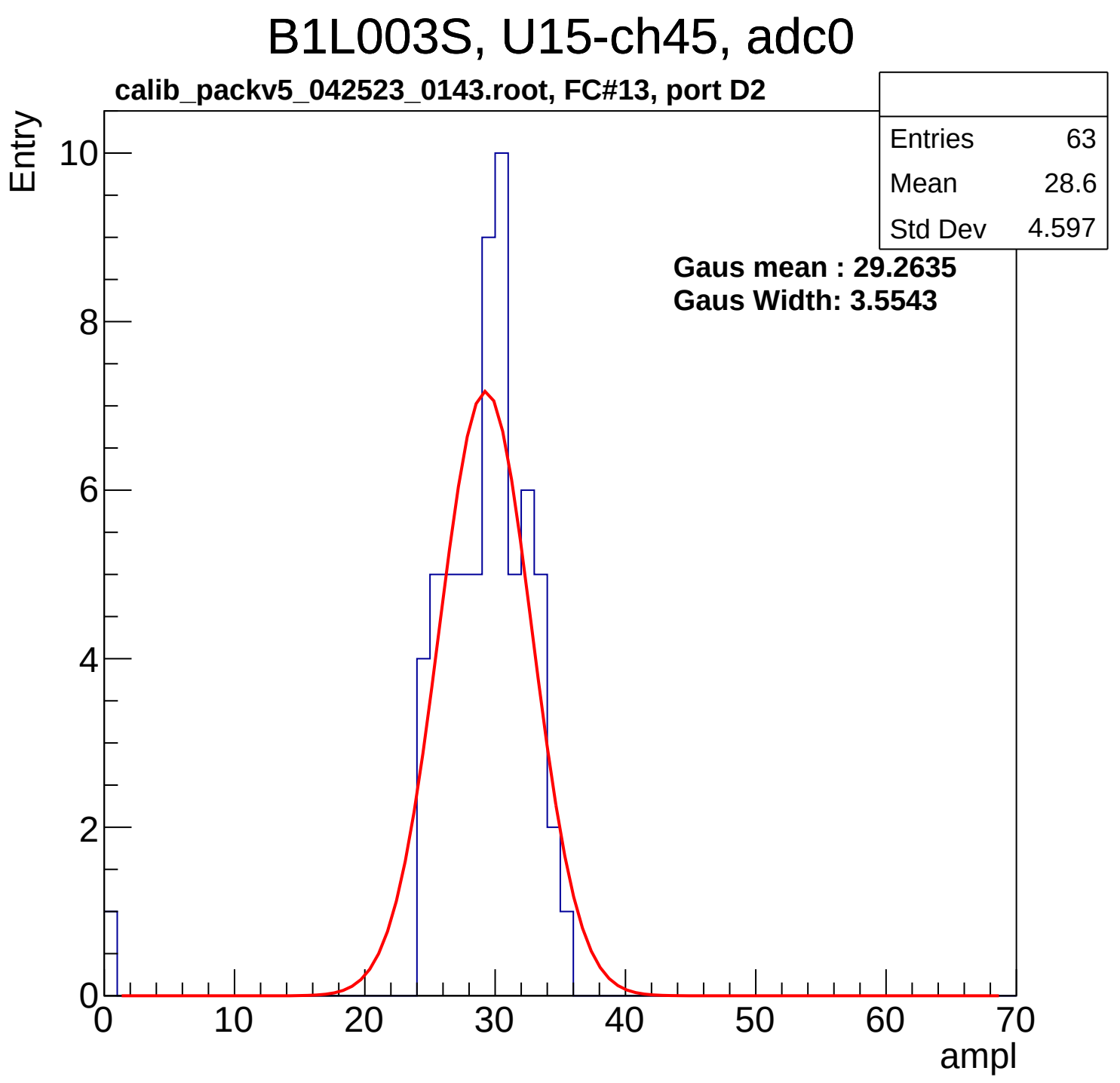

B1L003S, U15-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 91
Mean 47.29
Std Dev 3.812
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

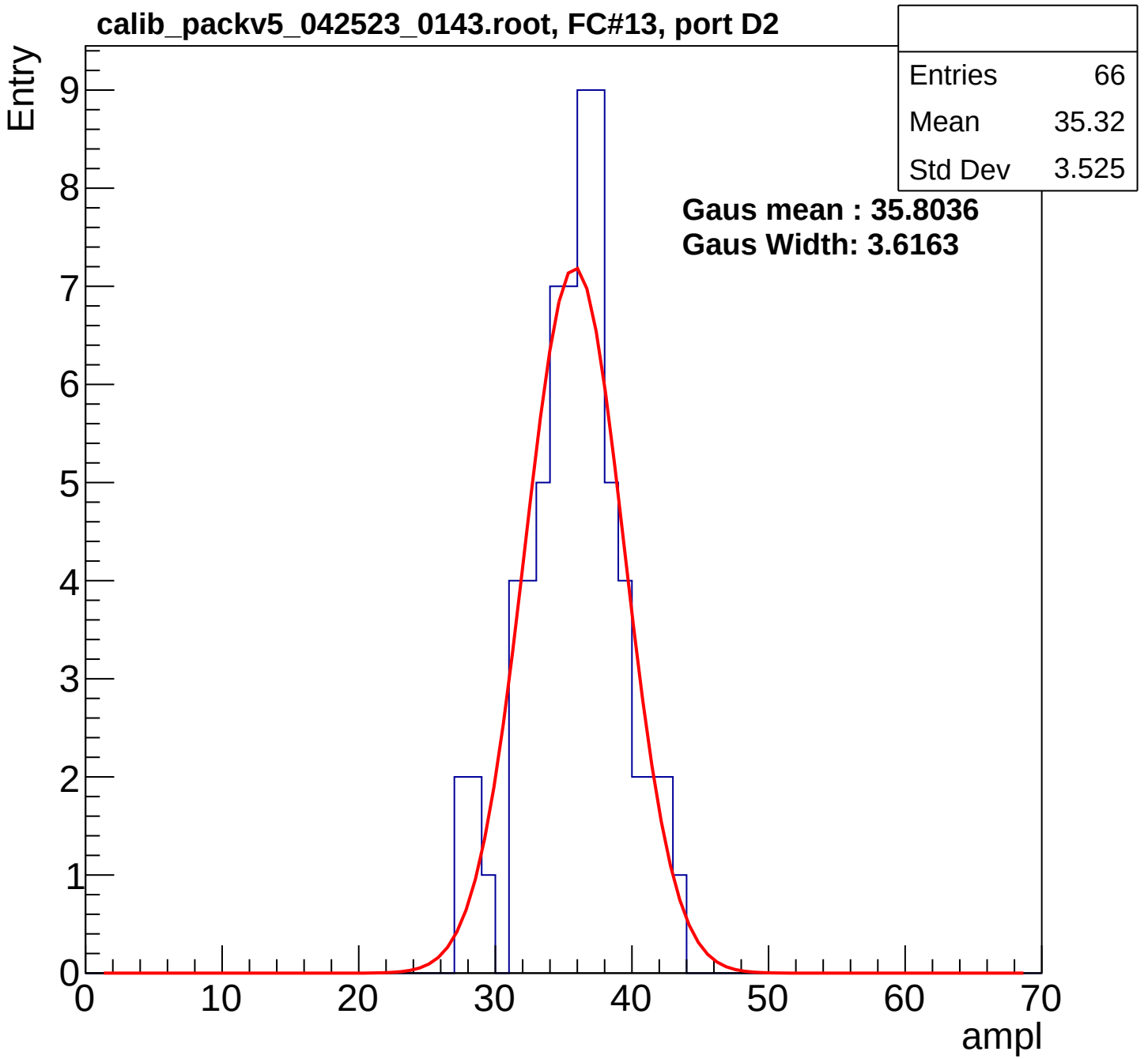

B1L003S, U15-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 53.97
Std Dev 3.15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.49
Std Dev 2.843
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61
Std Dev 2.031
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U15-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 25
Std Dev 30.62
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.6
Std Dev 4.597
Gaus mean : 29.2635
Gaus Width: 3.5543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.32
Std Dev 3.525
Gaus mean : 35.8036
Gaus Width: 3.6163
Entry
ampl
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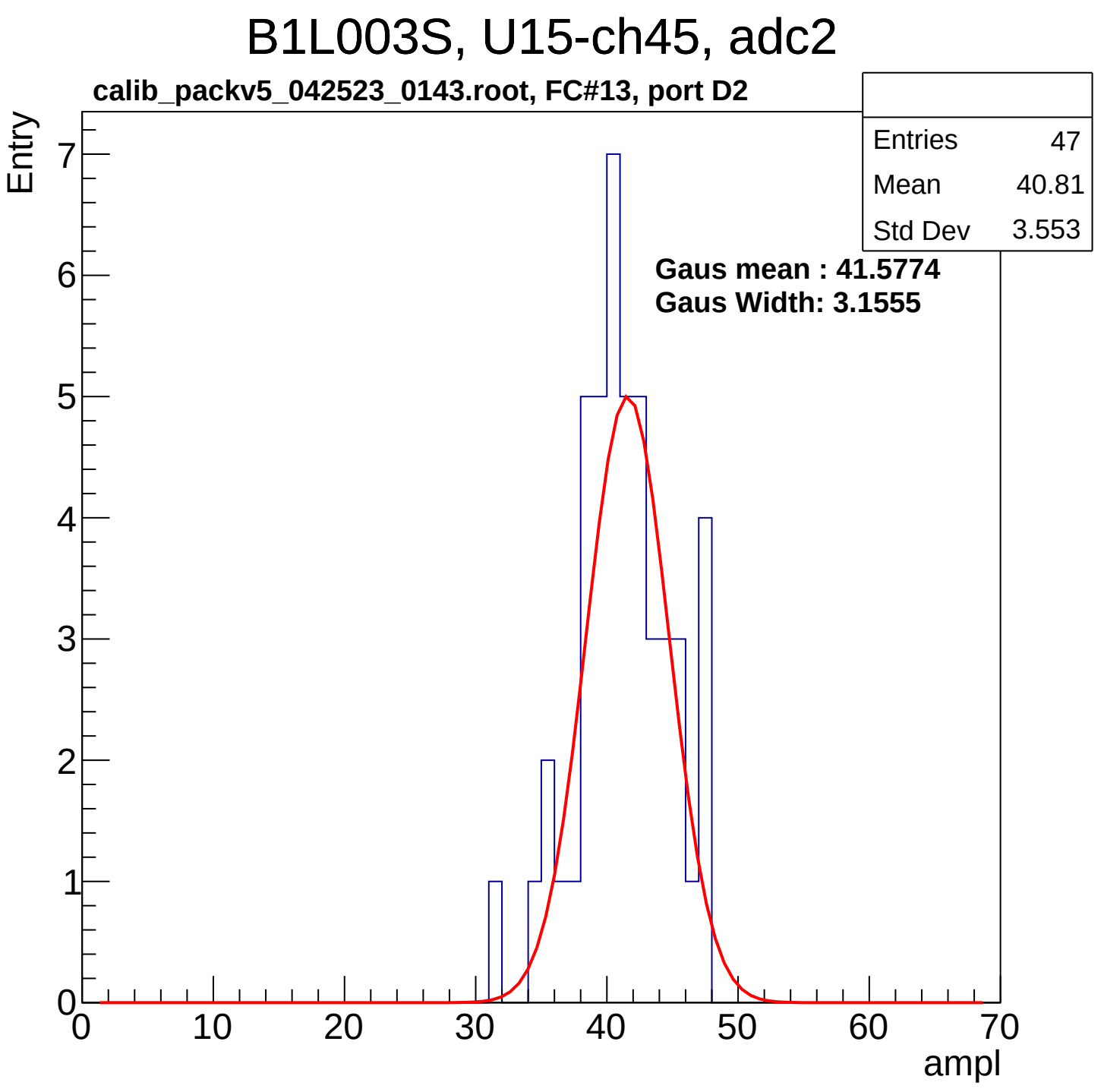
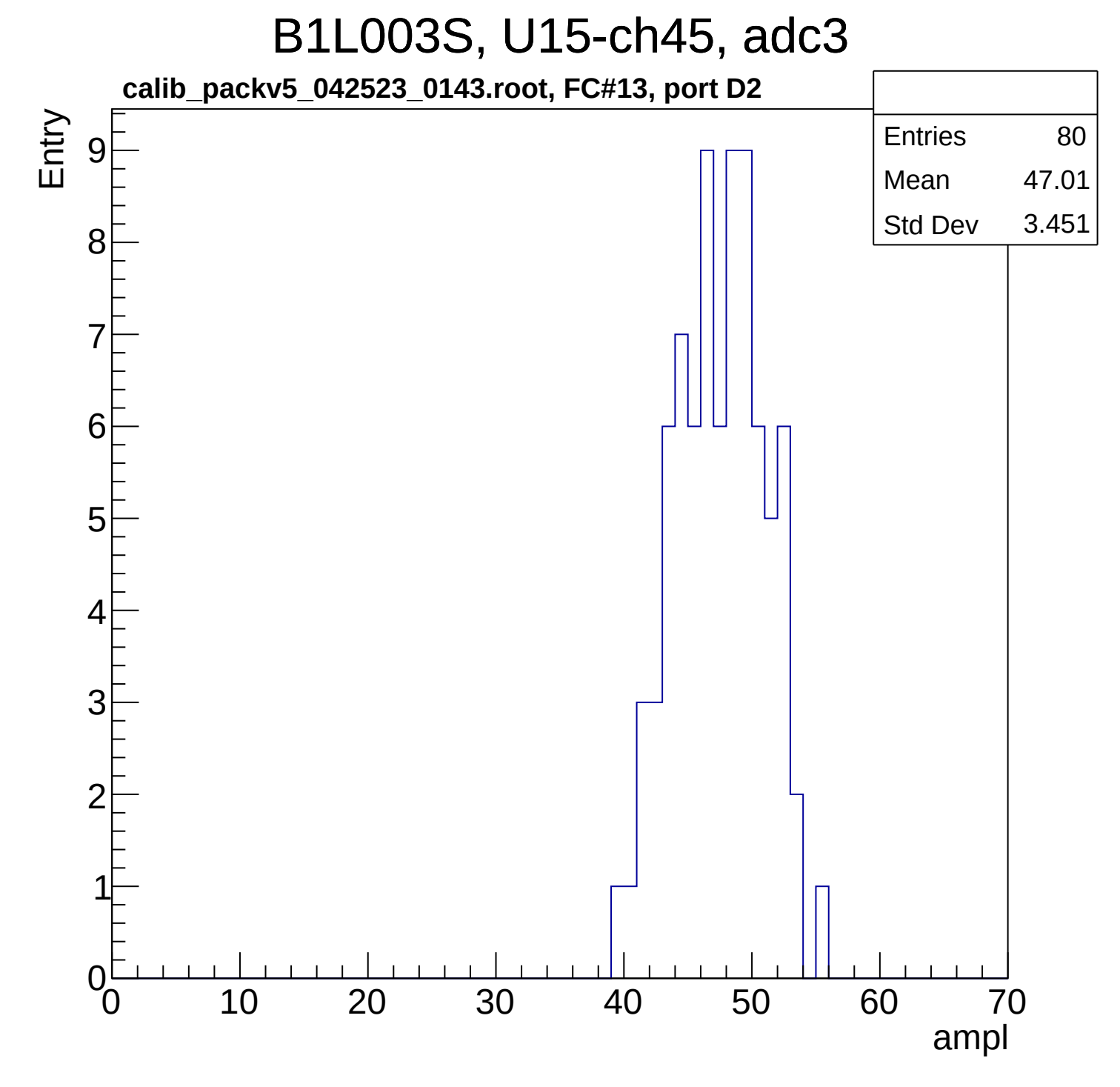
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 40.81
Std Dev 3.553
Gaus mean : 41.5774
Gaus Width: 3.1555
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 47.01
Std Dev 3.451
Entry
ampl
0
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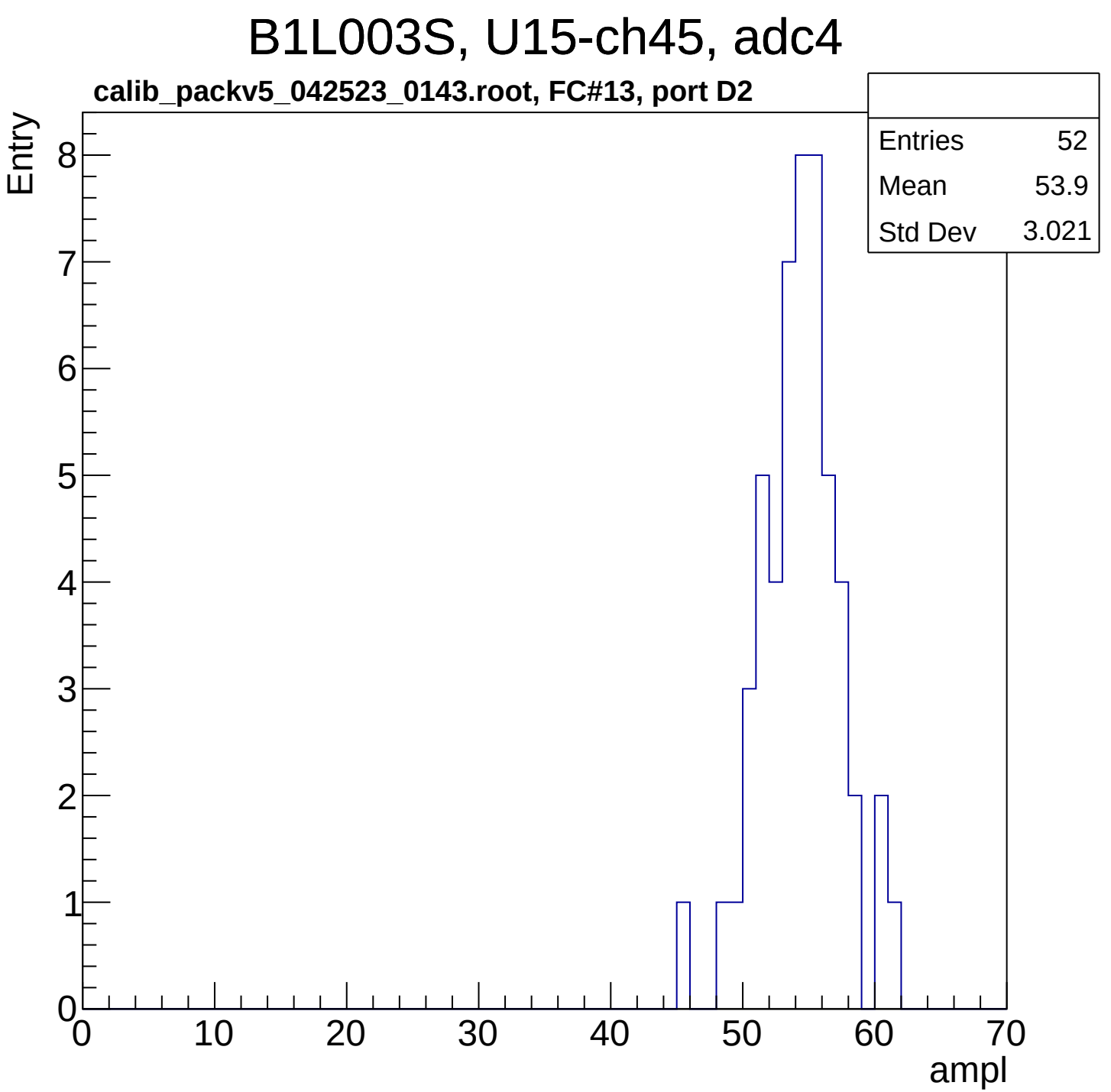
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53.9
Std Dev 3.021
Entry
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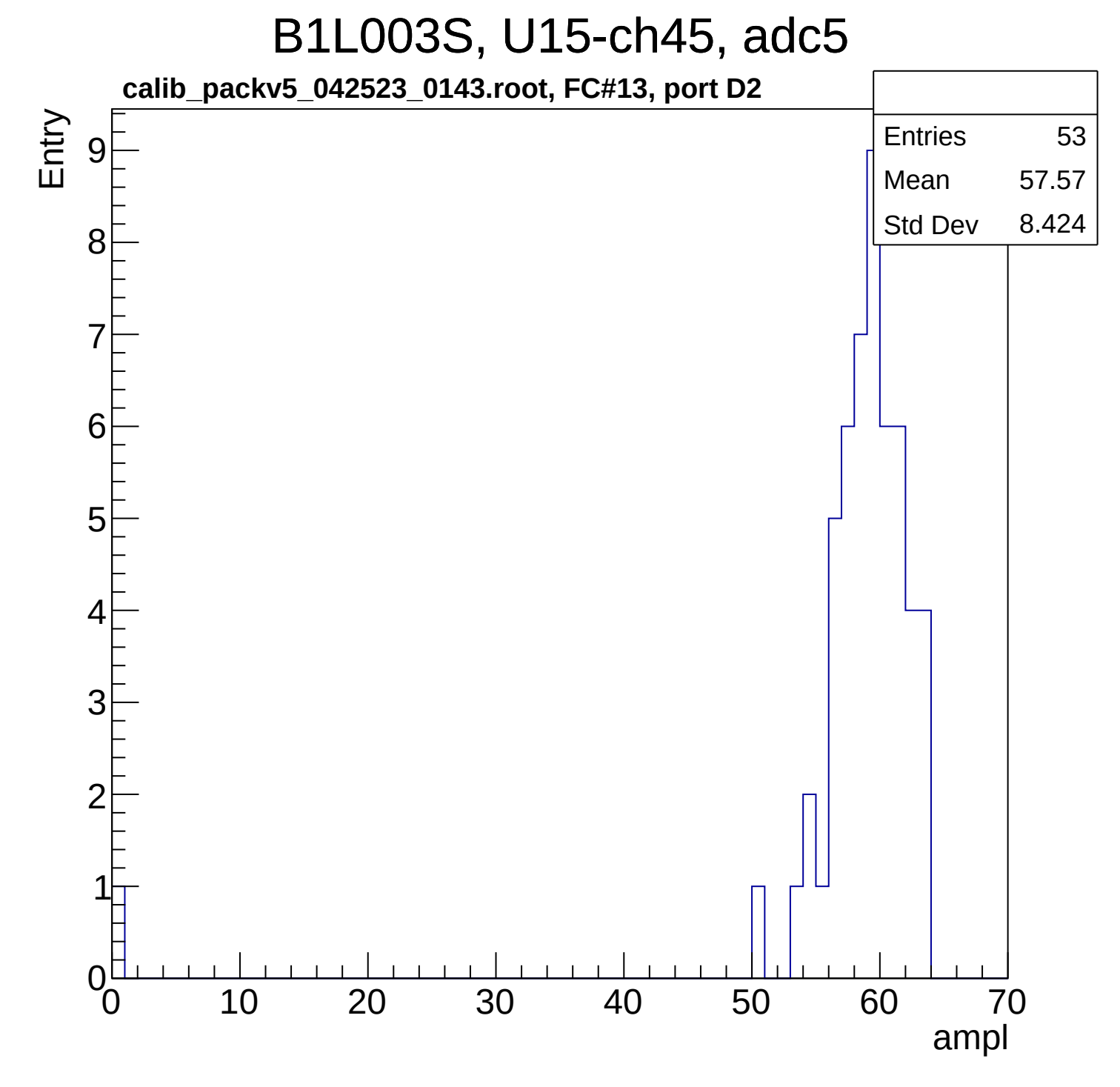
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 57.57
Std Dev 8.424
Entry
ampl
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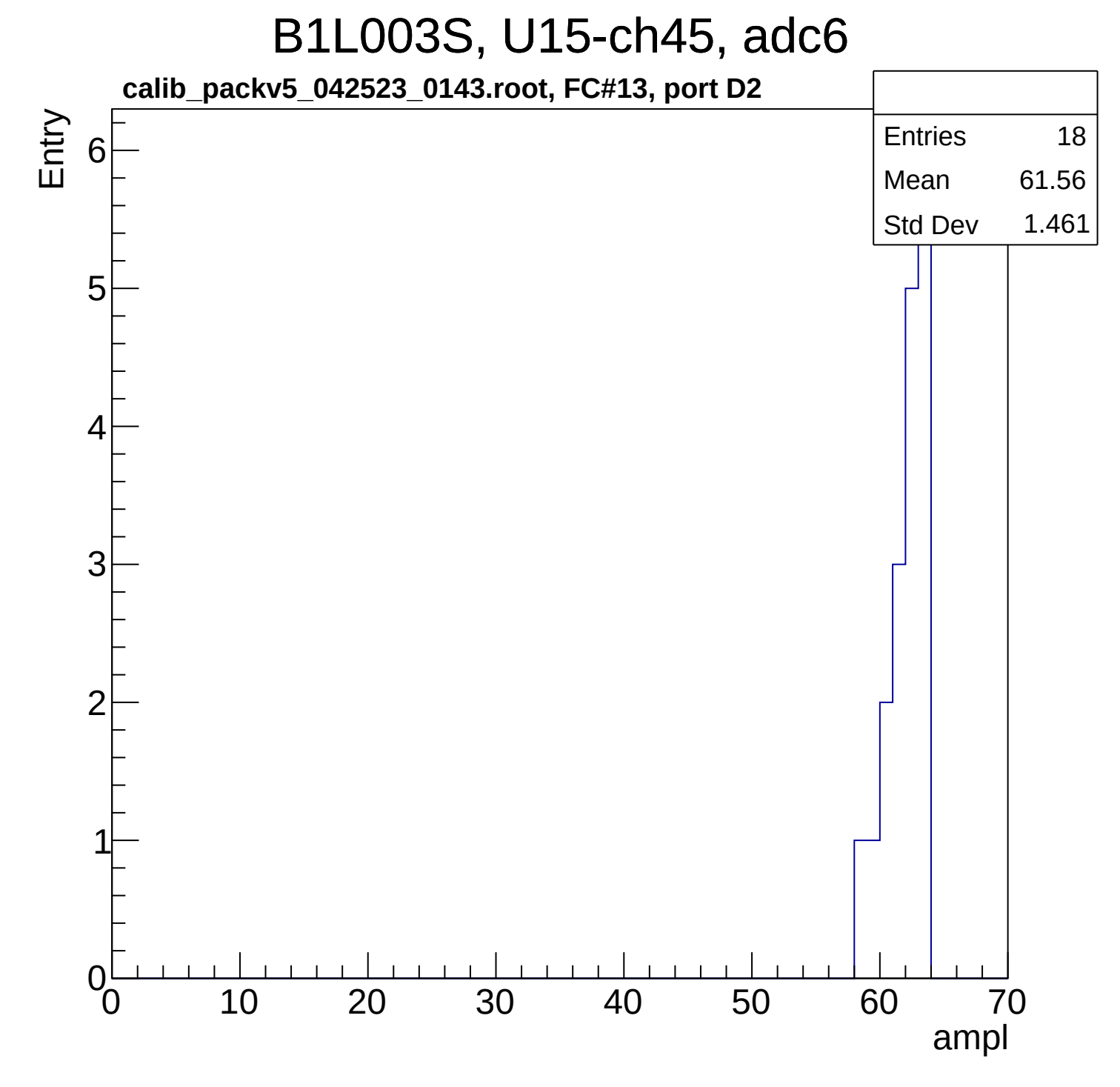
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 61.56
Std Dev 1.461
Entry
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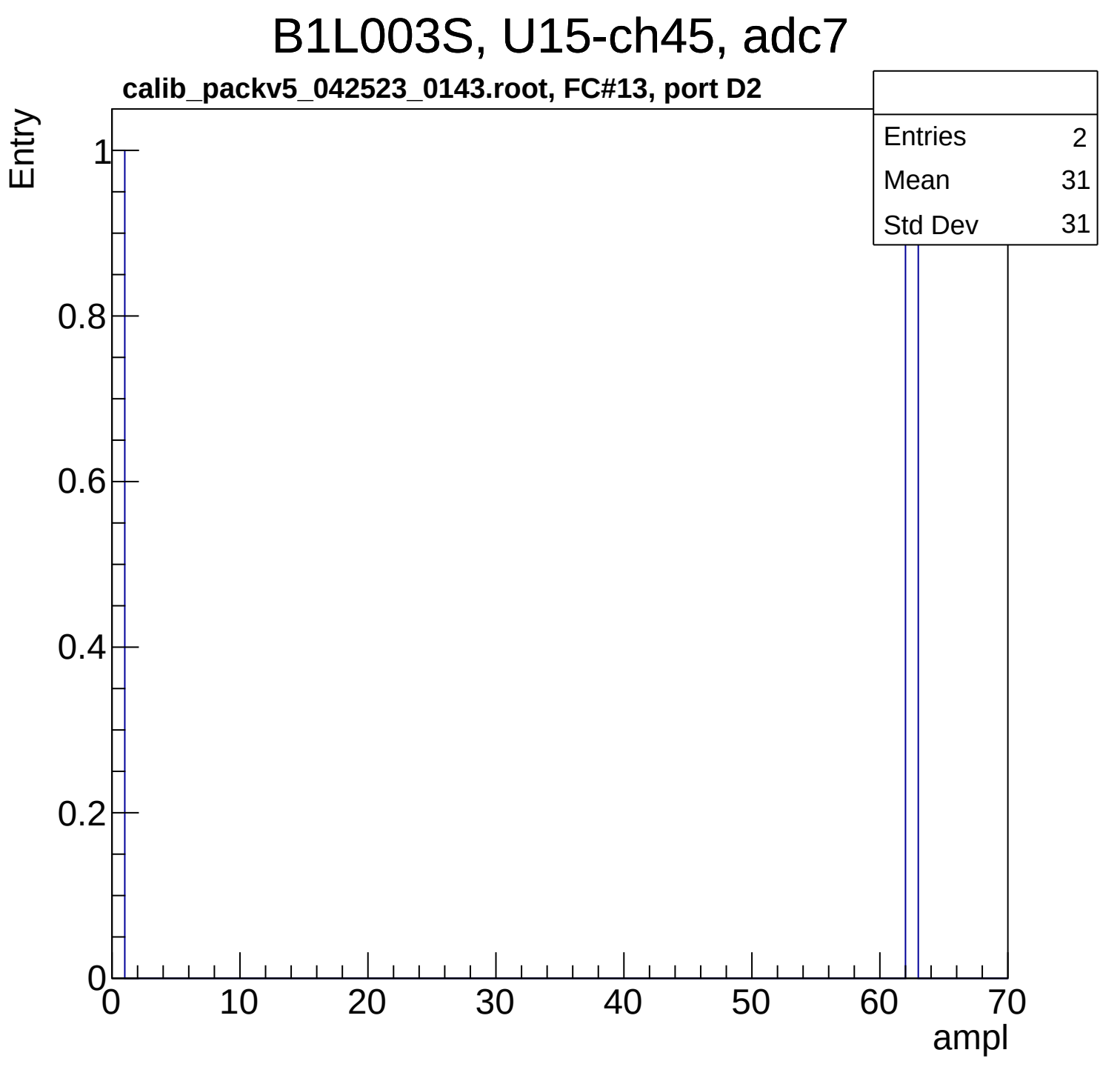
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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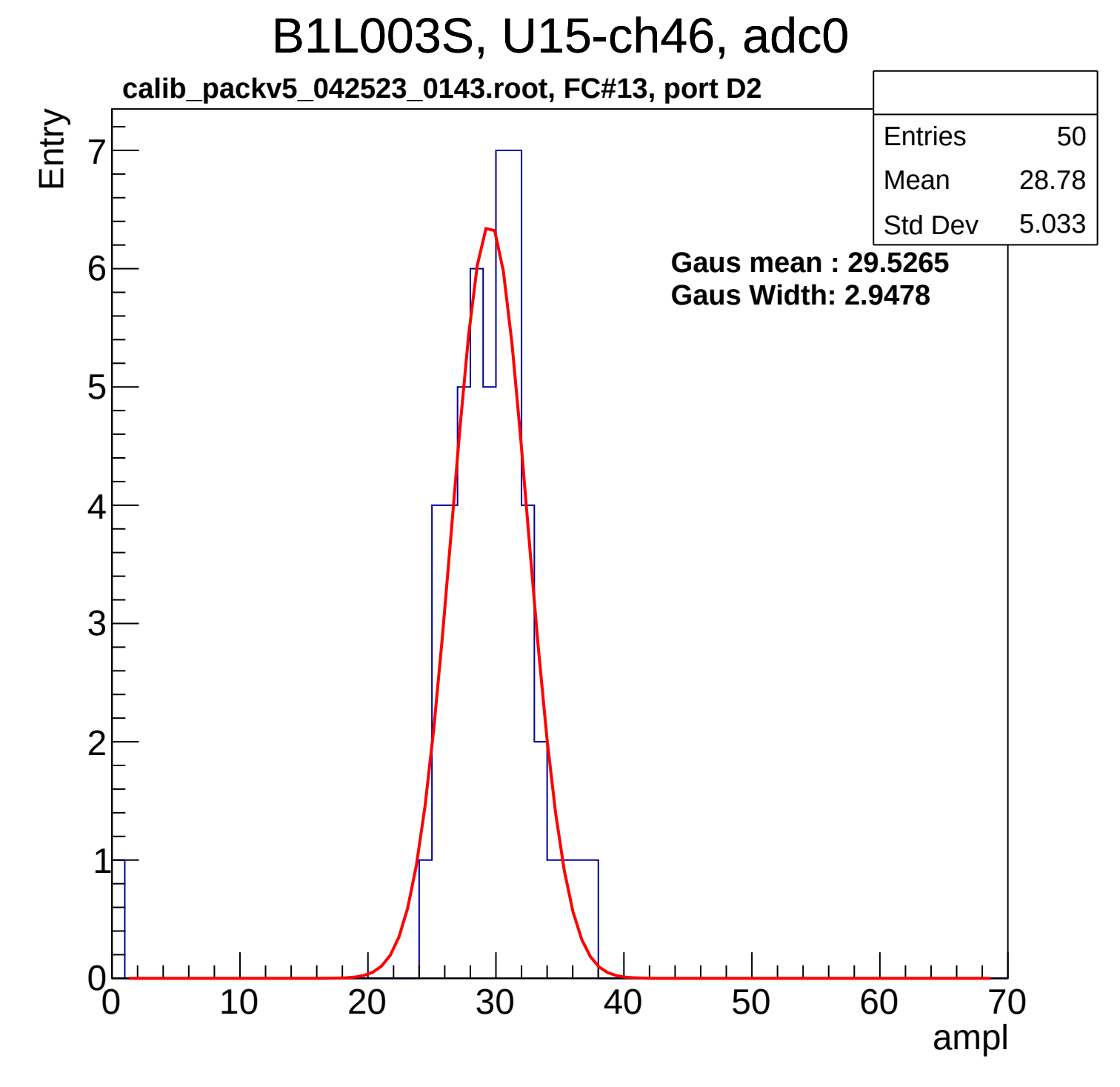
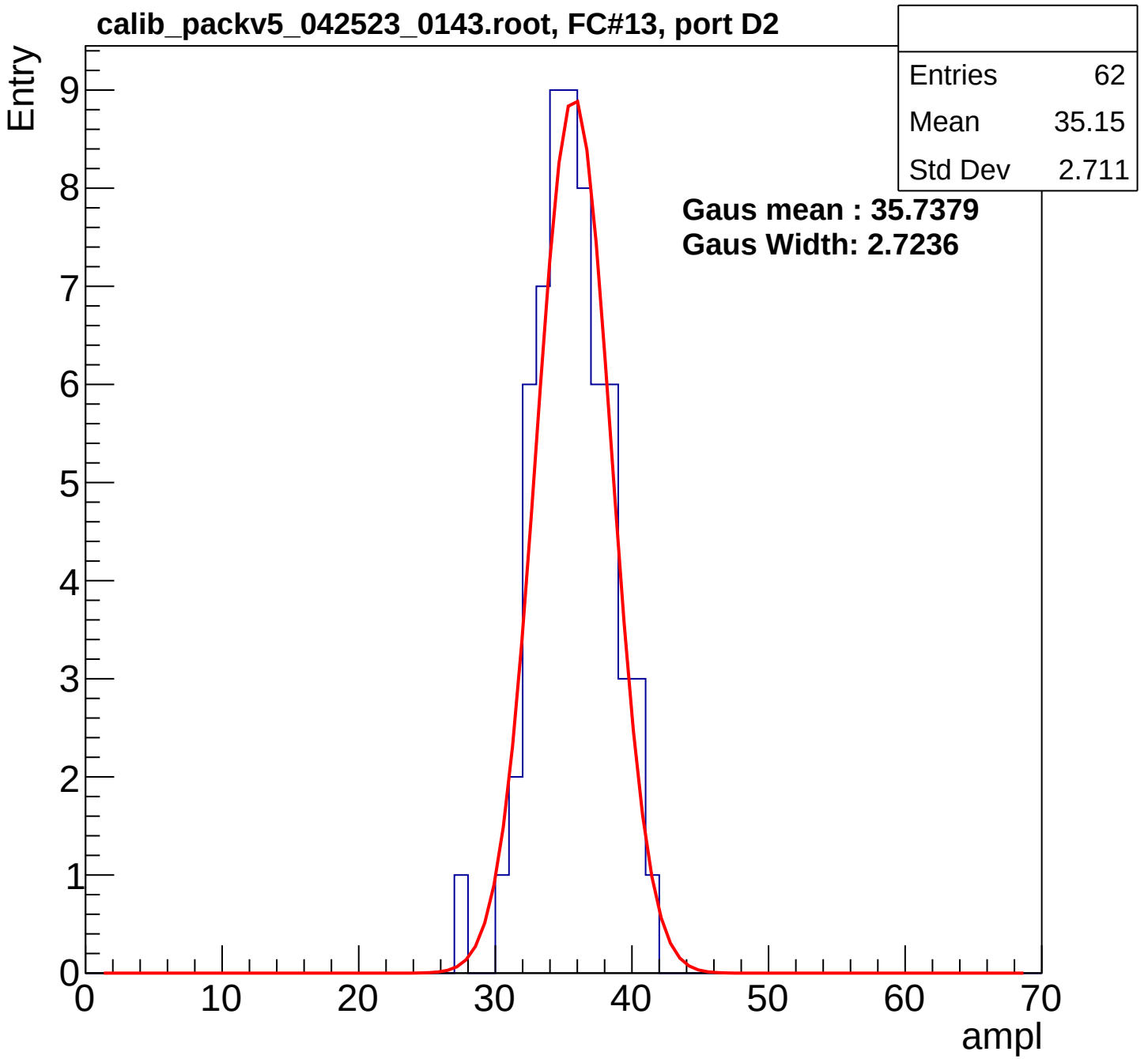
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 28.78
Std Dev 5.033
Gaus mean : 29.5265
Gaus Width: 2.9478
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.15
Std Dev 2.711
Gaus mean : 35.7379
Gaus Width: 2.7236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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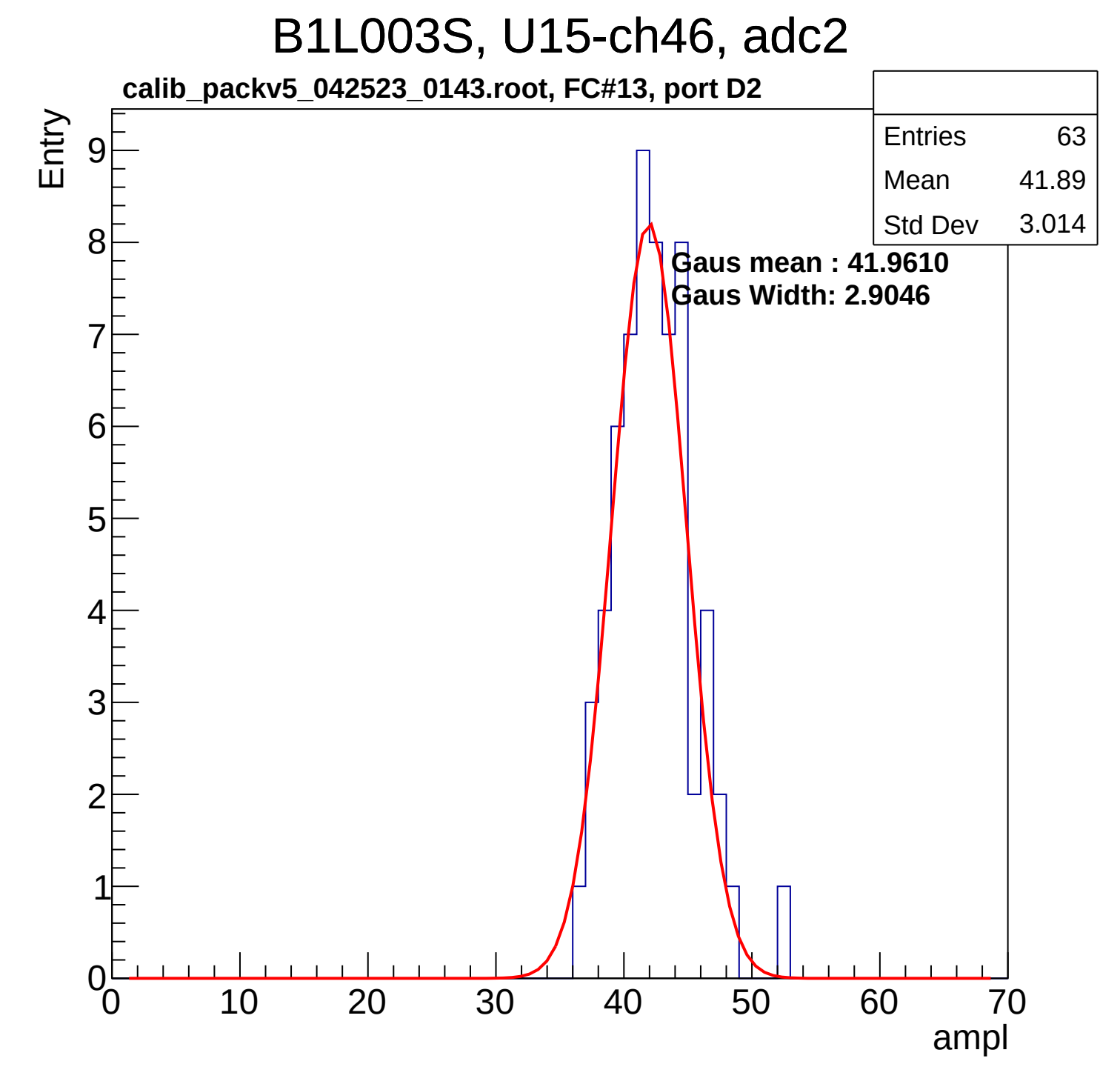
B1L003S, U15-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.89
Std Dev 3.014
Gaus mean : 41.9610
Gaus Width: 2.9046
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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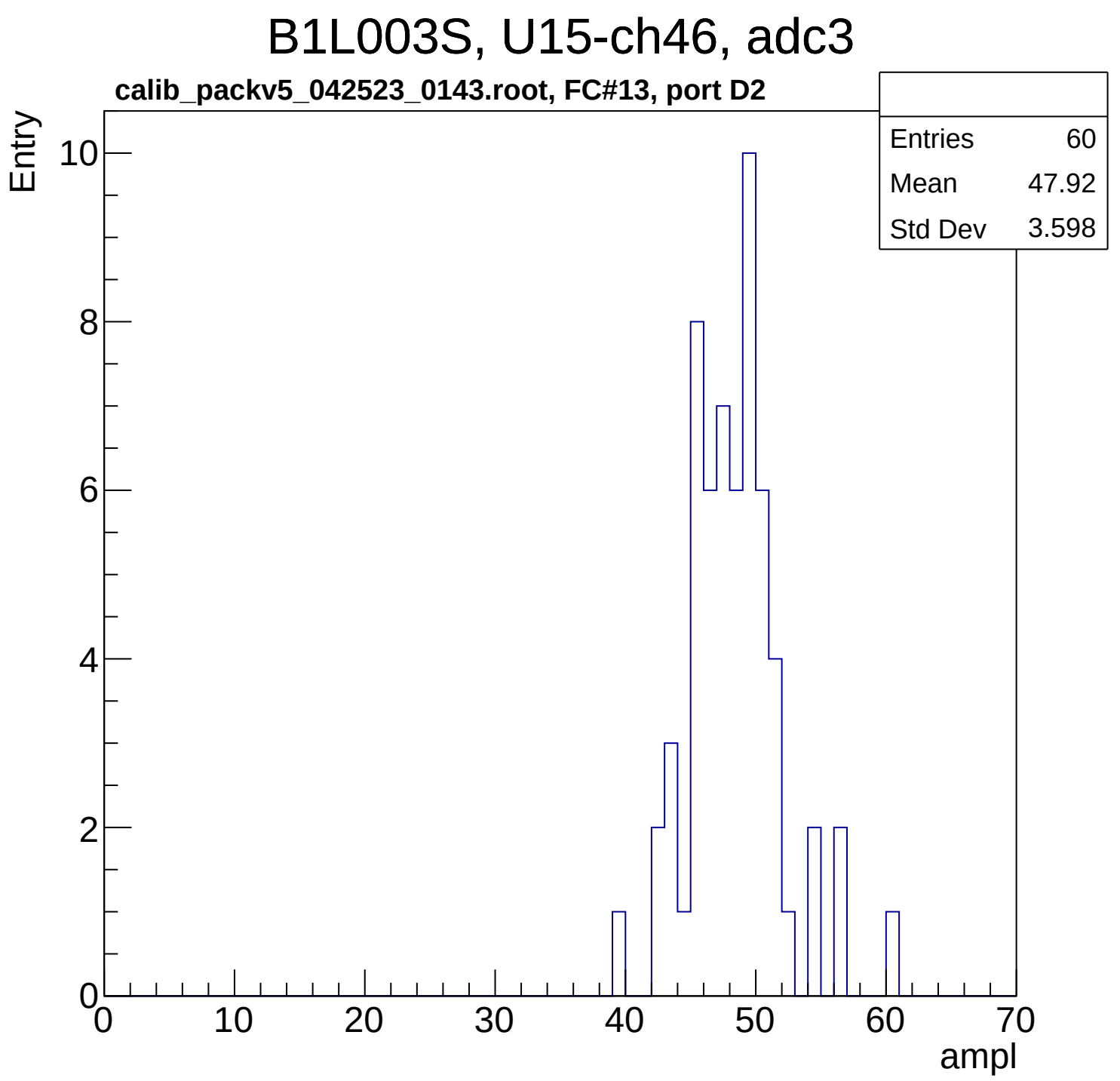
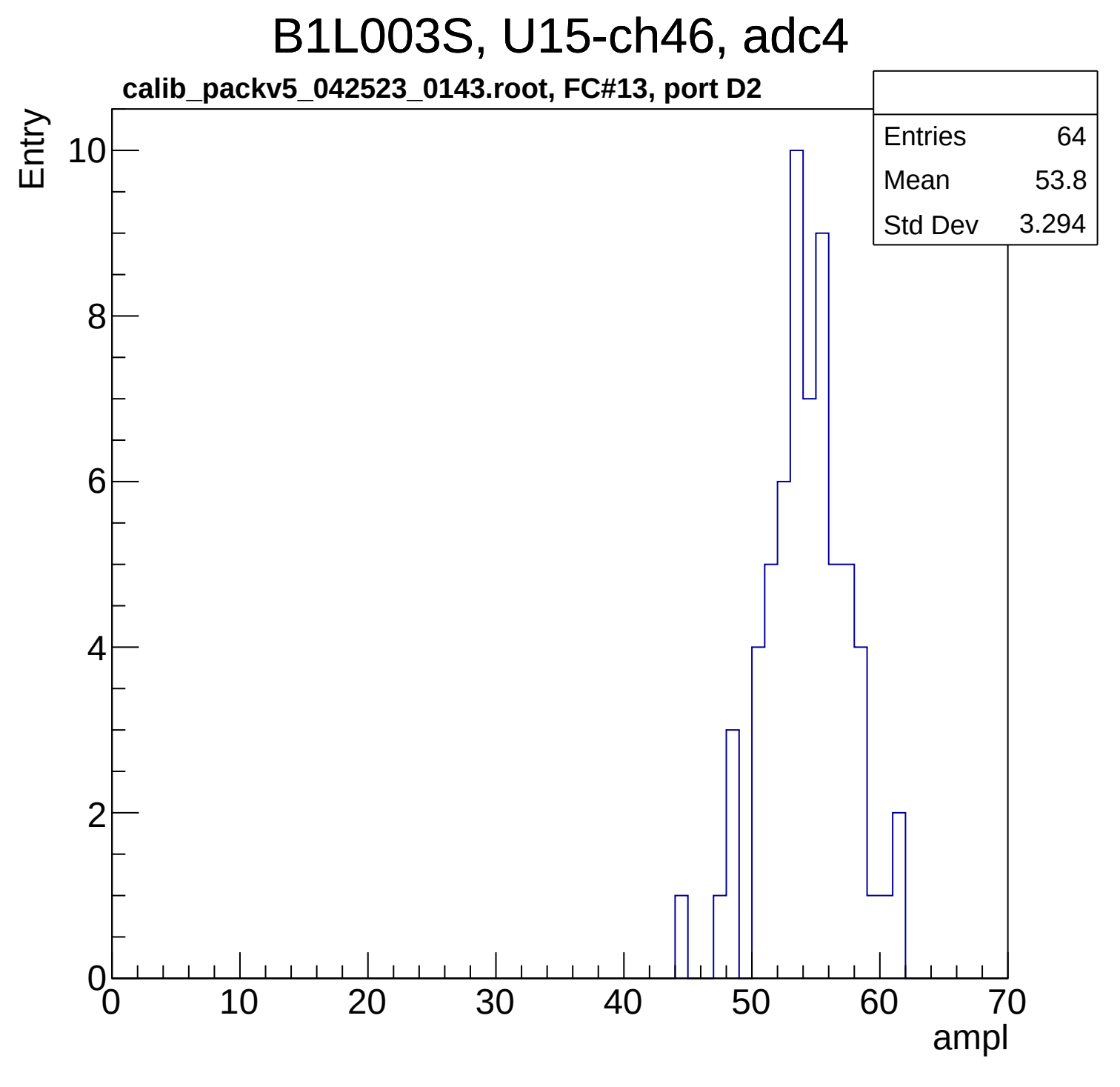
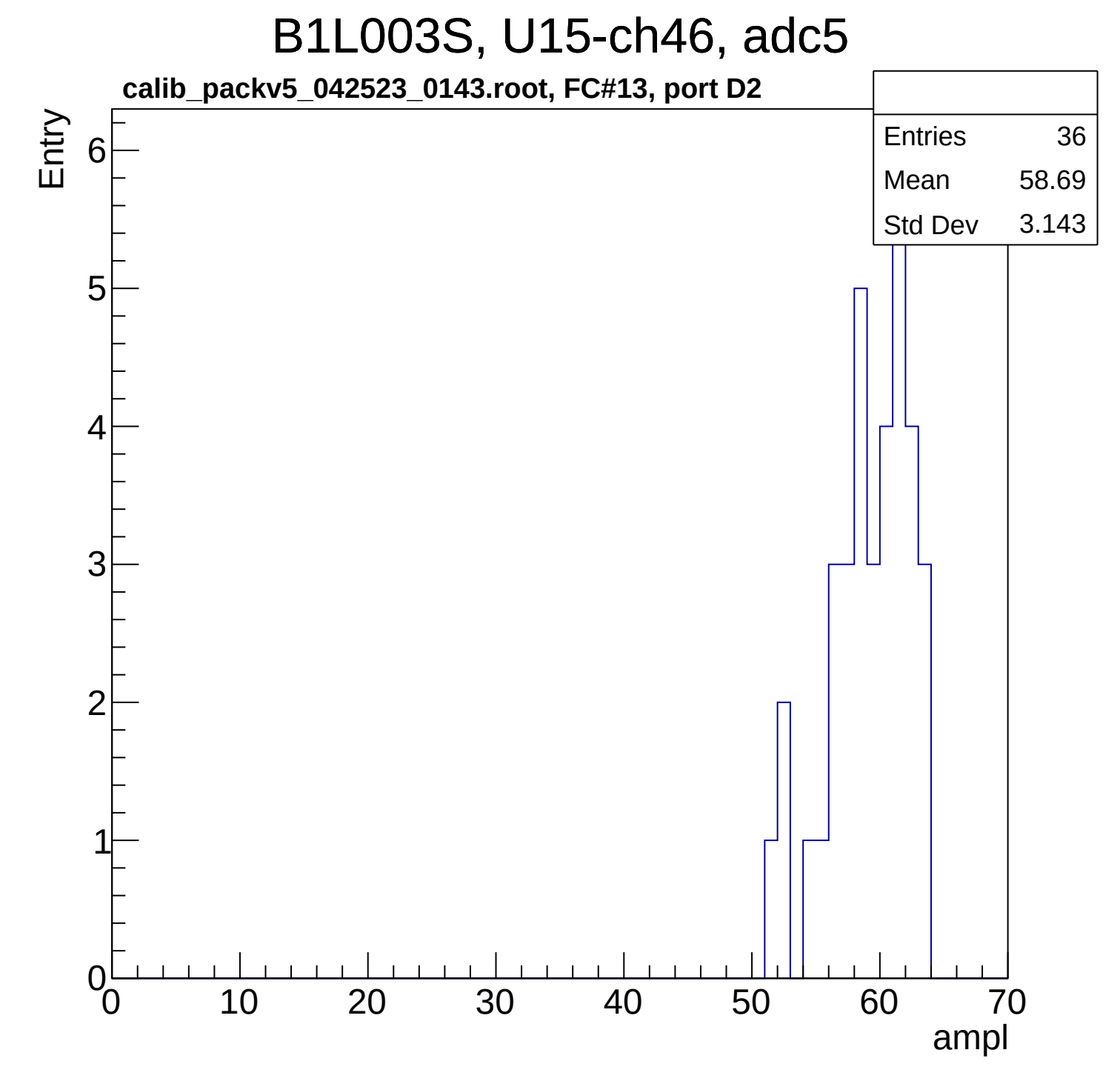

B1L003S, U15-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 47.92
Std Dev 3.598
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.8
Std Dev 3.294
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.69
Std Dev 3.143
Entry
ampl
0
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2
3
4
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6
0
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60
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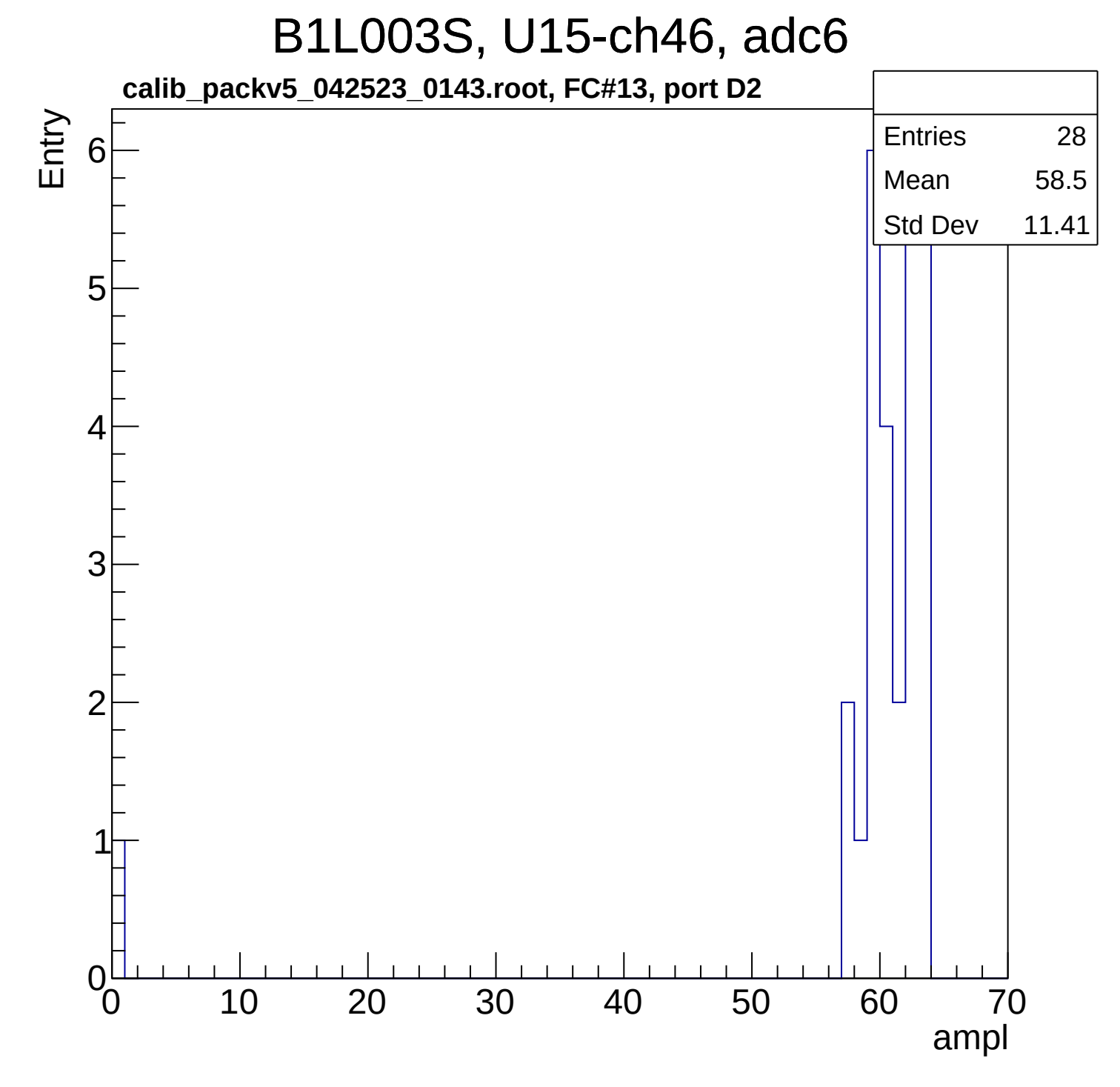
B1L003S, U15-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 28
Mean 58.5
Std Dev 11.41
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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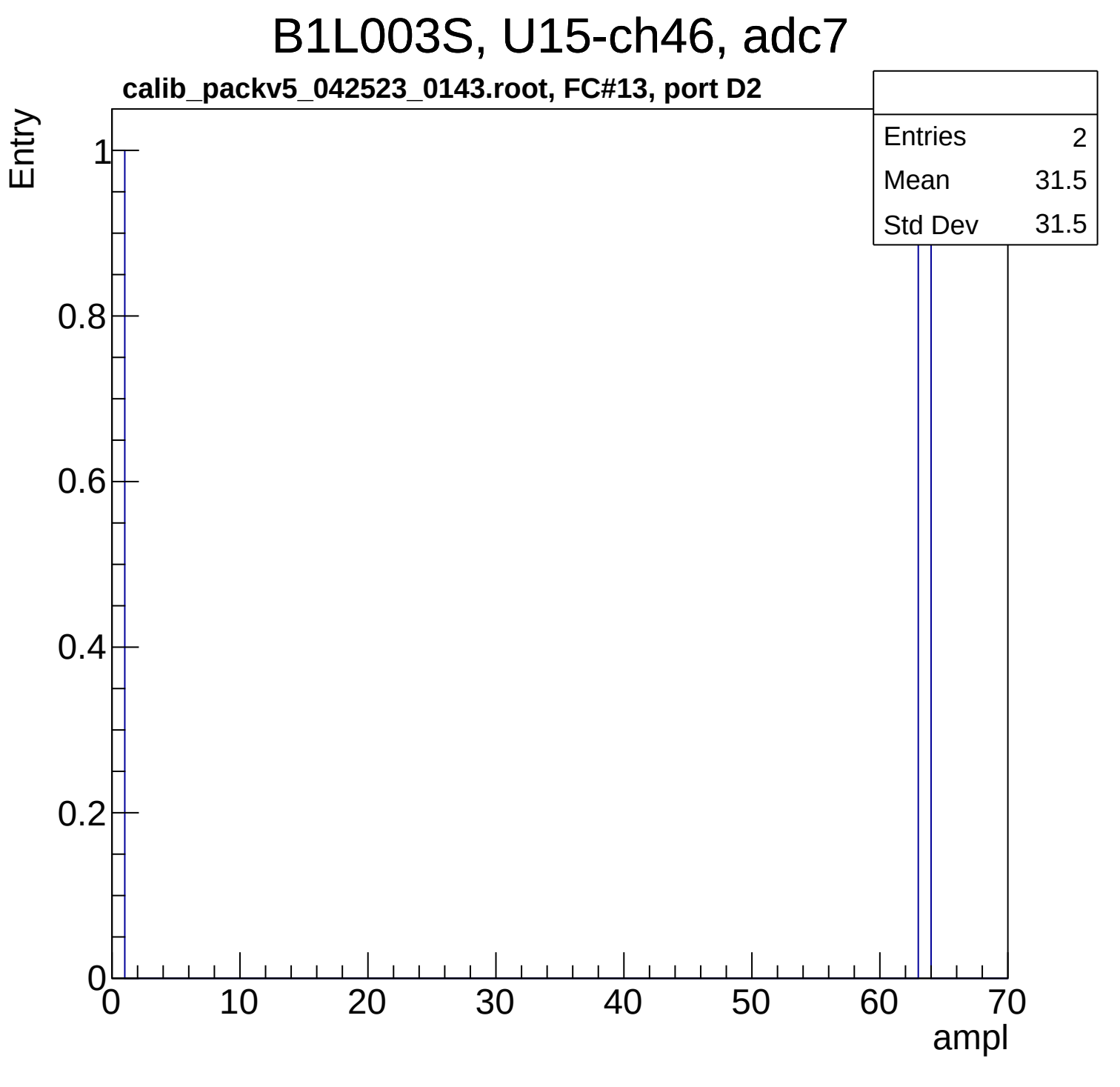
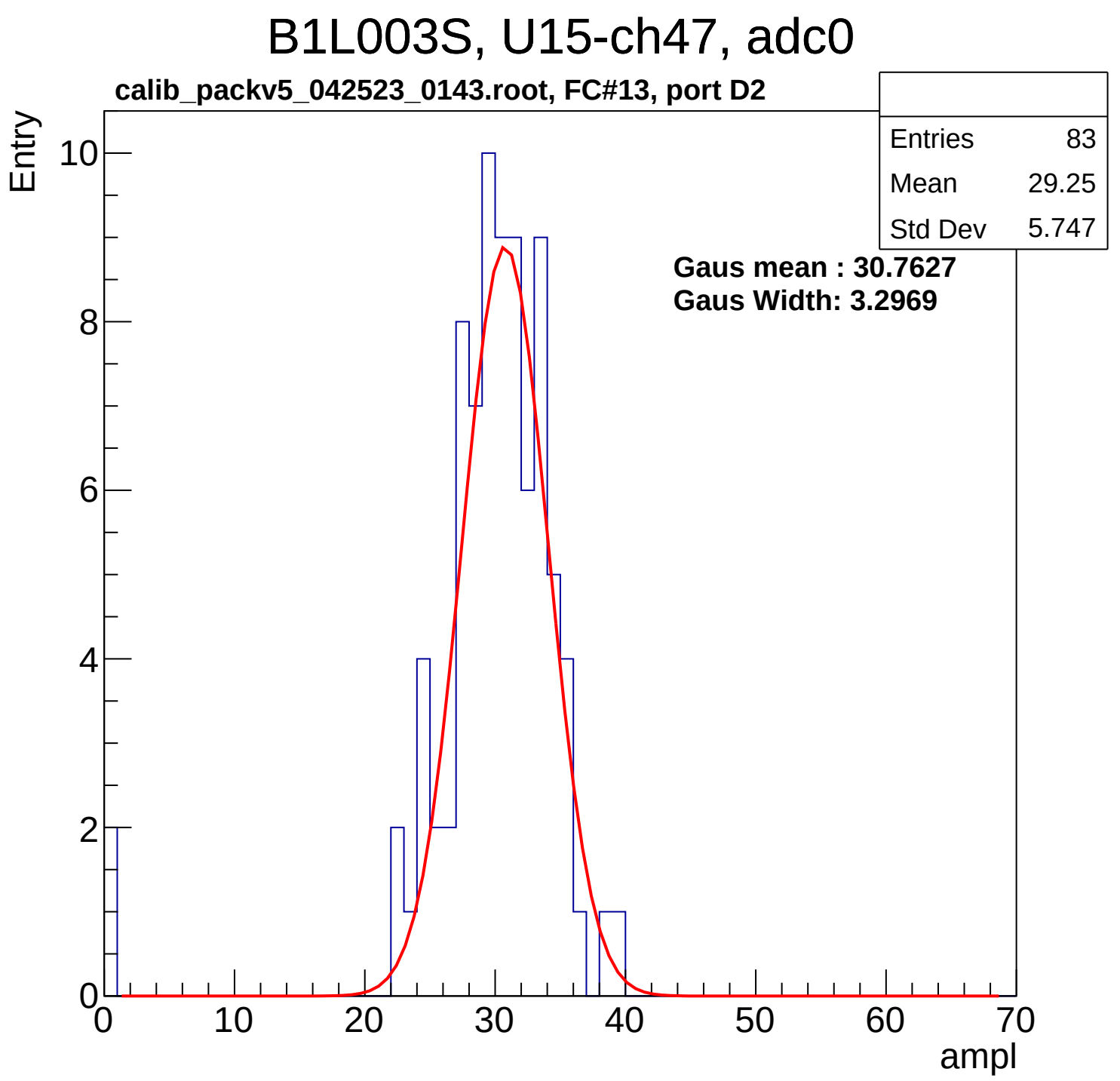

B1L003S, U15-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 29.25
Std Dev 5.747
Gaus mean : 30.7627
Gaus Width: 3.2969
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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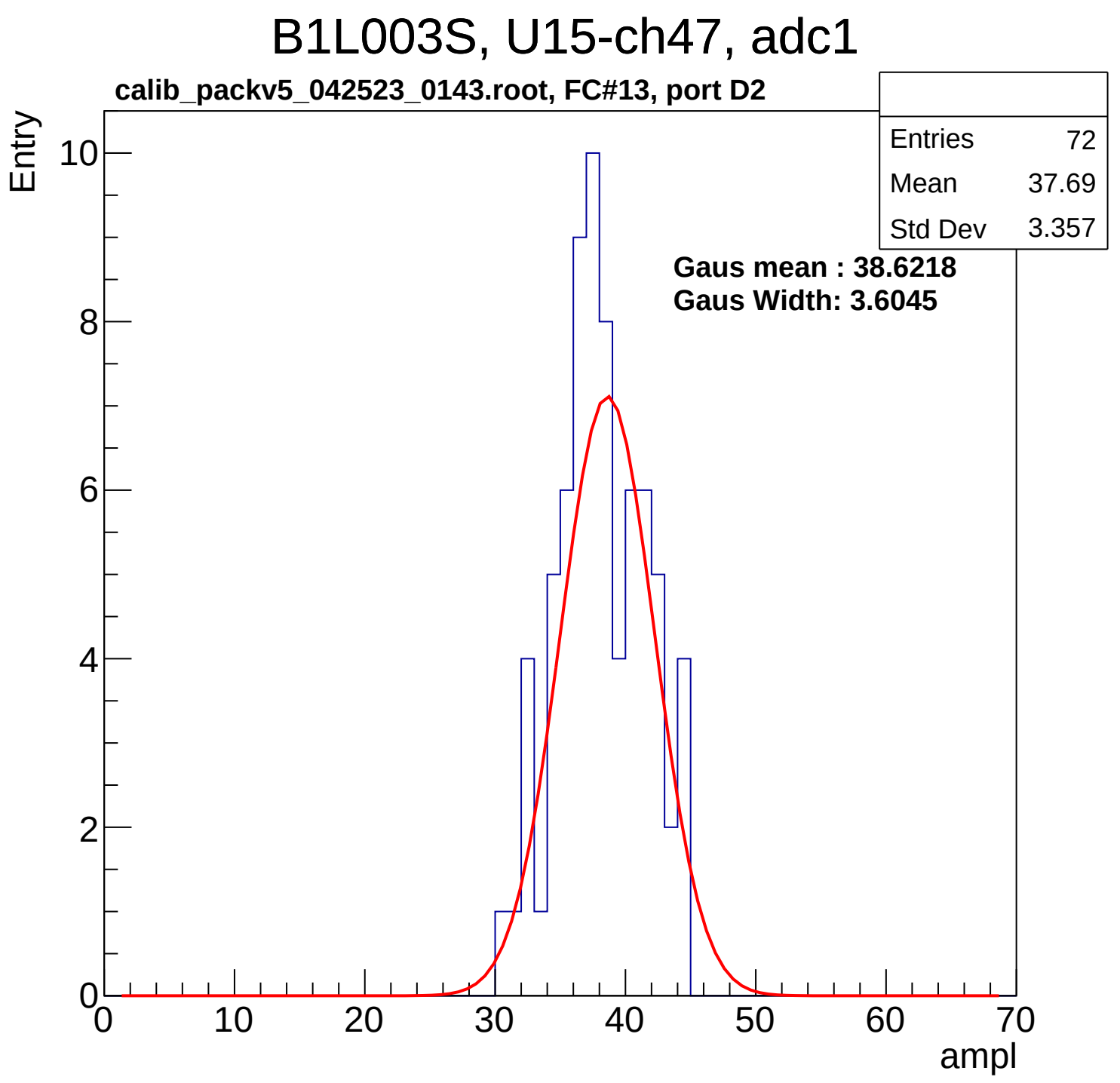
B1L003S, U15-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 37.69
Std Dev 3.357
Gaus mean : 38.6218
Gaus Width: 3.6045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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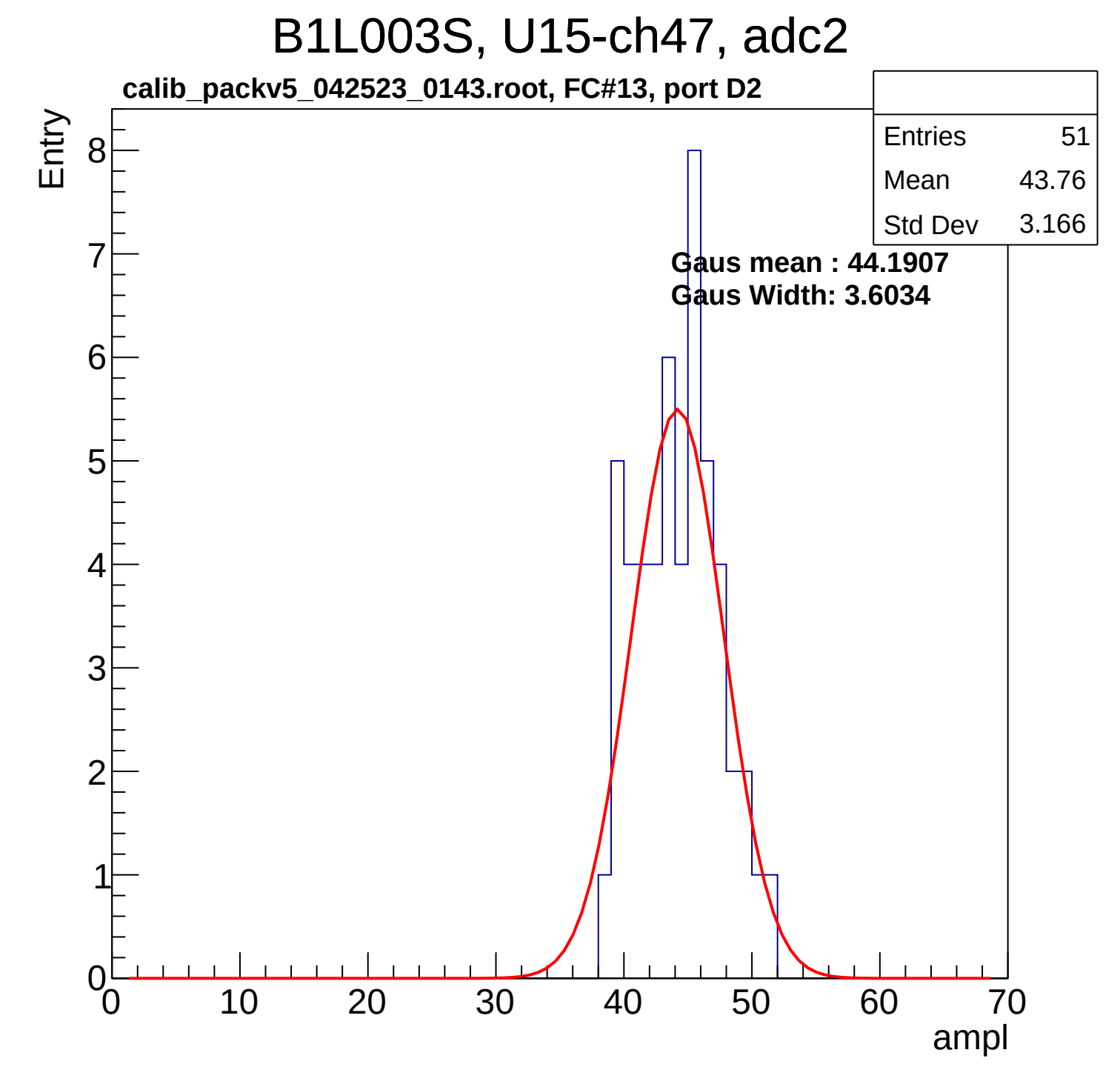
B1L003S, U15-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.76
Std Dev 3.166
Gaus mean : 44.1907
Gaus Width: 3.6034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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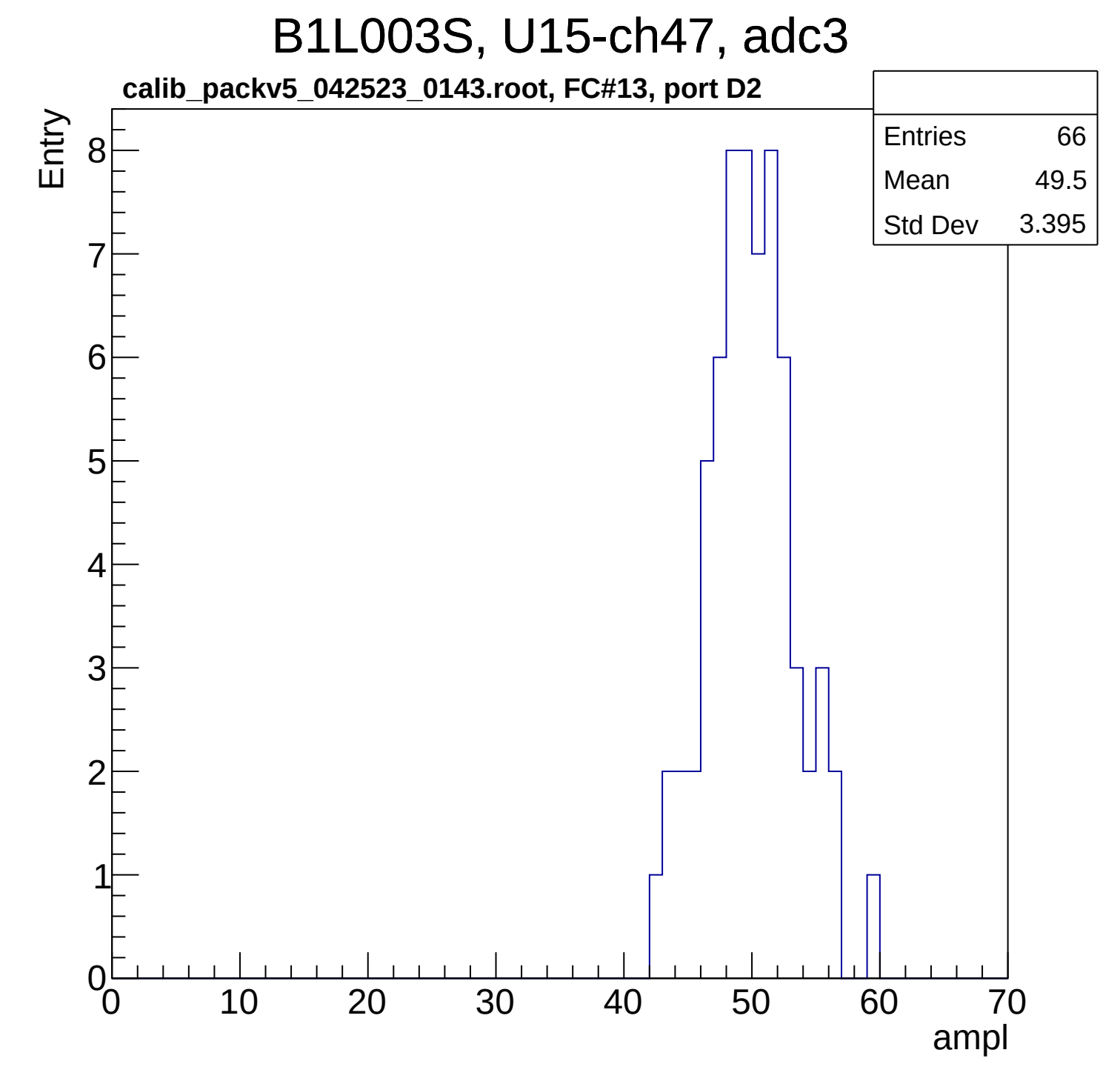
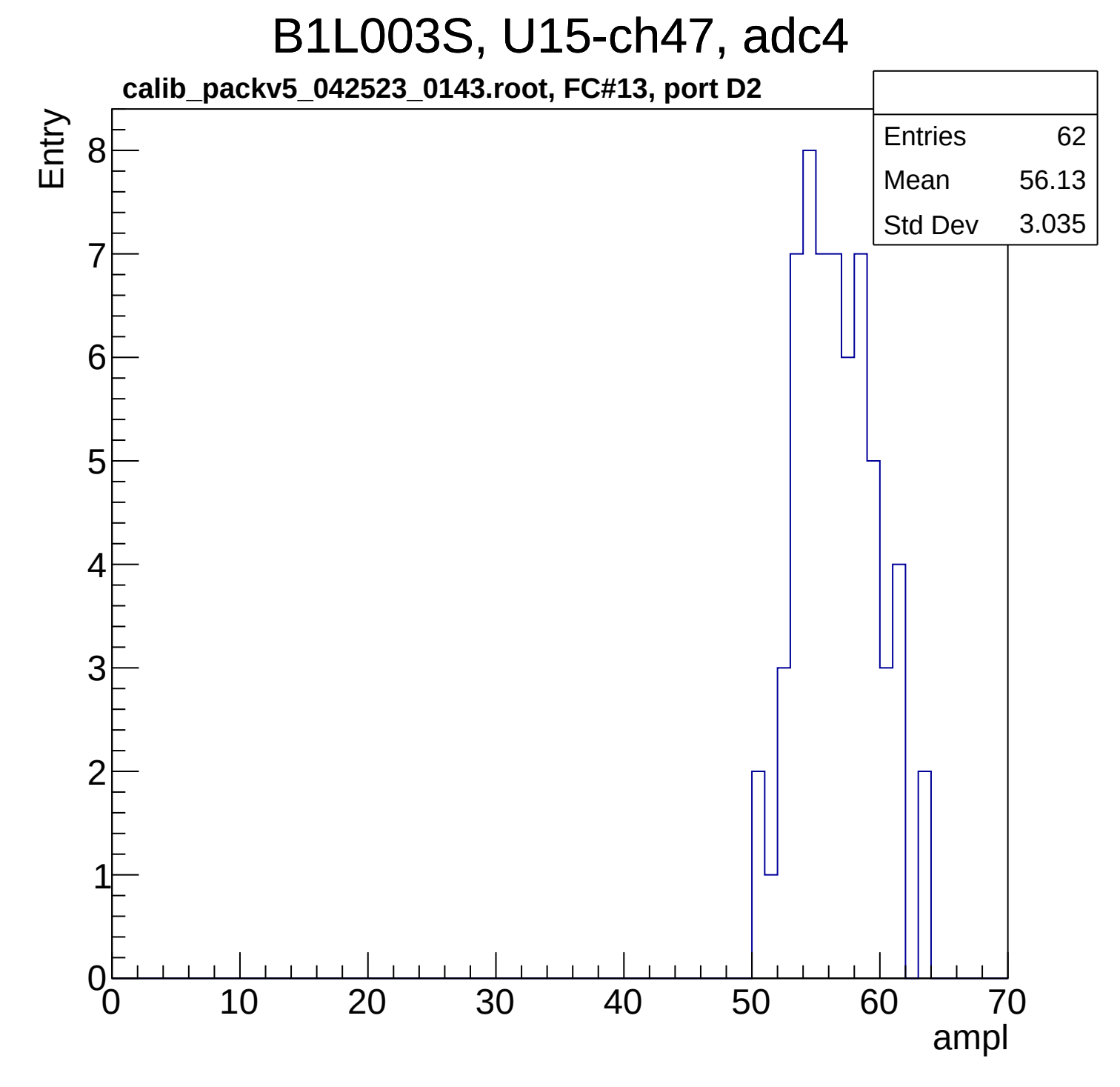

B1L003S, U15-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.5
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 56.13
Std Dev 3.035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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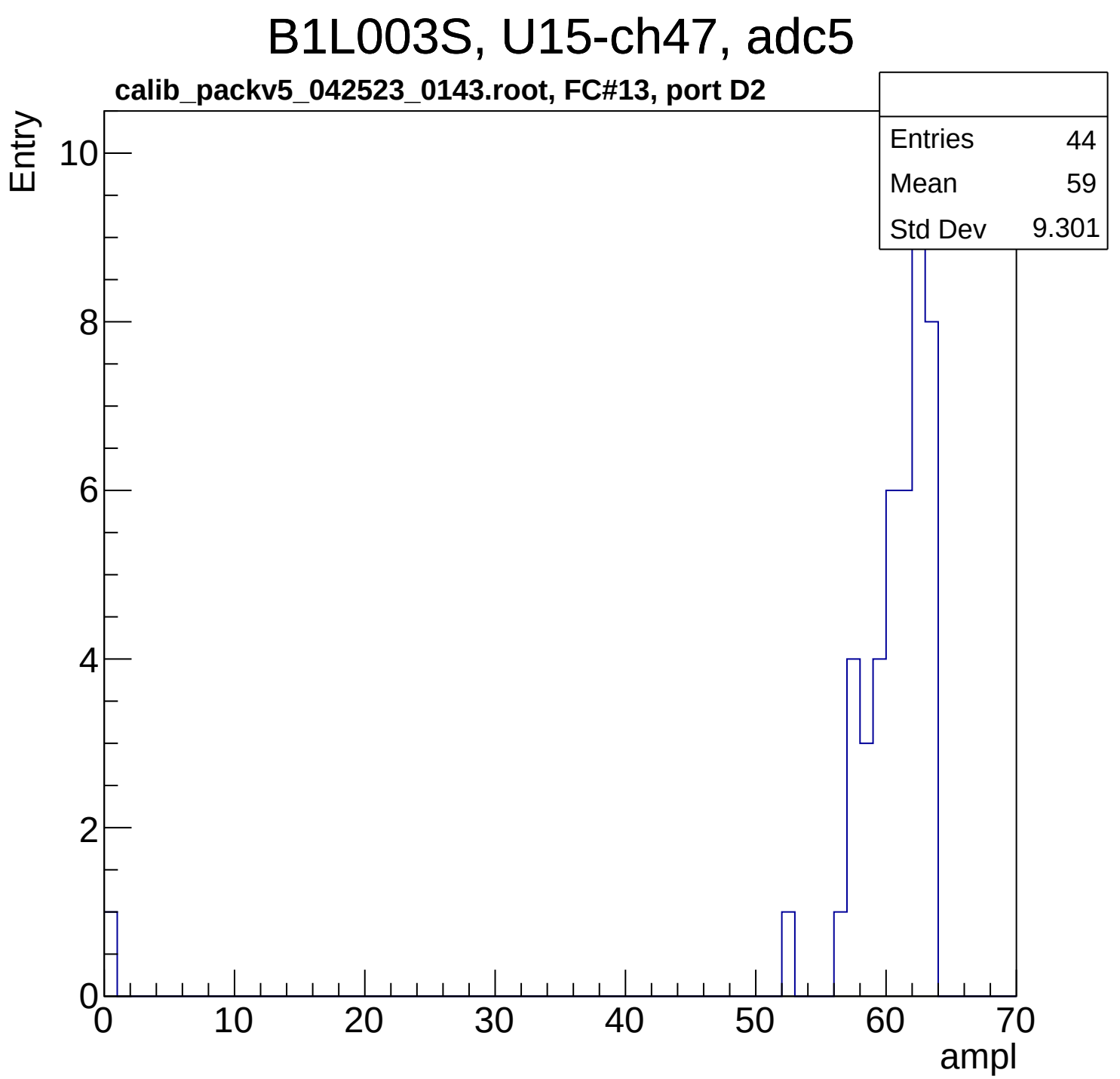
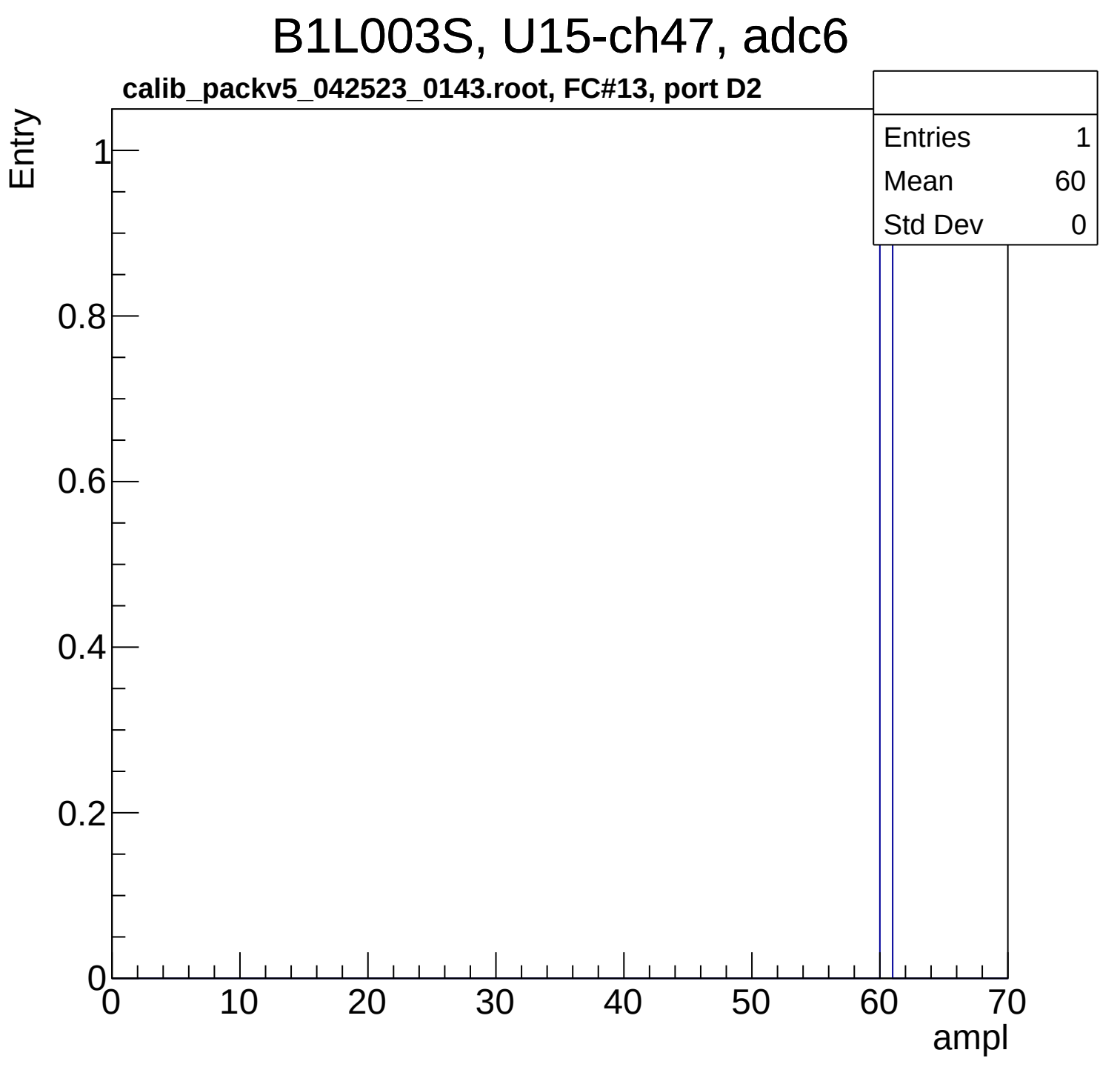
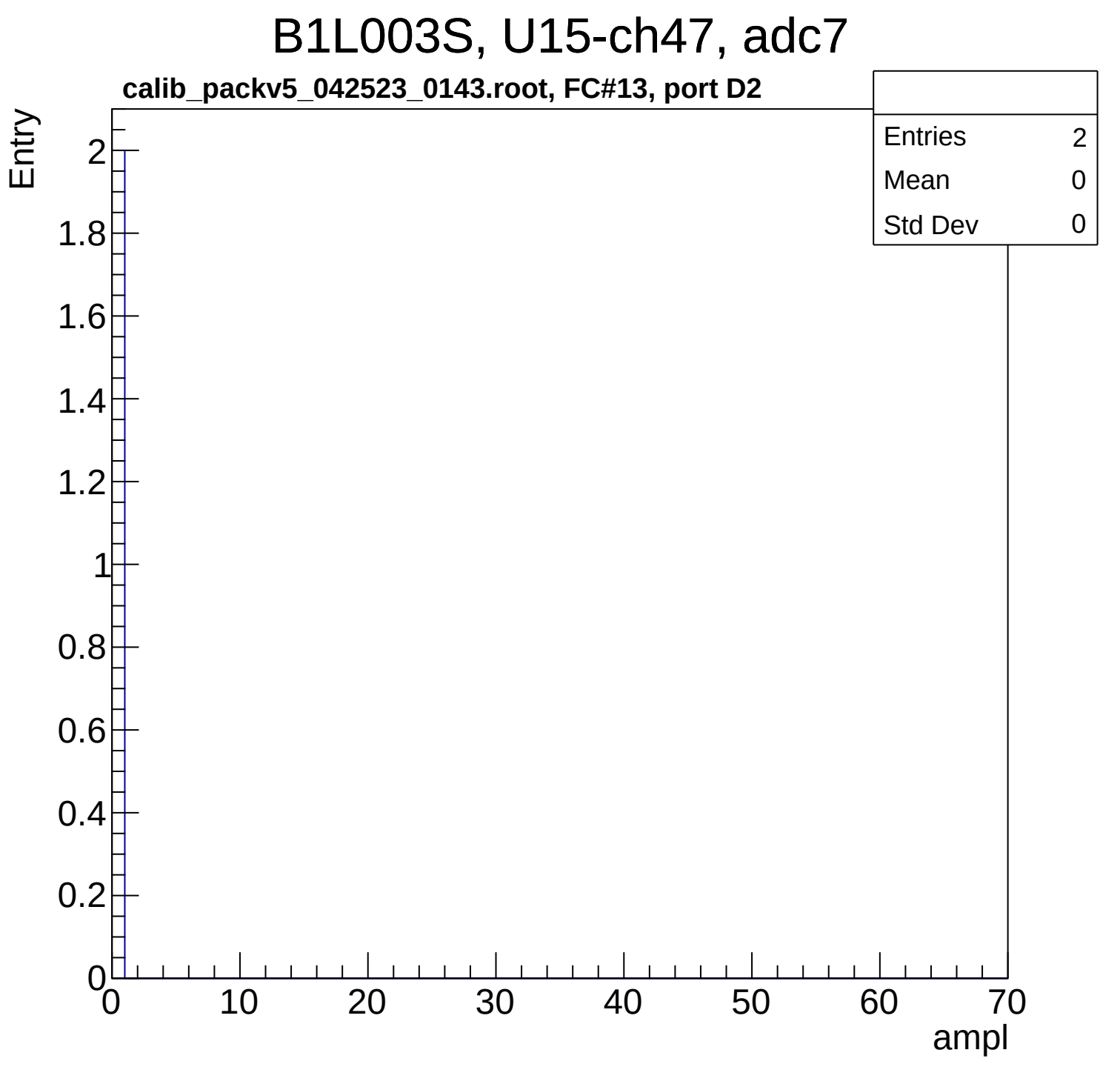

B1L003S, U15-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59
Std Dev 9.301
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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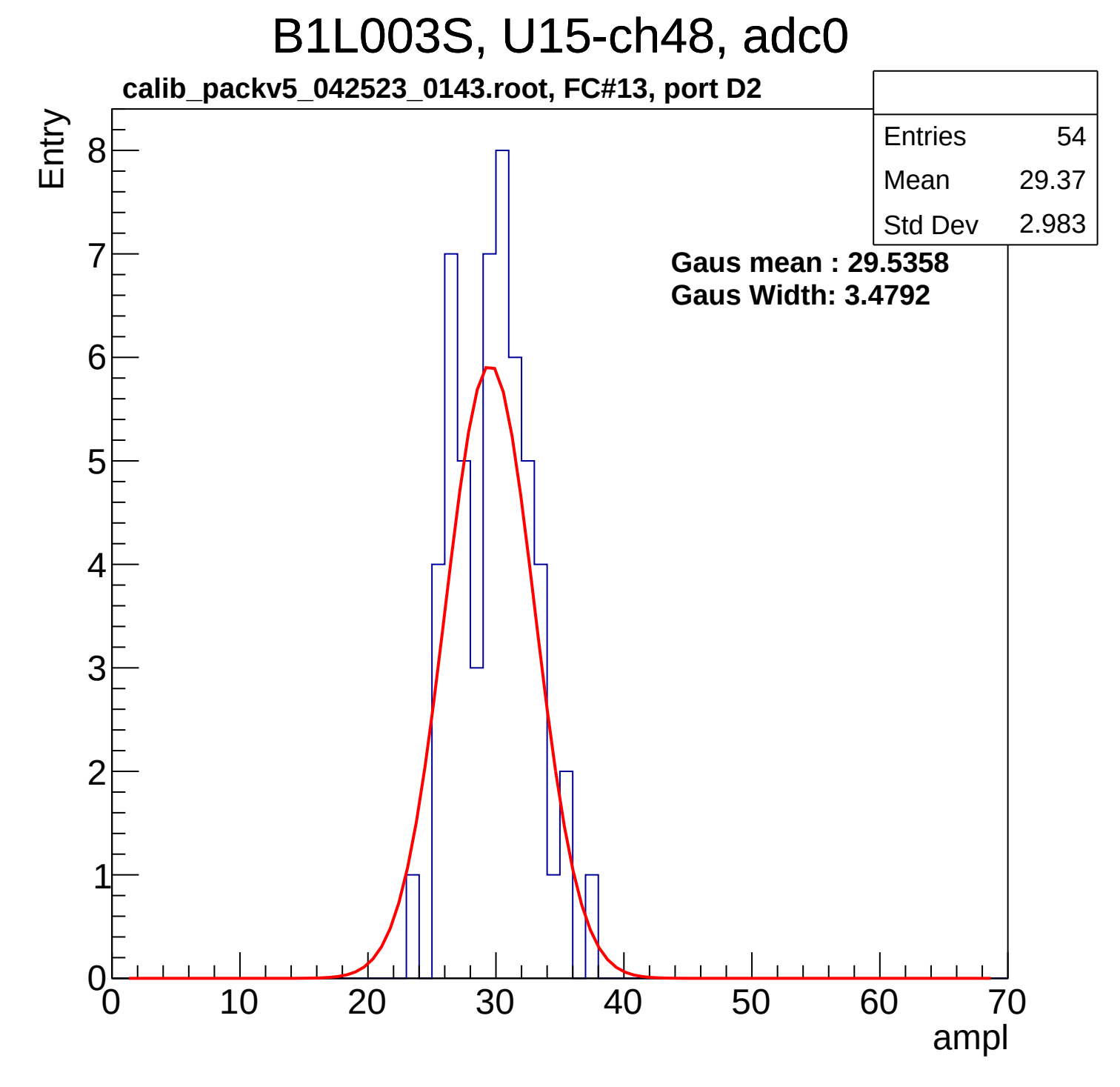
B1L003S, U15-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.37
Std Dev 2.983
Gaus mean : 29.5358
Gaus Width: 3.4792
Entry
ampl
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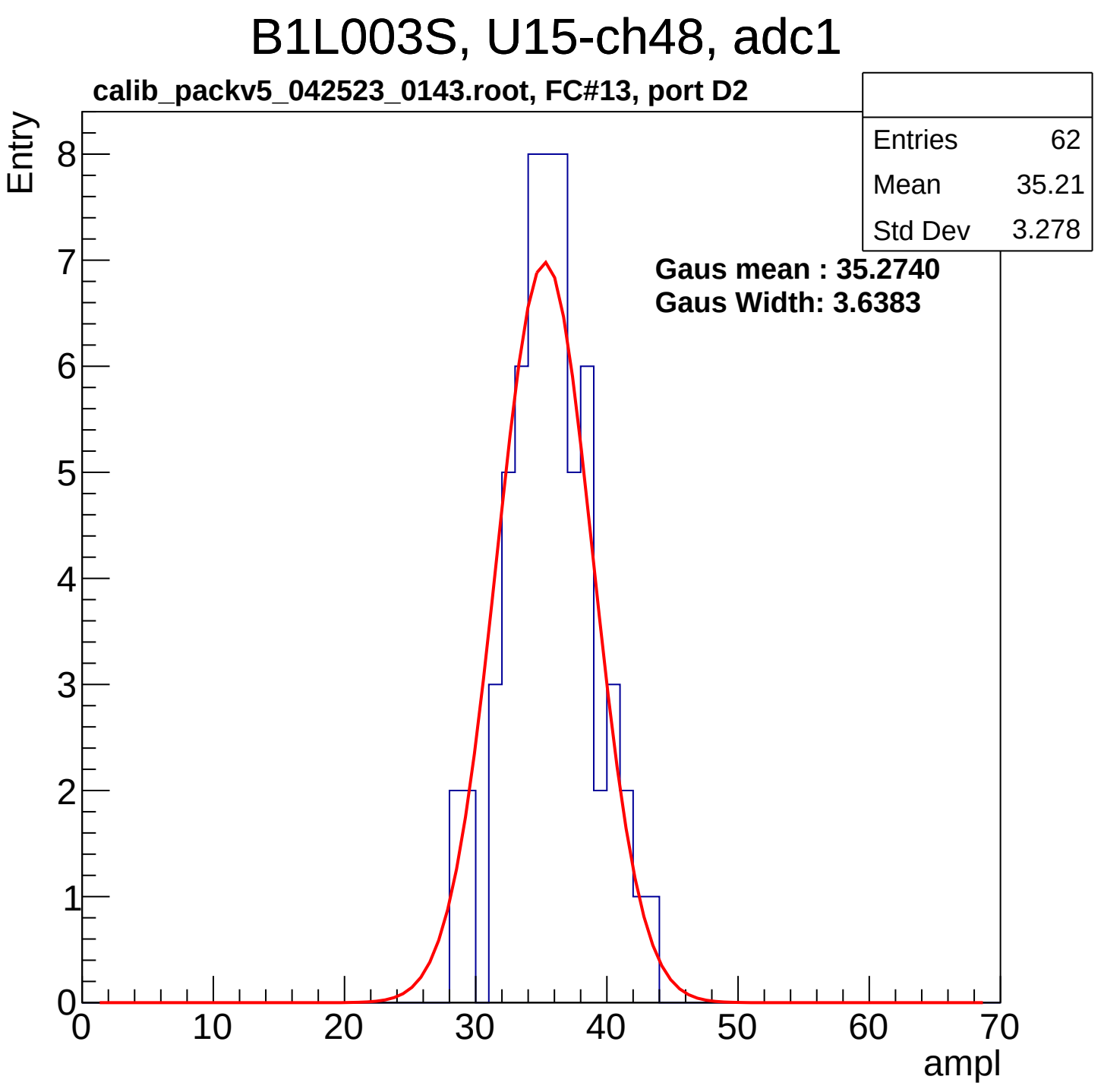
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.21
Std Dev 3.278
Gaus mean : 35.2740
Gaus Width: 3.6383
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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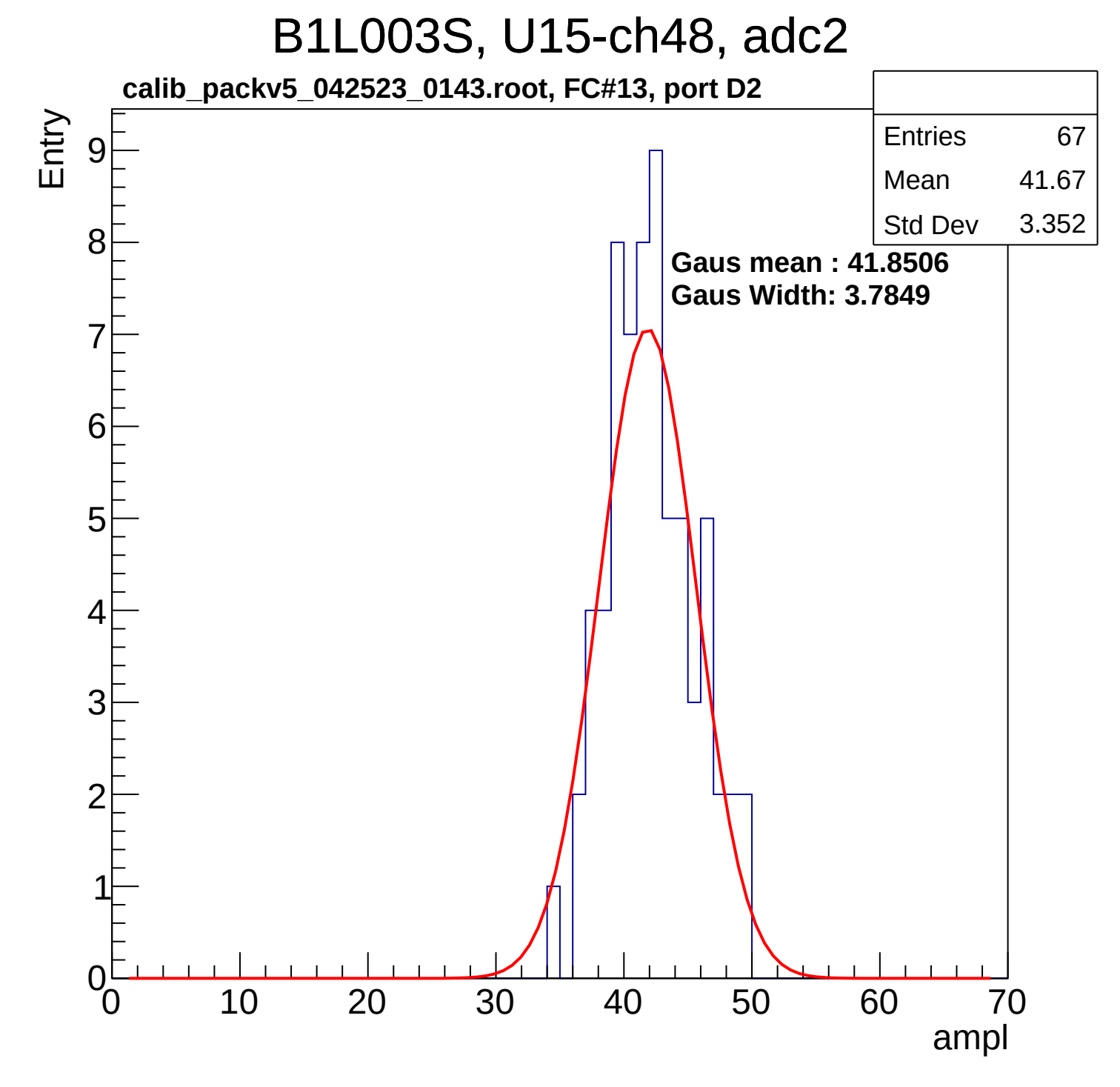
B1L003S, U15-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.67
Std Dev 3.352
Gaus mean : 41.8506
Gaus Width: 3.7849
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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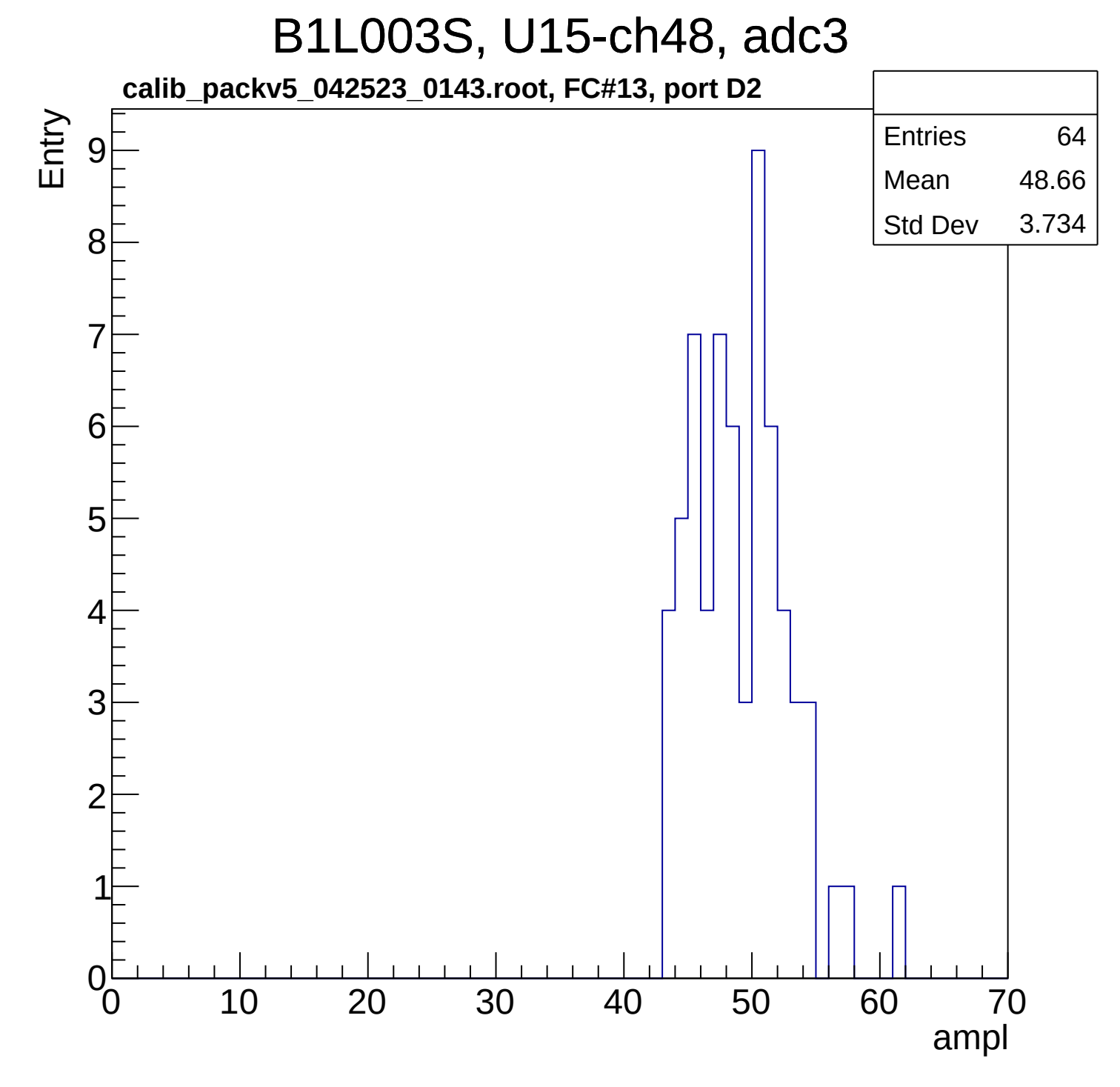
B1L003S, U15-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.66
Std Dev 3.734
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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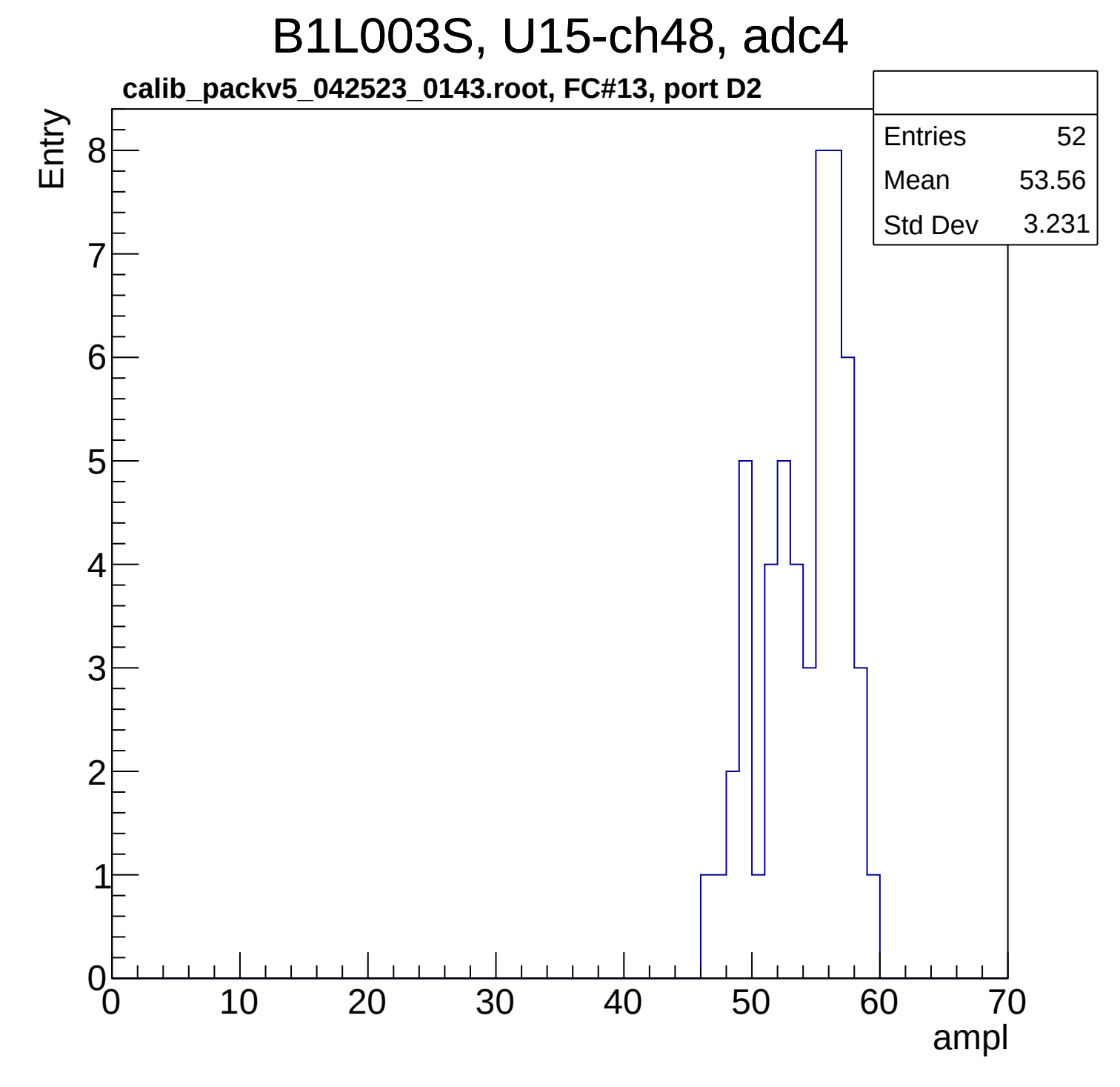
B1L003S, U15-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53.56
Std Dev 3.231
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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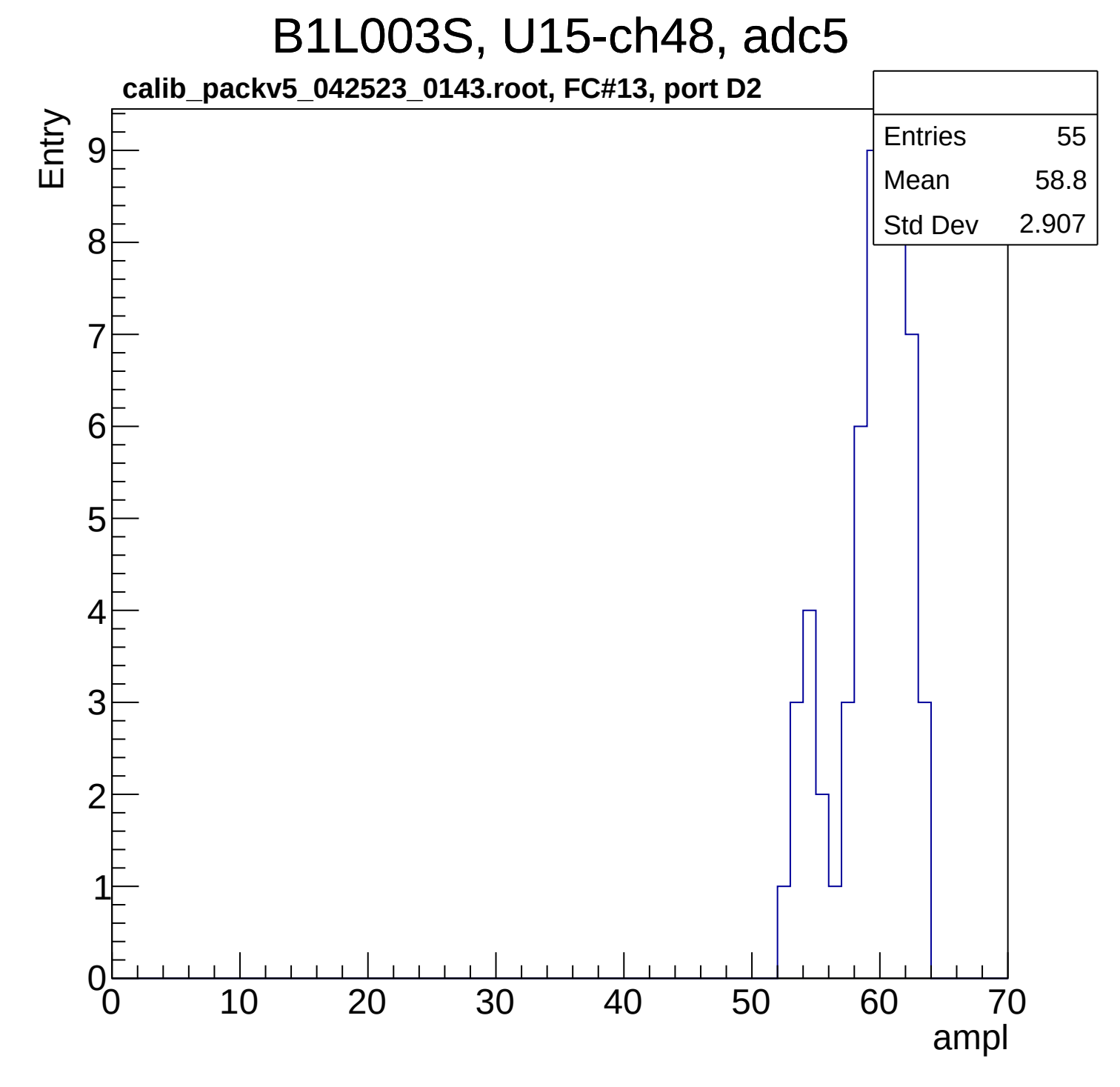
B1L003S, U15-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.8
Std Dev 2.907
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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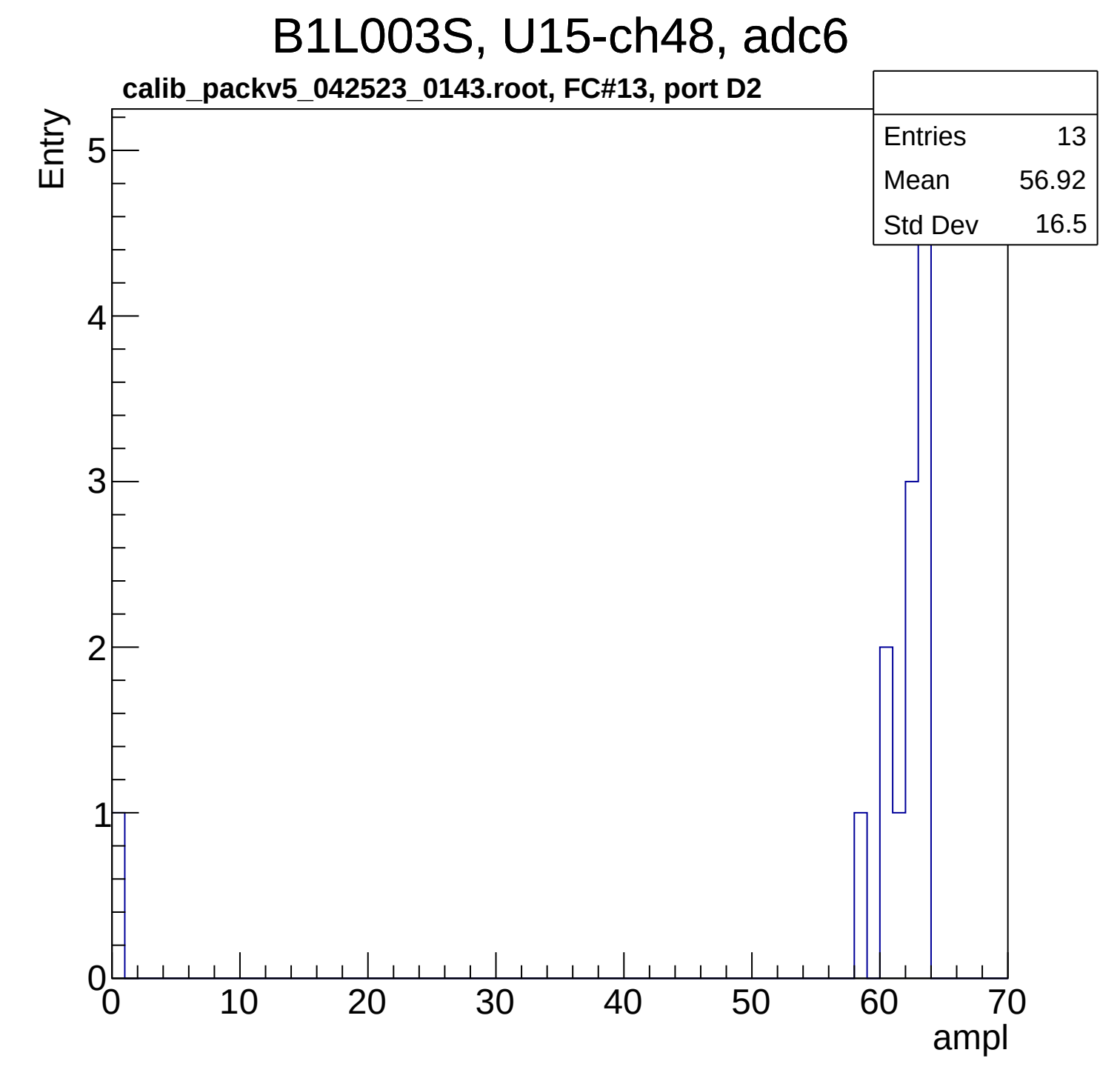
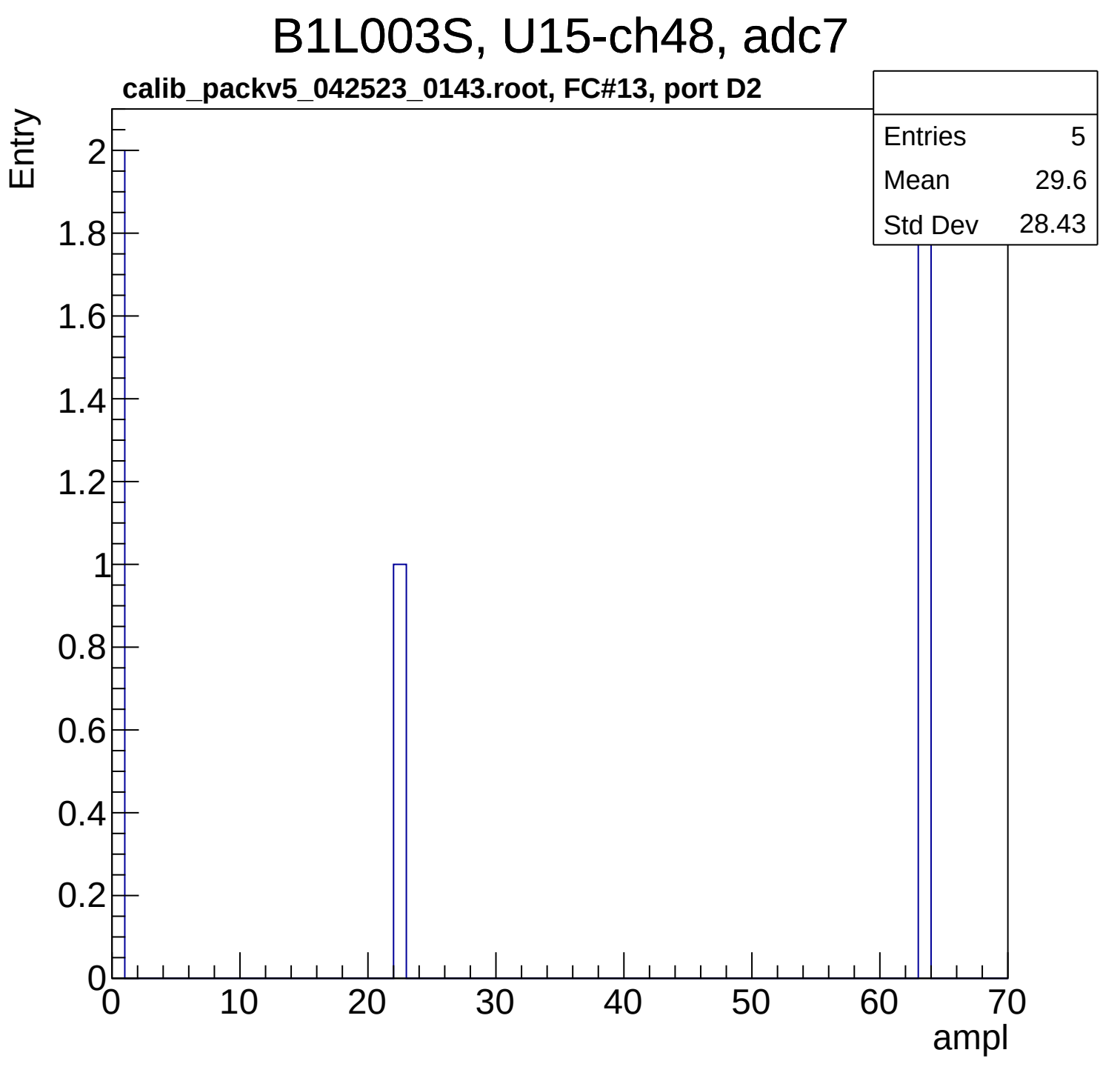

B1L003S, U15-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.92
Std Dev 16.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 29.6
Std Dev 28.43
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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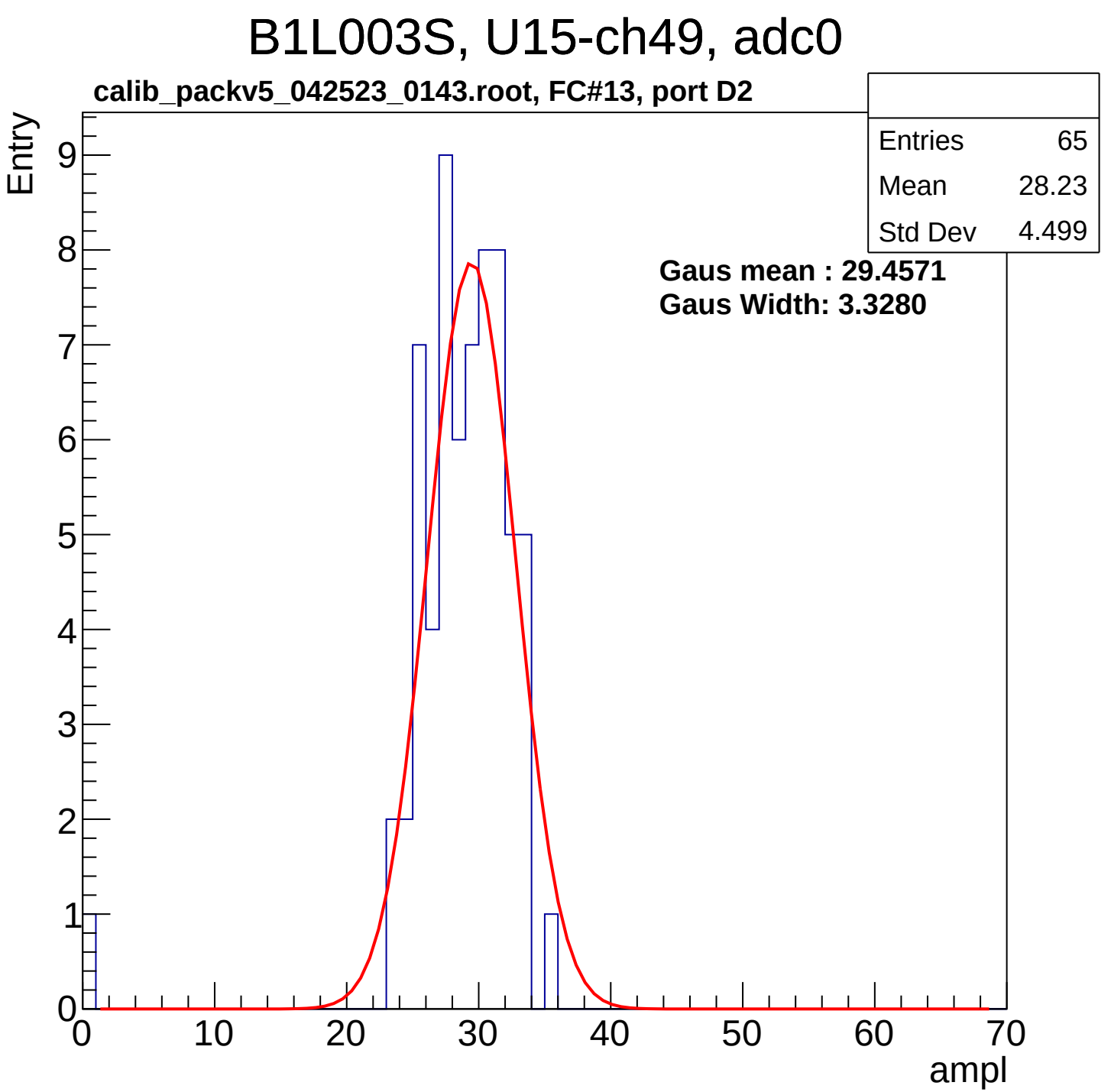
B1L003S, U15-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.23
Std Dev 4.499
Gaus mean : 29.4571
Gaus Width: 3.3280
Entry
ampl
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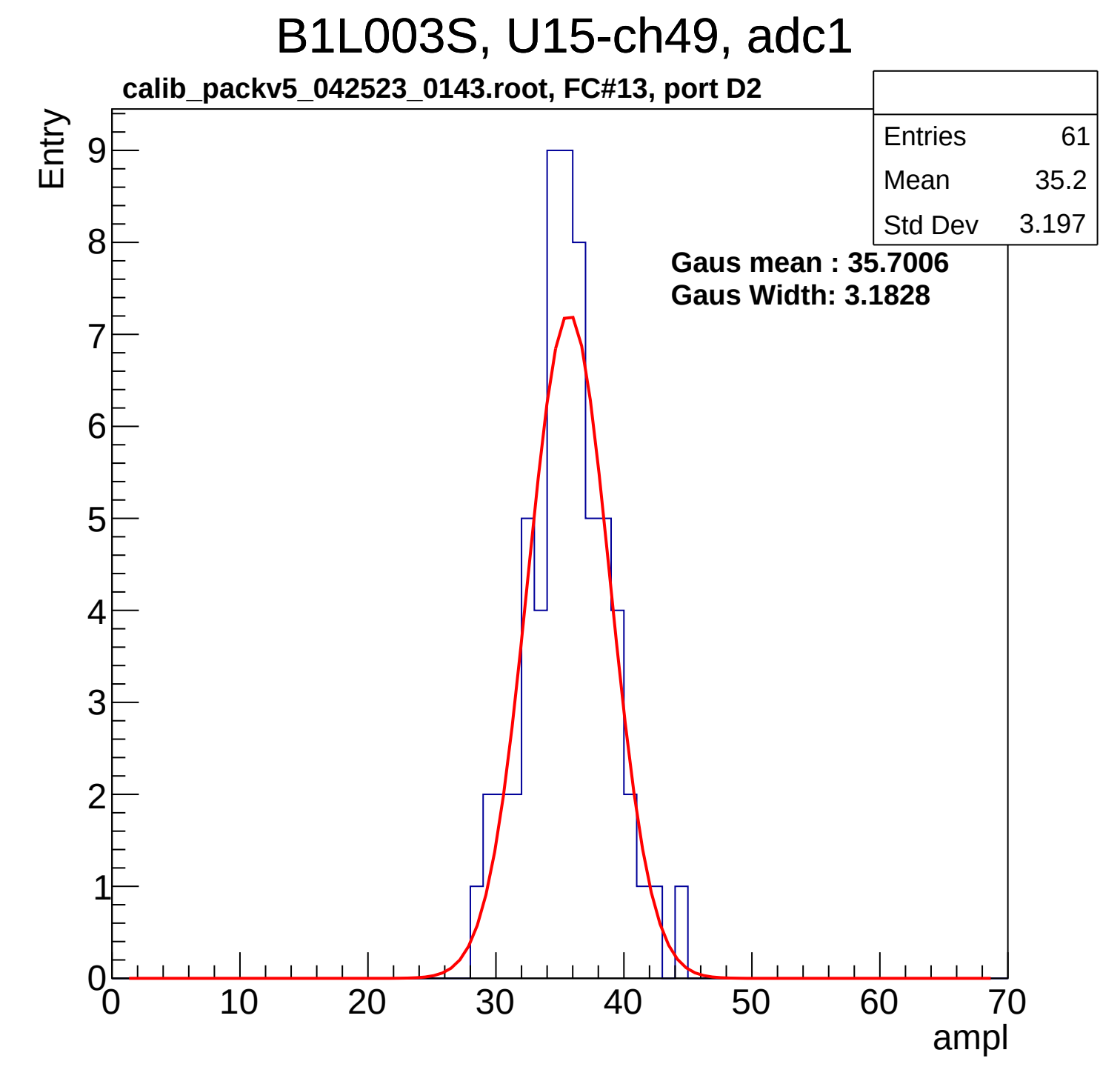
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.2
Std Dev 3.197
Gaus mean : 35.7006
Gaus Width: 3.1828
Entry
ampl
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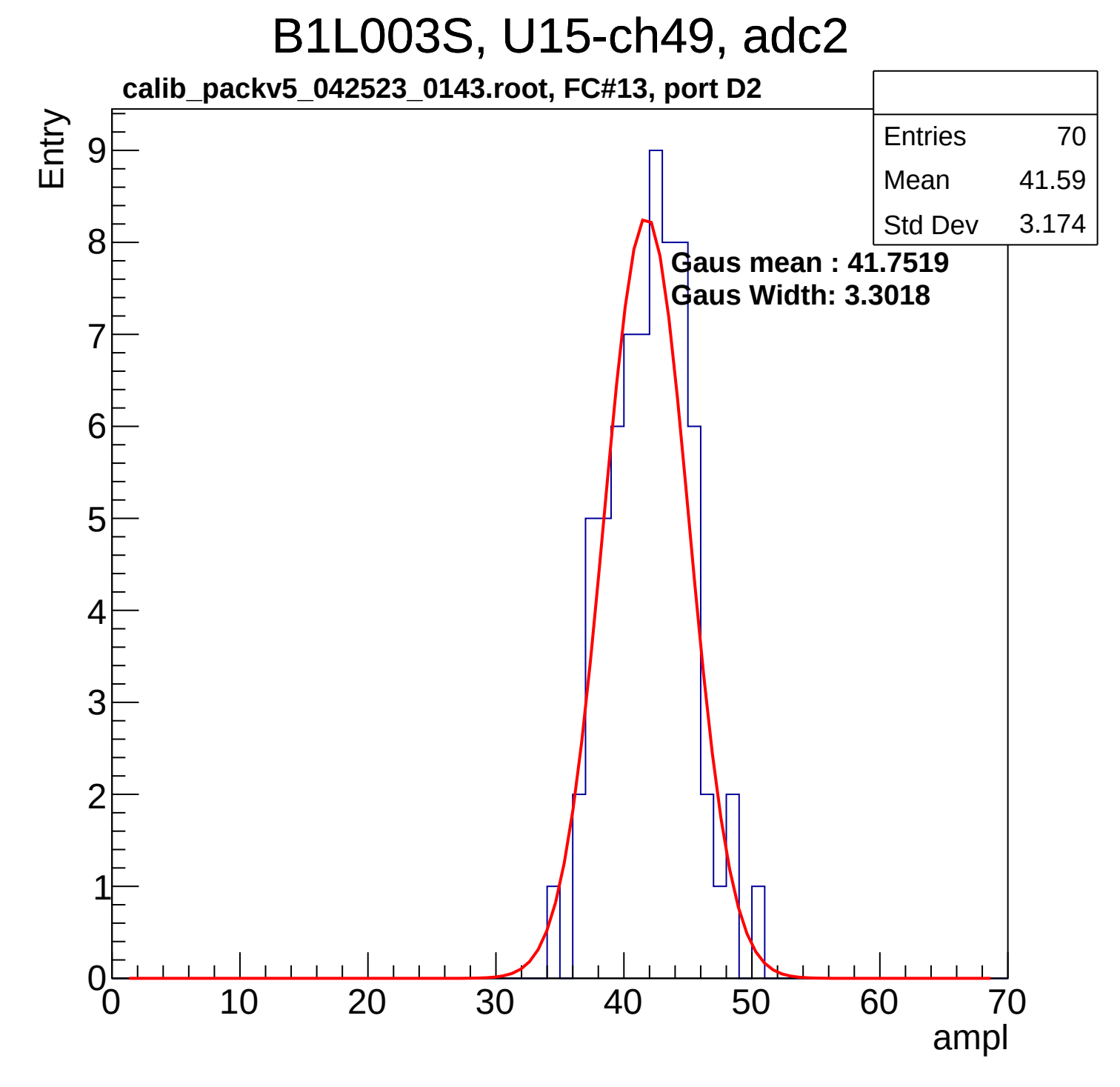
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.59
Std Dev 3.174
Gaus mean : 41.7519
Gaus Width: 3.3018
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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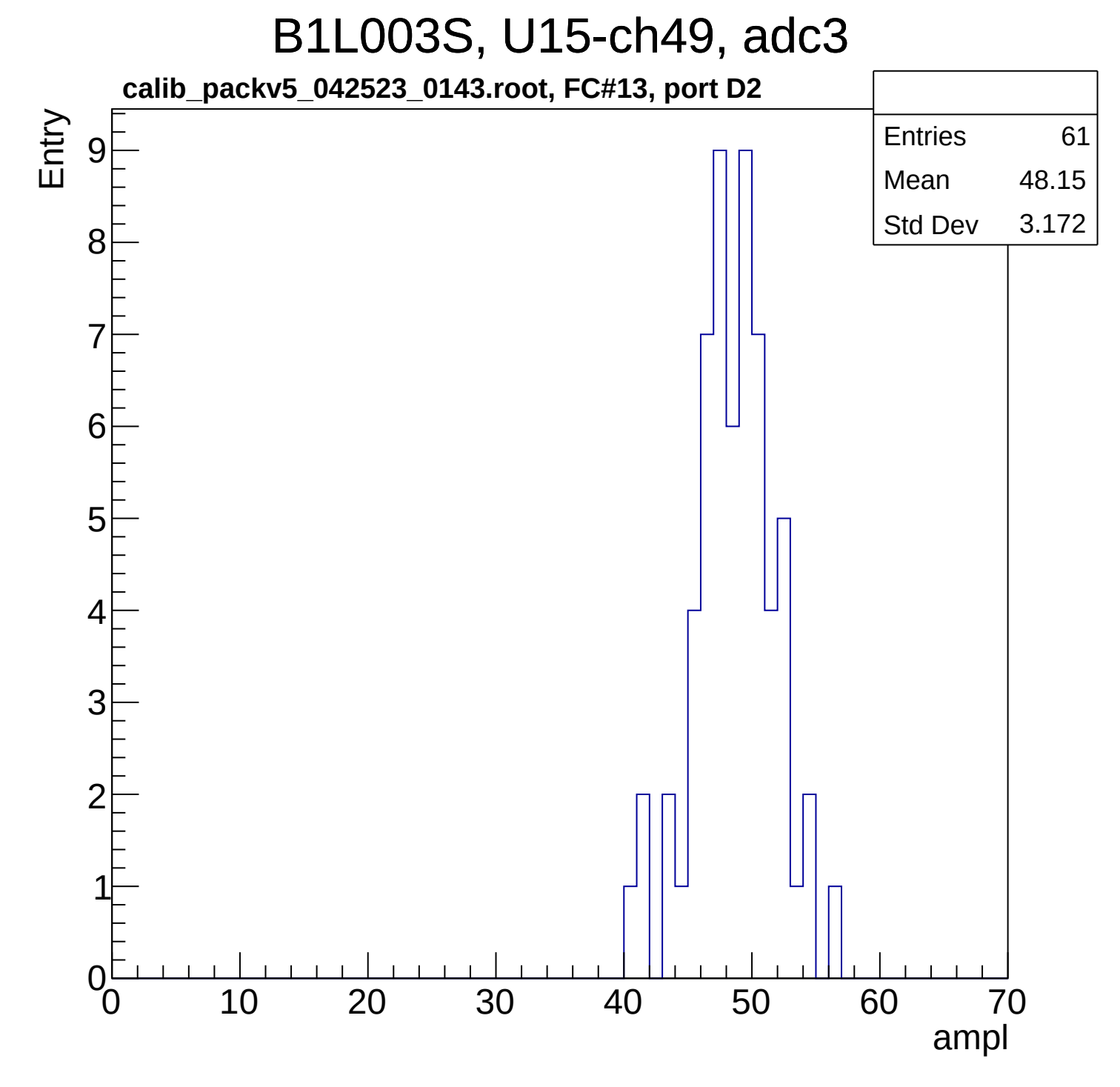
B1L003S, U15-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.15
Std Dev 3.172
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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60
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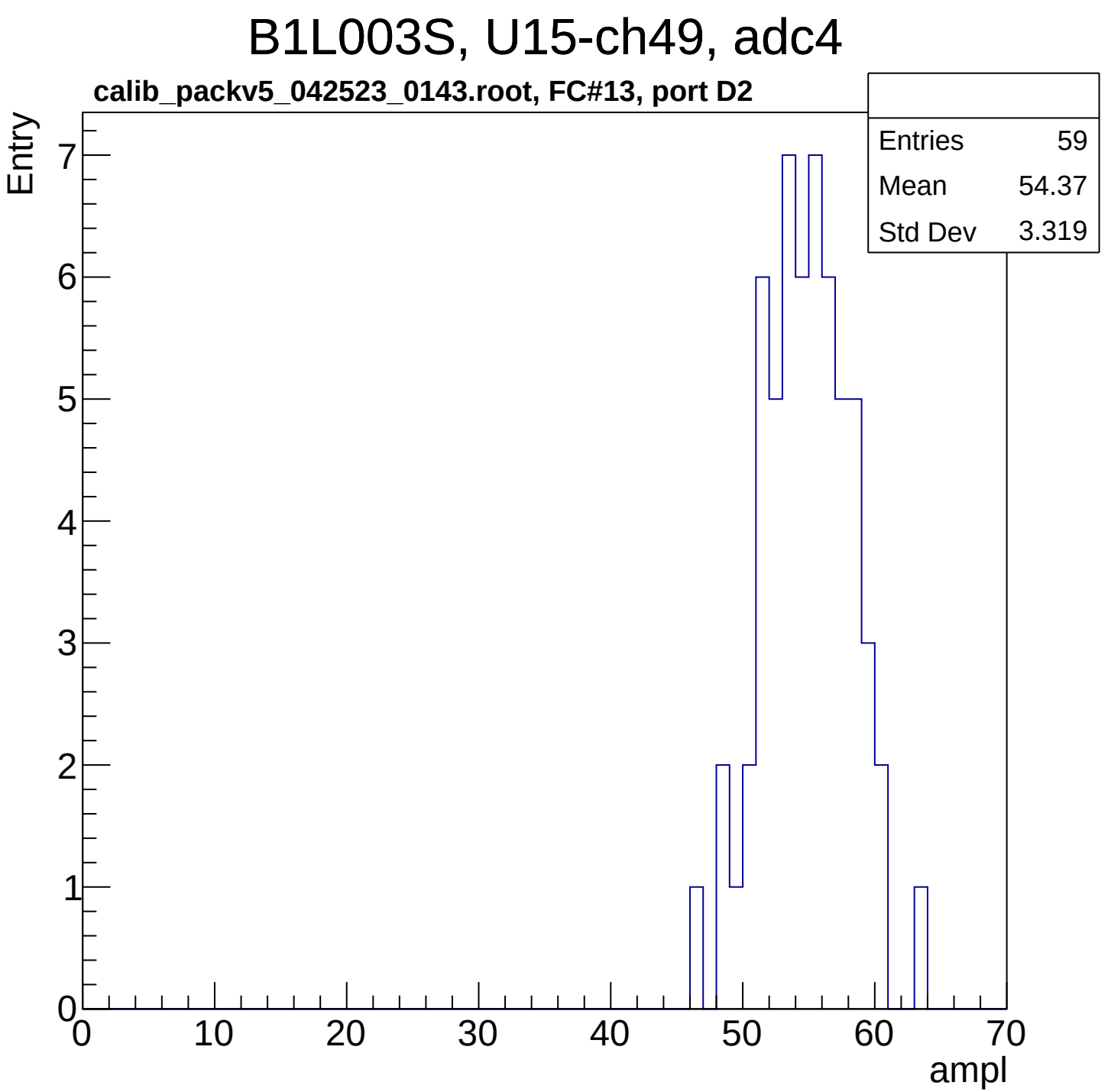
B1L003S, U15-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.37
Std Dev 3.319
Entry
ampl
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1
2
3
4
5
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0
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20
30
40
50
60
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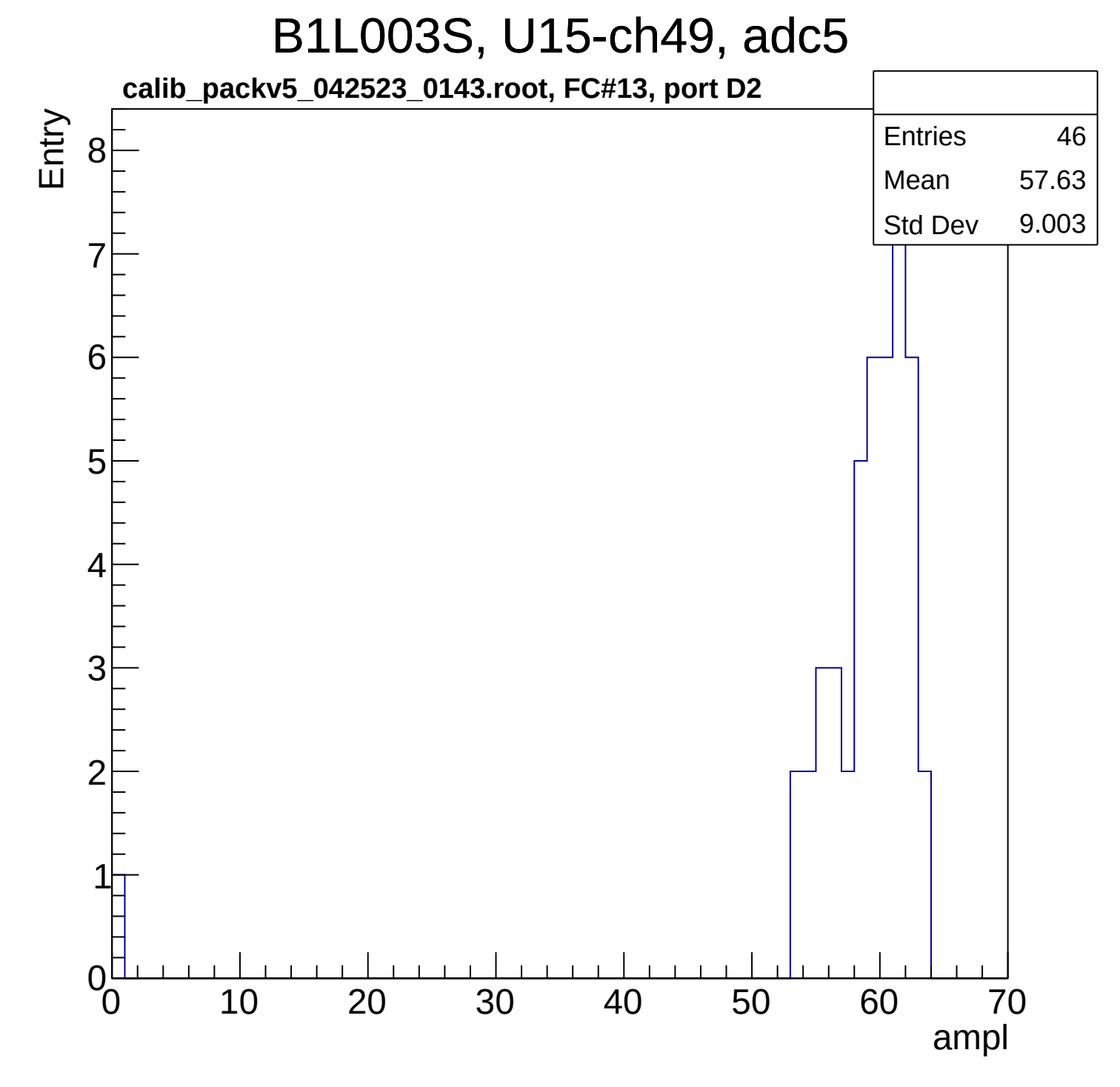
B1L003S, U15-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 57.63
Std Dev 9.003
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
50
60
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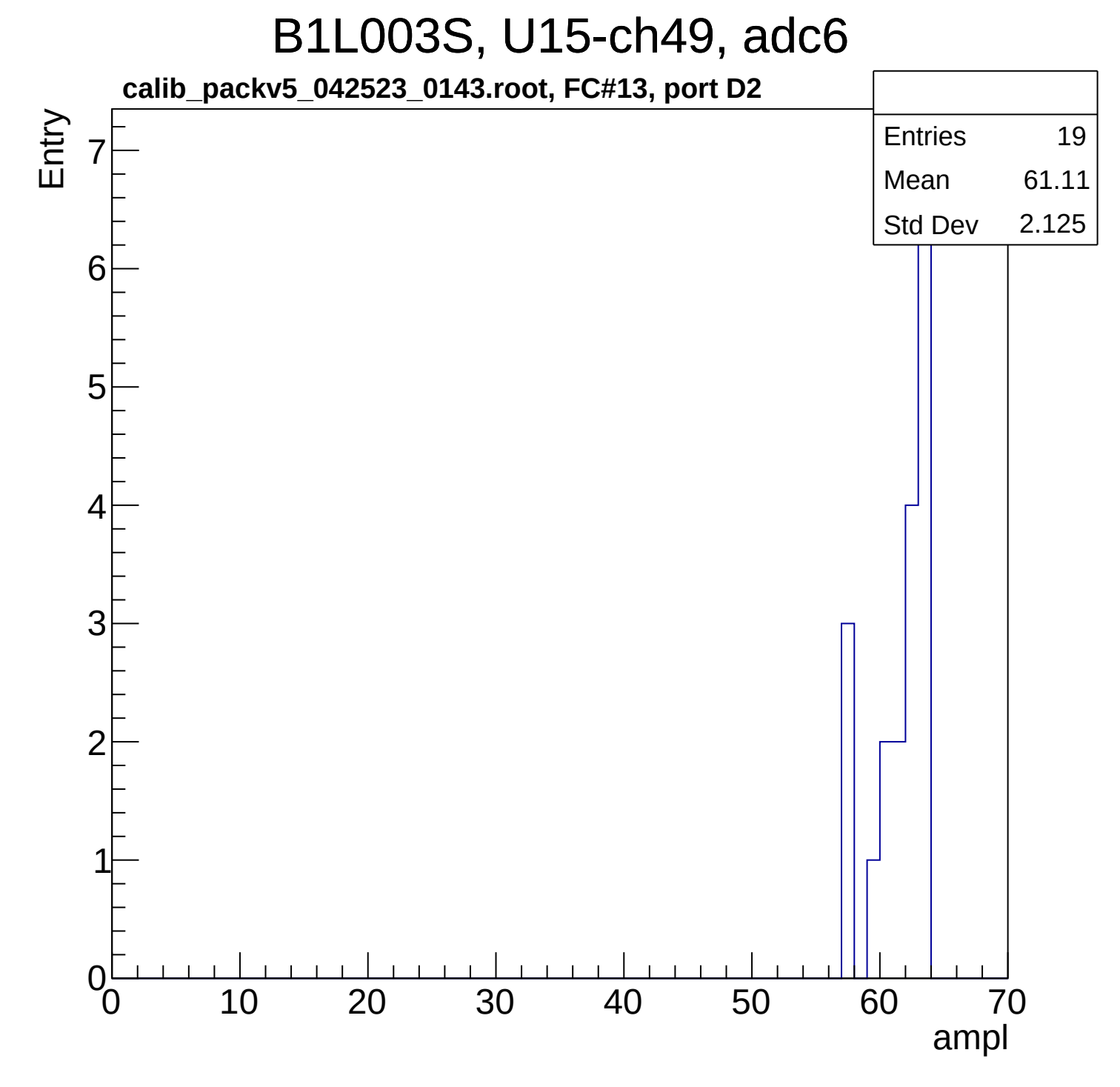
B1L003S, U15-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 61.11
Std Dev 2.125
Entry
ampl
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1
2
3
4
5
6
7
0
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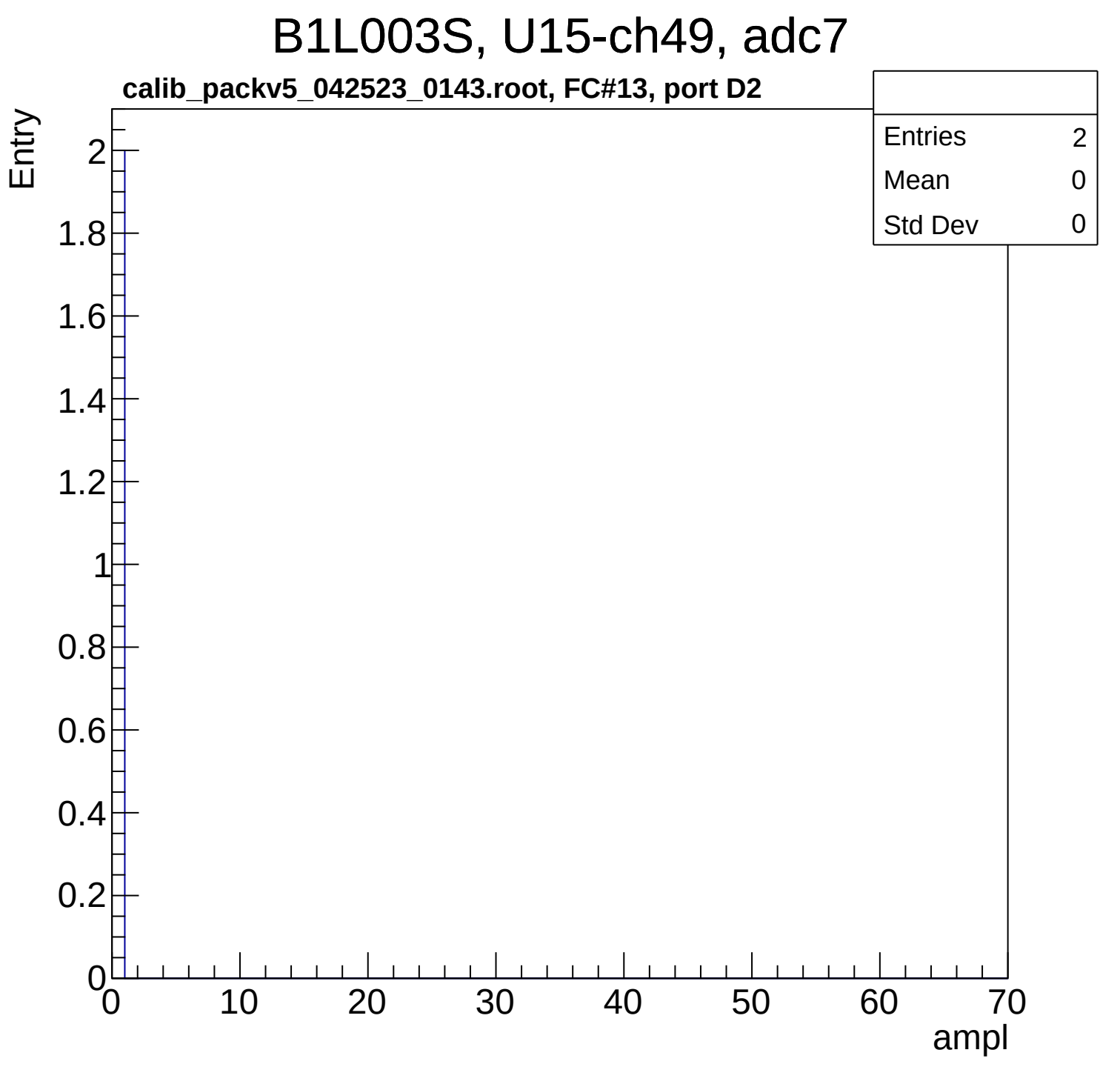
20
30
40
50
60
70


B1L003S, U15-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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50
60
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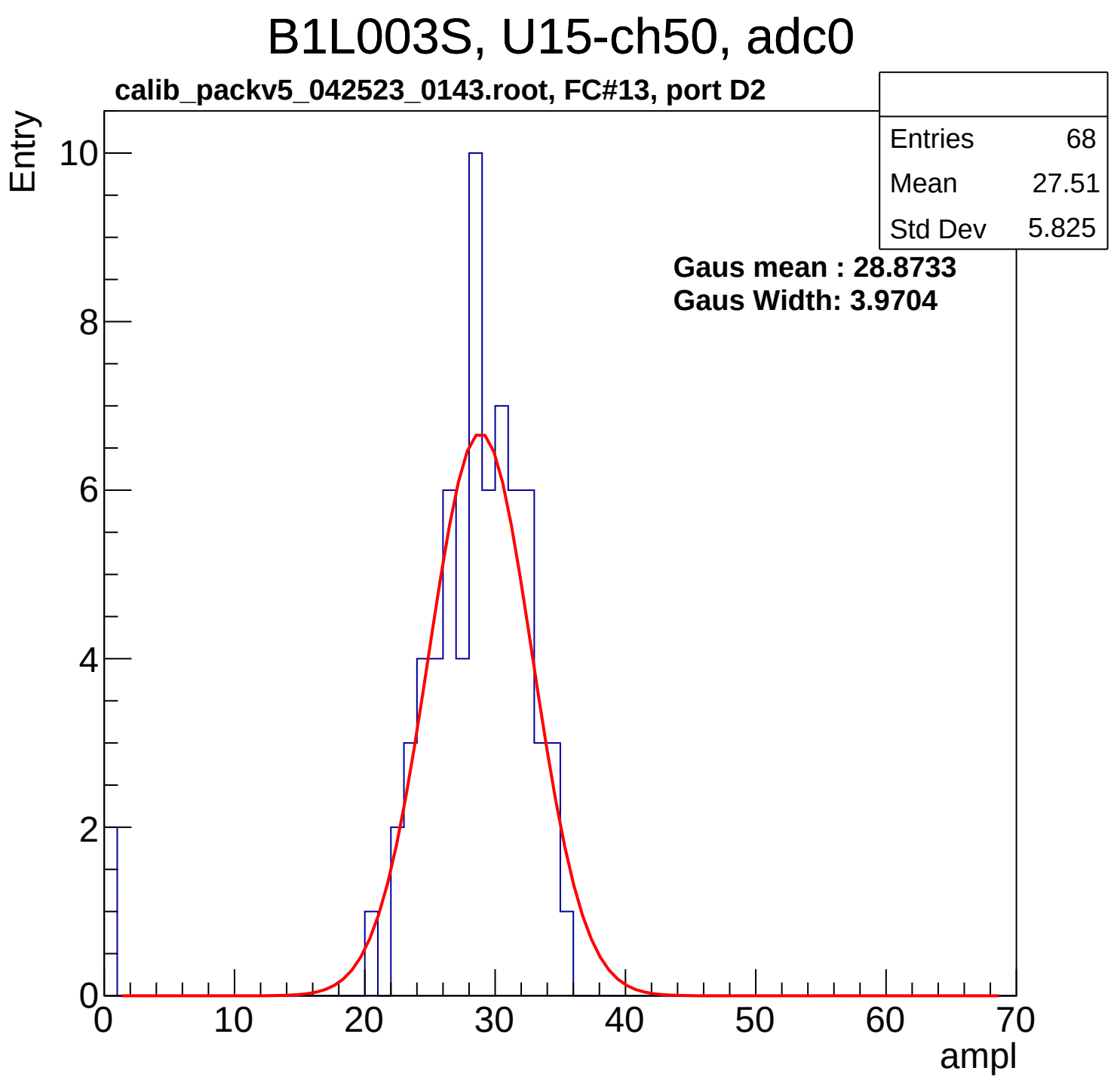
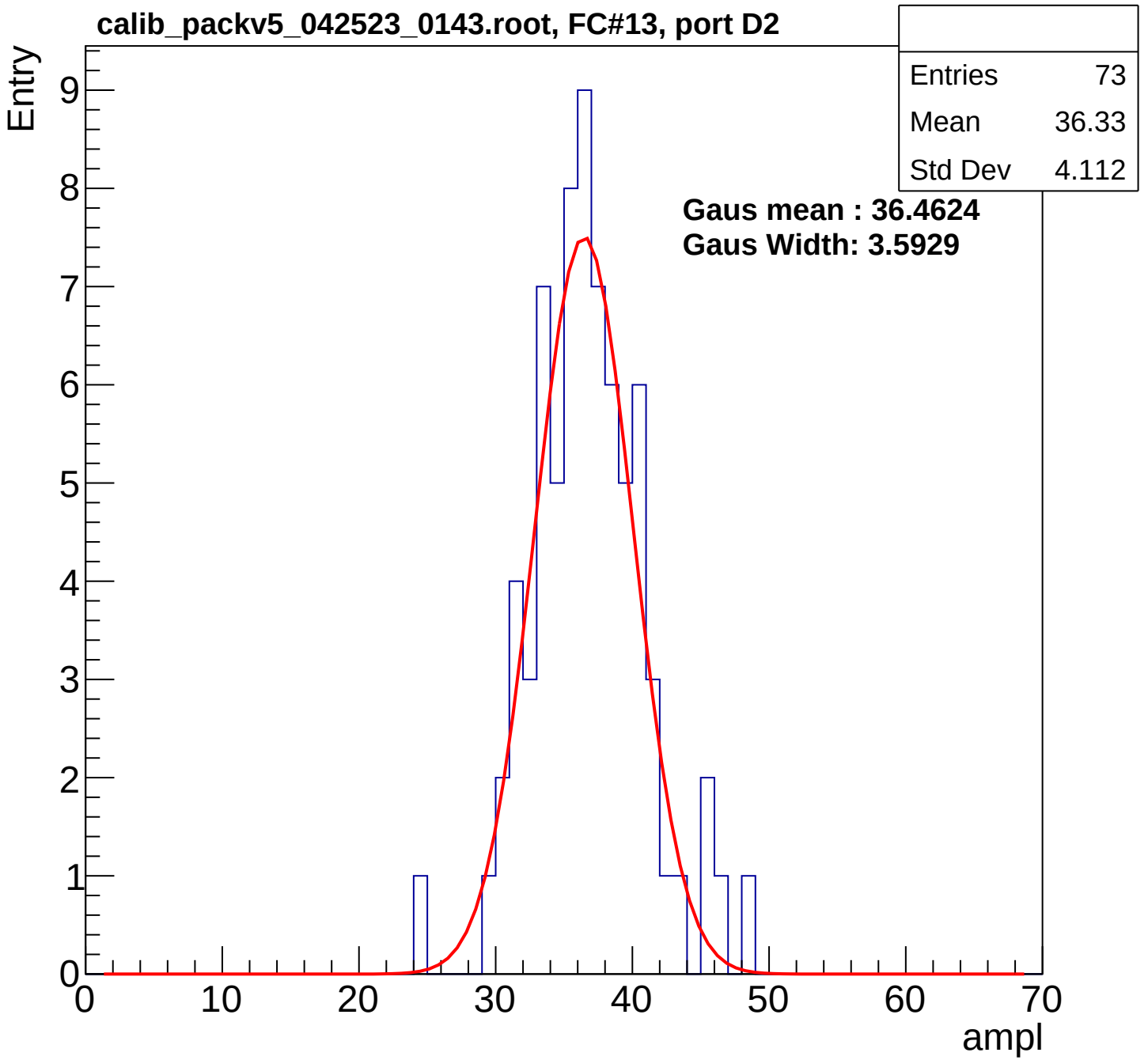

B1L003S, U15-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 27.51
Std Dev 5.825
Gaus mean : 28.8733
Gaus Width: 3.9704
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.33
Std Dev 4.112
Gaus mean : 36.4624
Gaus Width: 3.5929
Entry
ampl
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10
20
30
40
50
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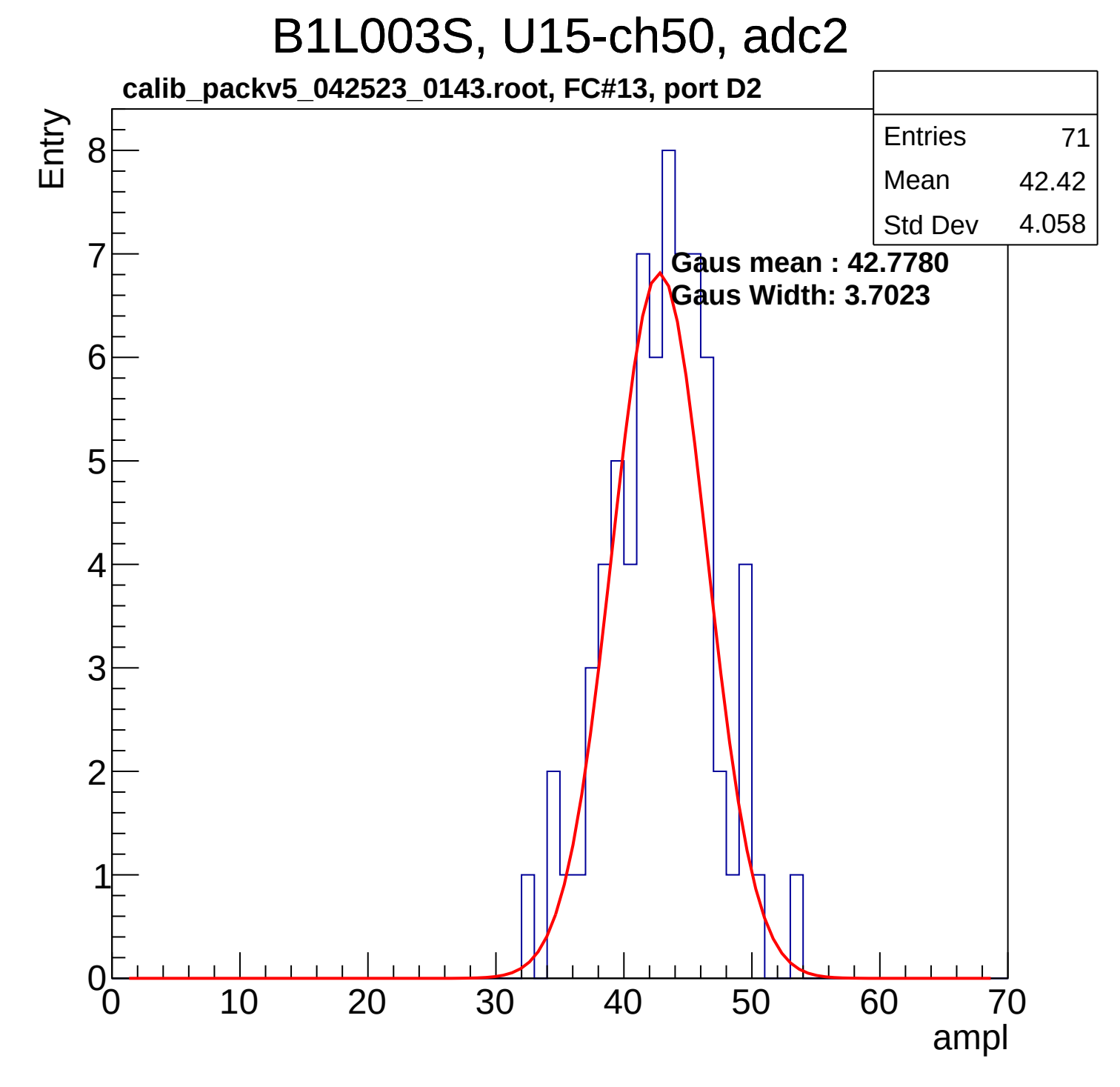
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.42
Std Dev 4.058
Gaus mean : 42.7780
Gaus Width: 3.7023
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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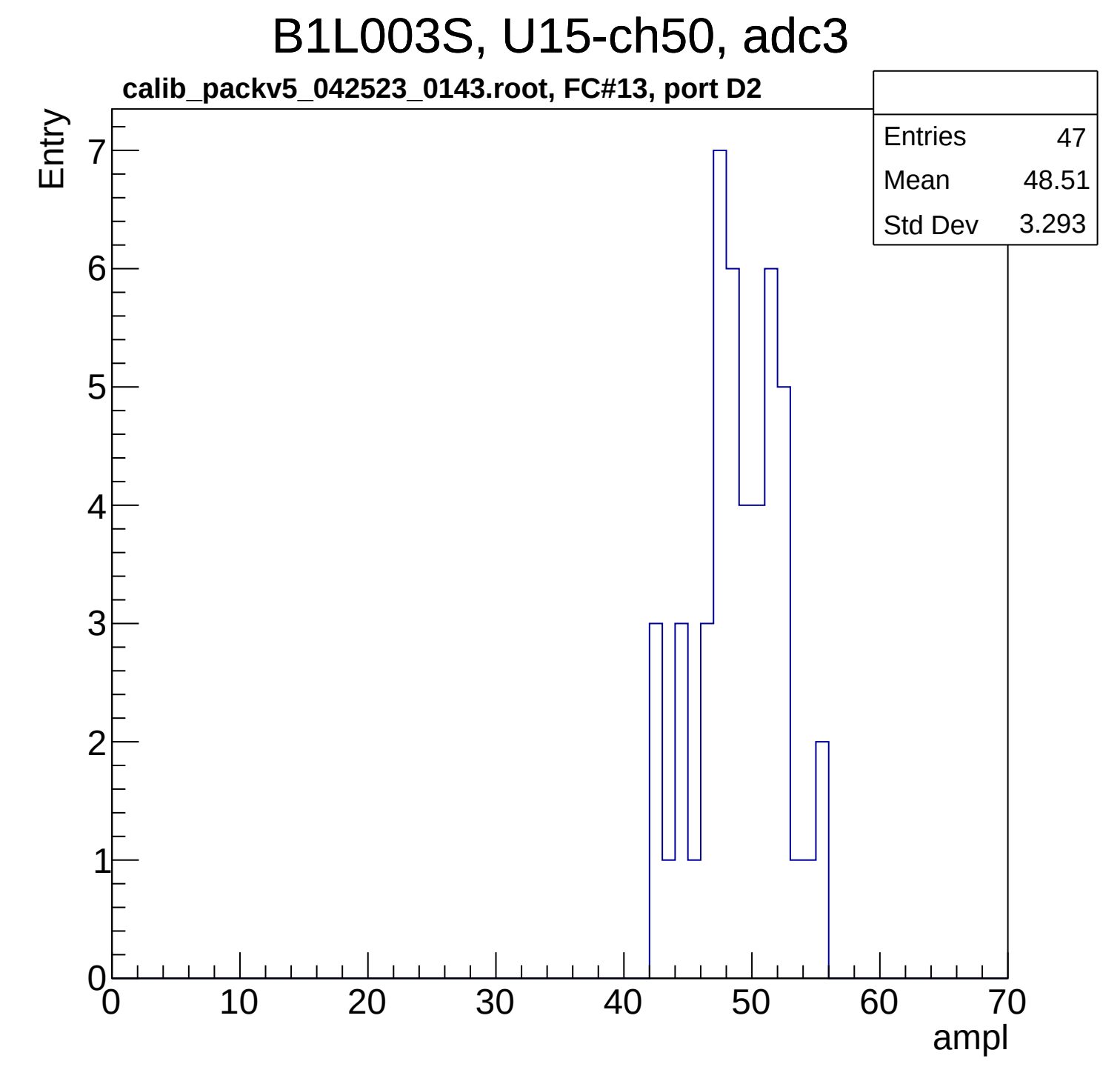
B1L003S, U15-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 48.51
Std Dev 3.293
Entry
ampl
0
1
2
3
4
5
6
7
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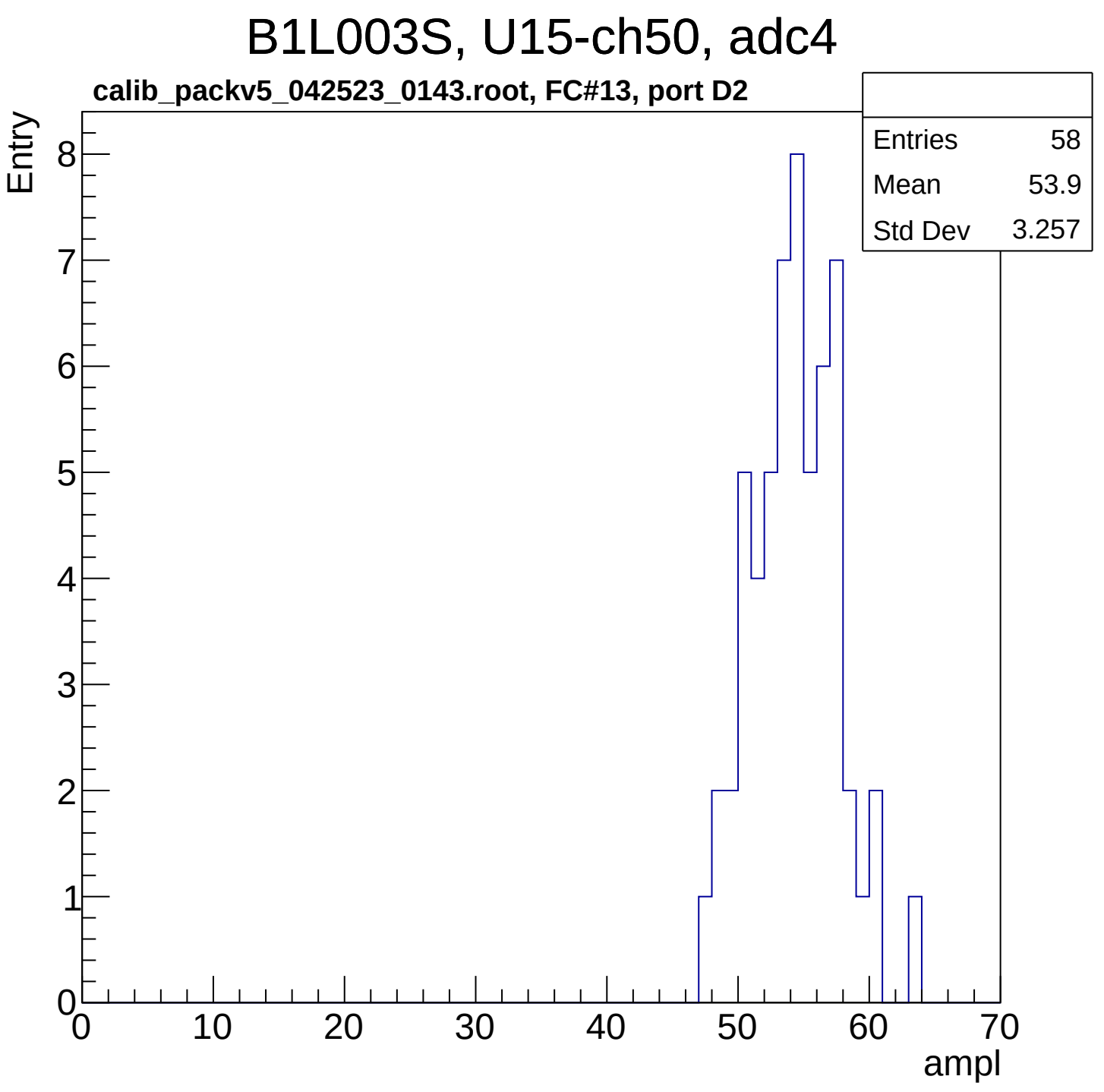
B1L003S, U15-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.9
Std Dev 3.257
Entry
ampl
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1
2
3
4
5
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7
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0
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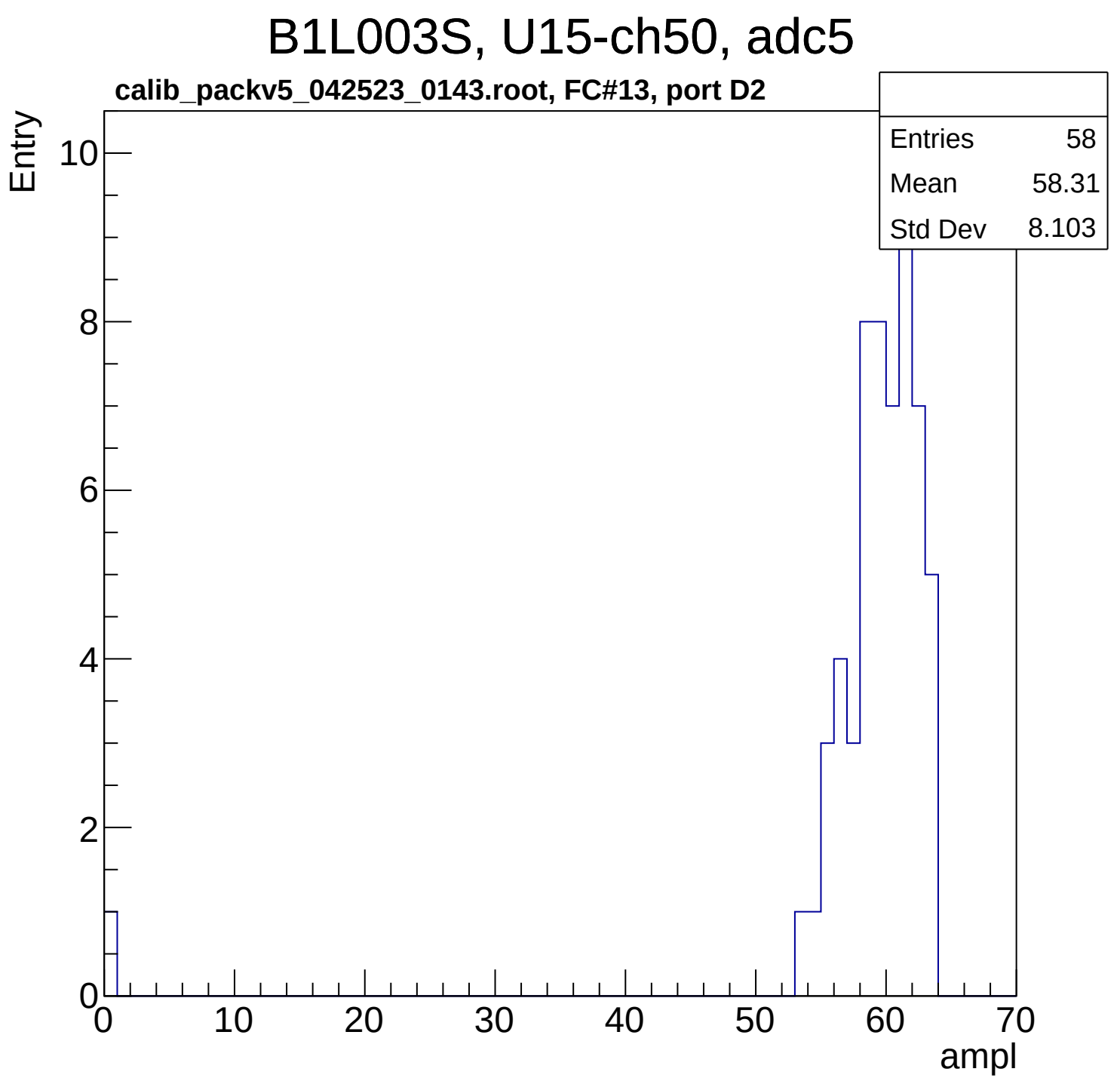
B1L003S, U15-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.31
Std Dev 8.103
Entry
0
2
4
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8
10
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10
20
30
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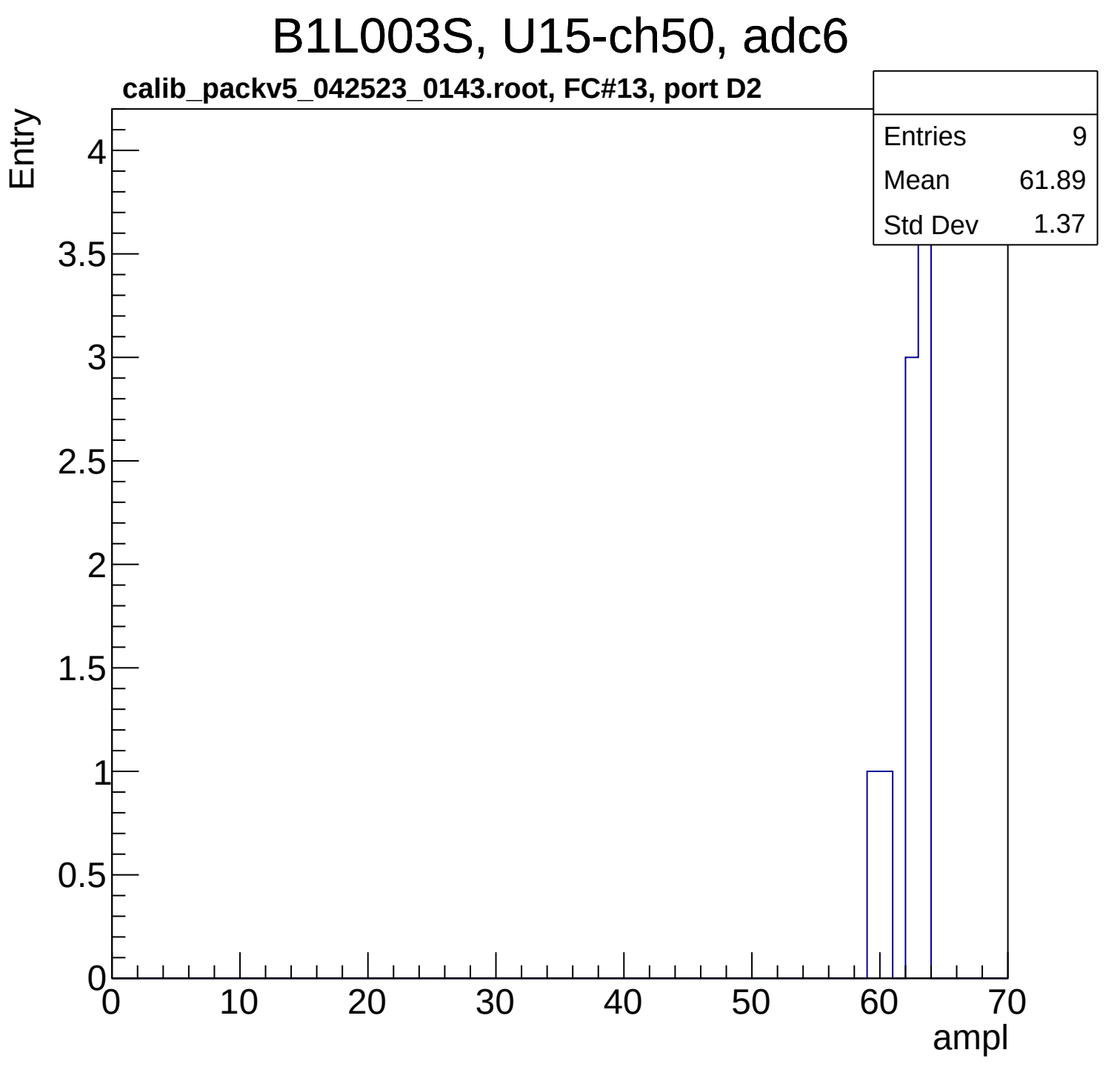
50
60
70
ampl


B1L003S, U15-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.89
Std Dev 1.37
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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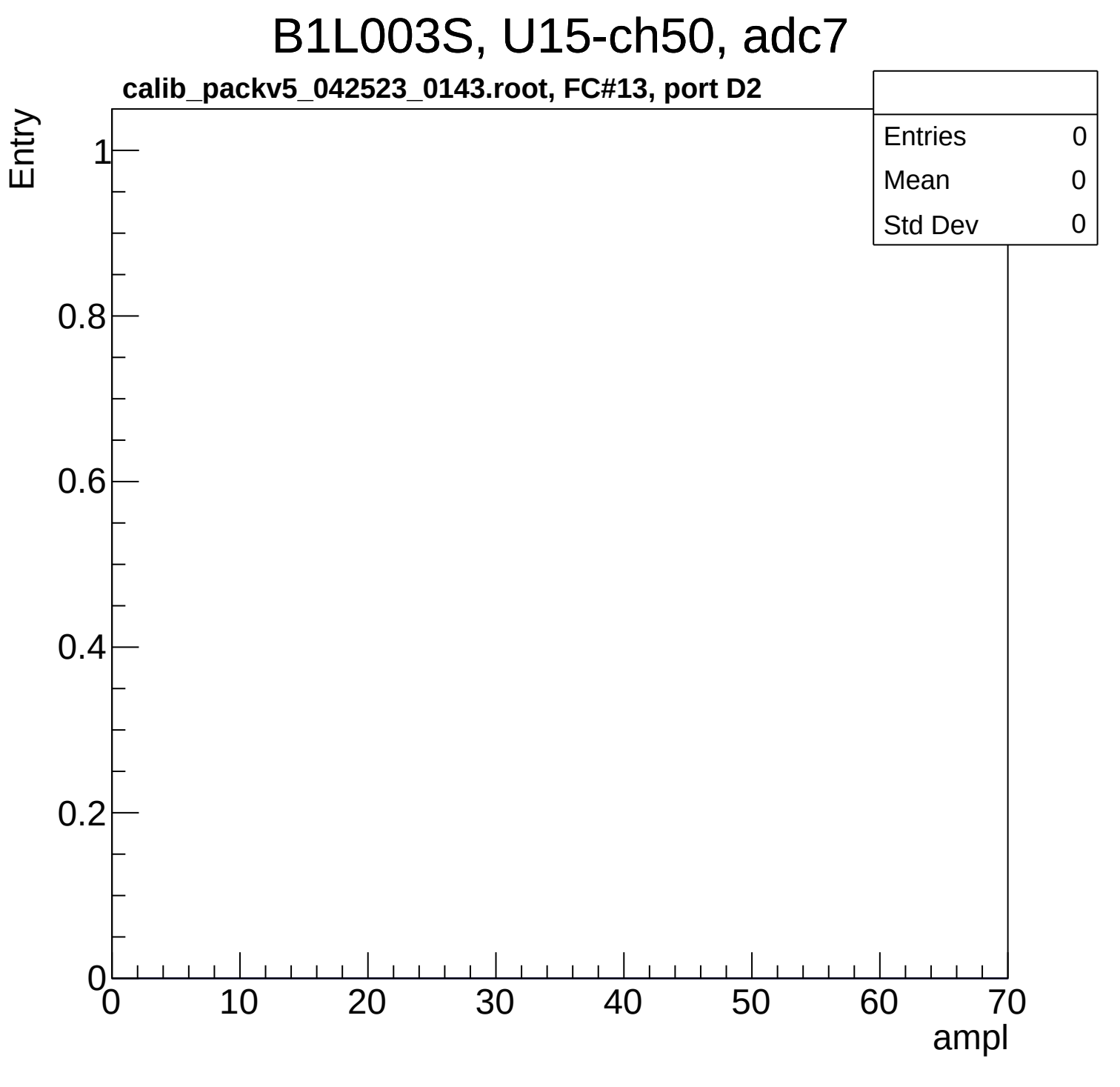
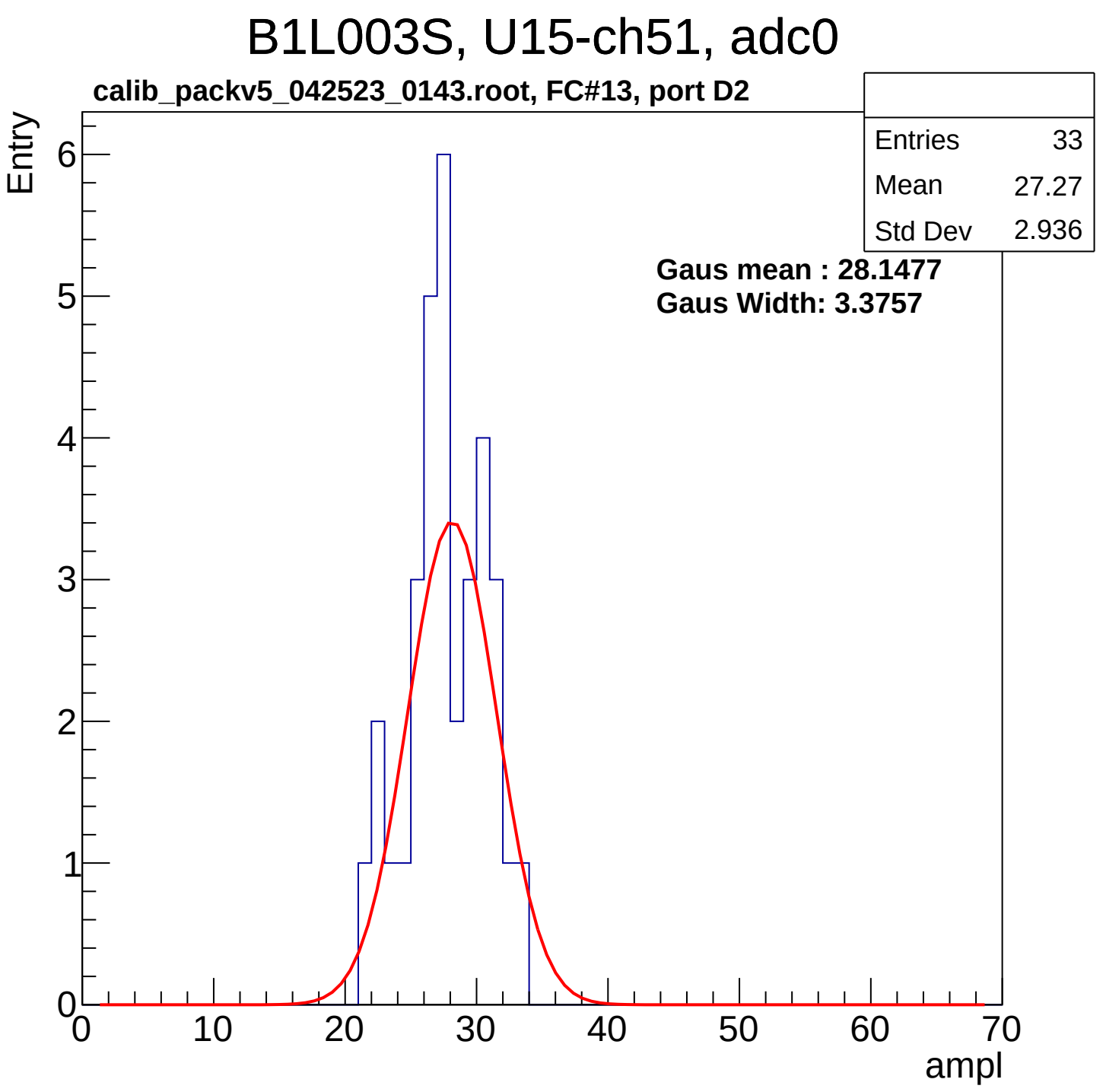

B1L003S, U15-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 27.27
Std Dev 2.936
Gaus mean : 28.1477
Gaus Width: 3.3757
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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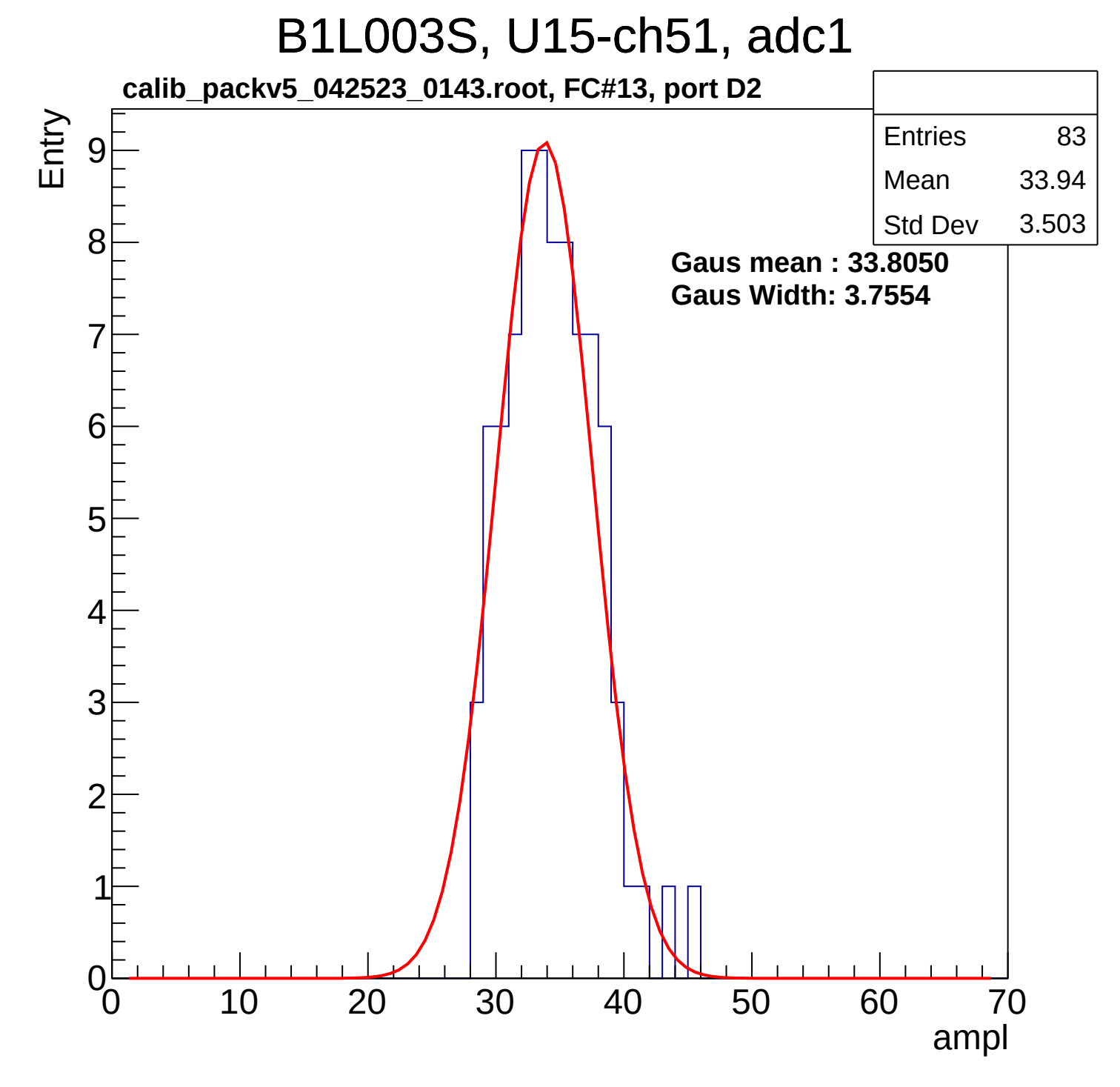
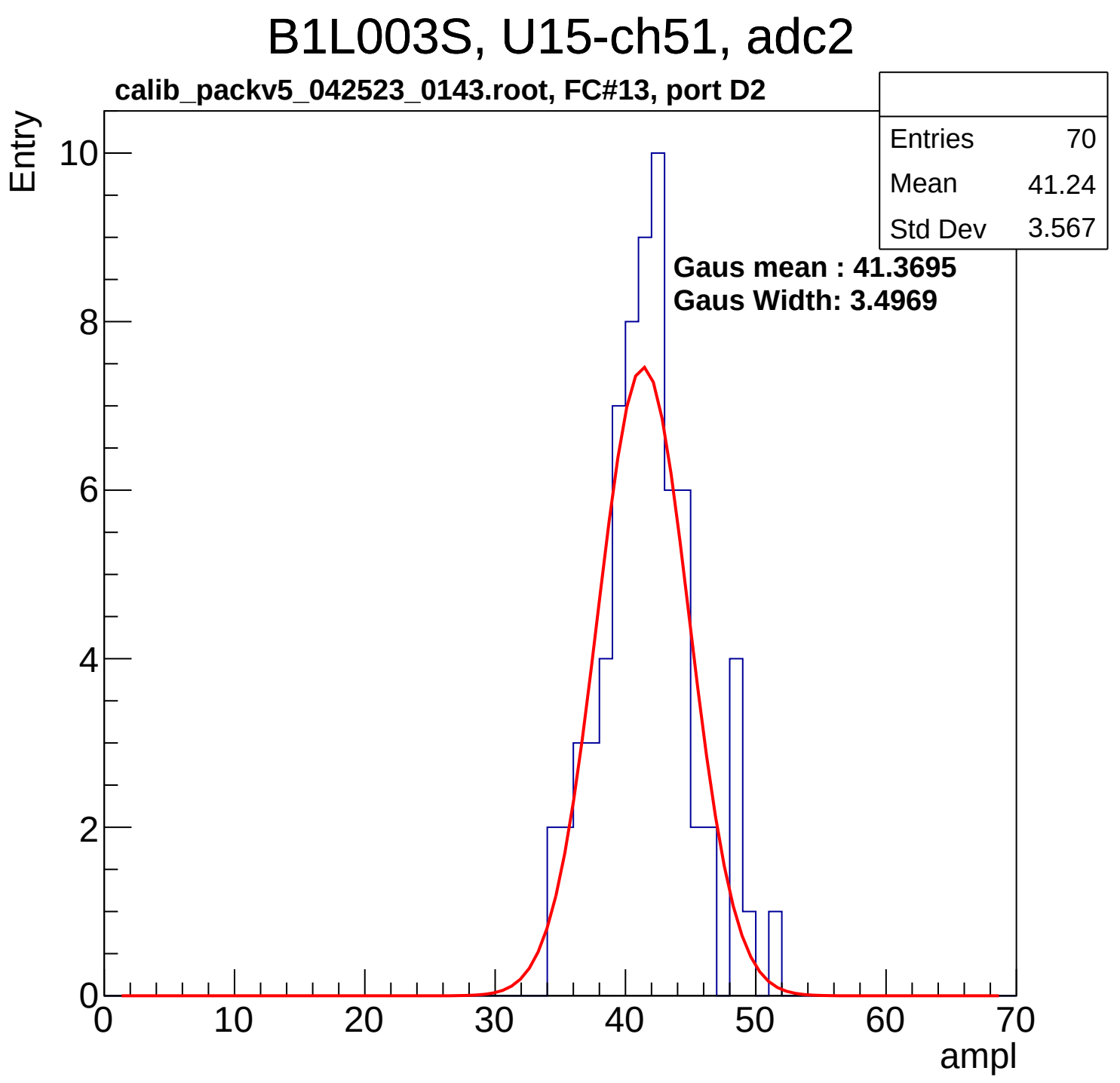
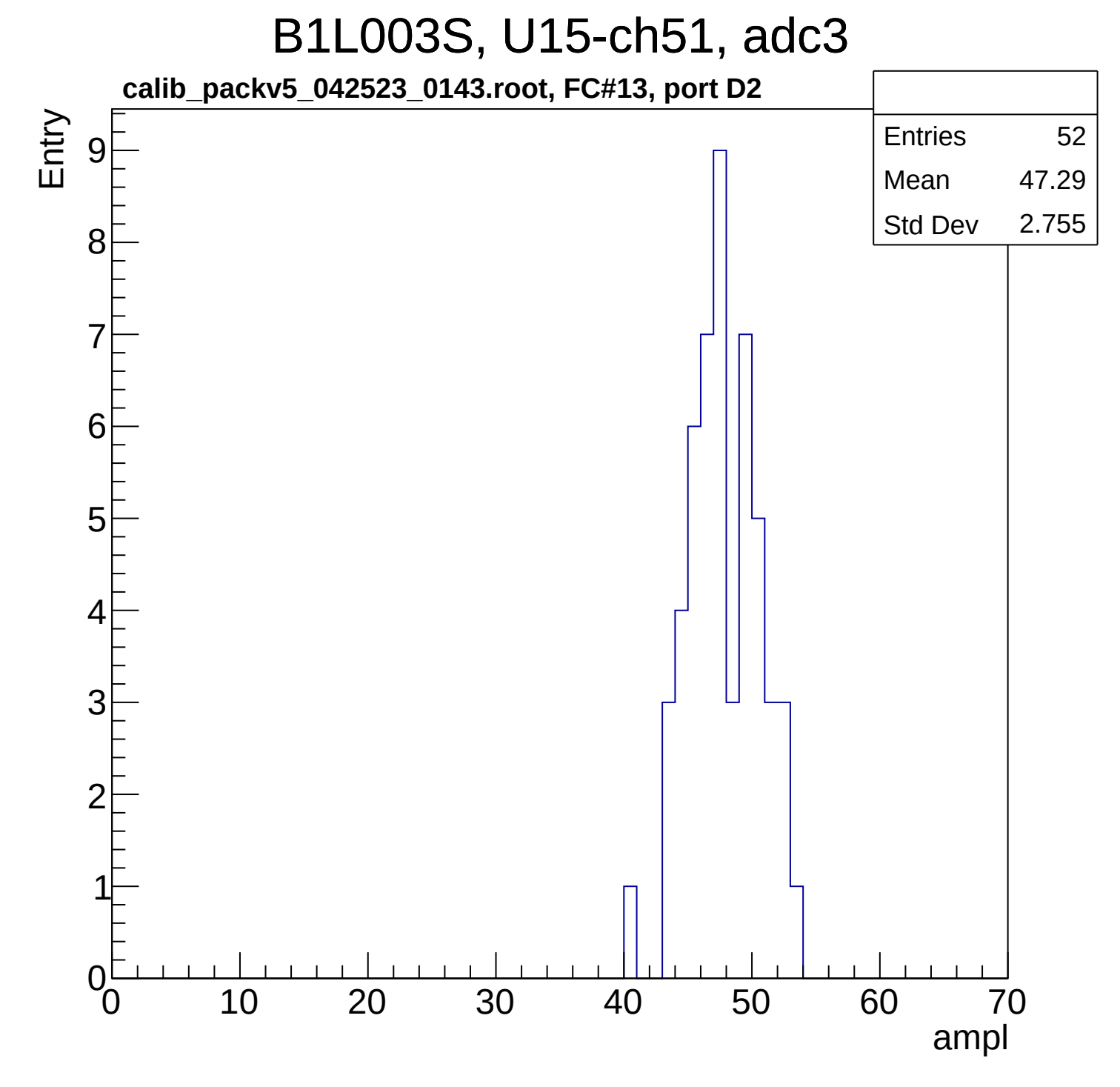

B1L003S, U15-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 33.94
Std Dev 3.503
Gaus mean : 33.8050
Gaus Width: 3.7554
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 41.24
Std Dev 3.567
Gaus mean : 41.3695
Gaus Width: 3.4969
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 47.29
Std Dev 2.755
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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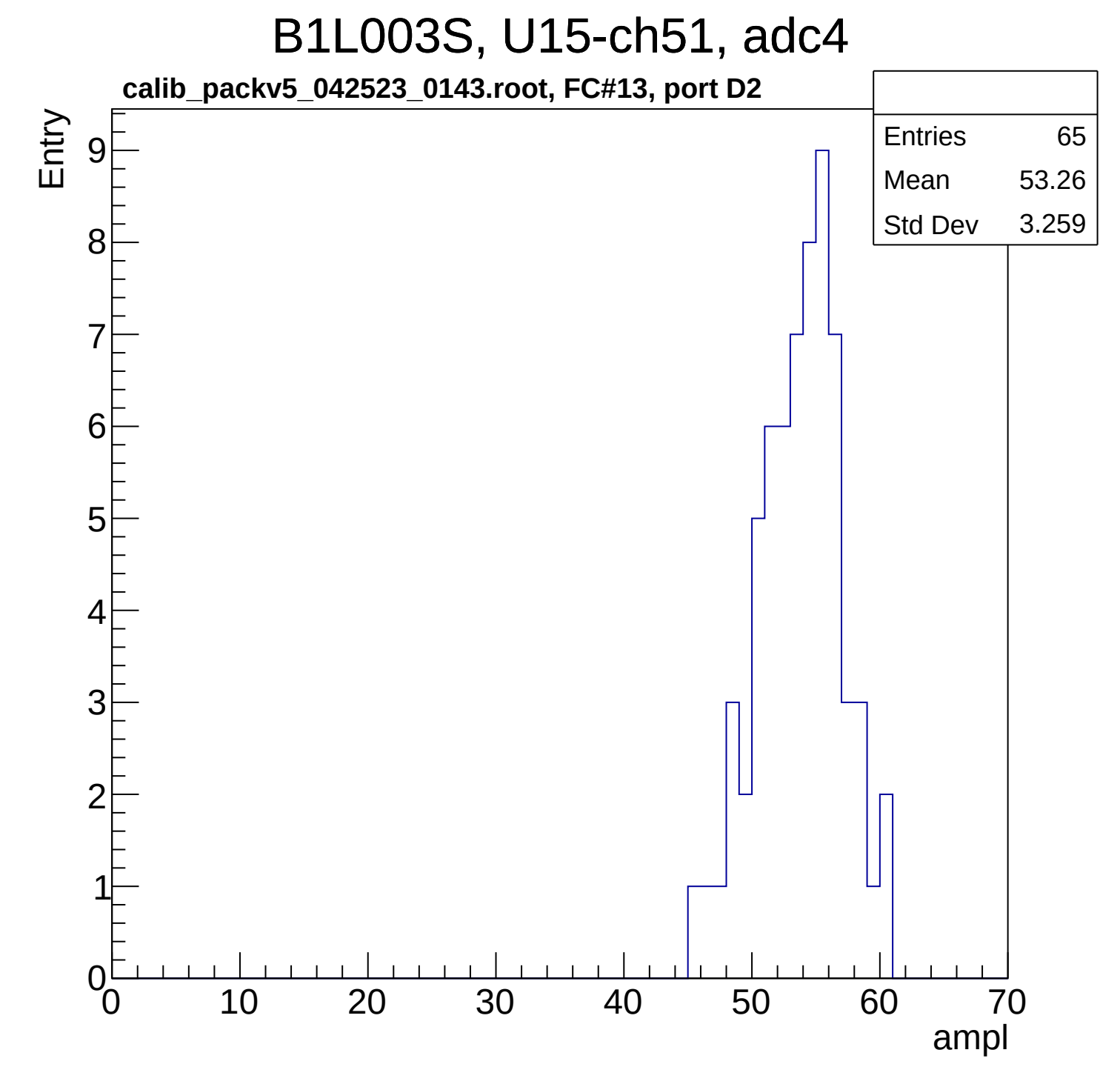
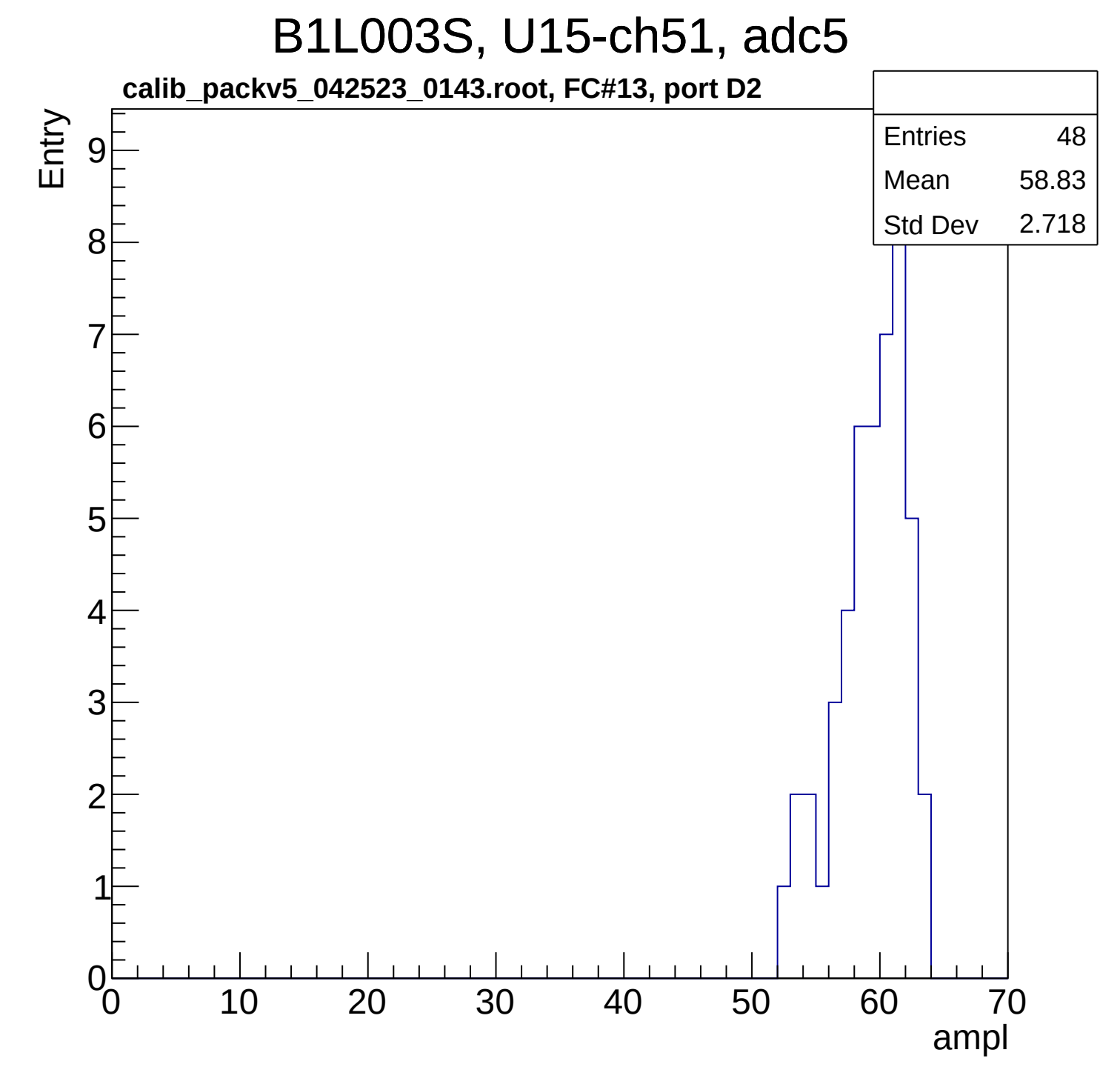
40
50
60
70


B1L003S, U15-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.26
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.83
Std Dev 2.718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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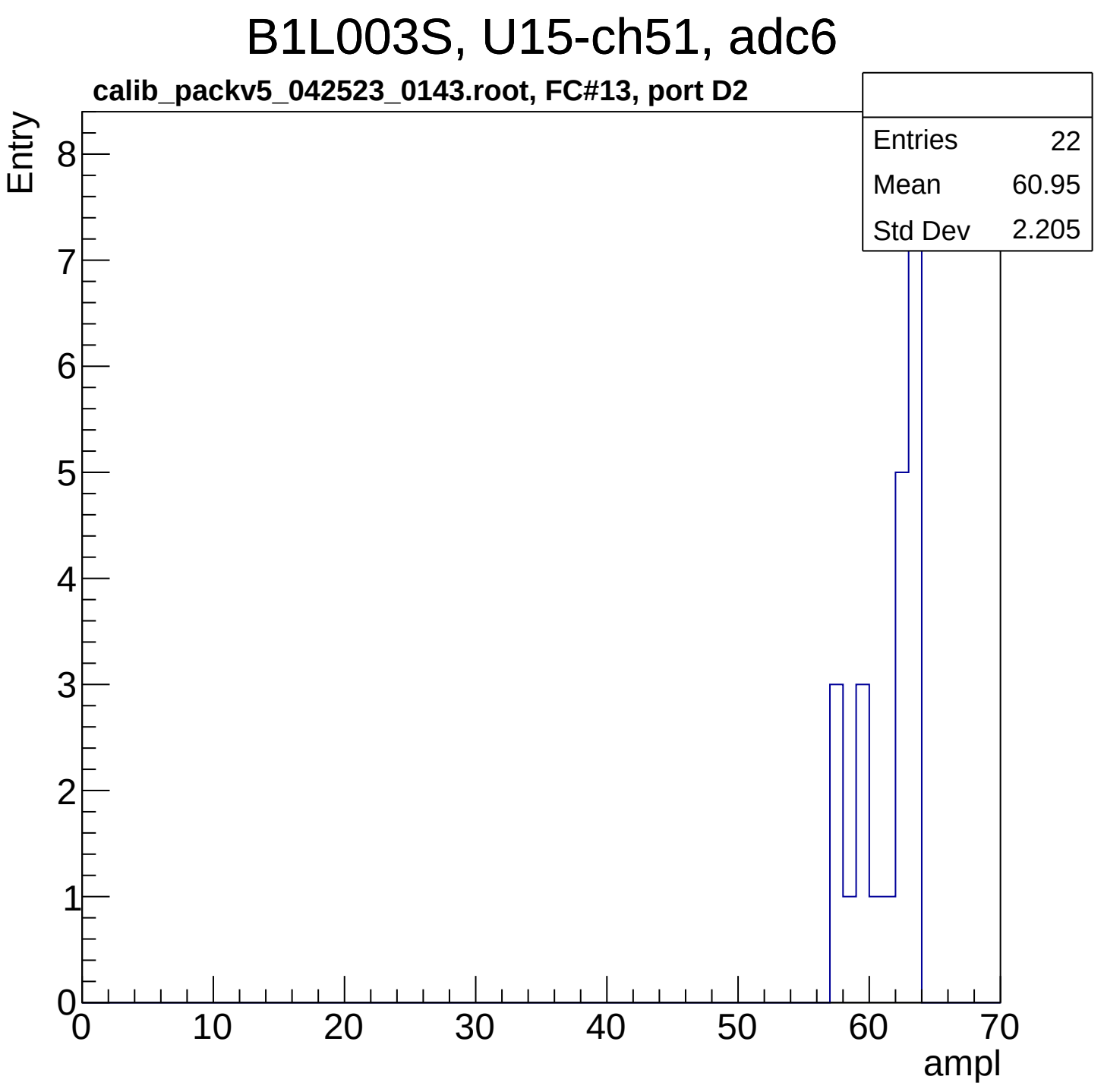
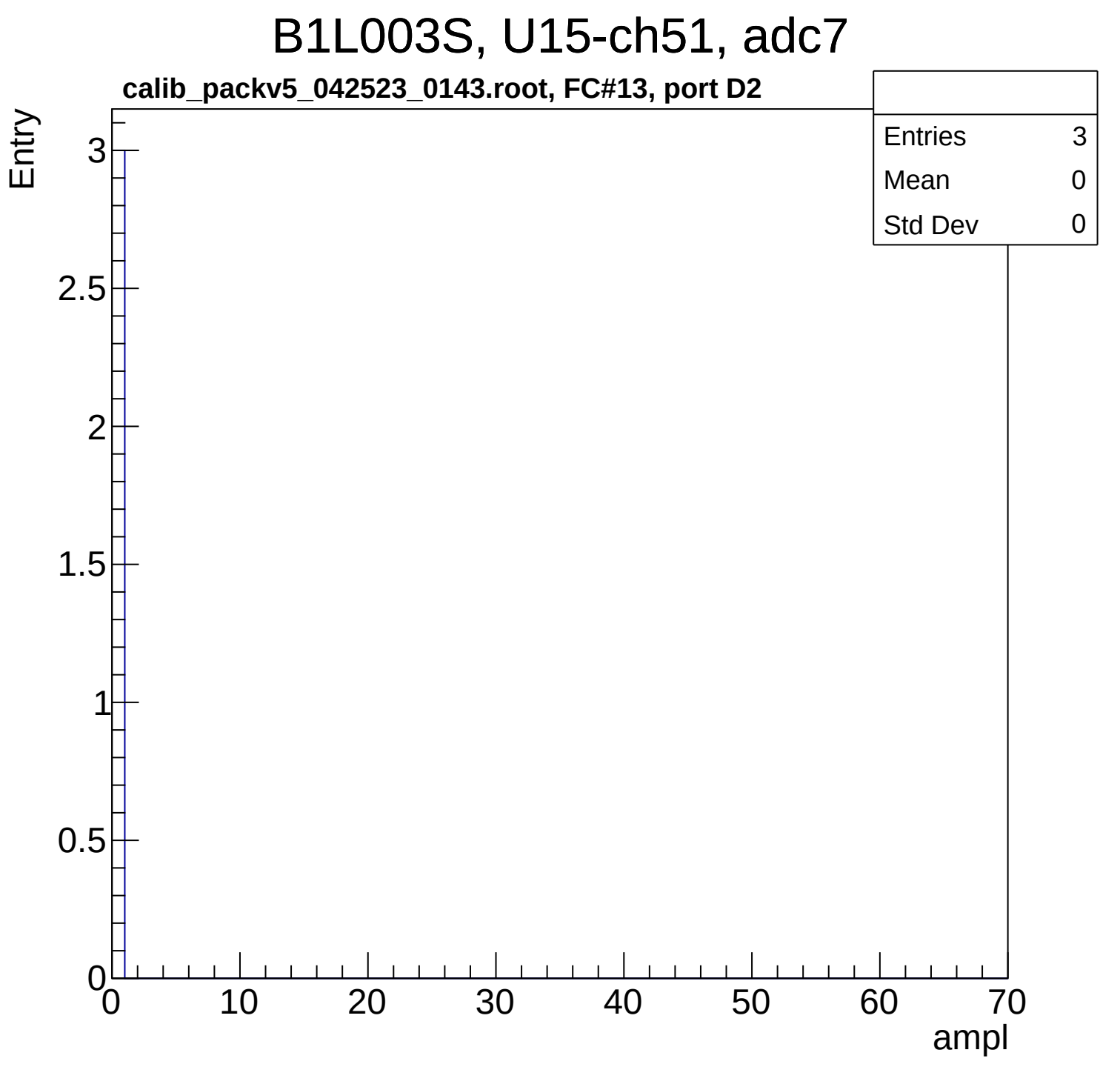
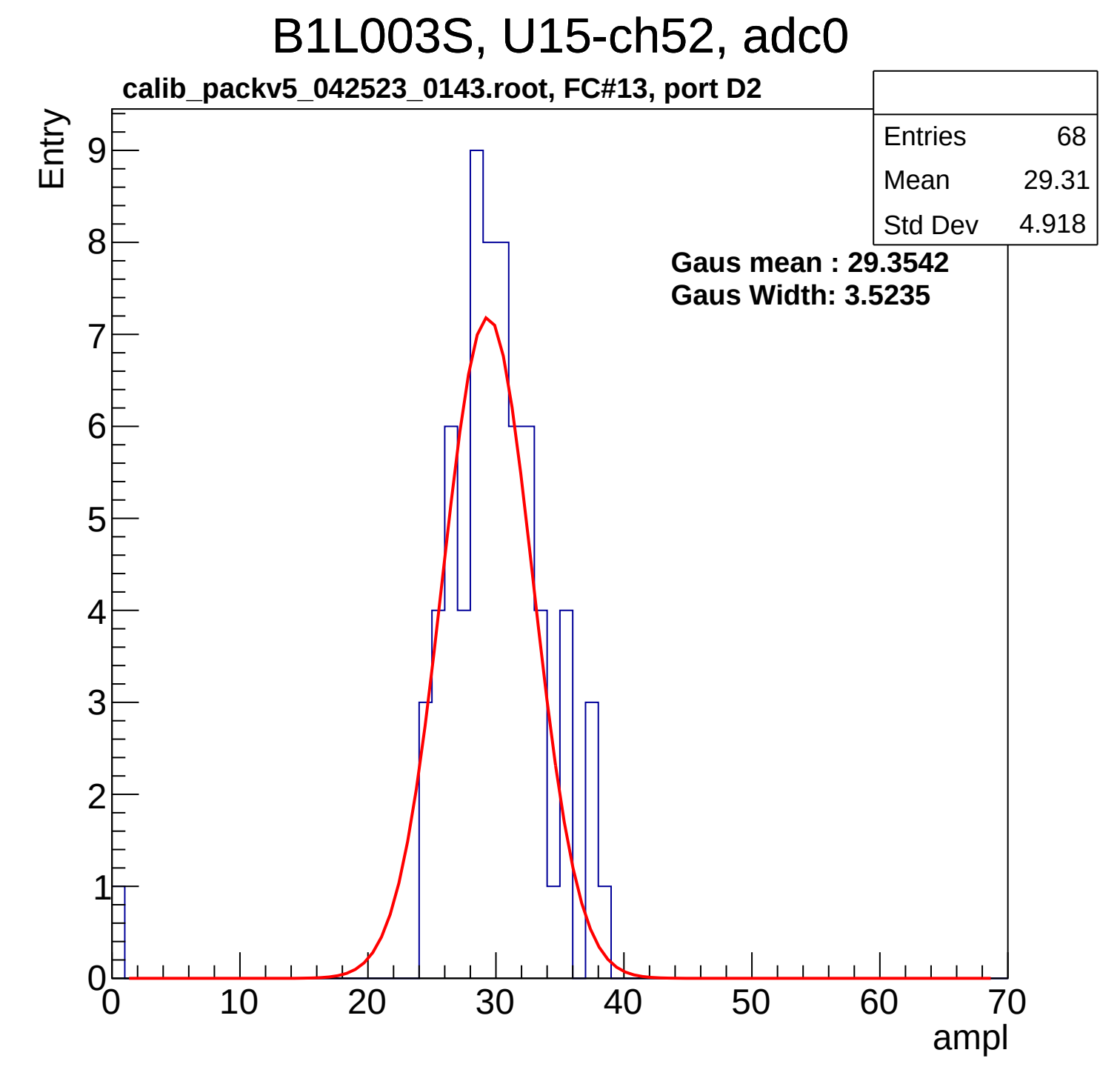
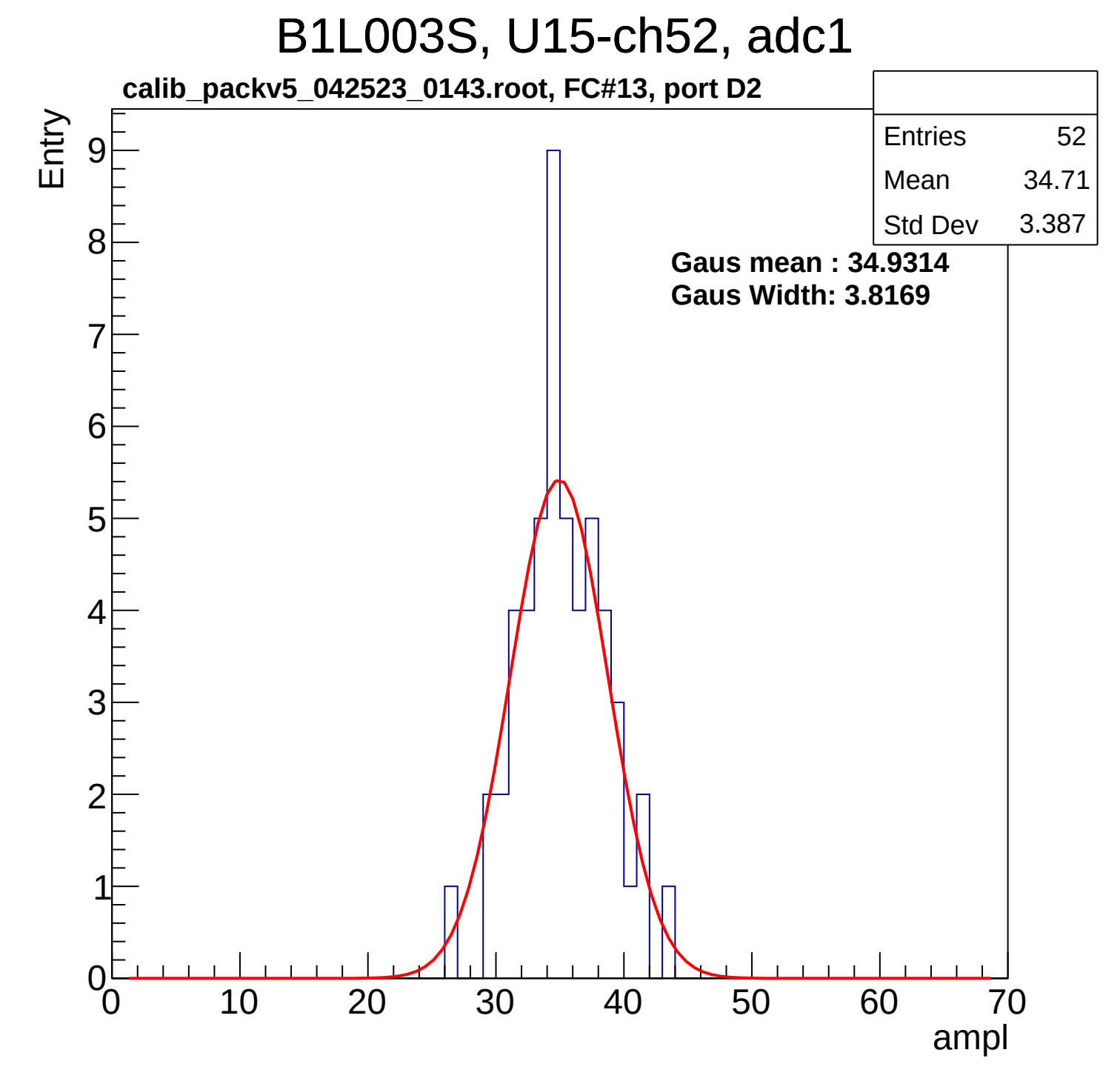
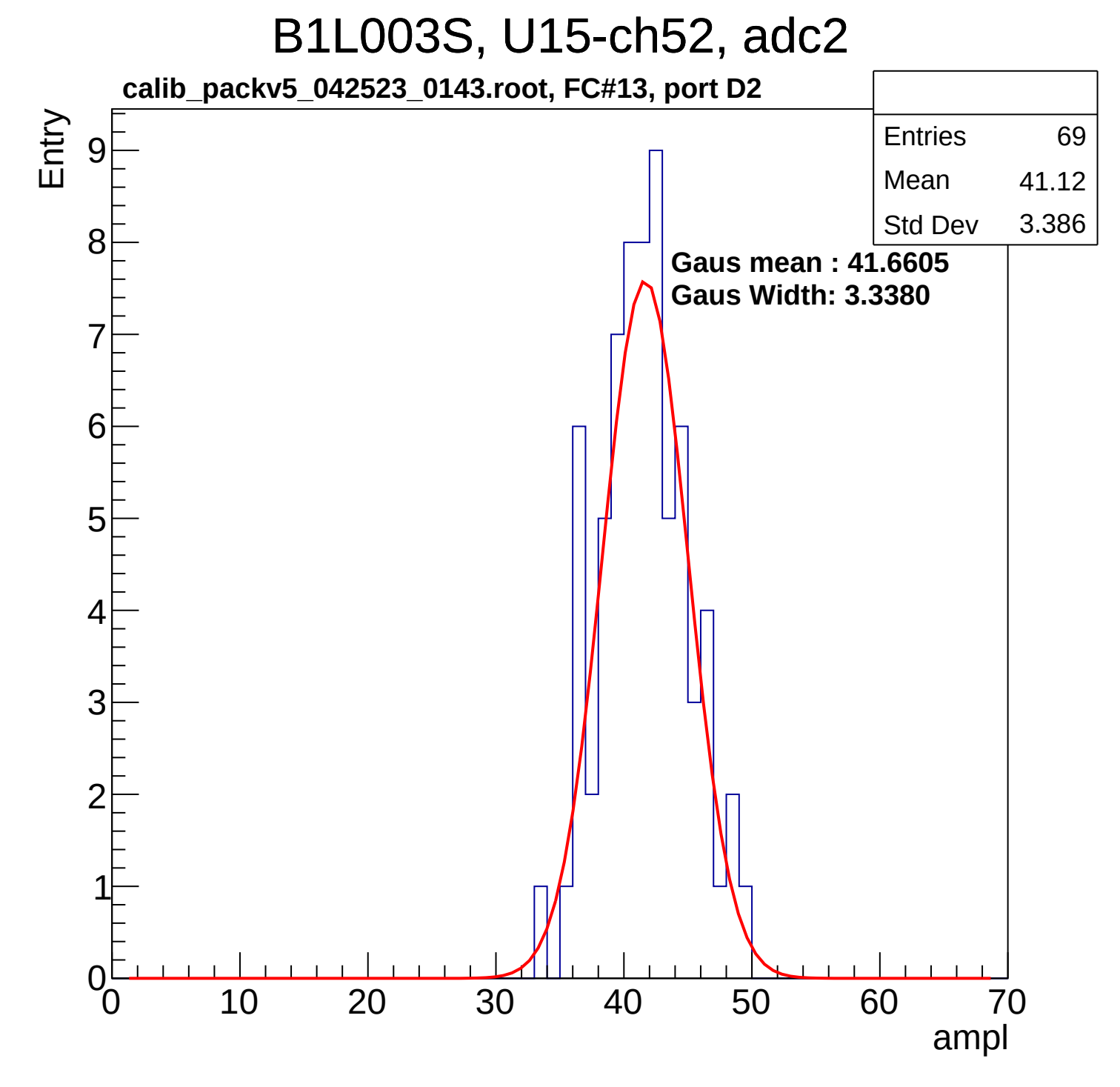
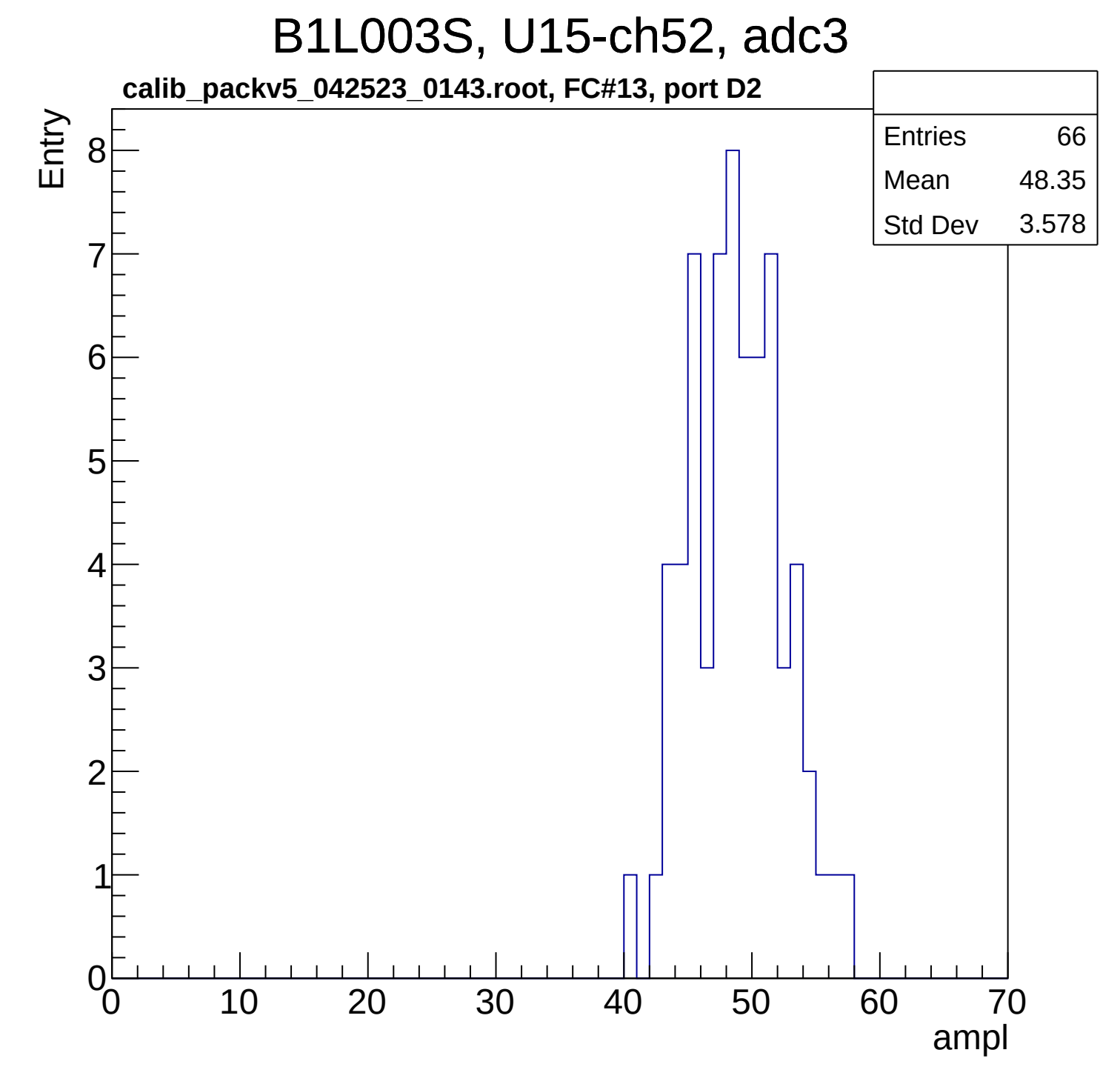

B1L003S, U15-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 60.95
Std Dev 2.205
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

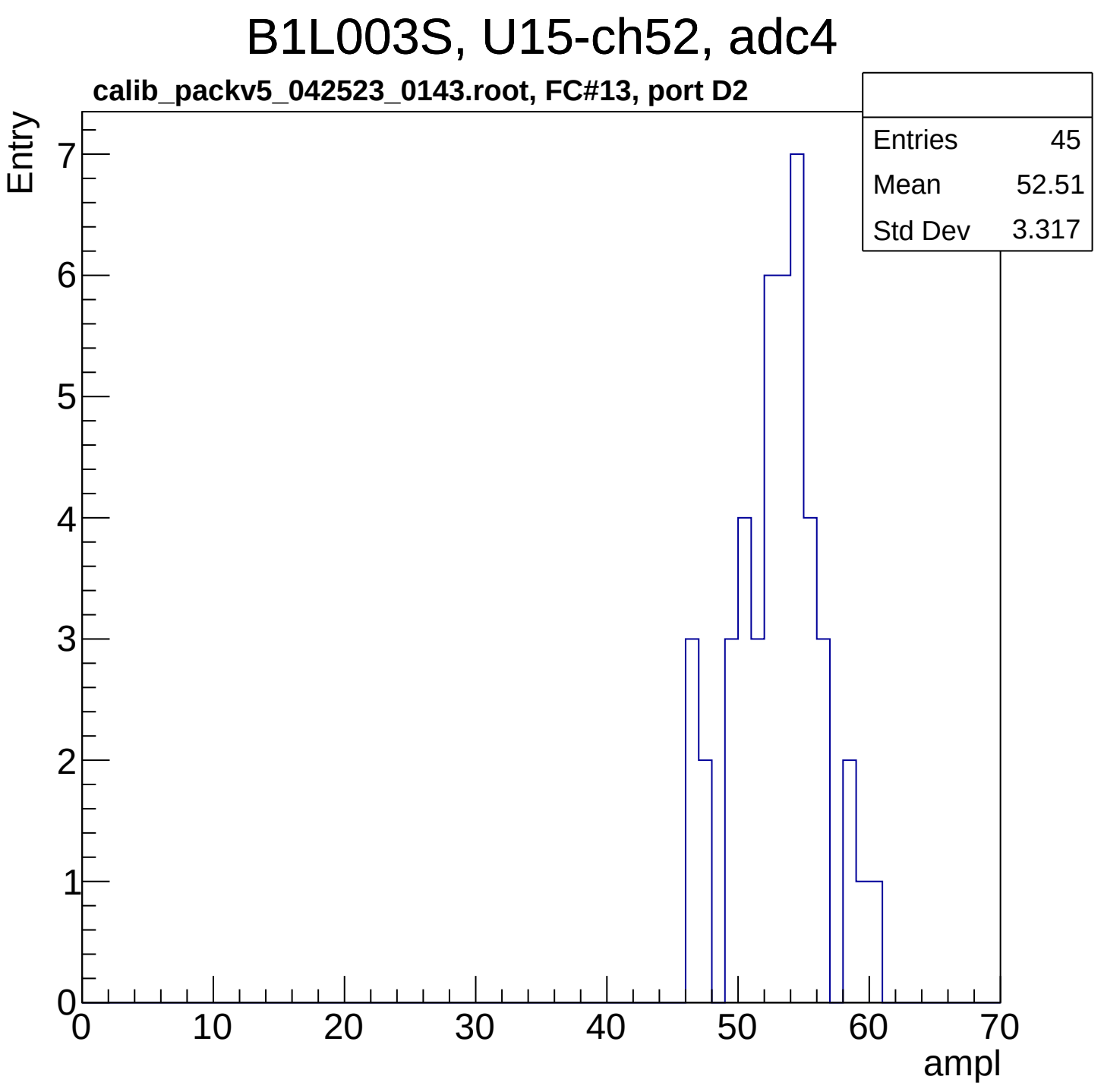

B1L003S, U15-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

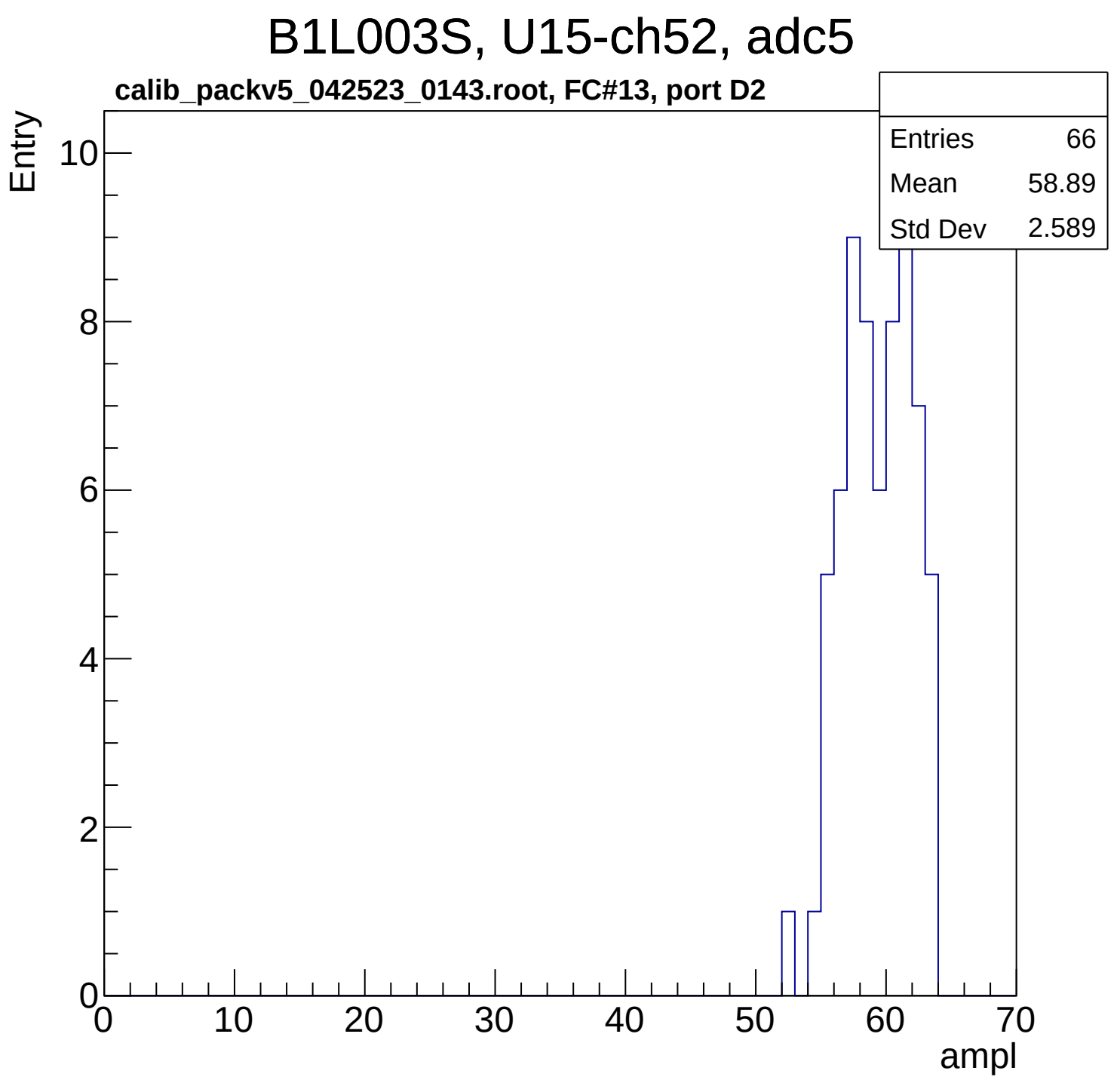

B1L003S, U15-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 29.31
Std Dev 4.918
Gaus mean : 29.3542
Gaus Width: 3.5235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

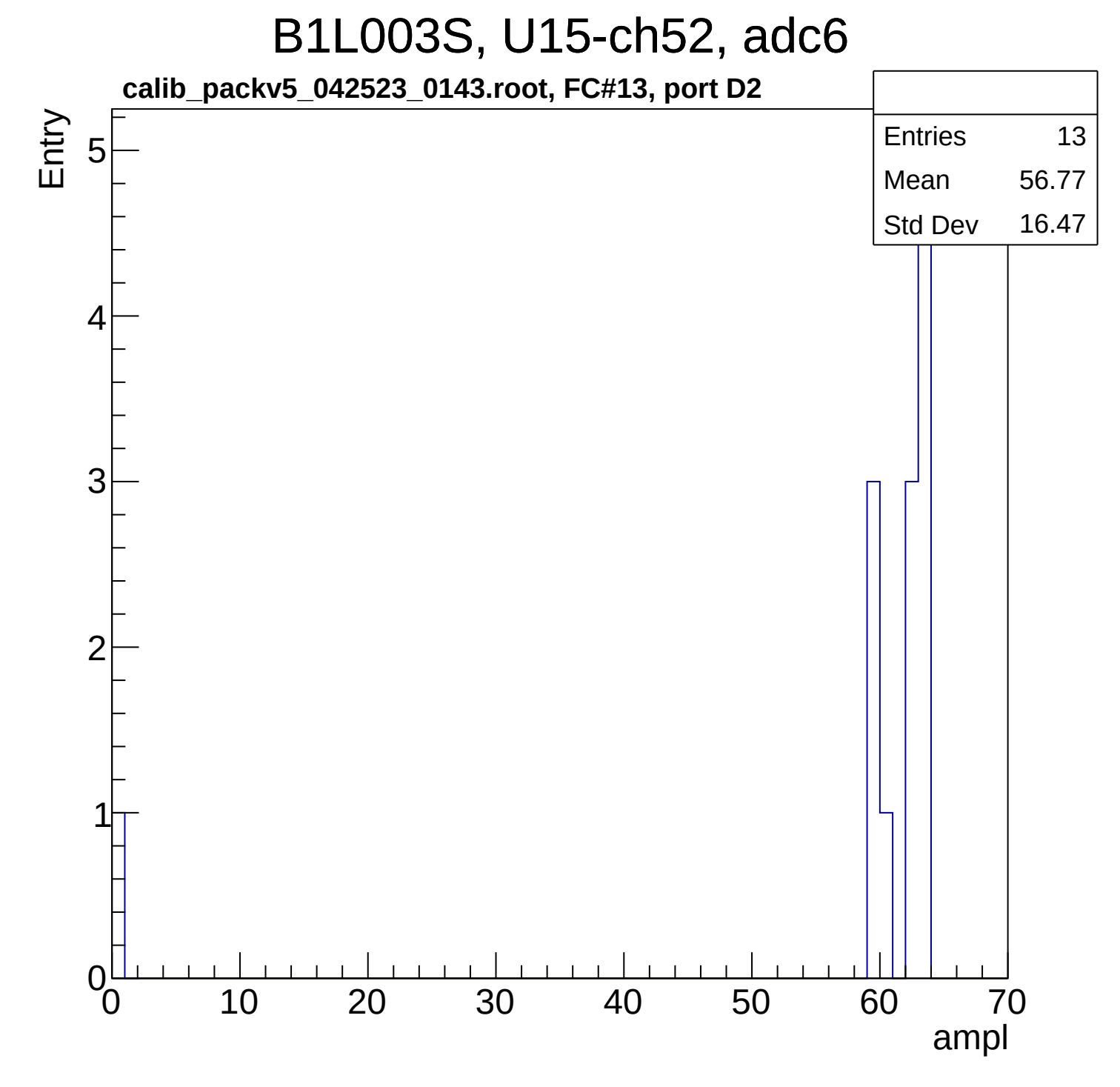

B1L003S, U15-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 34.71
Std Dev 3.387
Gaus mean : 34.9314
Gaus Width: 3.8169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

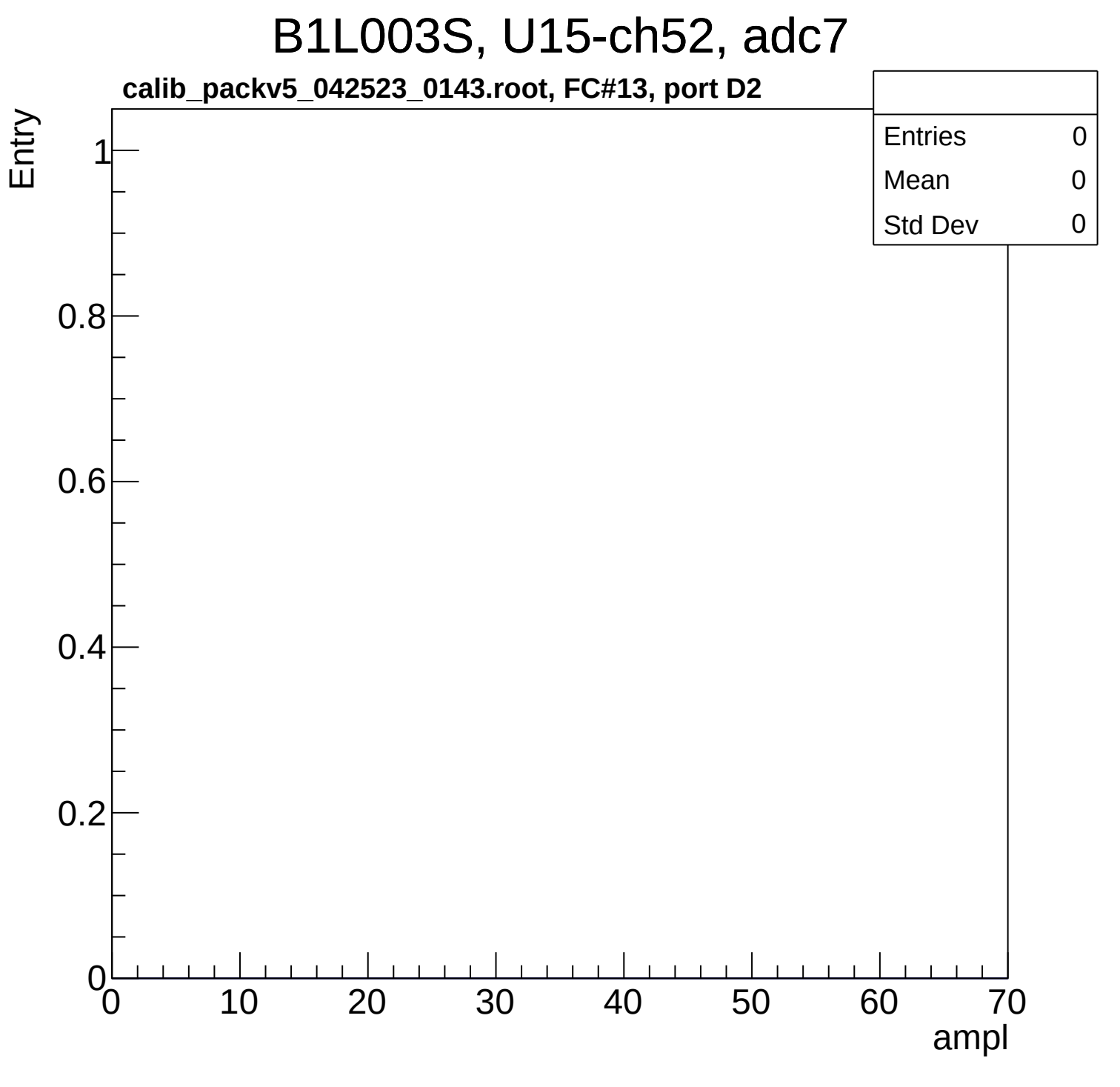

B1L003S, U15-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.12
Std Dev 3.386
Gaus mean : 41.6605
Gaus Width: 3.3380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

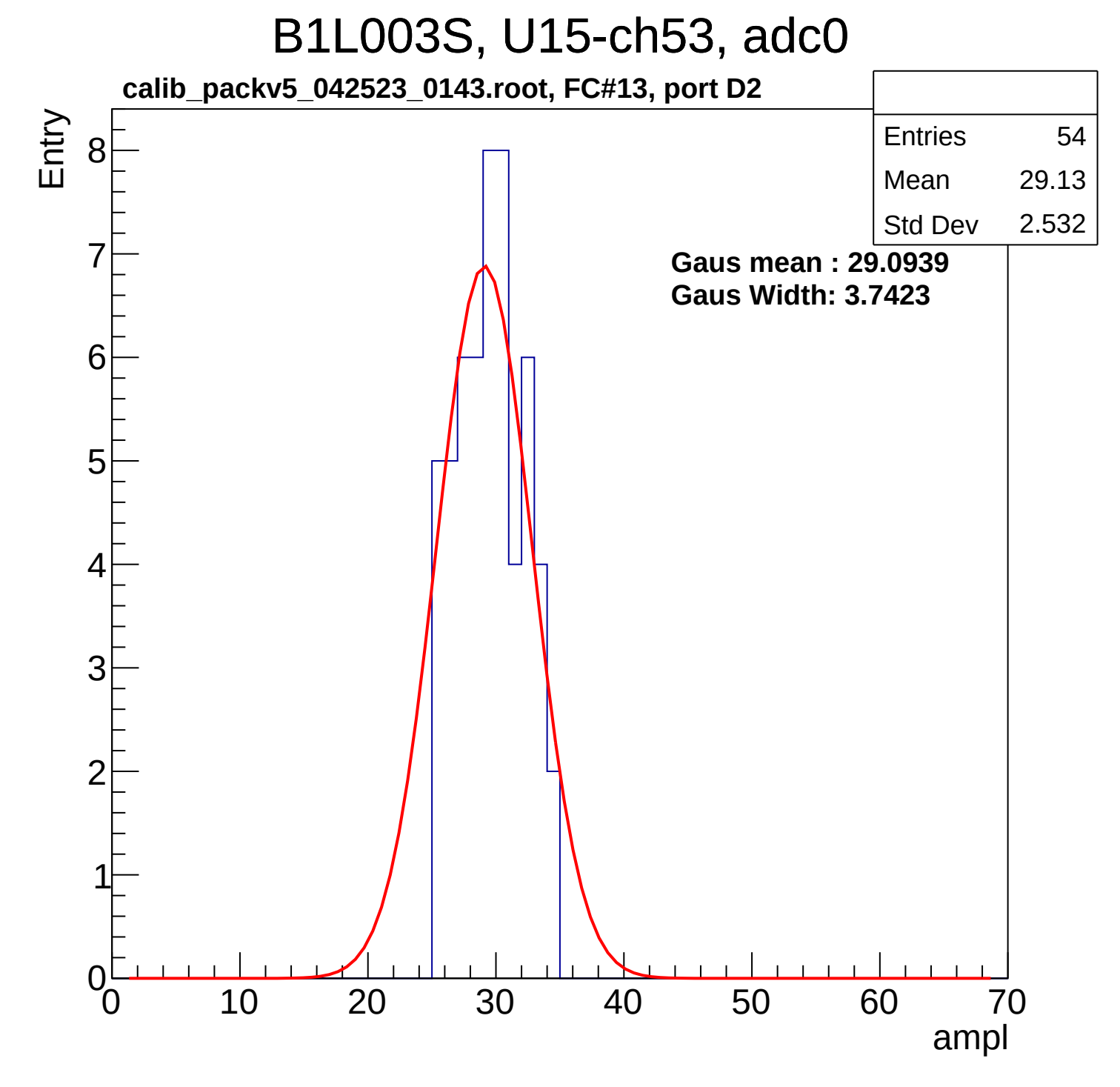

B1L003S, U15-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.35
Std Dev 3.578
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

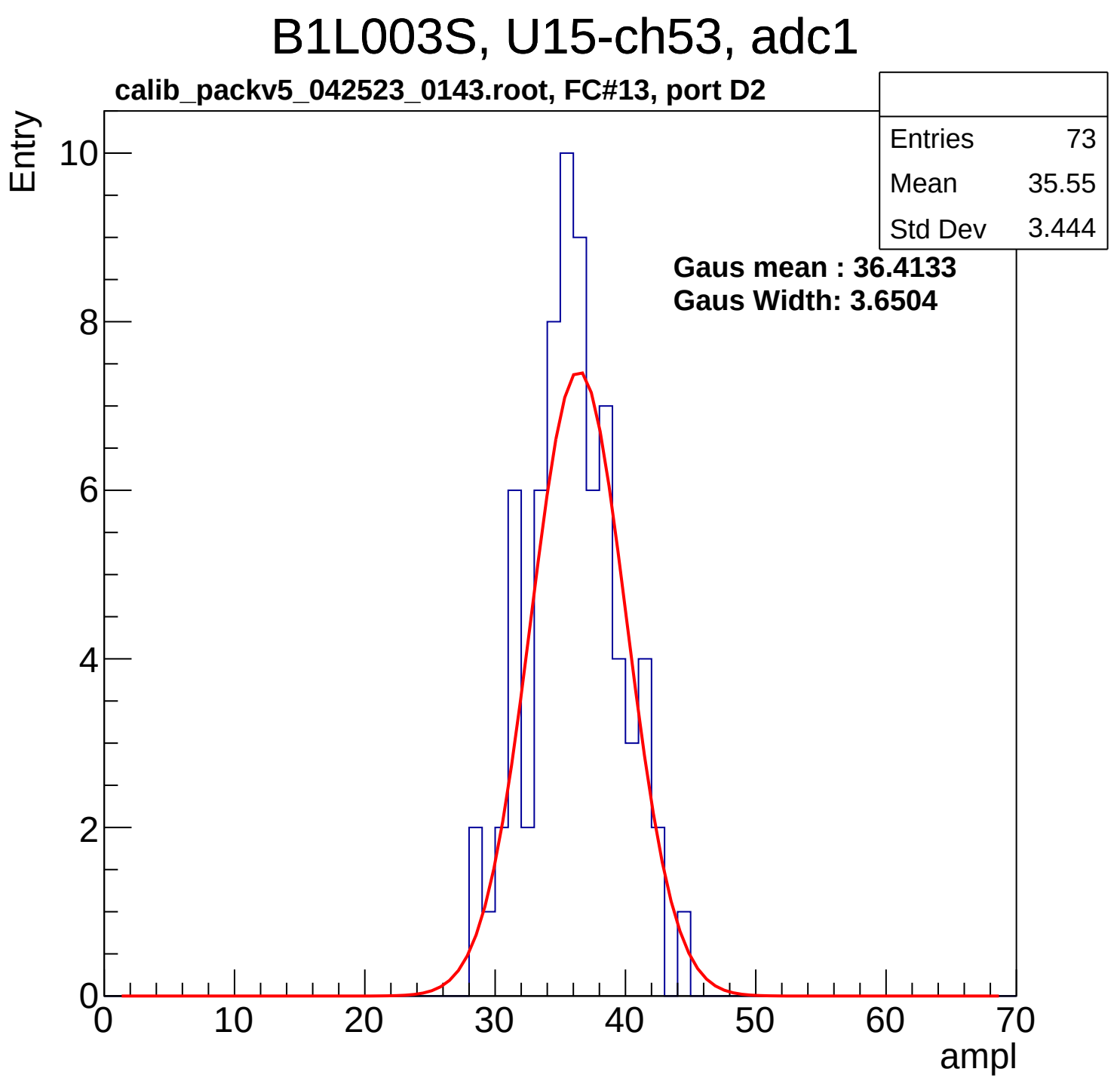

B1L003S, U15-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 52.51
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

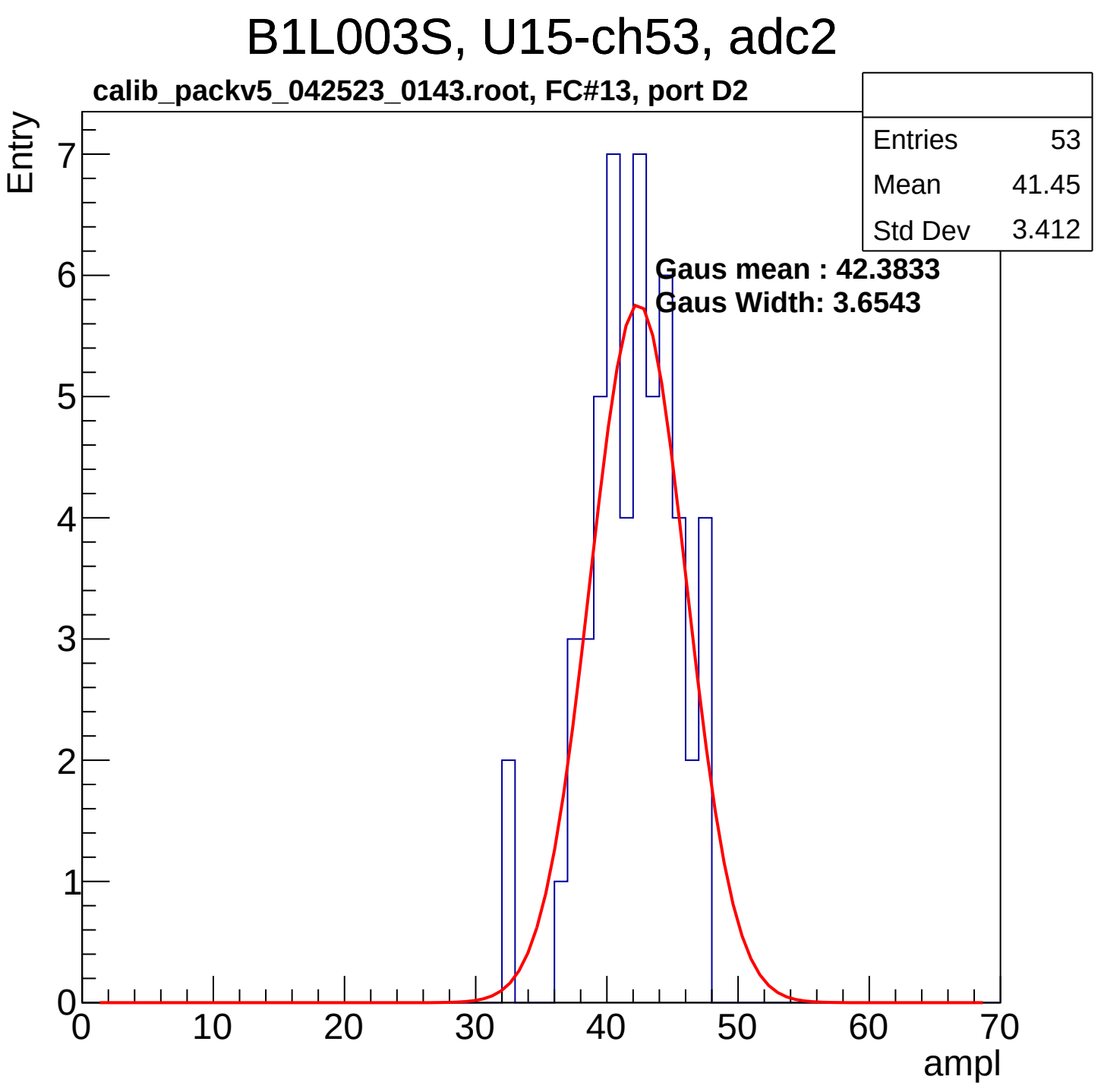

B1L003S, U15-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 58.89
Std Dev 2.589
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

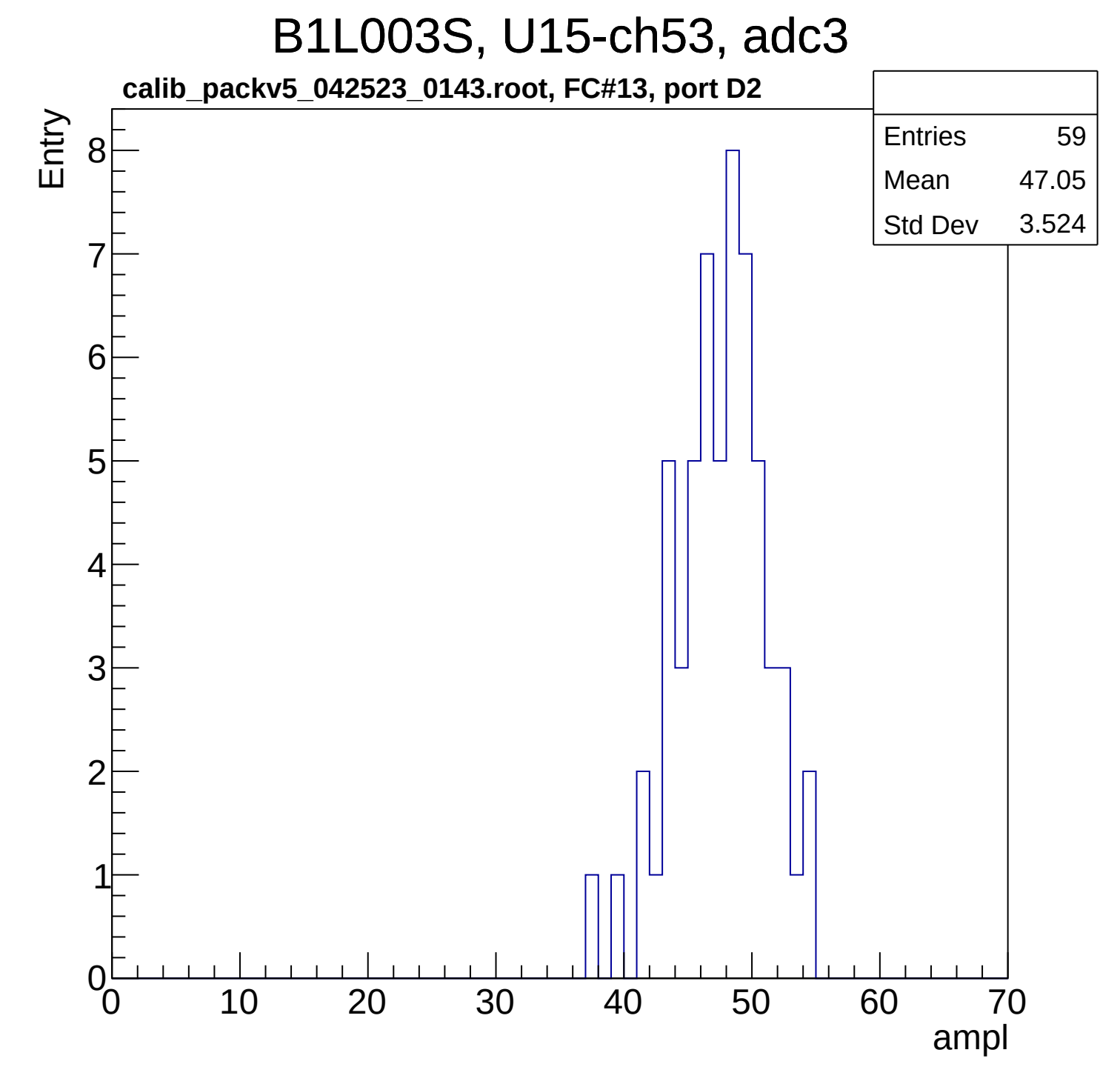

B1L003S, U15-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.77
Std Dev 16.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.13
Std Dev 2.532
Gaus mean : 29.0939
Gaus Width: 3.7423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.55
Std Dev 3.444
Gaus mean : 36.4133
Gaus Width: 3.6504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 41.45
Std Dev 3.412
Gaus mean : 42.3833
Gaus Width: 3.6543
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 47.05
Std Dev 3.524
Entry
ampl
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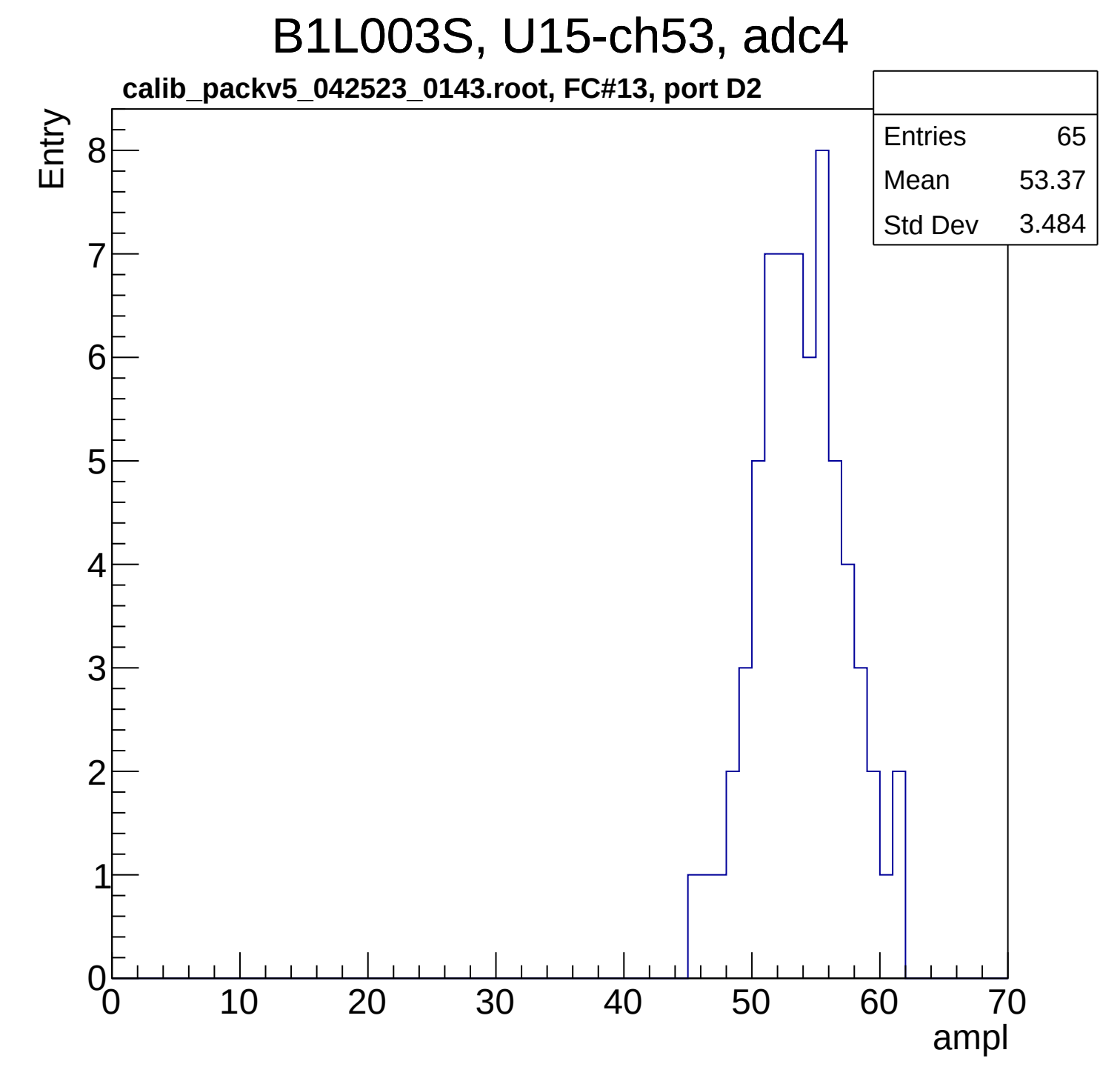
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.37
Std Dev 3.484
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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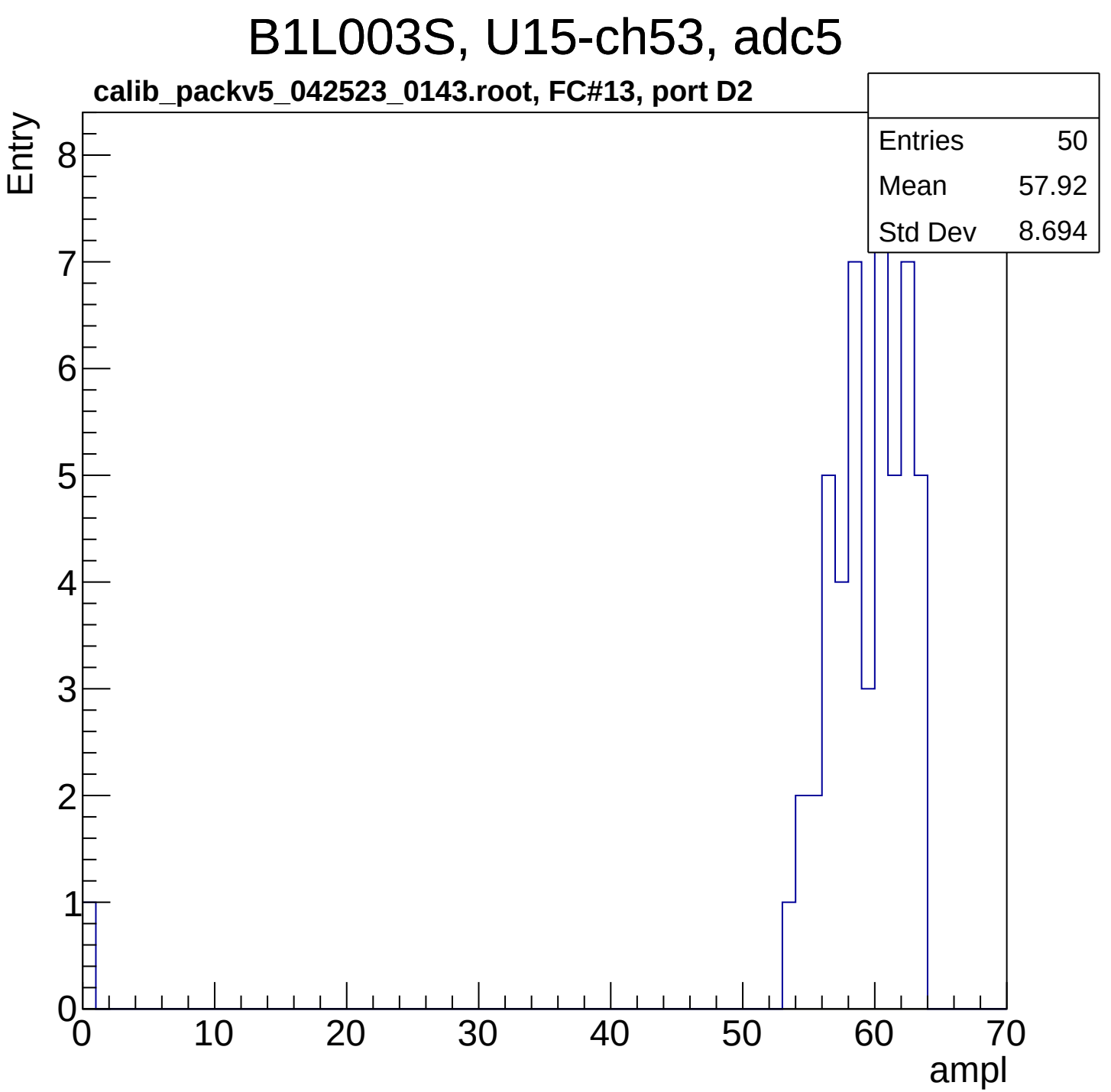
B1L003S, U15-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 57.92
Std Dev 8.694
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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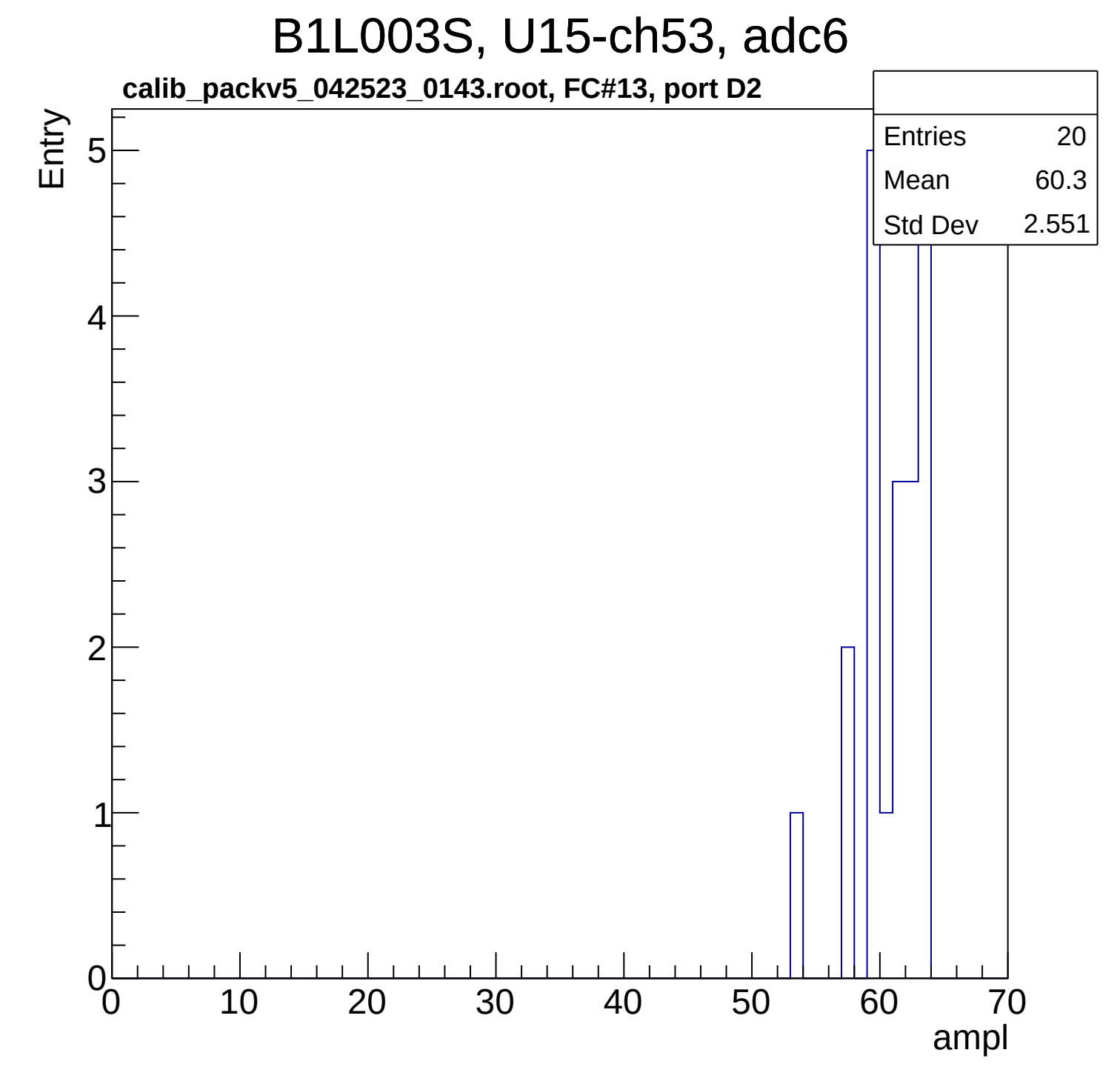
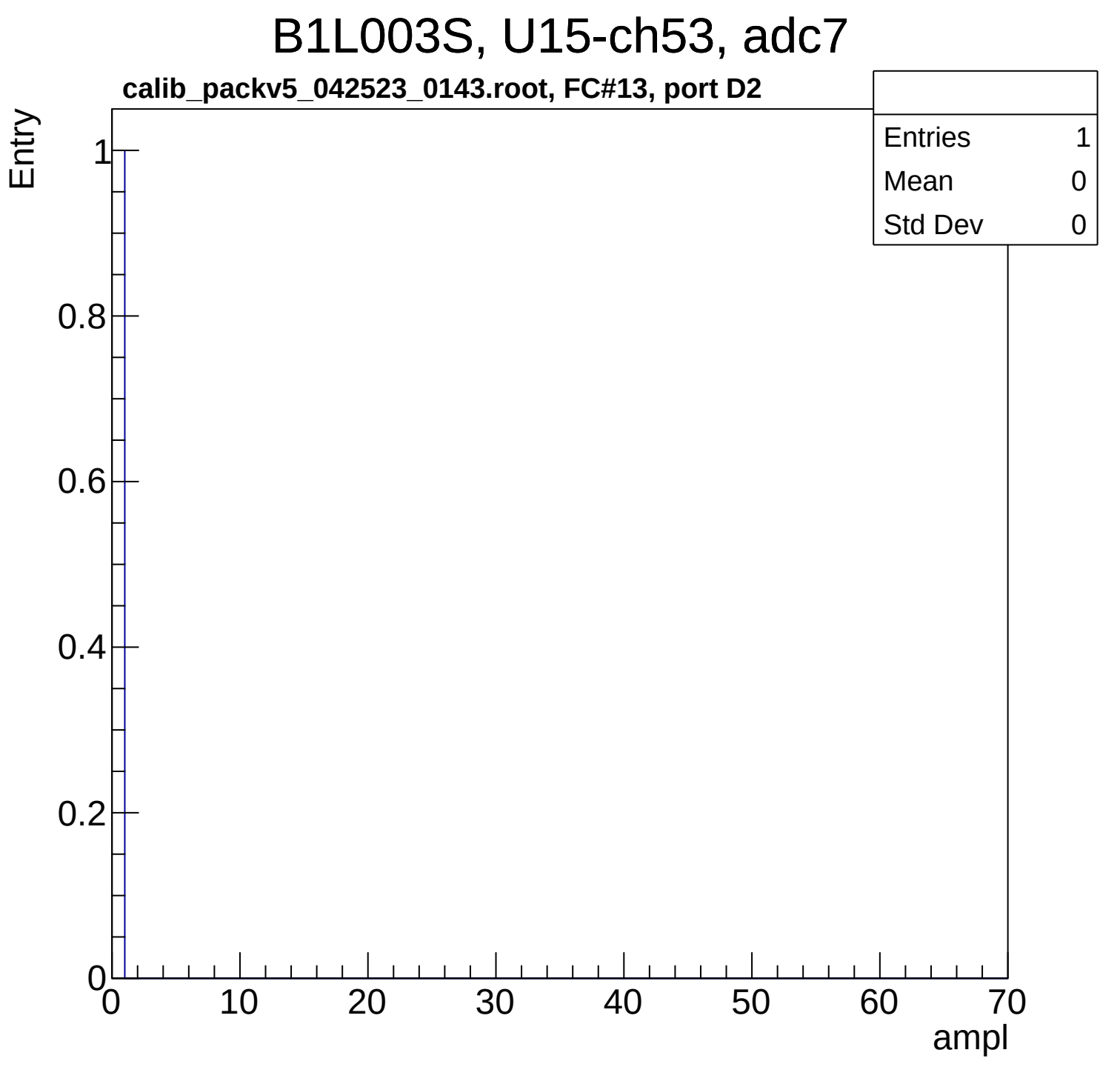

B1L003S, U15-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 60.3
Std Dev 2.551
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U15-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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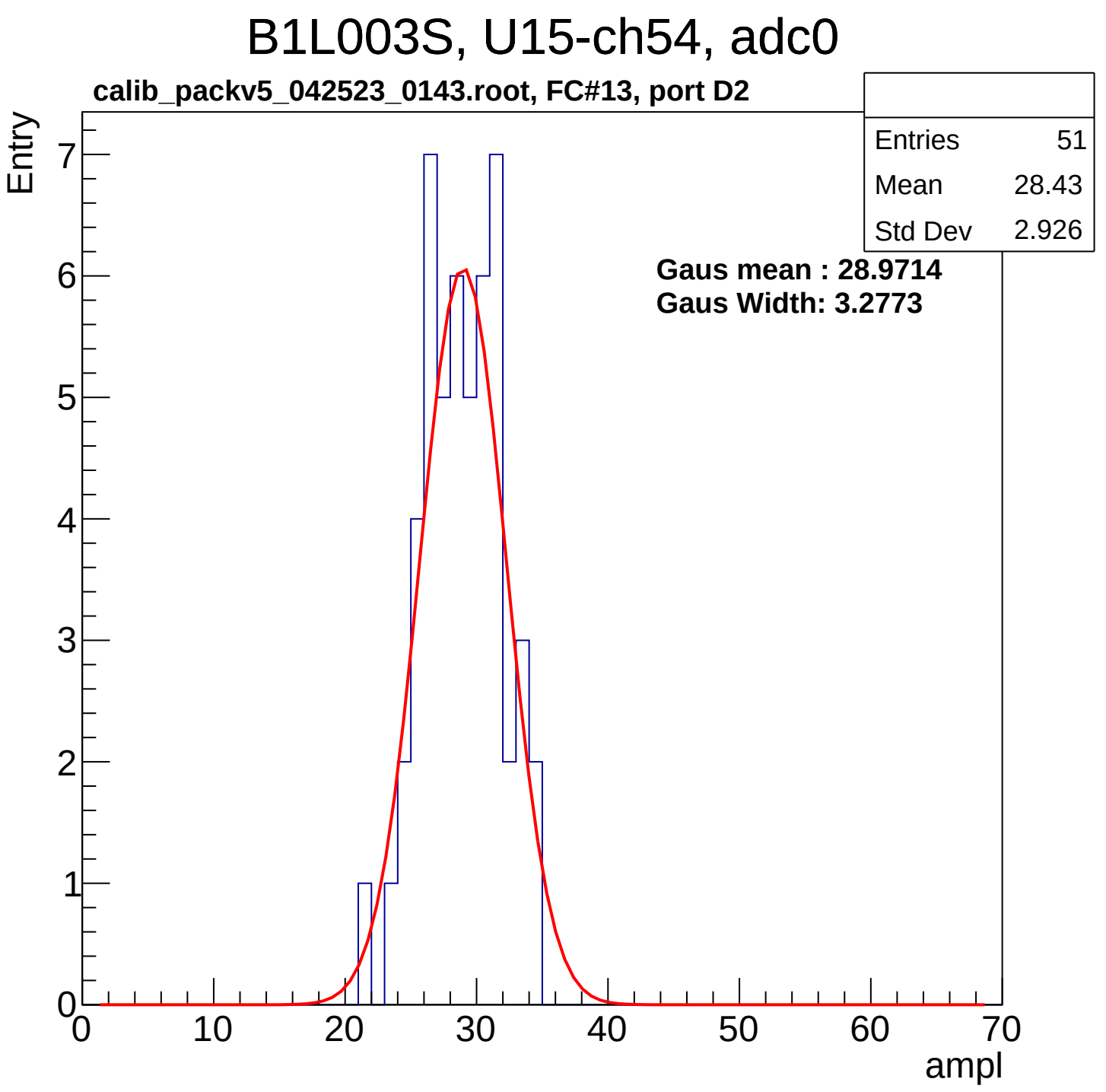
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.43
Std Dev 2.926
Gaus mean : 28.9714
Gaus Width: 3.2773
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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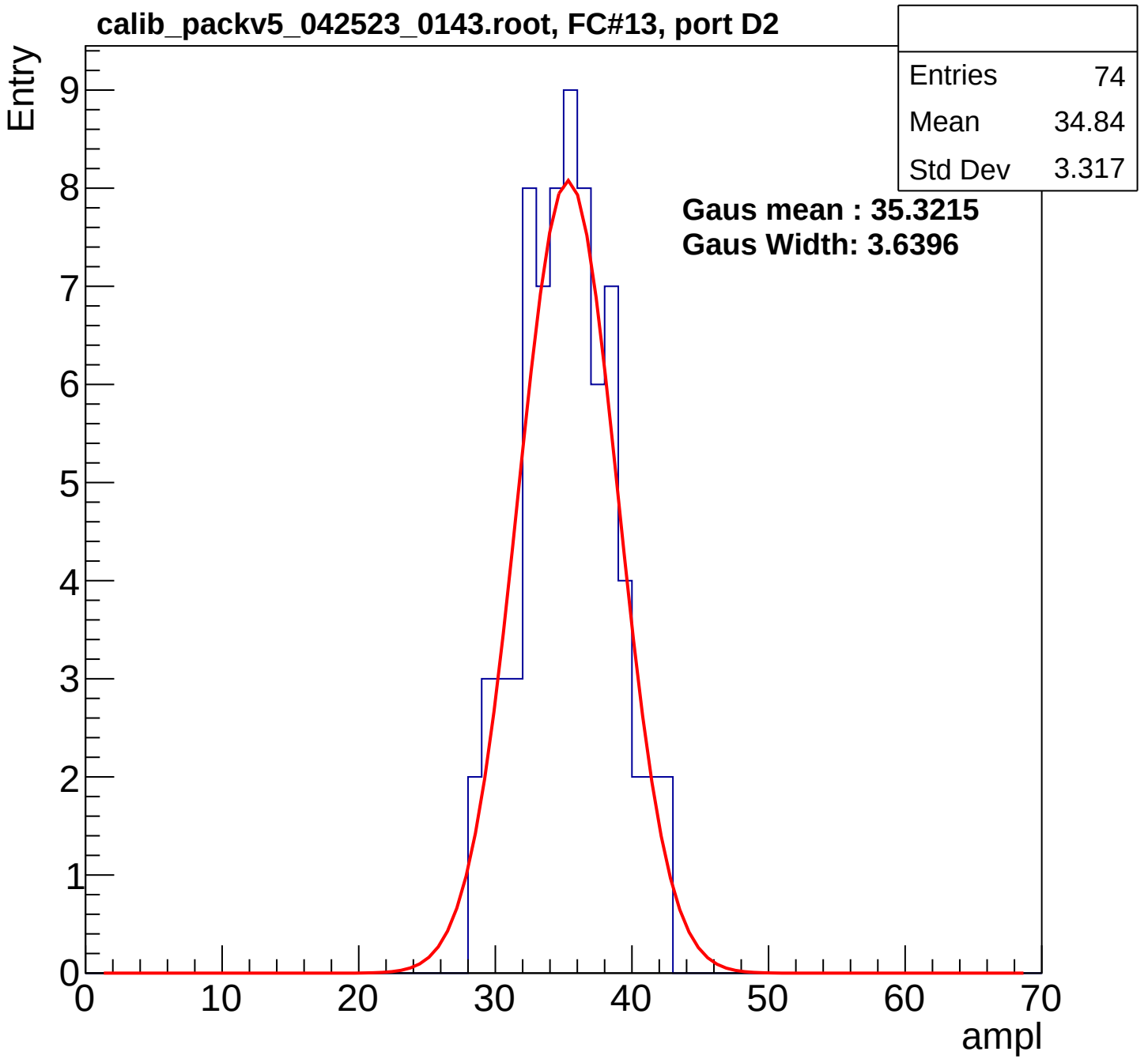
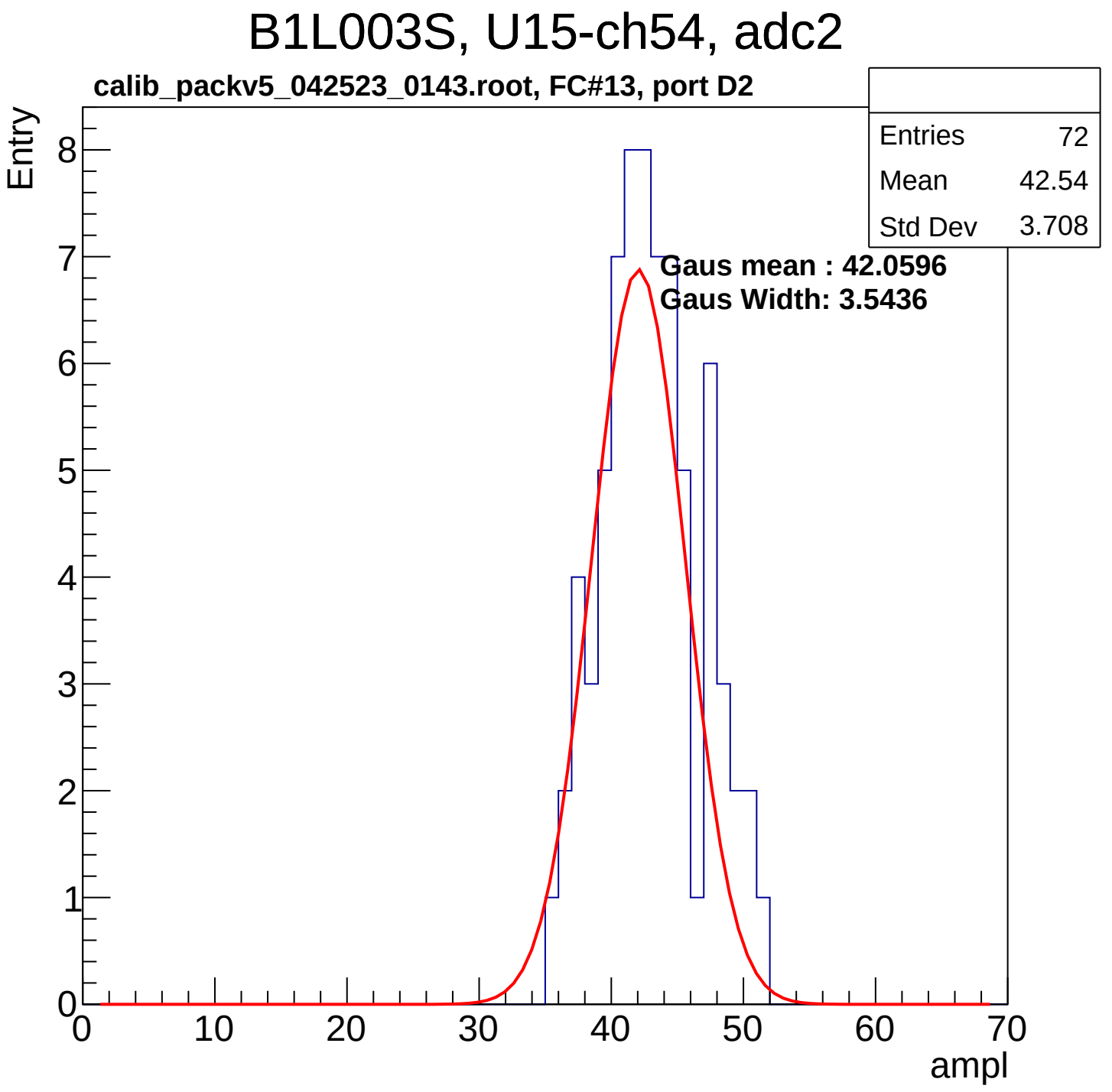
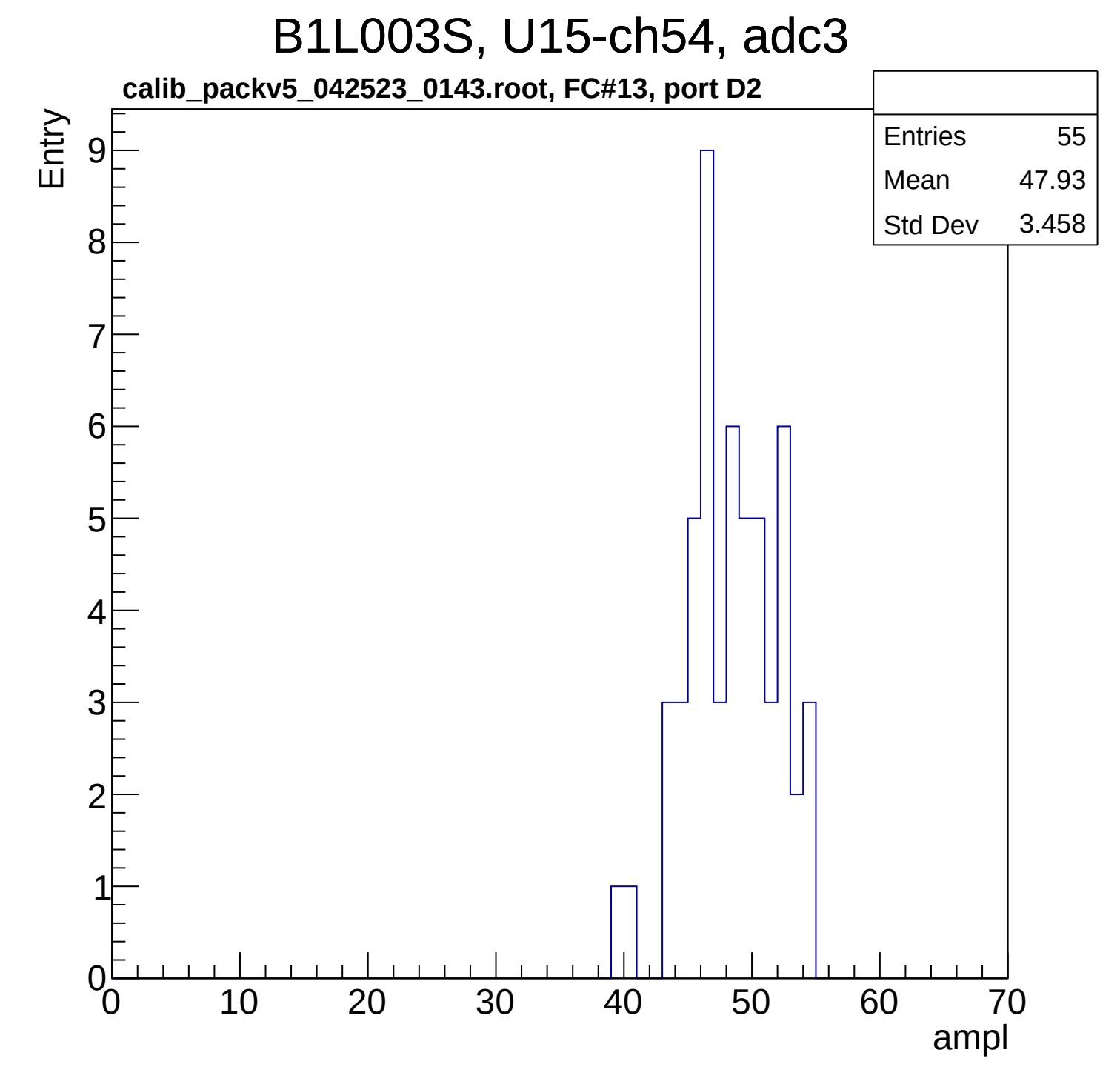

B1L003S, U15-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 34.84
Std Dev 3.317
Gaus mean : 35.3215
Gaus Width: 3.6396
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.54
Std Dev 3.708
Gaus mean : 42.0596
Gaus Width: 3.5436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 47.93
Std Dev 3.458
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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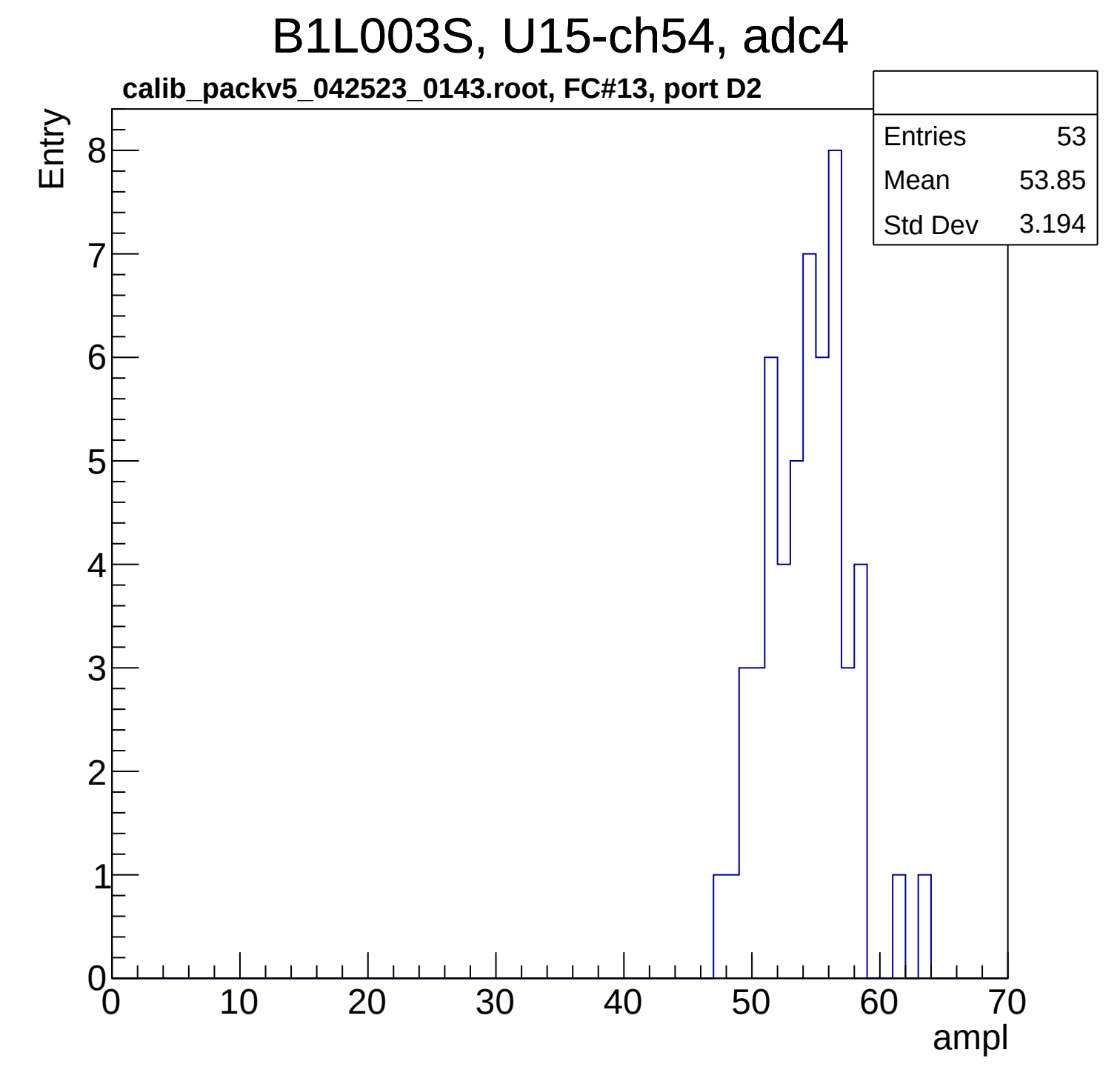
B1L003S, U15-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 53.85
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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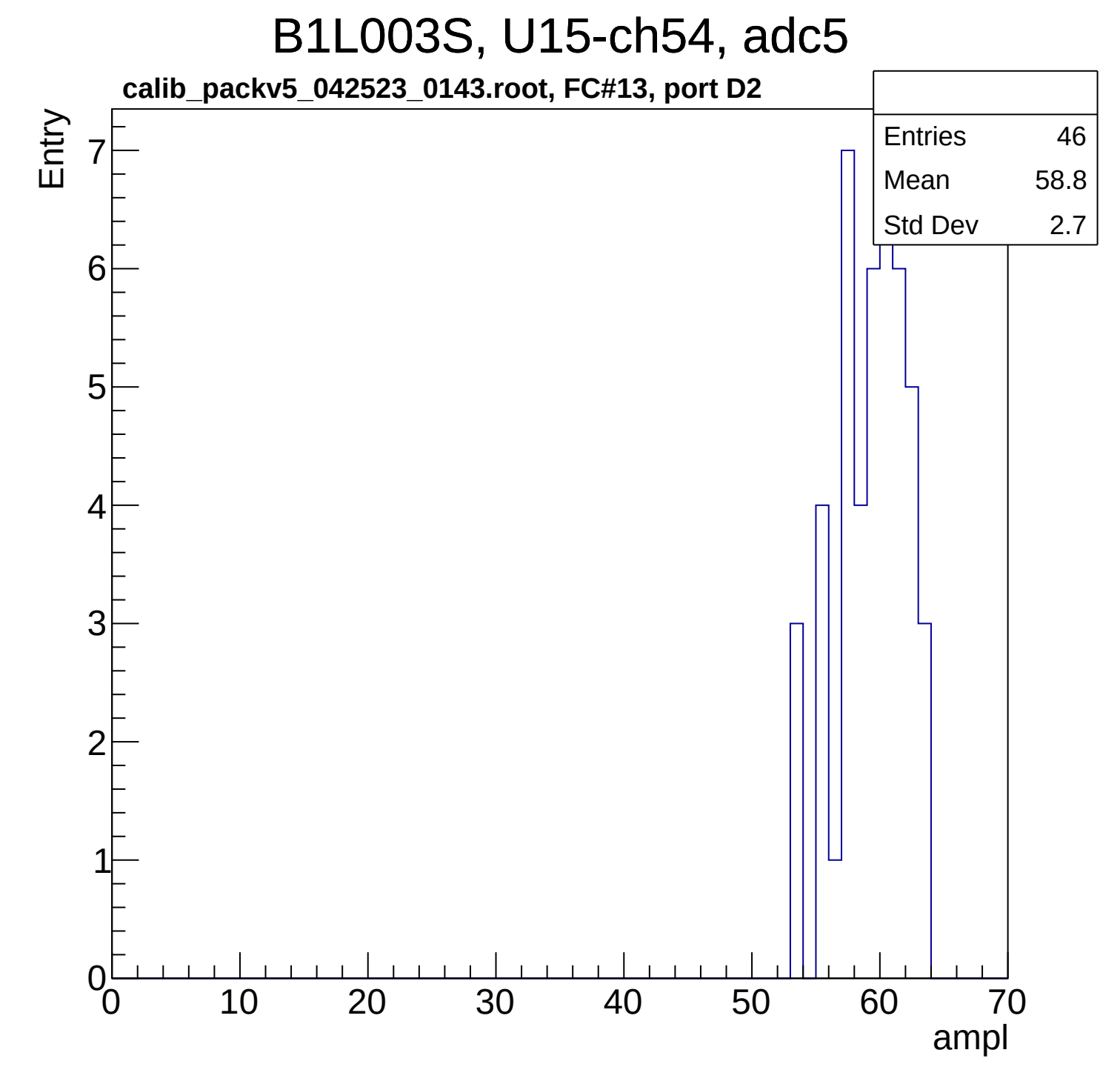
B1L003S, U15-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.8
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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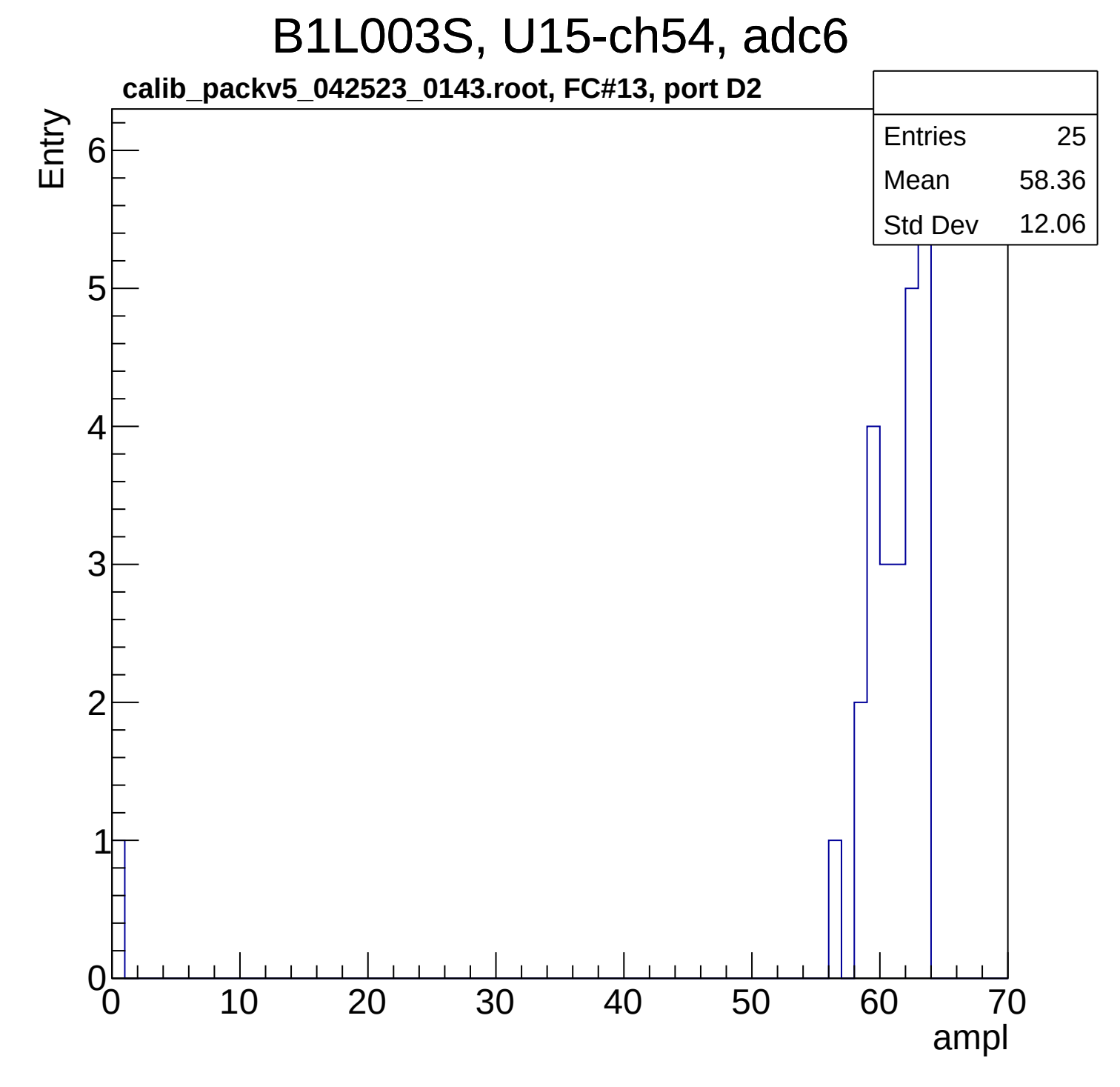
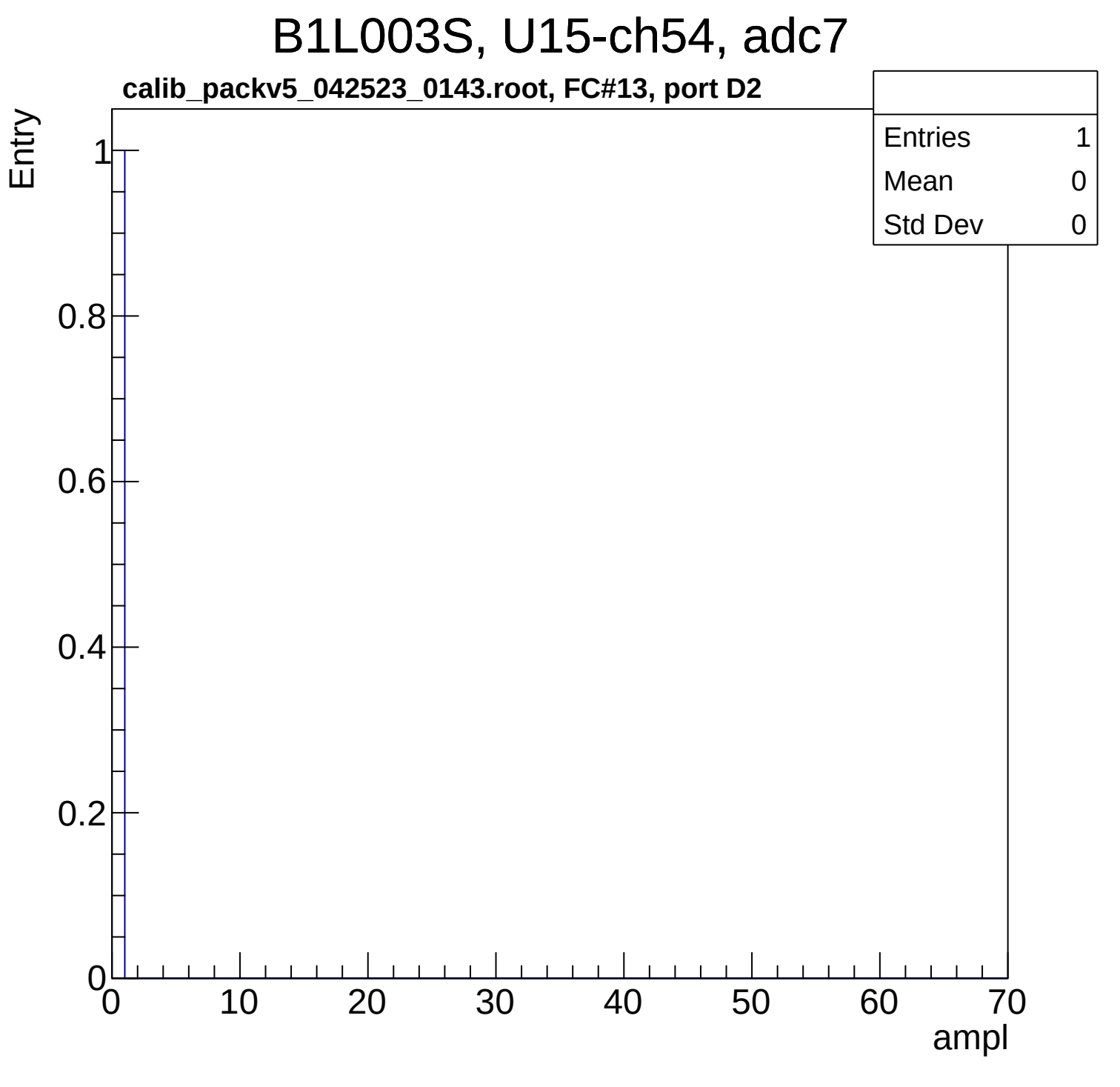
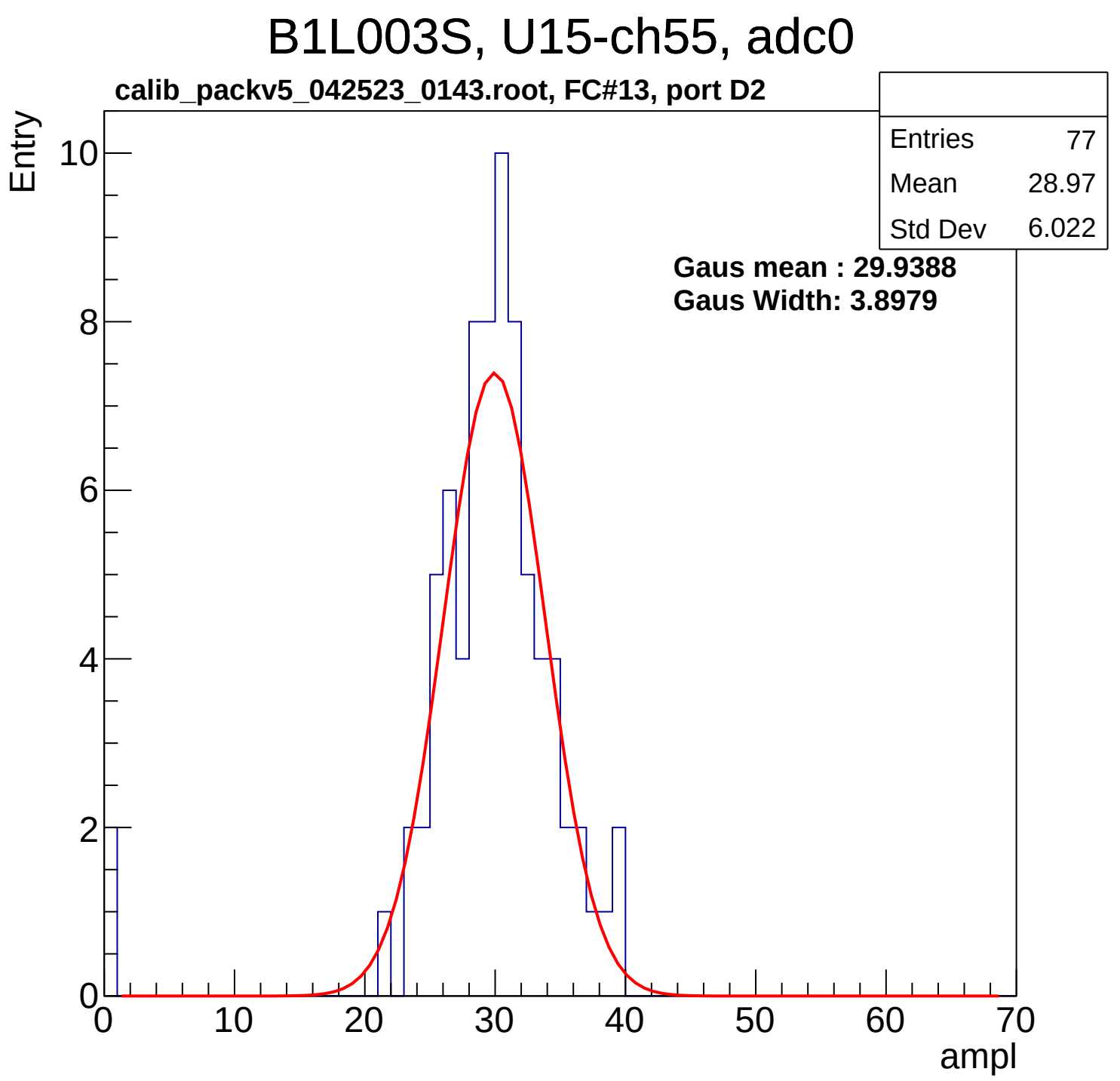

B1L003S, U15-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 25
Mean 58.36
Std Dev 12.06
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.97
Std Dev 6.022
Gaus mean : 29.9388
Gaus Width: 3.8979
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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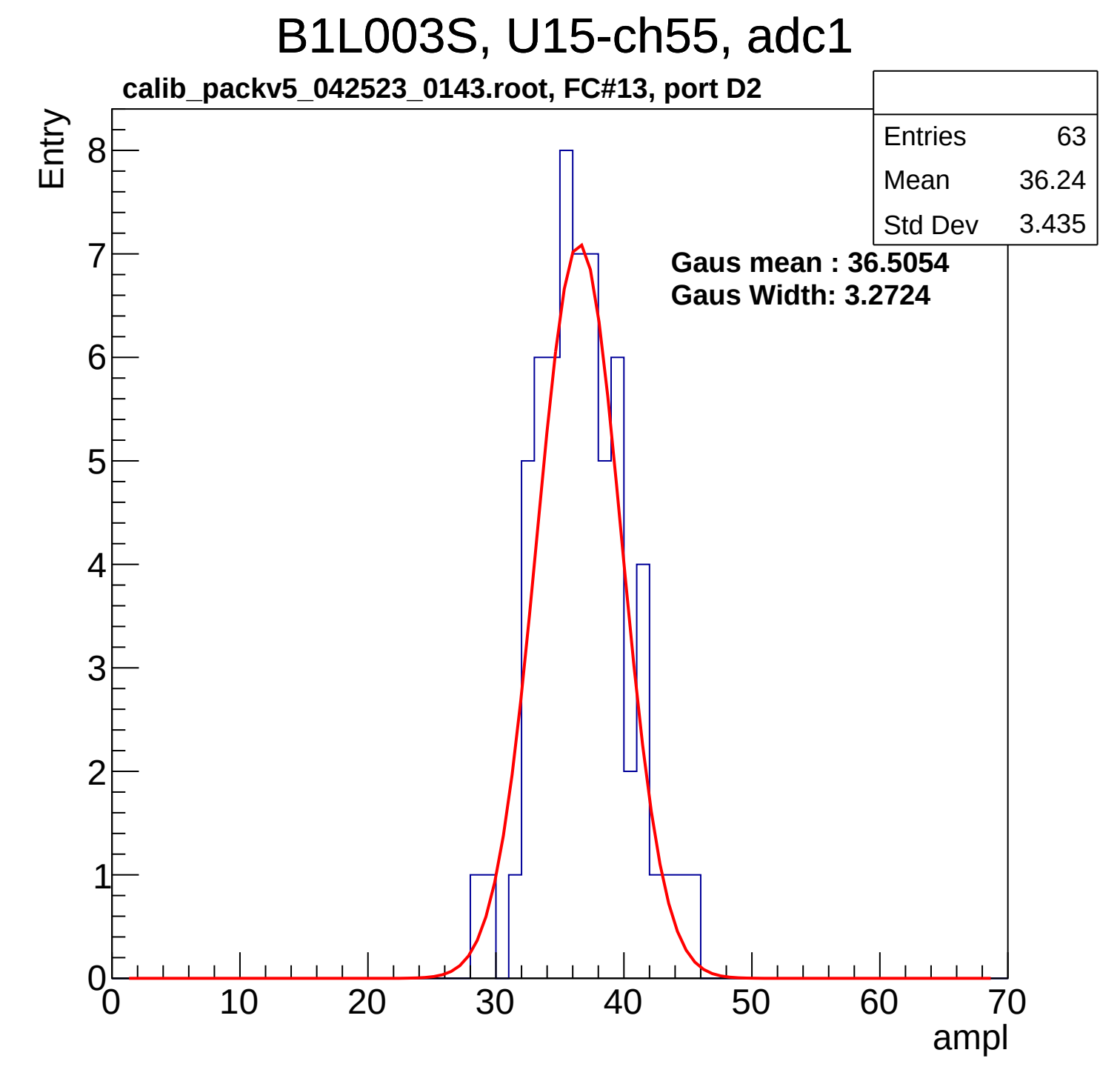
B1L003S, U15-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.24
Std Dev 3.435
Gaus mean : 36.5054
Gaus Width: 3.2724
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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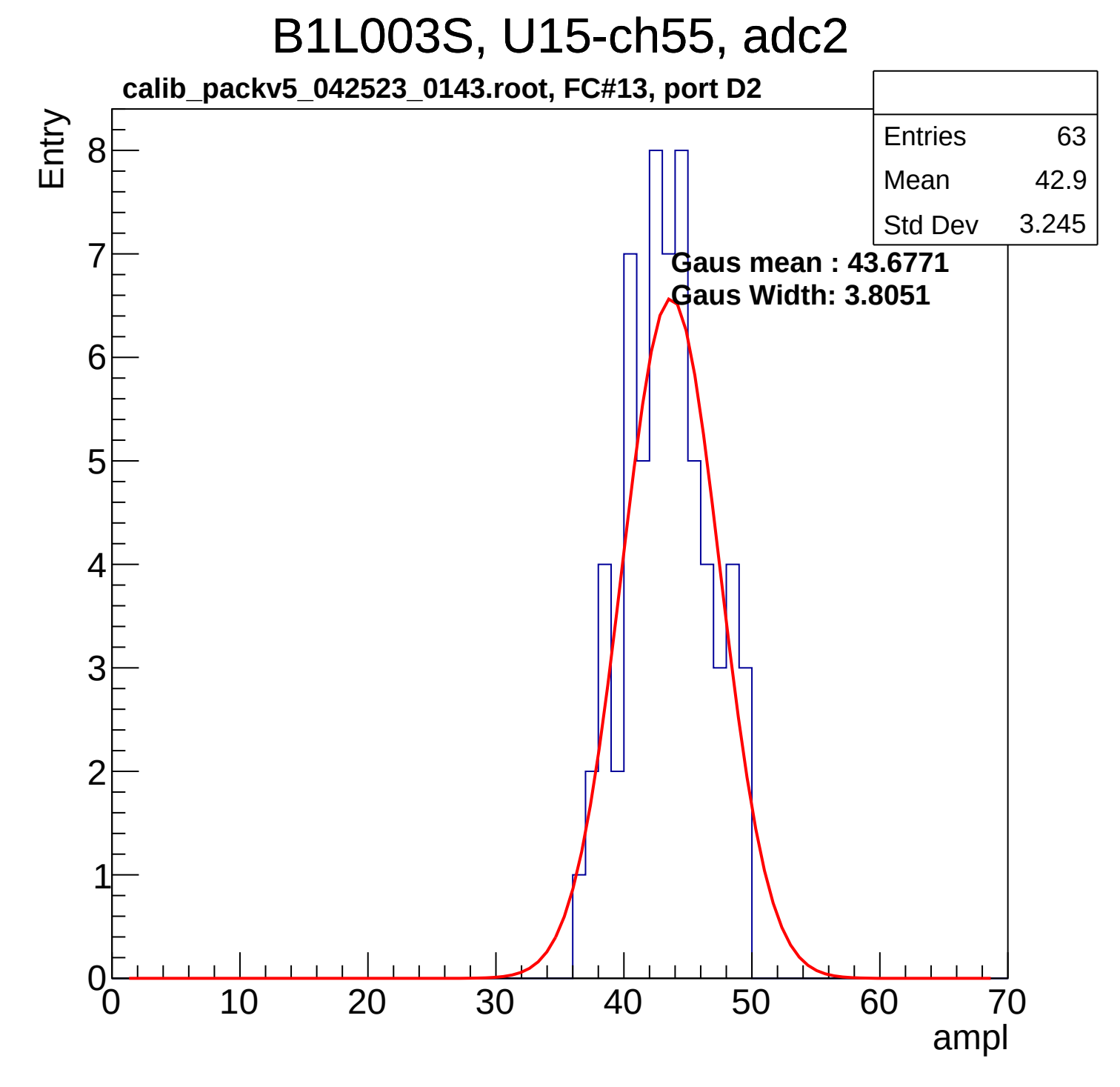
B1L003S, U15-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.9
Std Dev 3.245
Gaus mean : 43.6771
Gaus Width: 3.8051
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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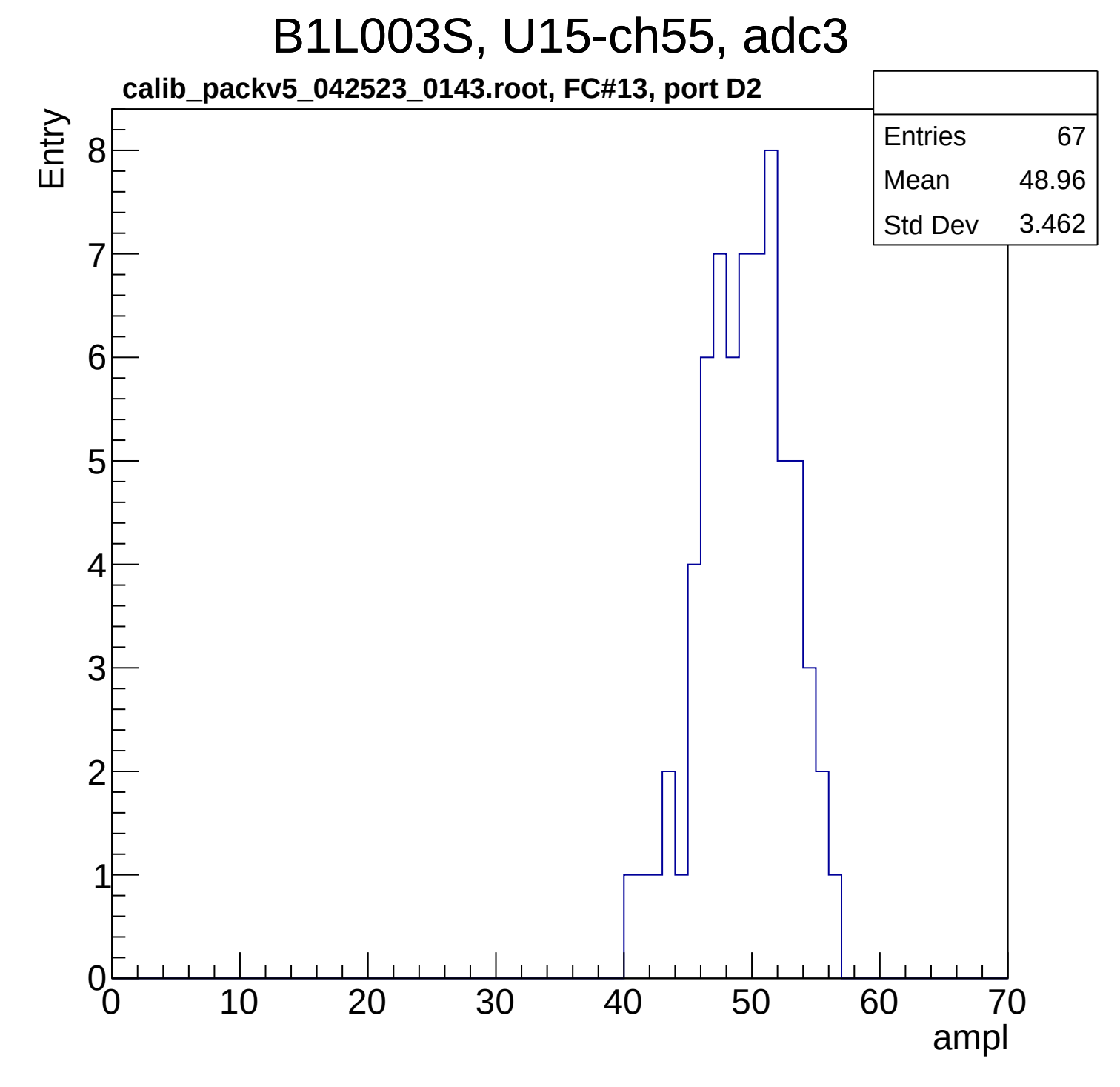
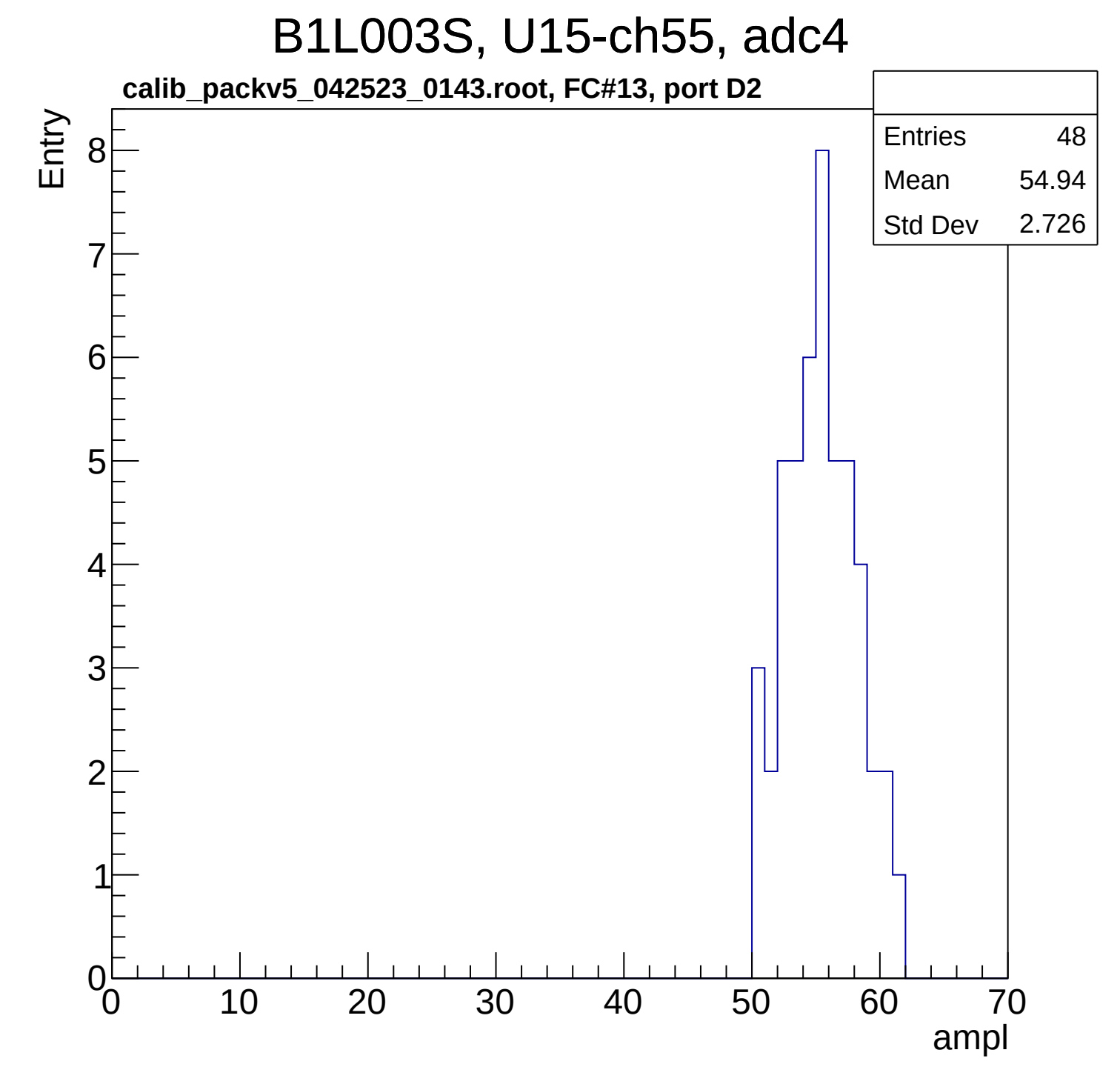

B1L003S, U15-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.96
Std Dev 3.462
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.94
Std Dev 2.726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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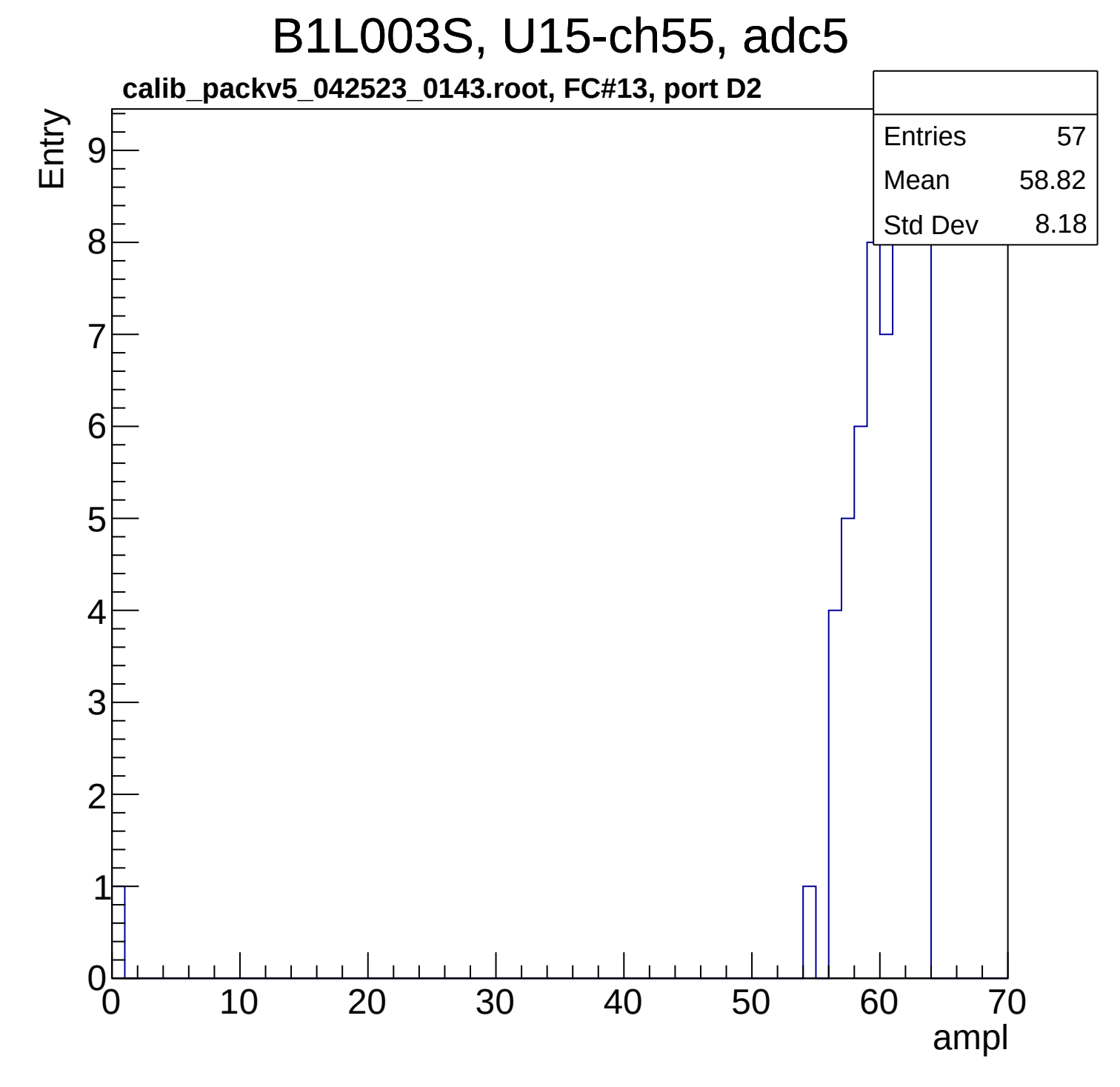
B1L003S, U15-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.82
Std Dev 8.18
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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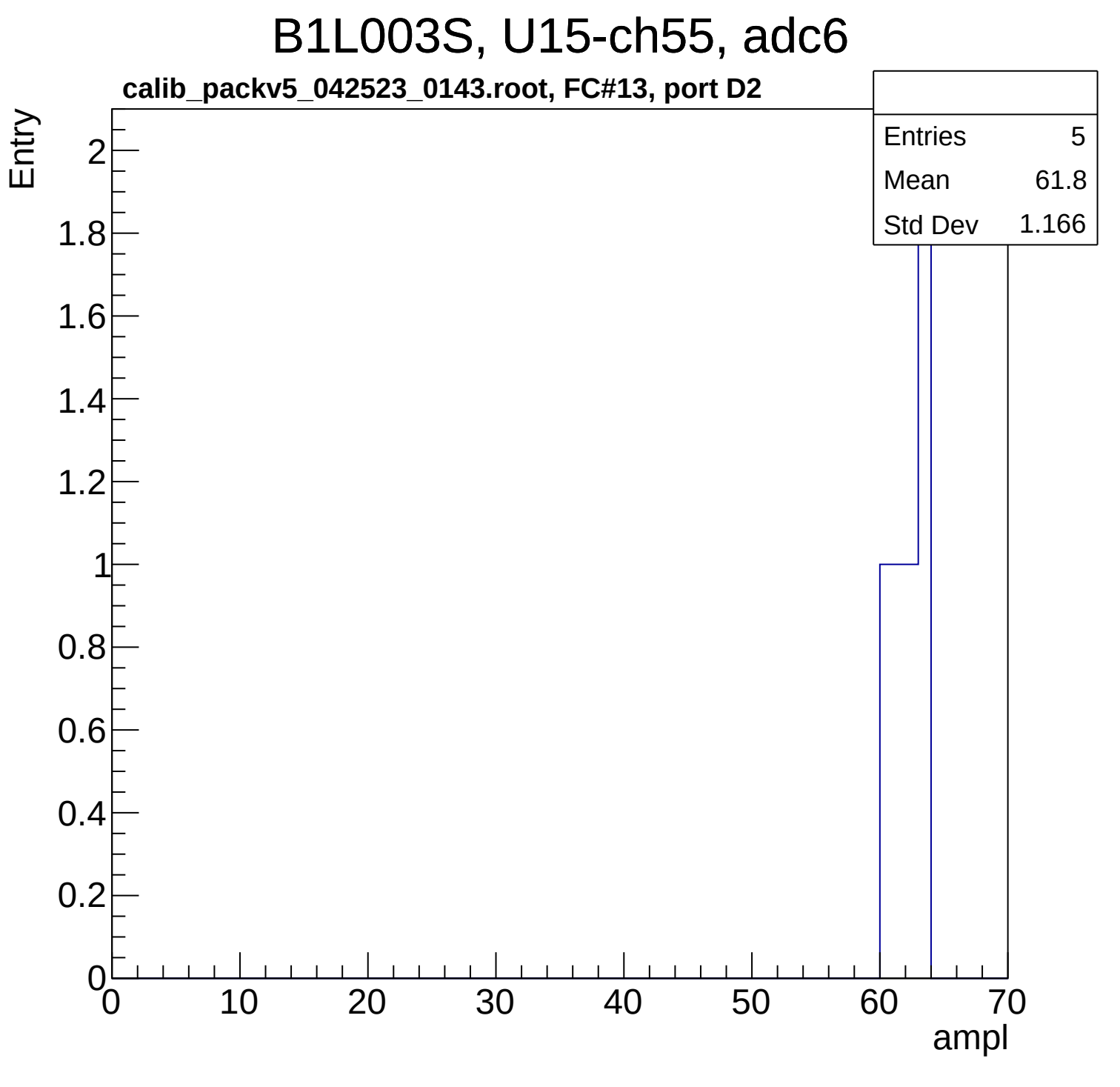
B1L003S, U15-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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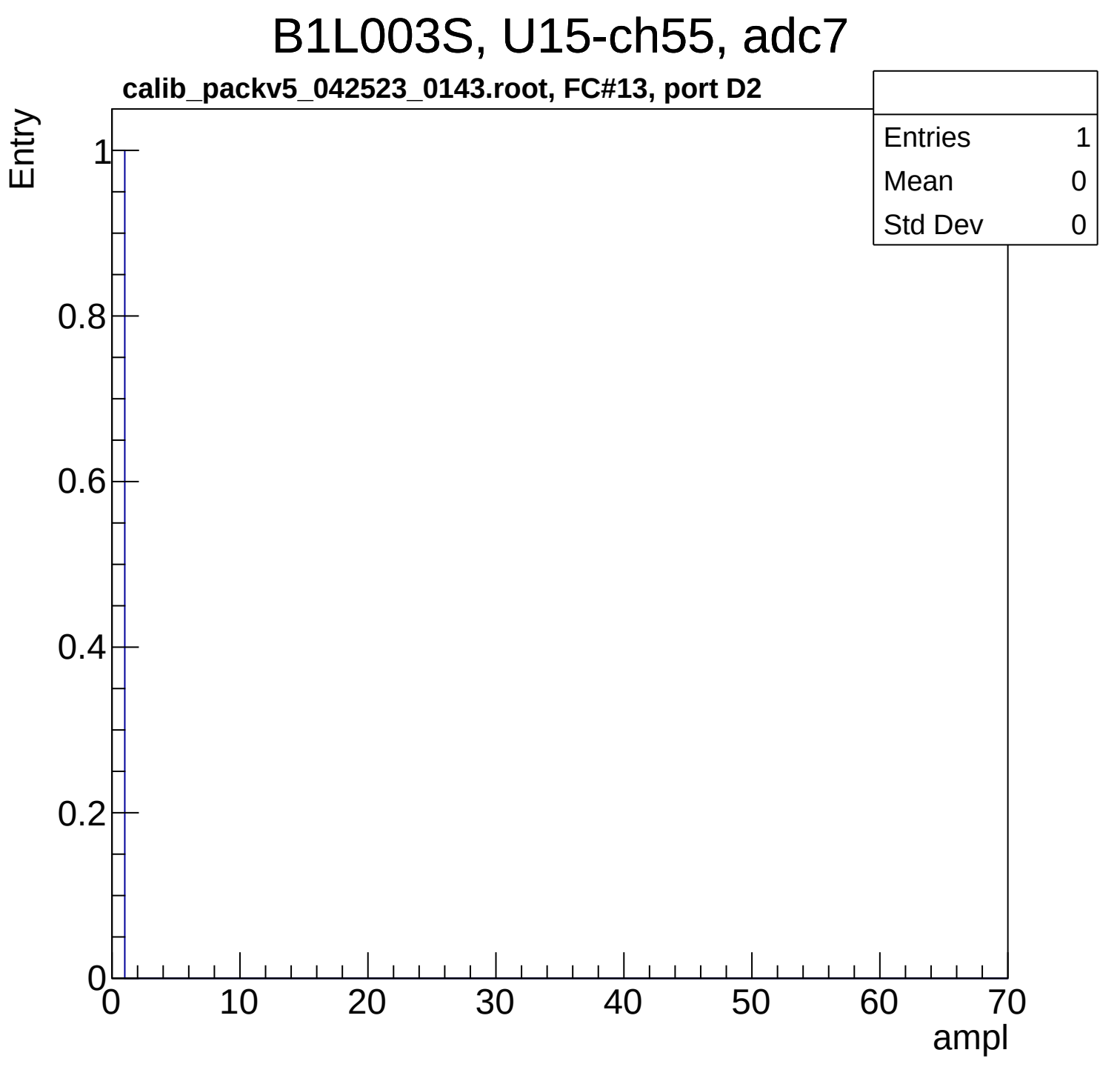
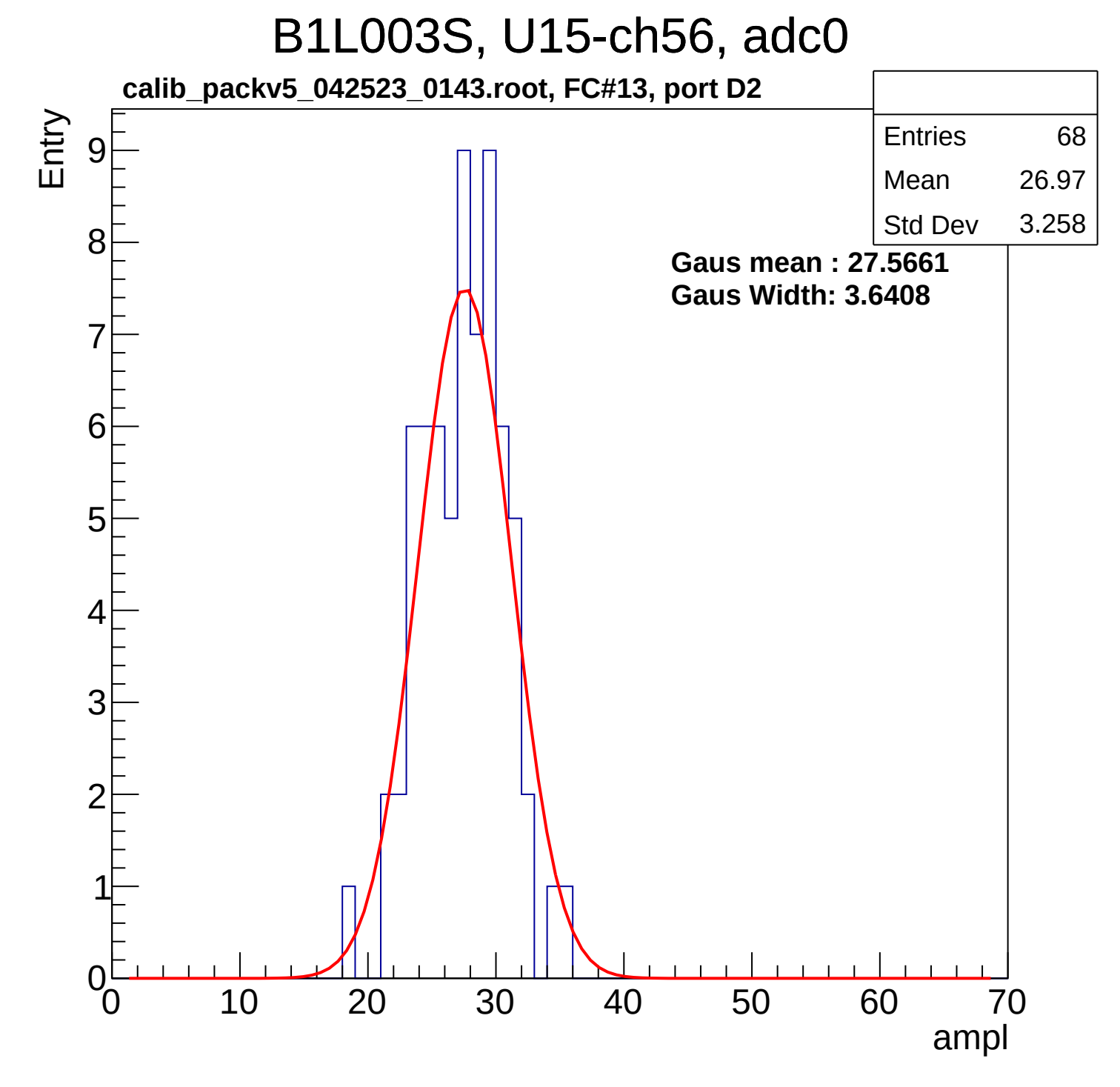
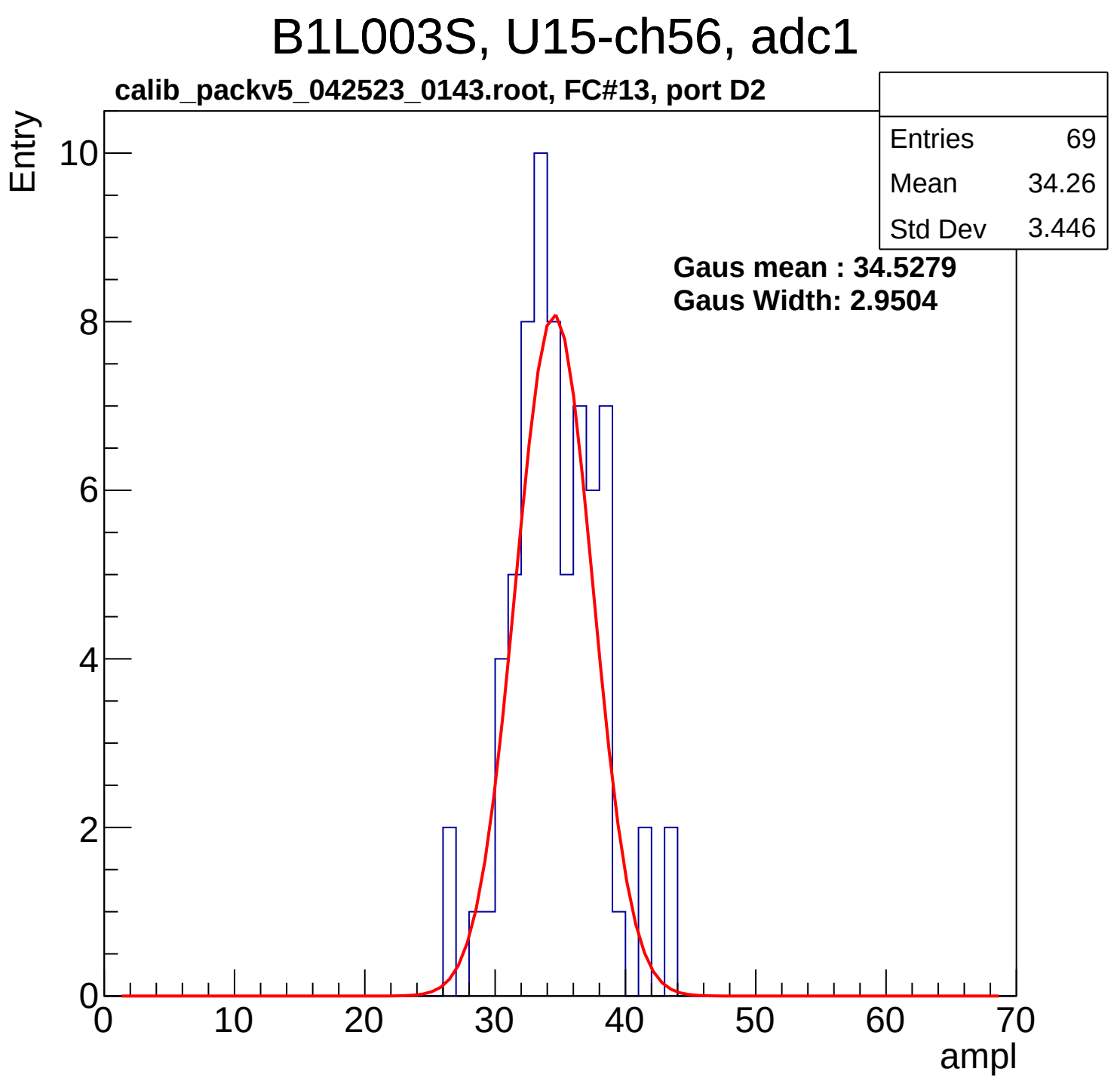
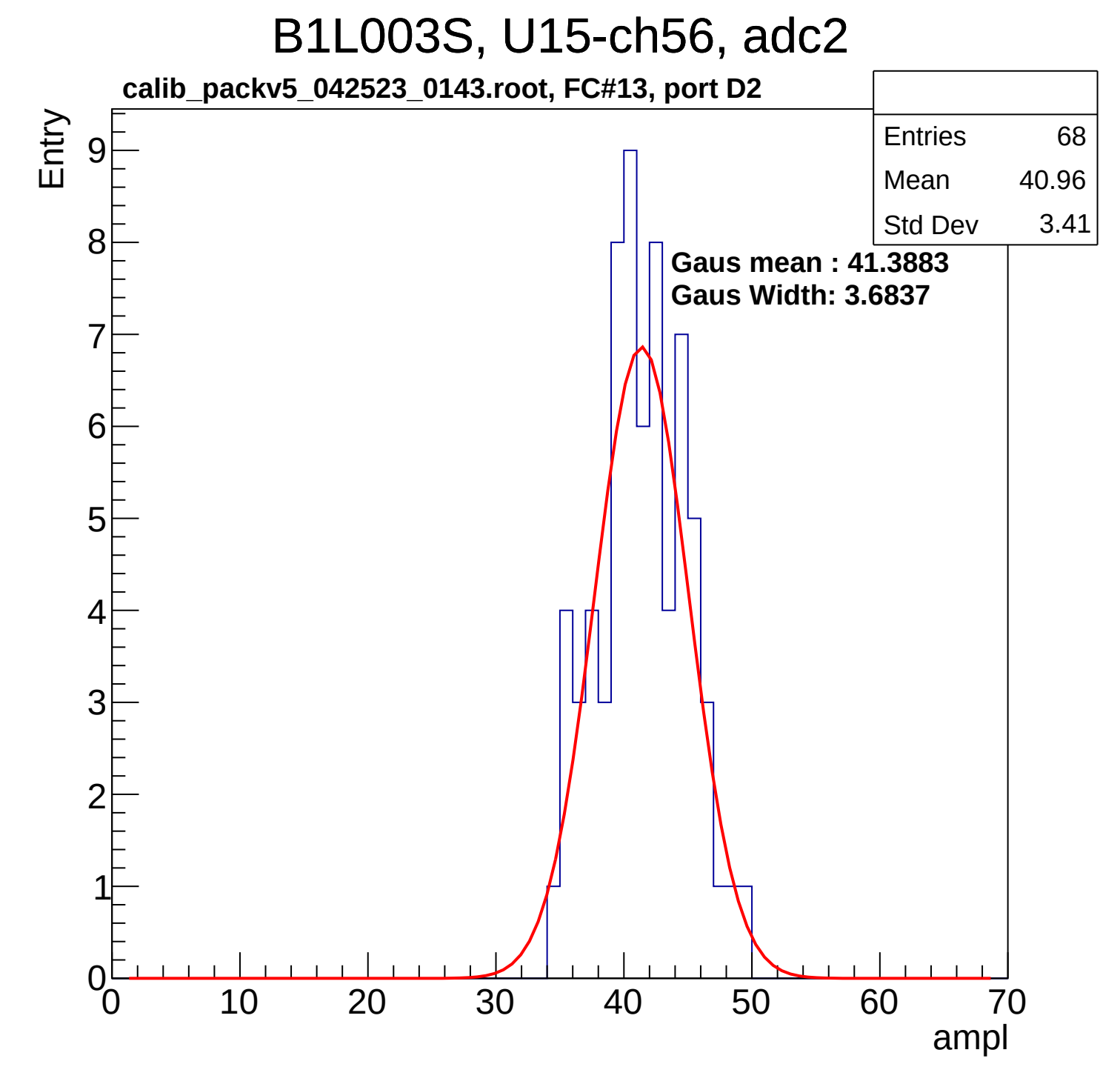

B1L003S, U15-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 26.97
Std Dev 3.258
Gaus mean : 27.5661
Gaus Width: 3.6408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 34.26
Std Dev 3.446
Gaus mean : 34.5279
Gaus Width: 2.9504
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 40.96
Std Dev 3.41
Gaus mean : 41.3883
Gaus Width: 3.6837
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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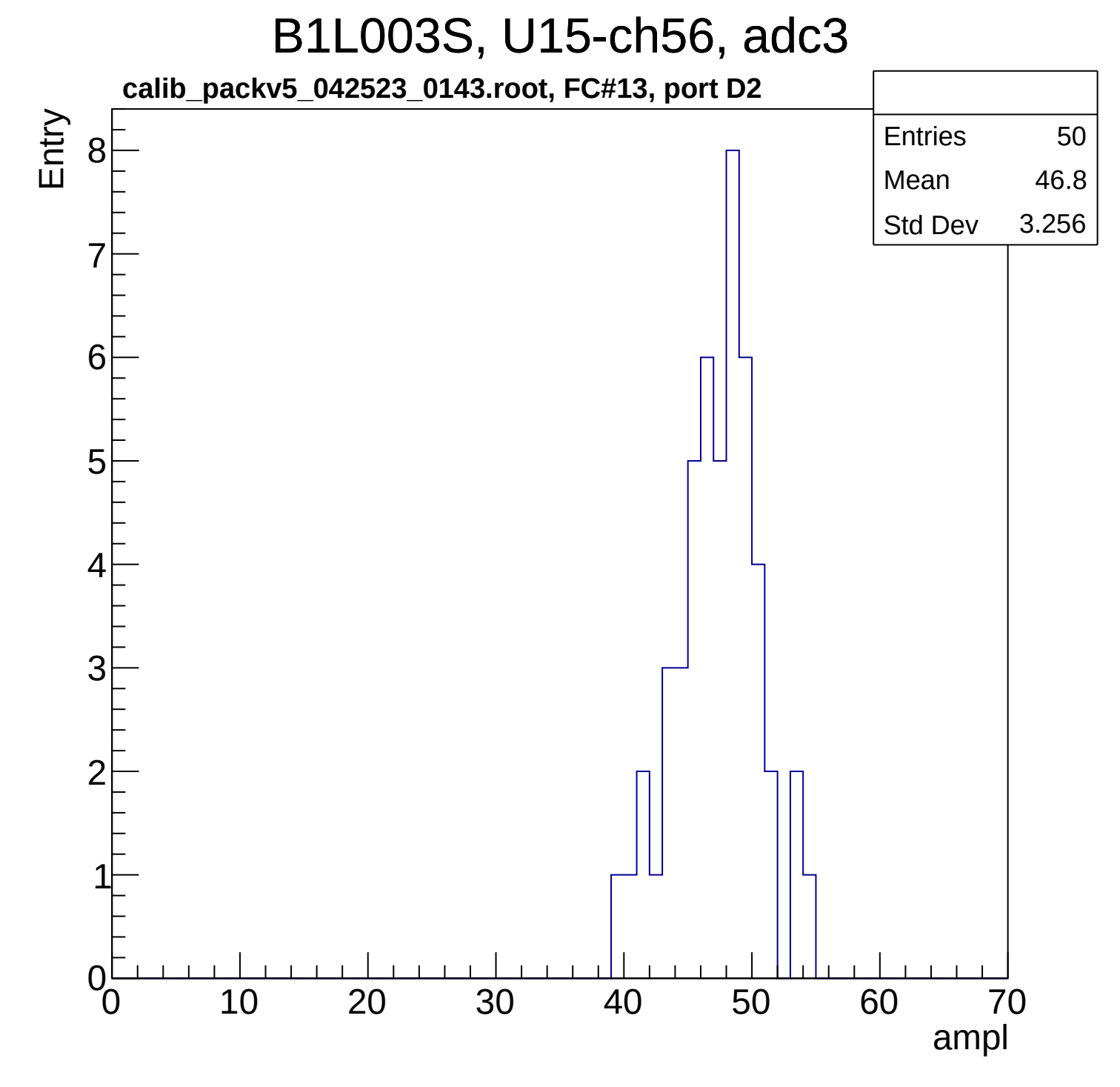
B1L003S, U15-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 46.8
Std Dev 3.256
Entry
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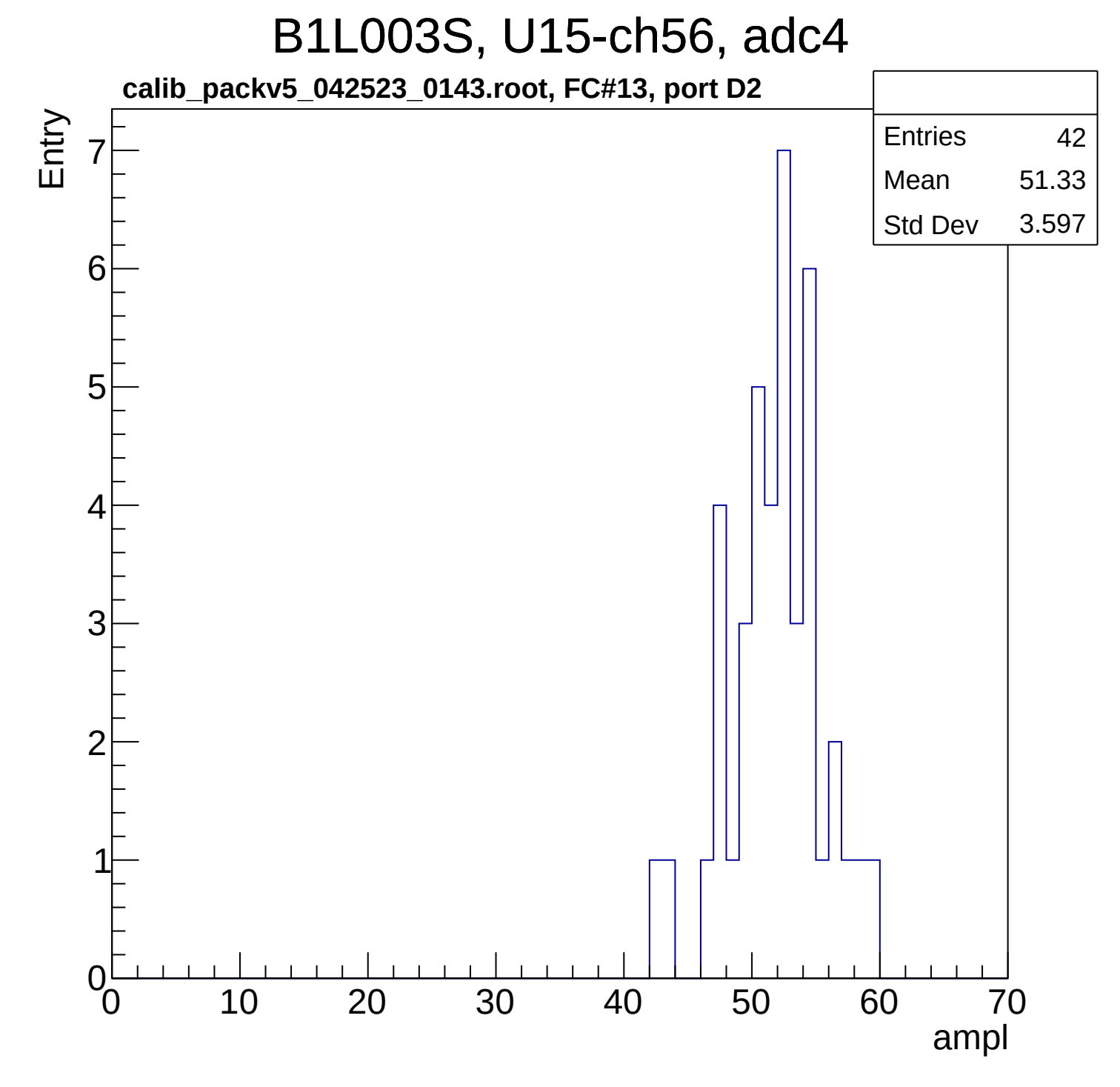
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 51.33
Std Dev 3.597
Entry
ampl
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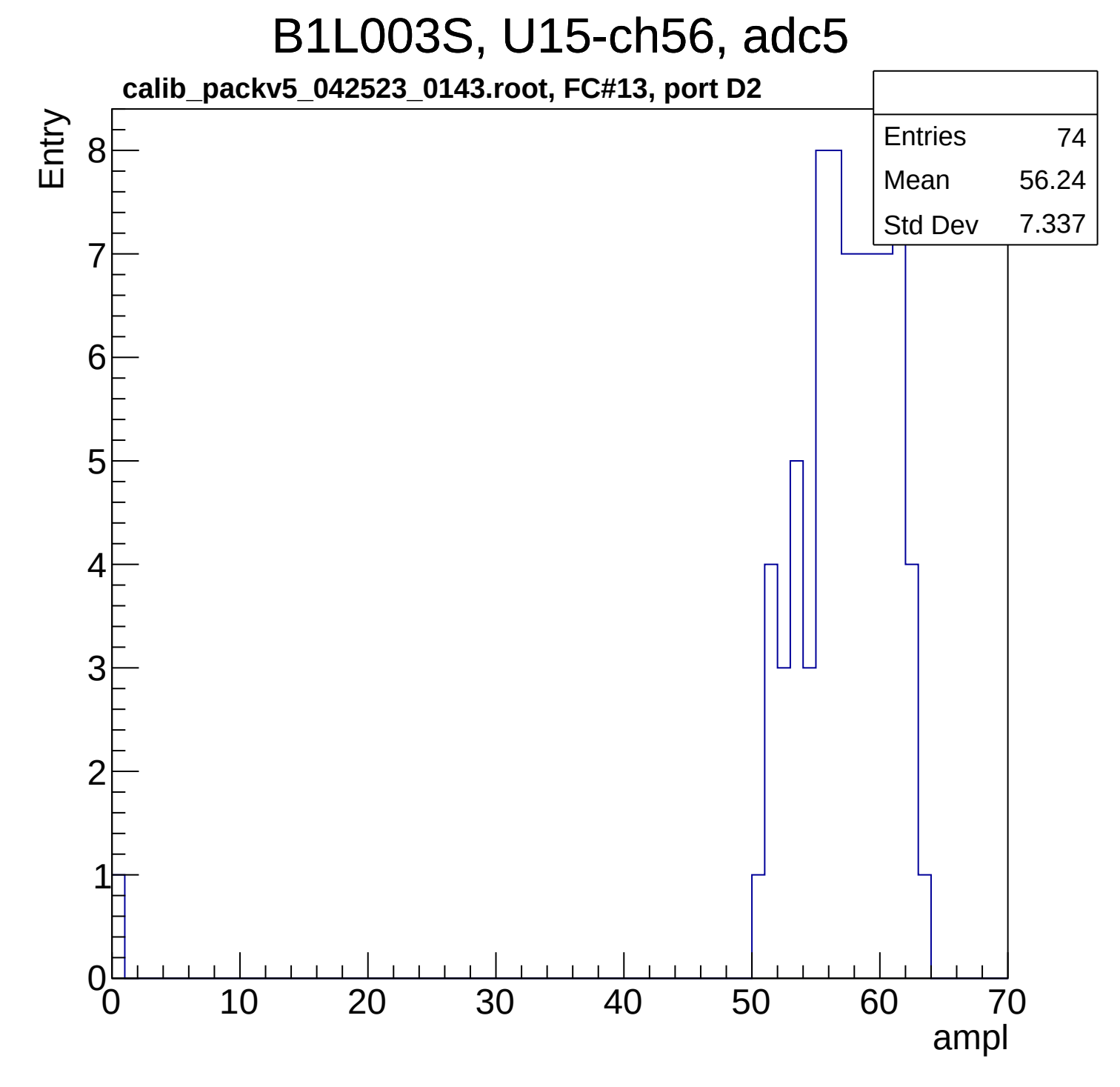
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 56.24
Std Dev 7.337
Entry
ampl
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1
2
3
4
5
6
7
8
0
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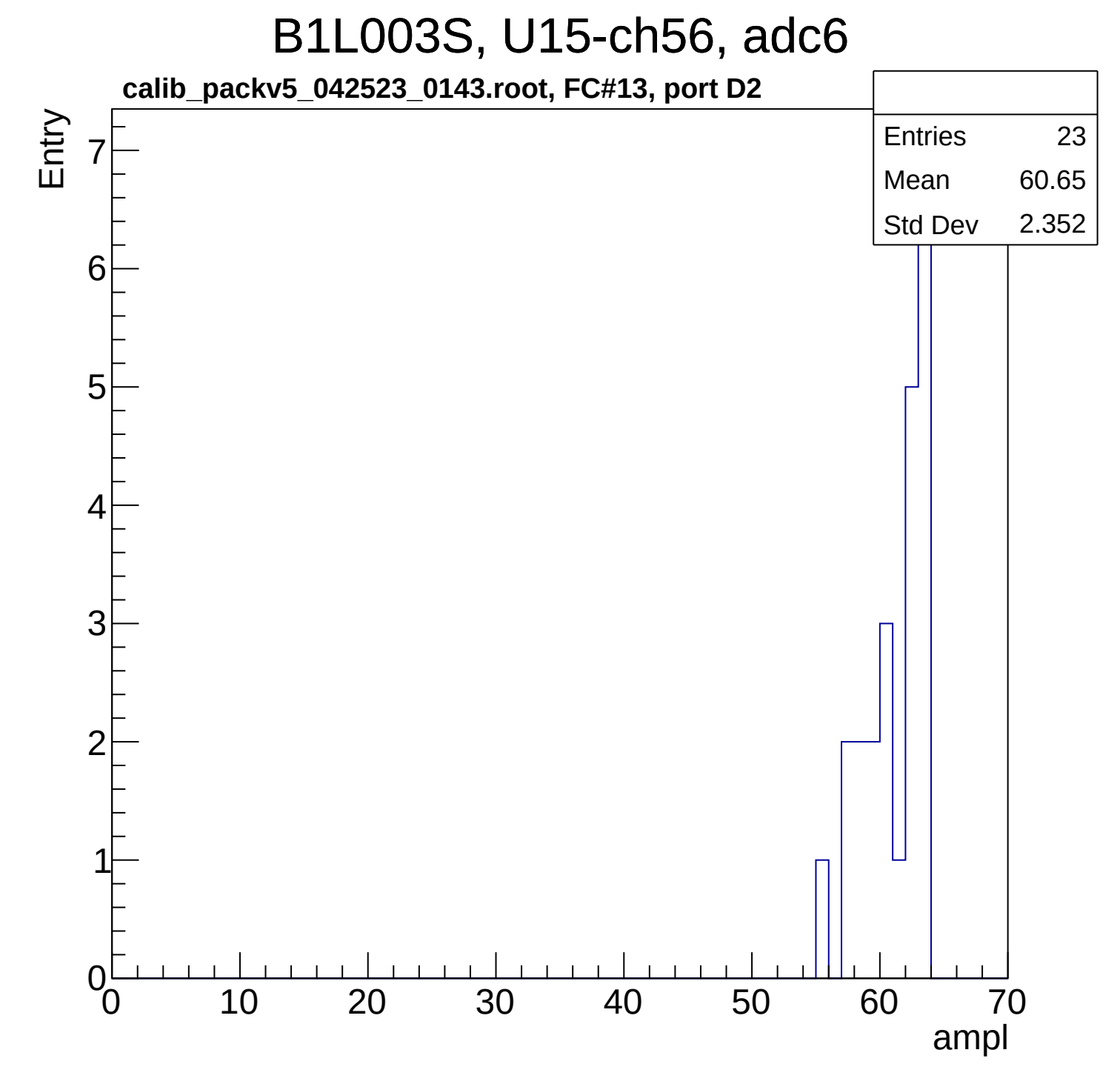
20
30
40
50
60
70


B1L003S, U15-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 60.65
Std Dev 2.352
Entry
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1
2
3
4
5
6
7
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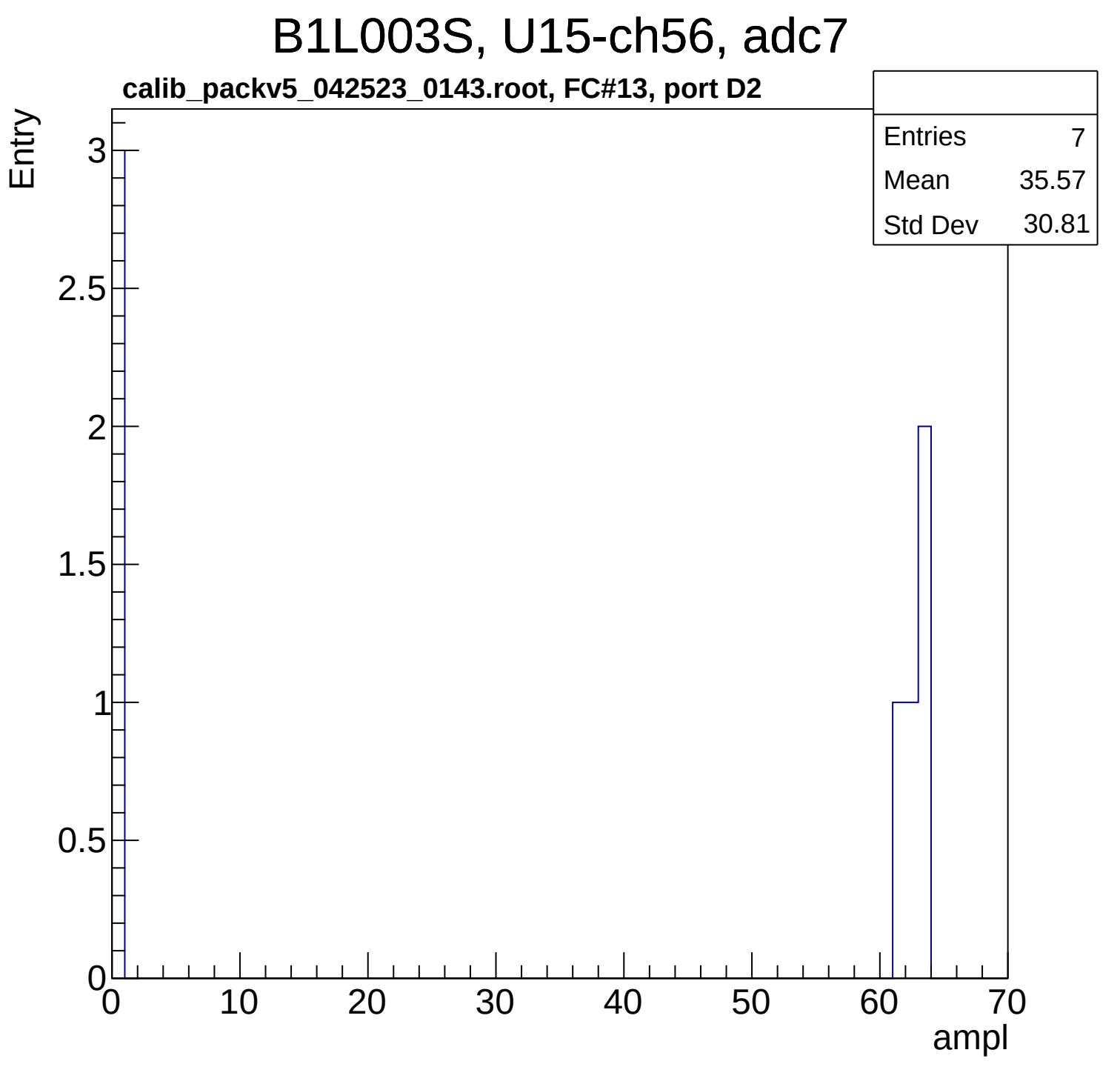
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 35.57
Std Dev 30.81
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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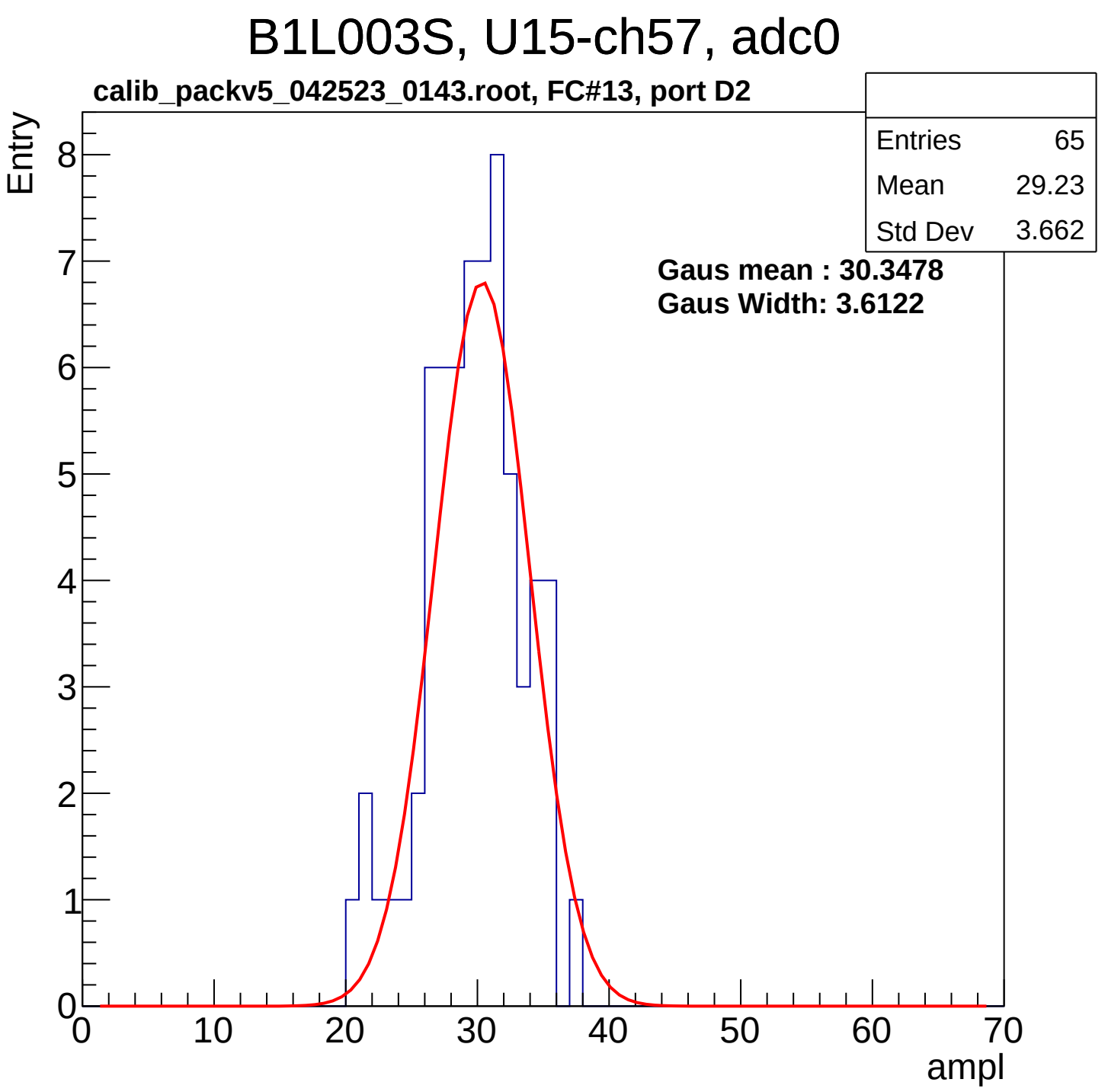
40
50
60
70


B1L003S, U15-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.23
Std Dev 3.662
Gaus mean : 30.3478
Gaus Width: 3.6122
Entry
ampl
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1
2
3
4
5
6
7
8
0
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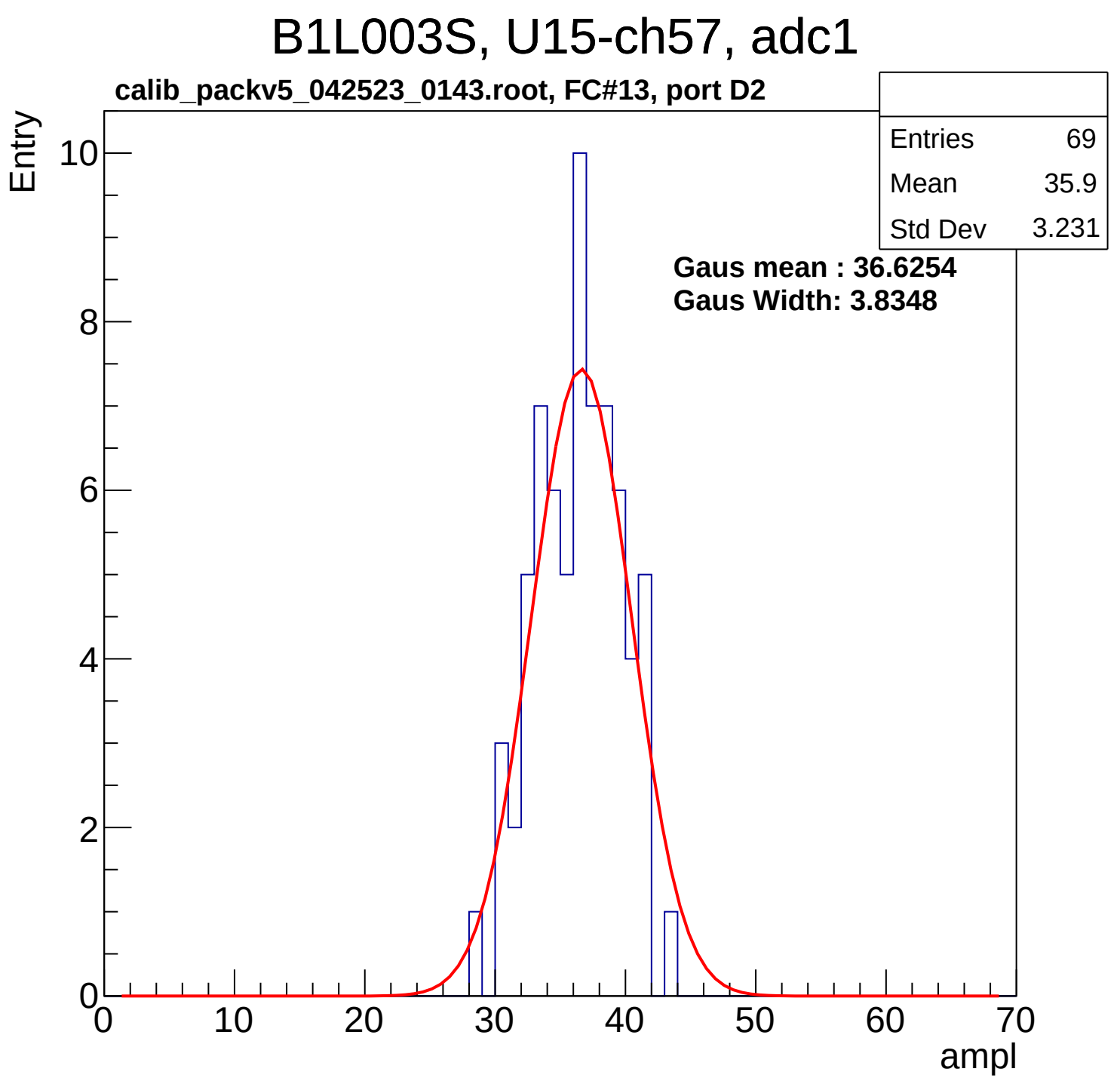
20
30
40
50
60
70


B1L003S, U15-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.9
Std Dev 3.231
Gaus mean : 36.6254
Gaus Width: 3.8348
Entry
ampl
0
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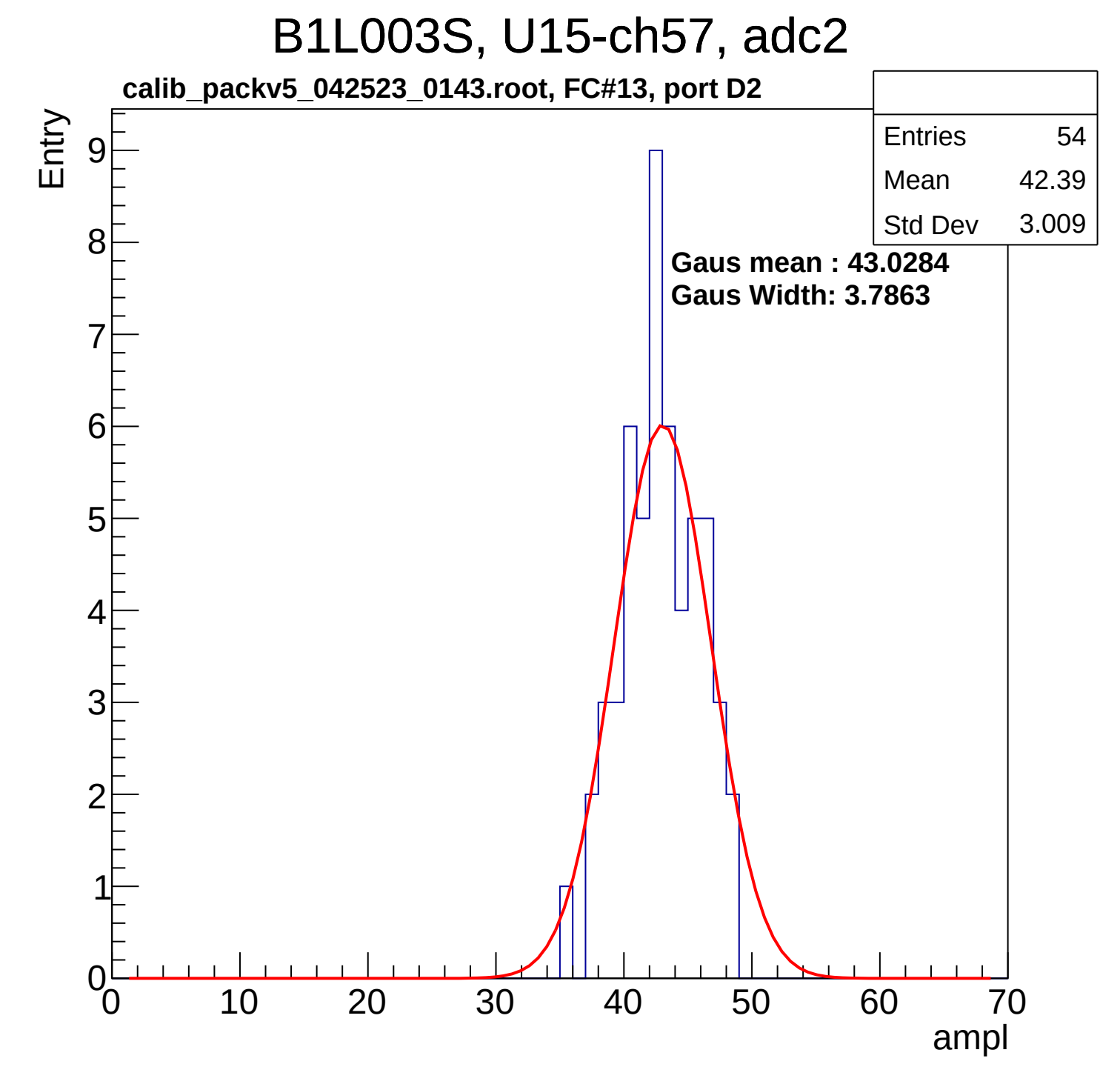
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.39
Std Dev 3.009
Gaus mean : 43.0284
Gaus Width: 3.7863
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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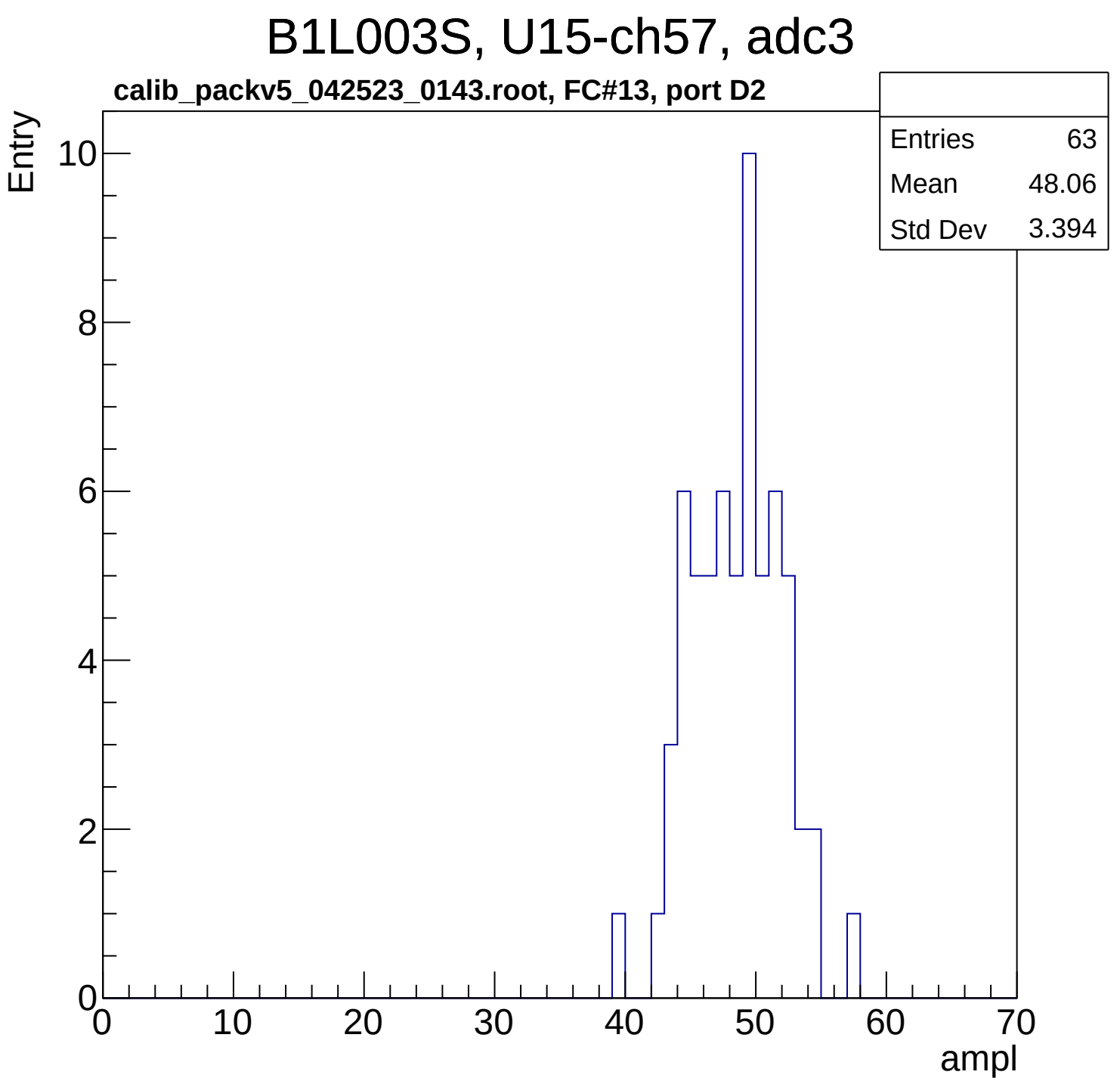
B1L003S, U15-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.06
Std Dev 3.394
Entry
ampl
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2
4
6
8
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0
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60
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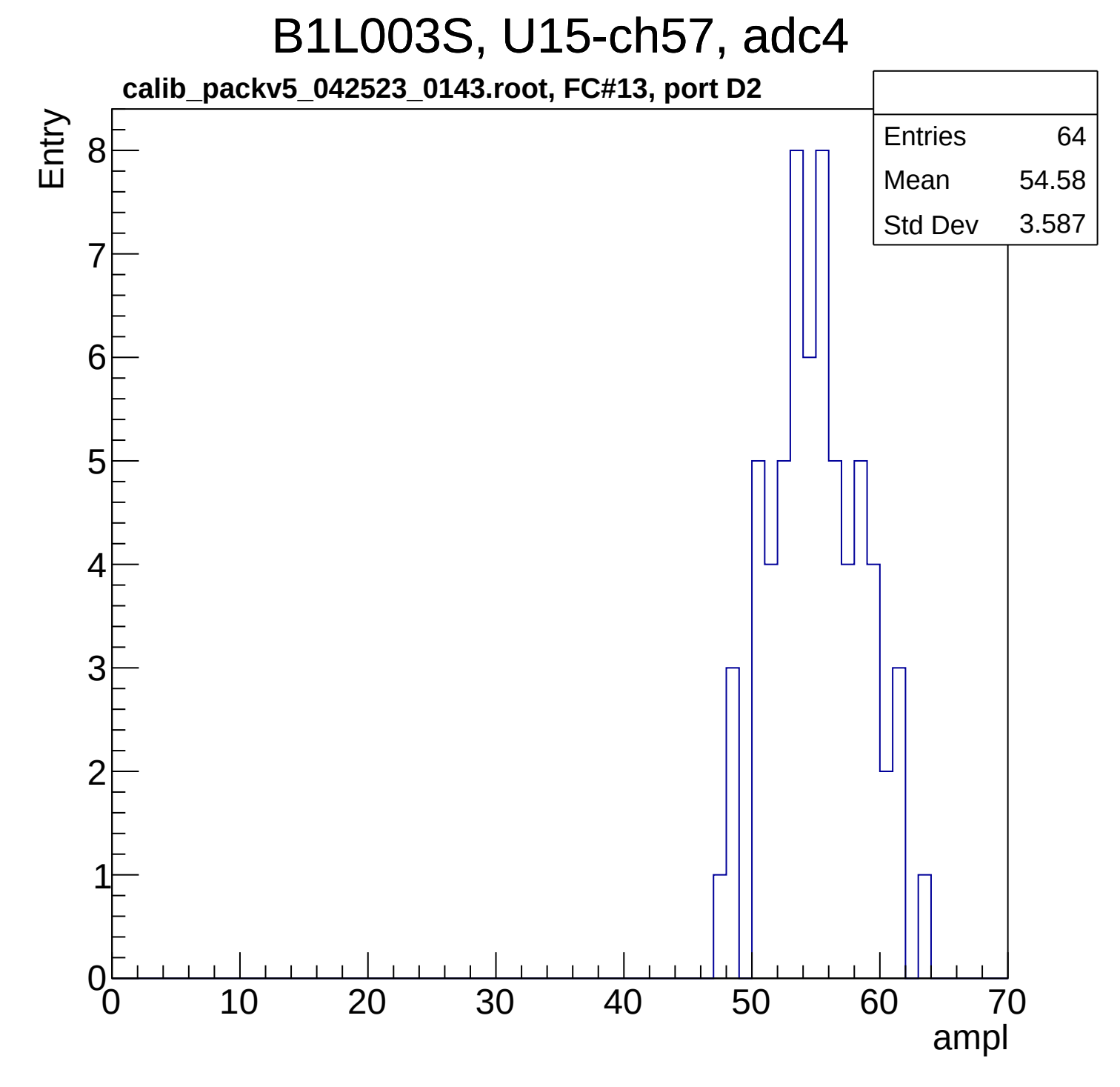
B1L003S, U15-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.58
Std Dev 3.587
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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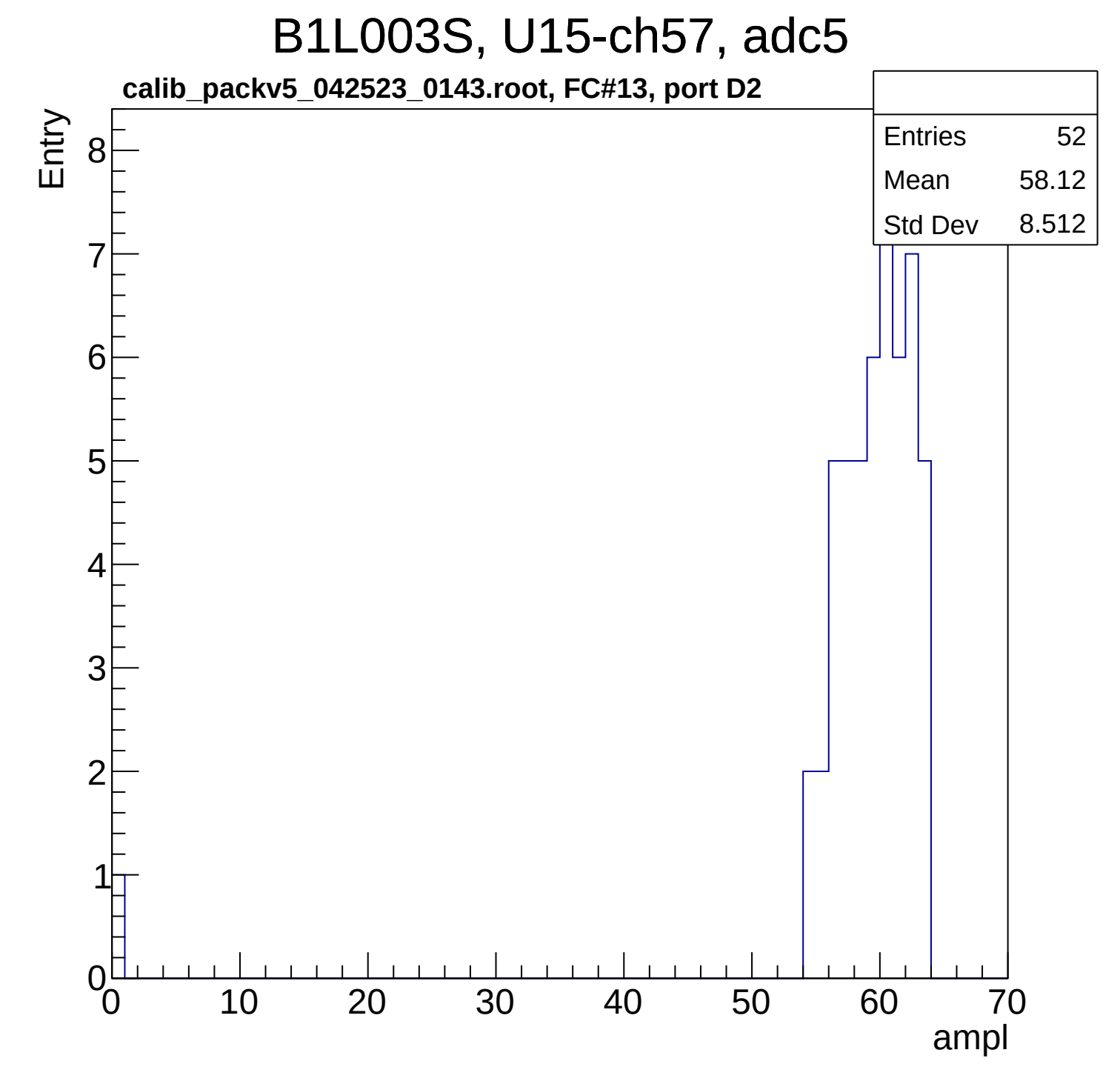
B1L003S, U15-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.12
Std Dev 8.512
Entry
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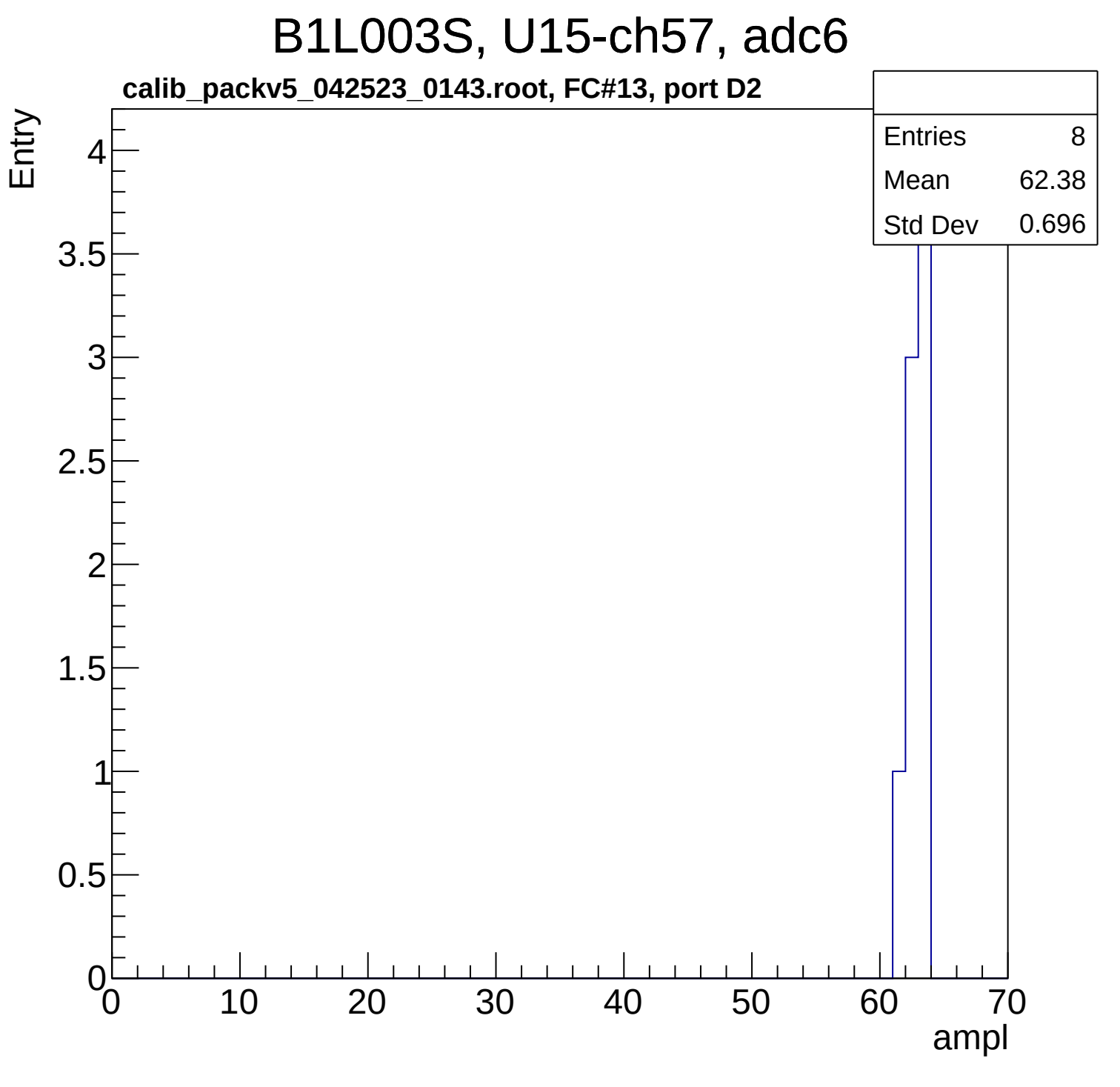
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
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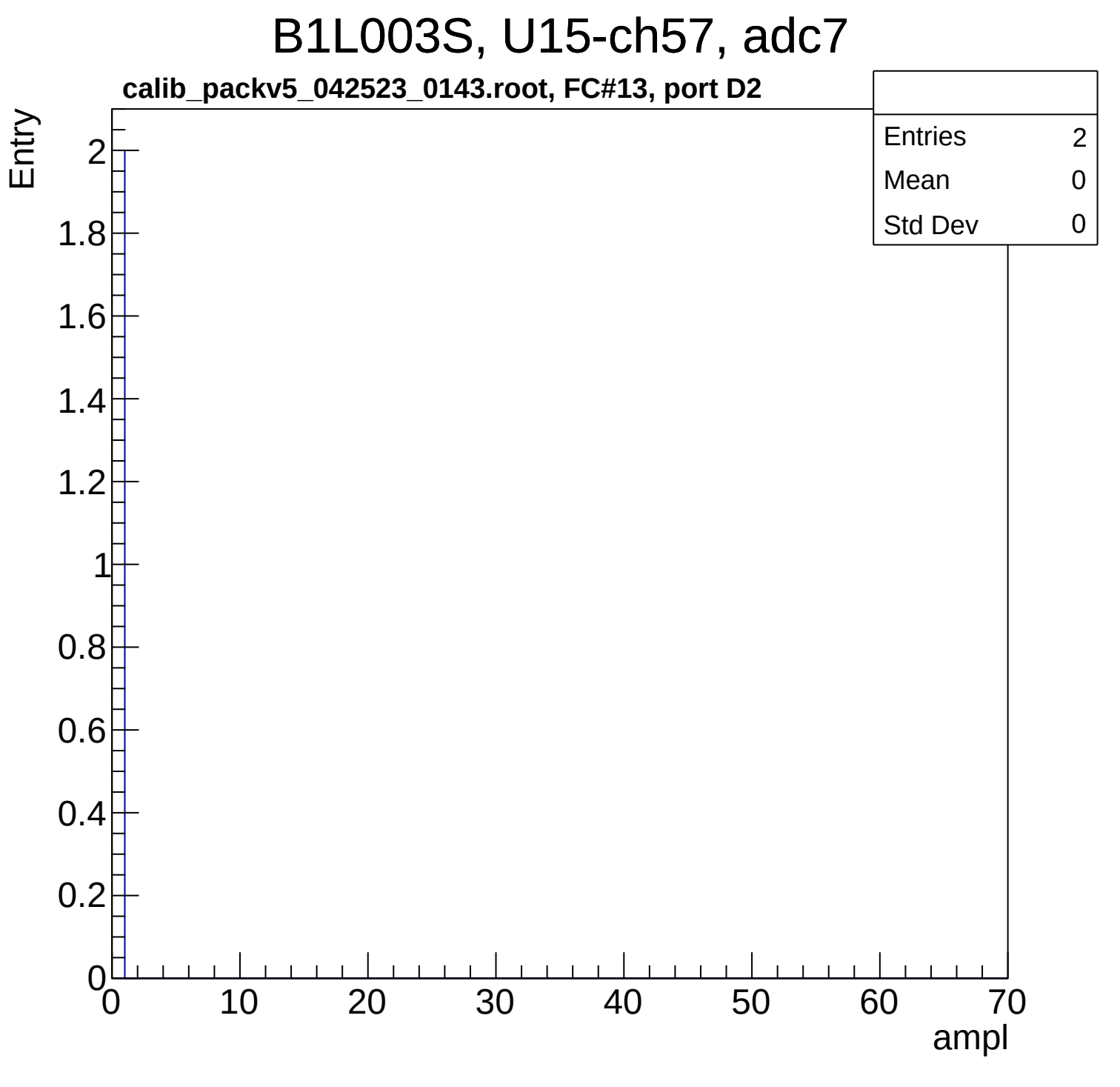
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
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40
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60
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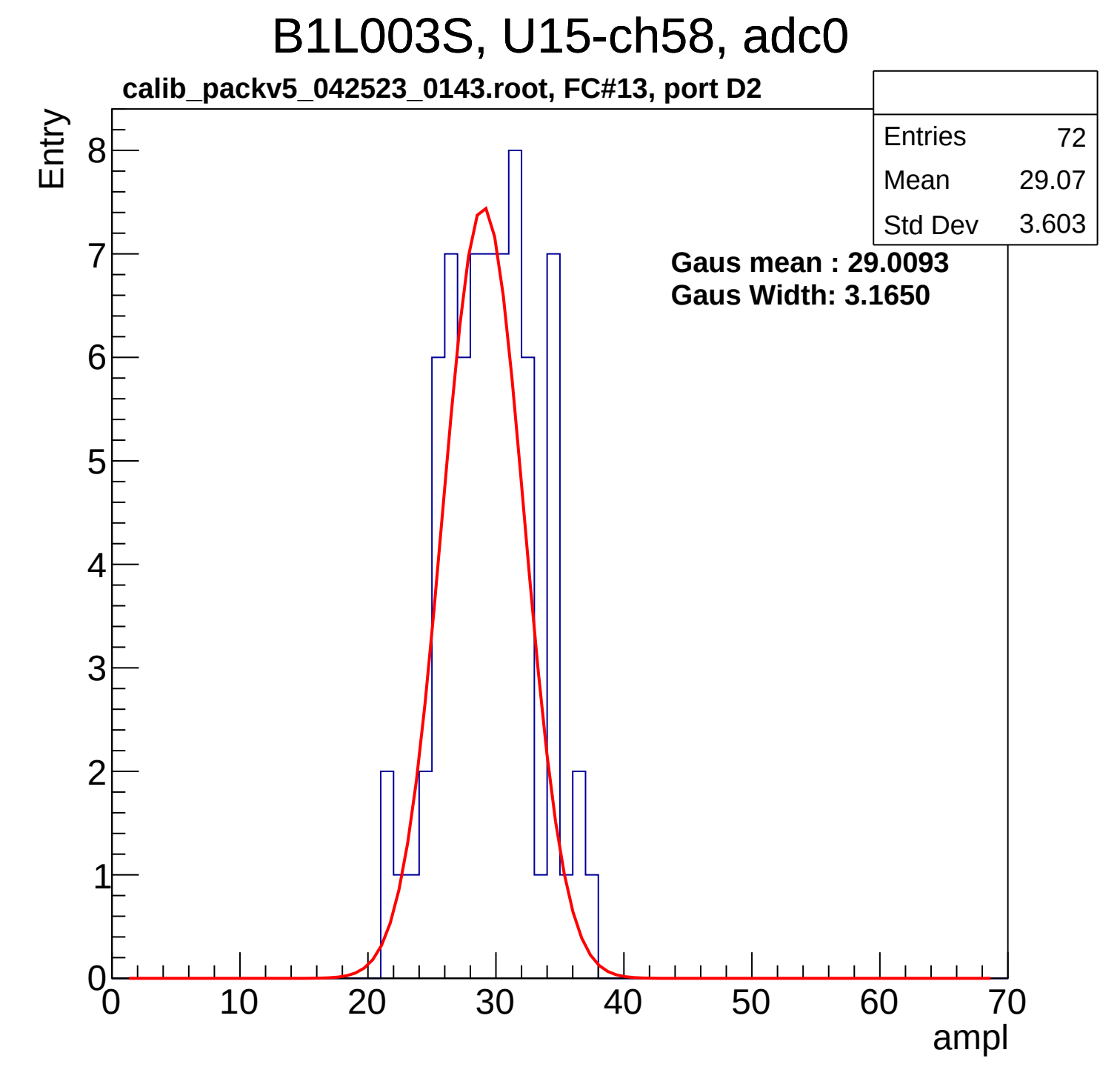
B1L003S, U15-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 29.07
Std Dev 3.603
Gaus mean : 29.0093
Gaus Width: 3.1650
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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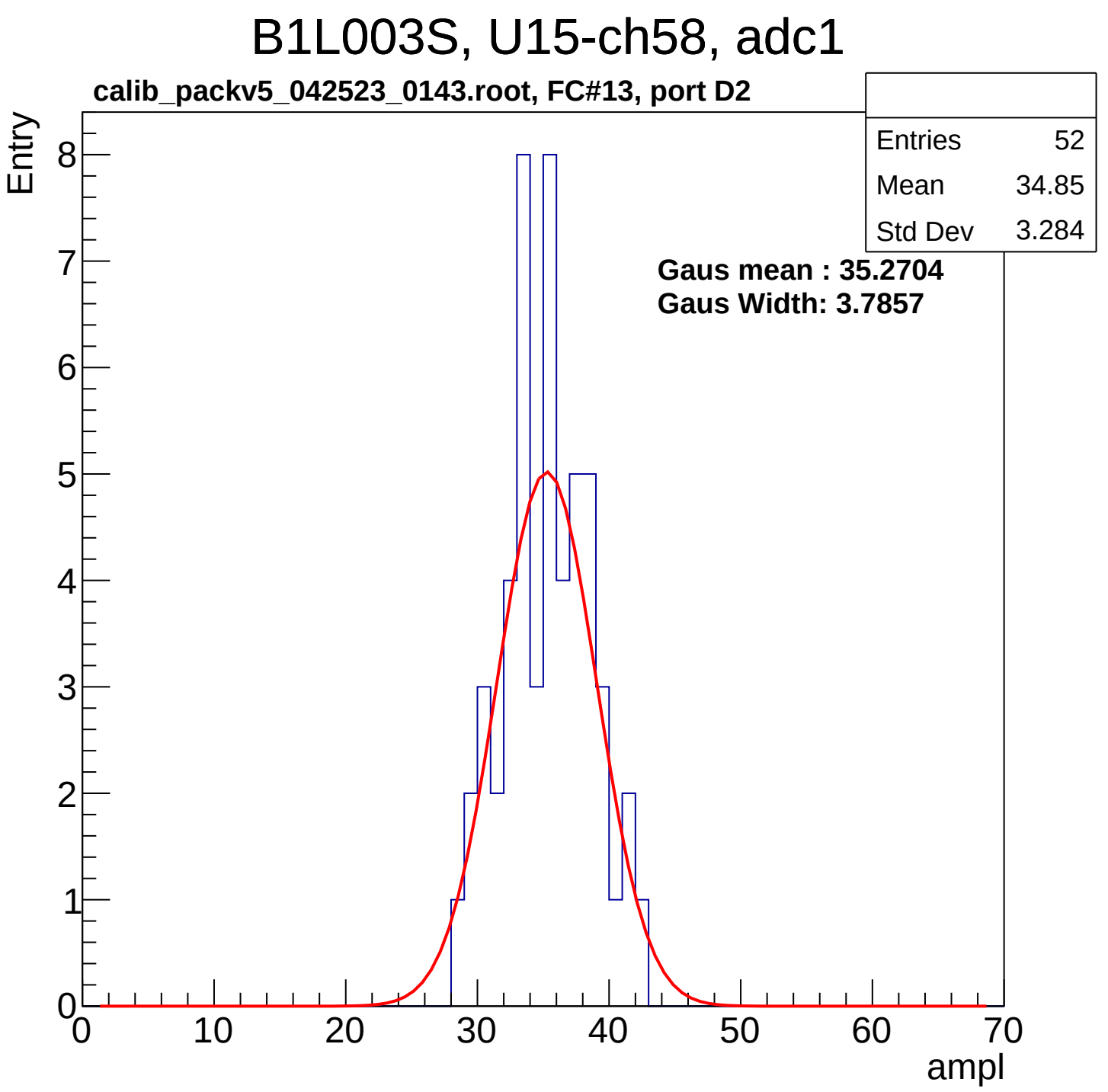
B1L003S, U15-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 34.85
Std Dev 3.284
Gaus mean : 35.2704
Gaus Width: 3.7857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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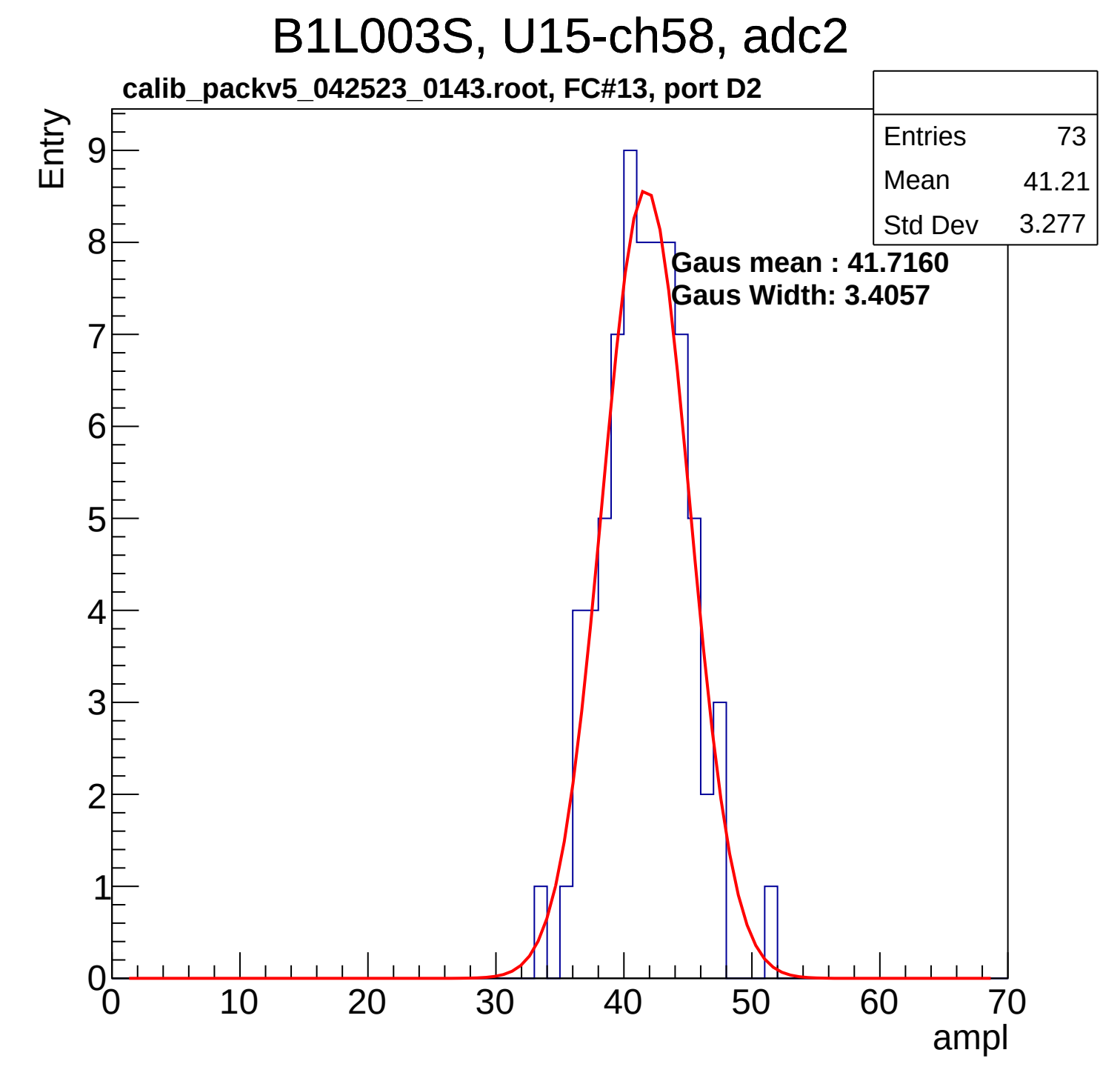
B1L003S, U15-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.21
Std Dev 3.277
Gaus mean : 41.7160
Gaus Width: 3.4057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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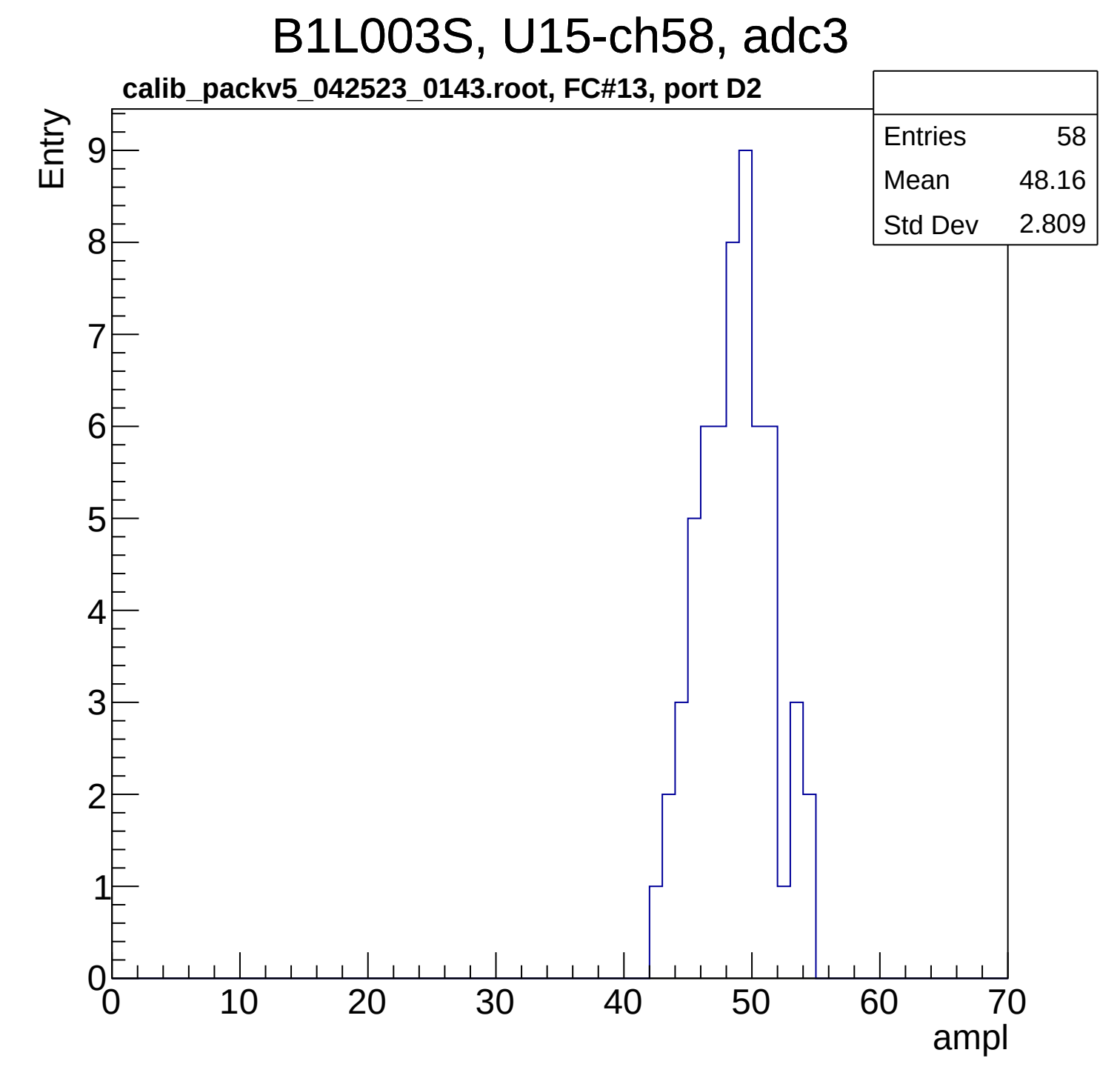
B1L003S, U15-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.16
Std Dev 2.809
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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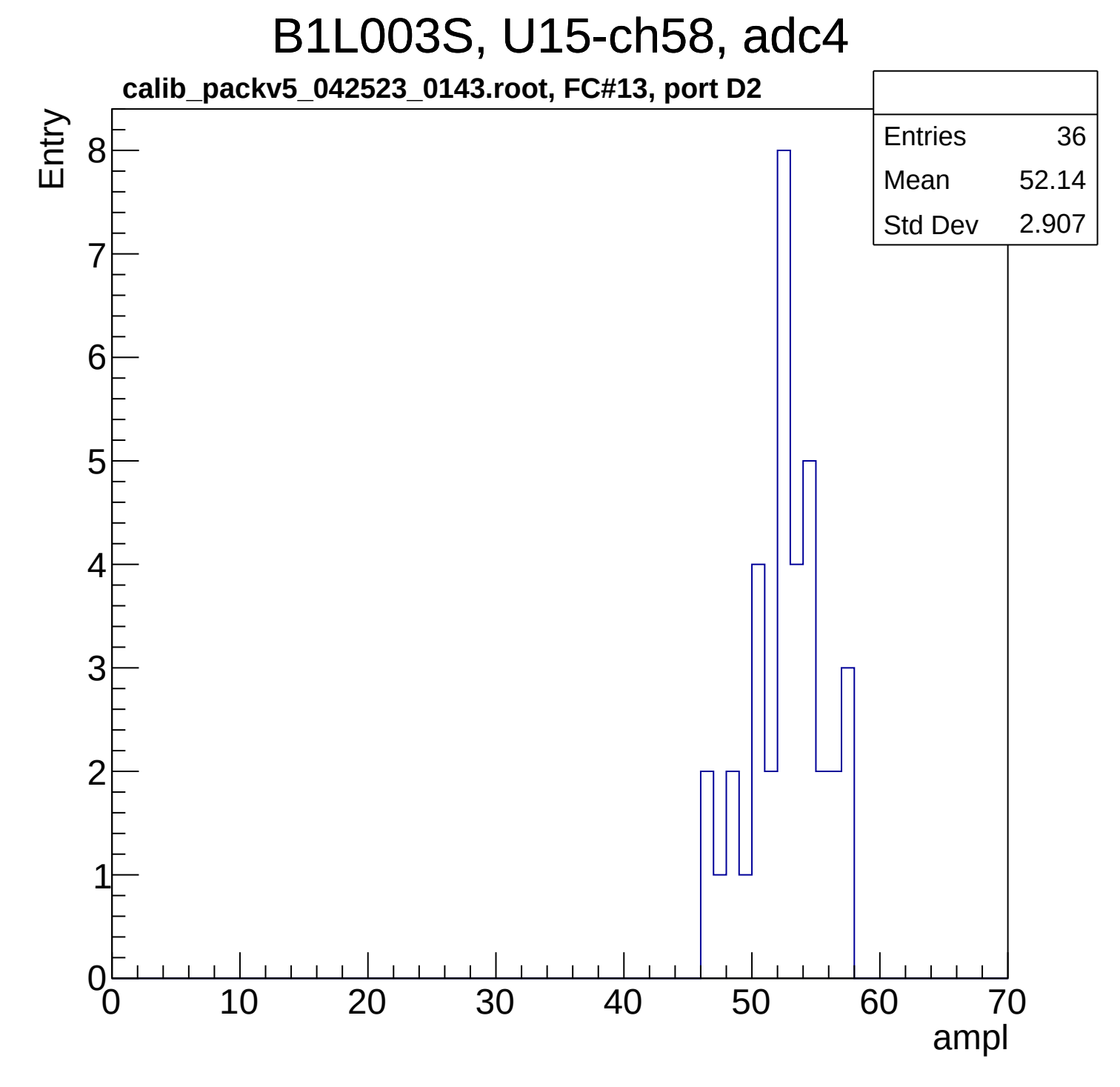
B1L003S, U15-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 52.14
Std Dev 2.907
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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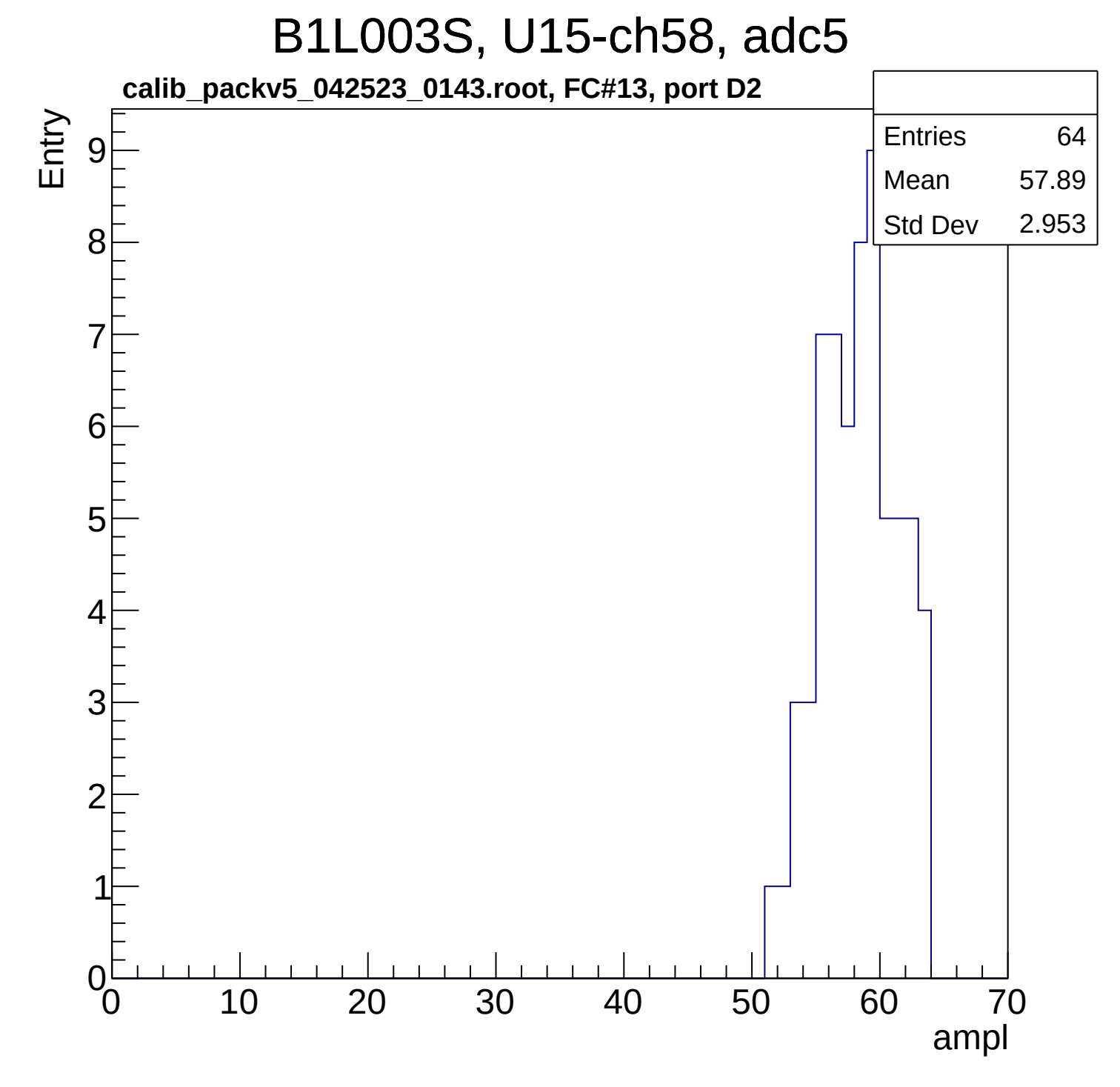
B1L003S, U15-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 57.89
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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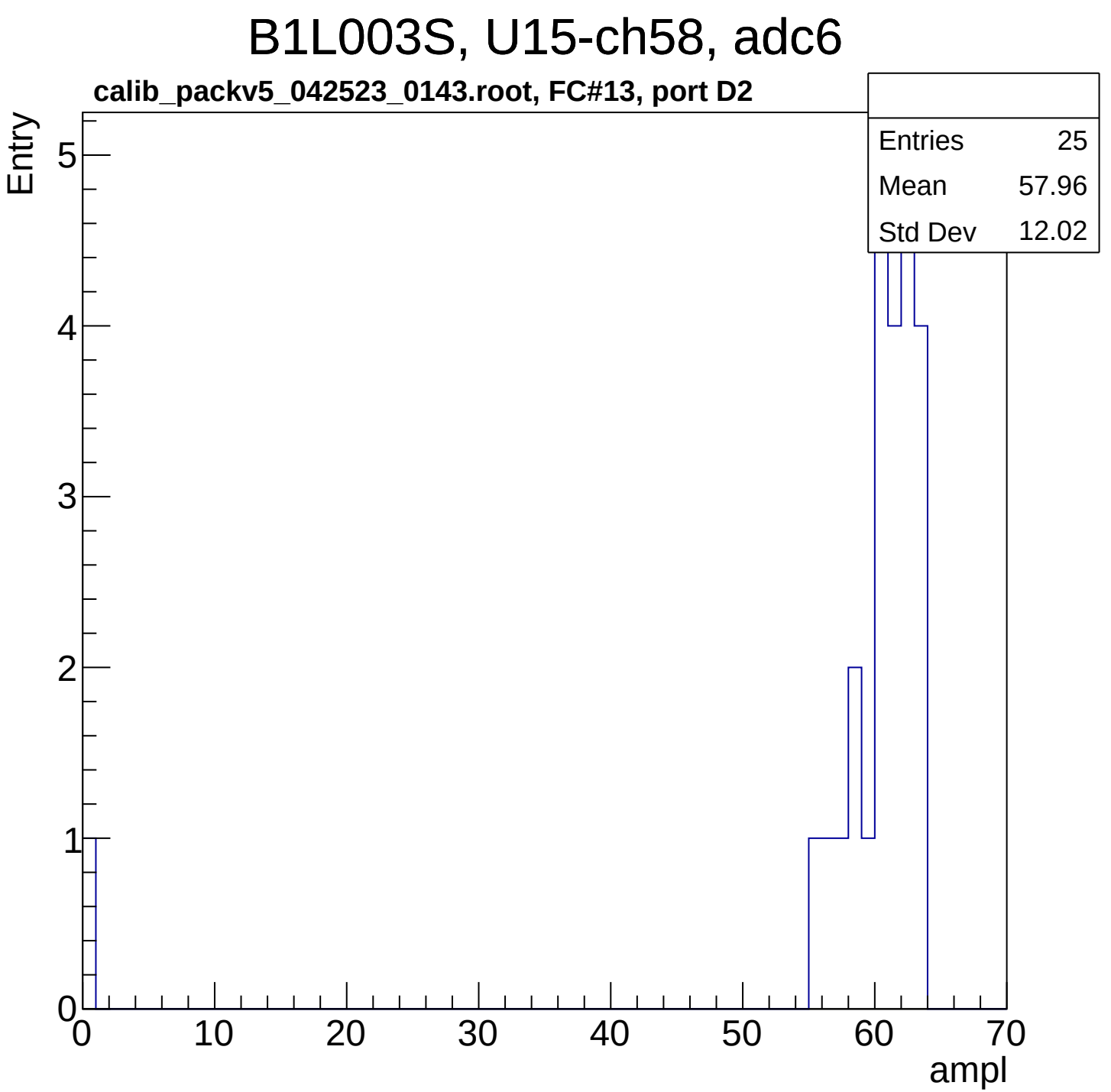
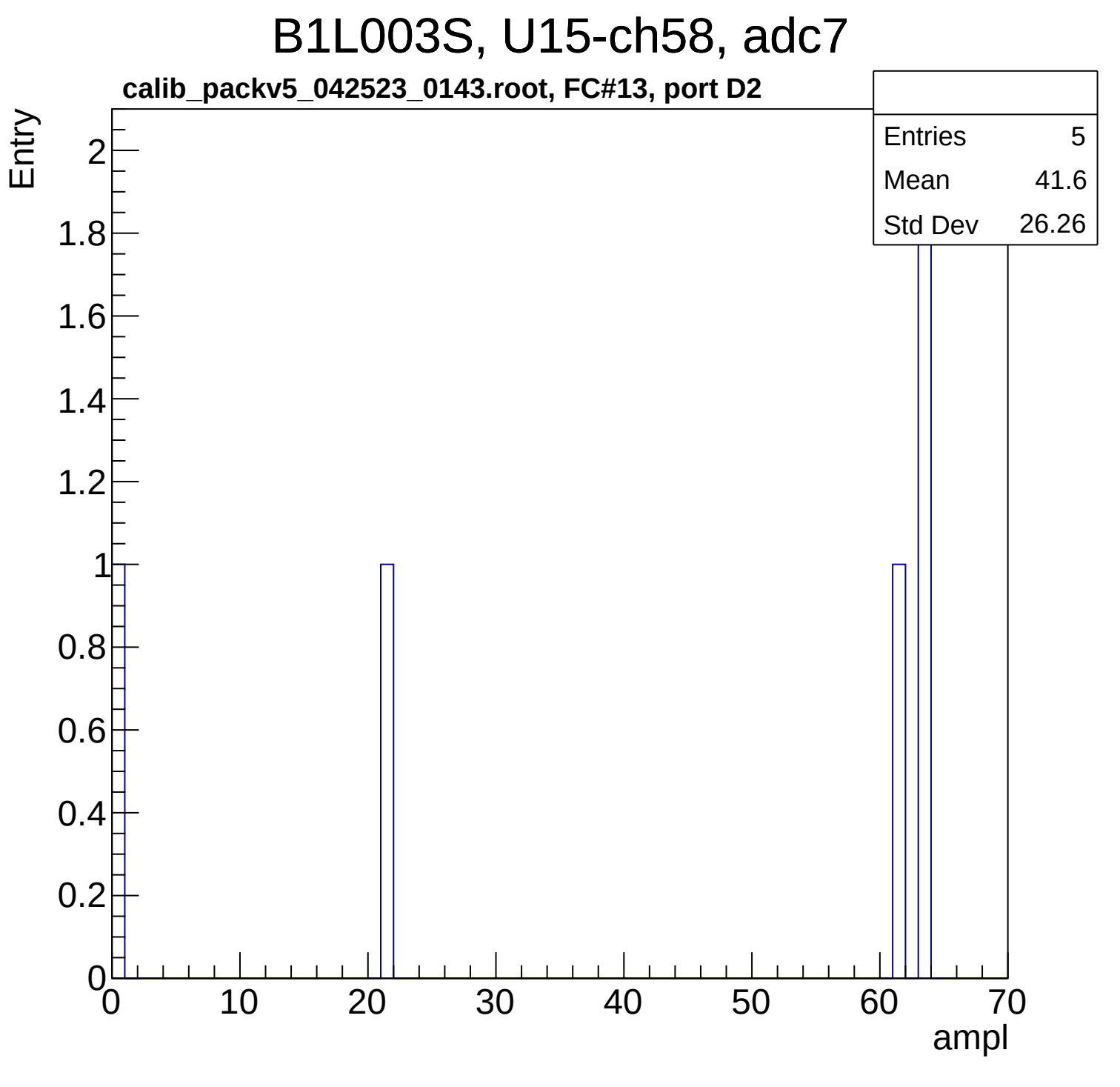

B1L003S, U15-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 25
Mean 57.96
Std Dev 12.02
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 41.6
Std Dev 26.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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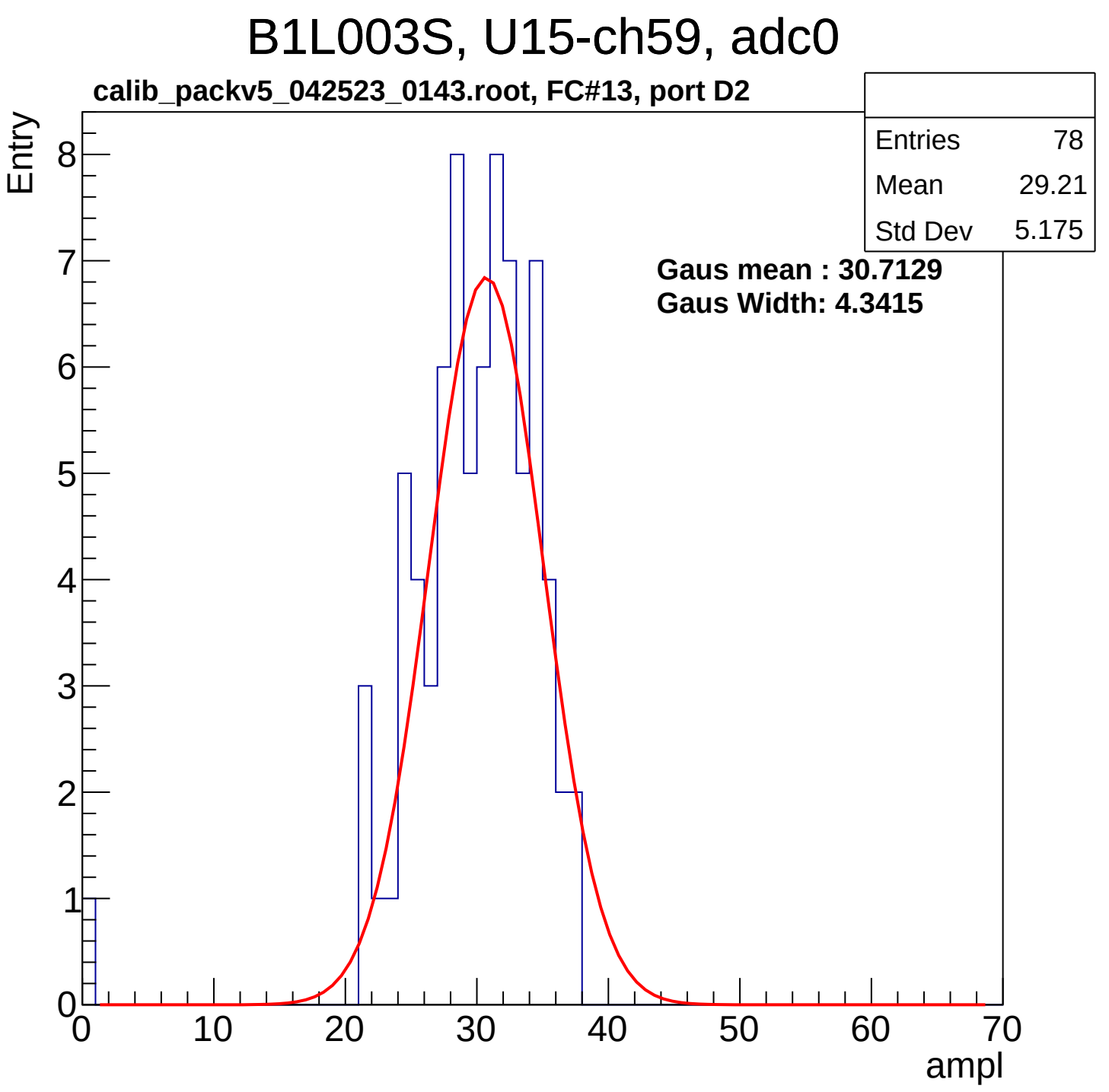
B1L003S, U15-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.21
Std Dev 5.175
Gaus mean : 30.7129
Gaus Width: 4.3415
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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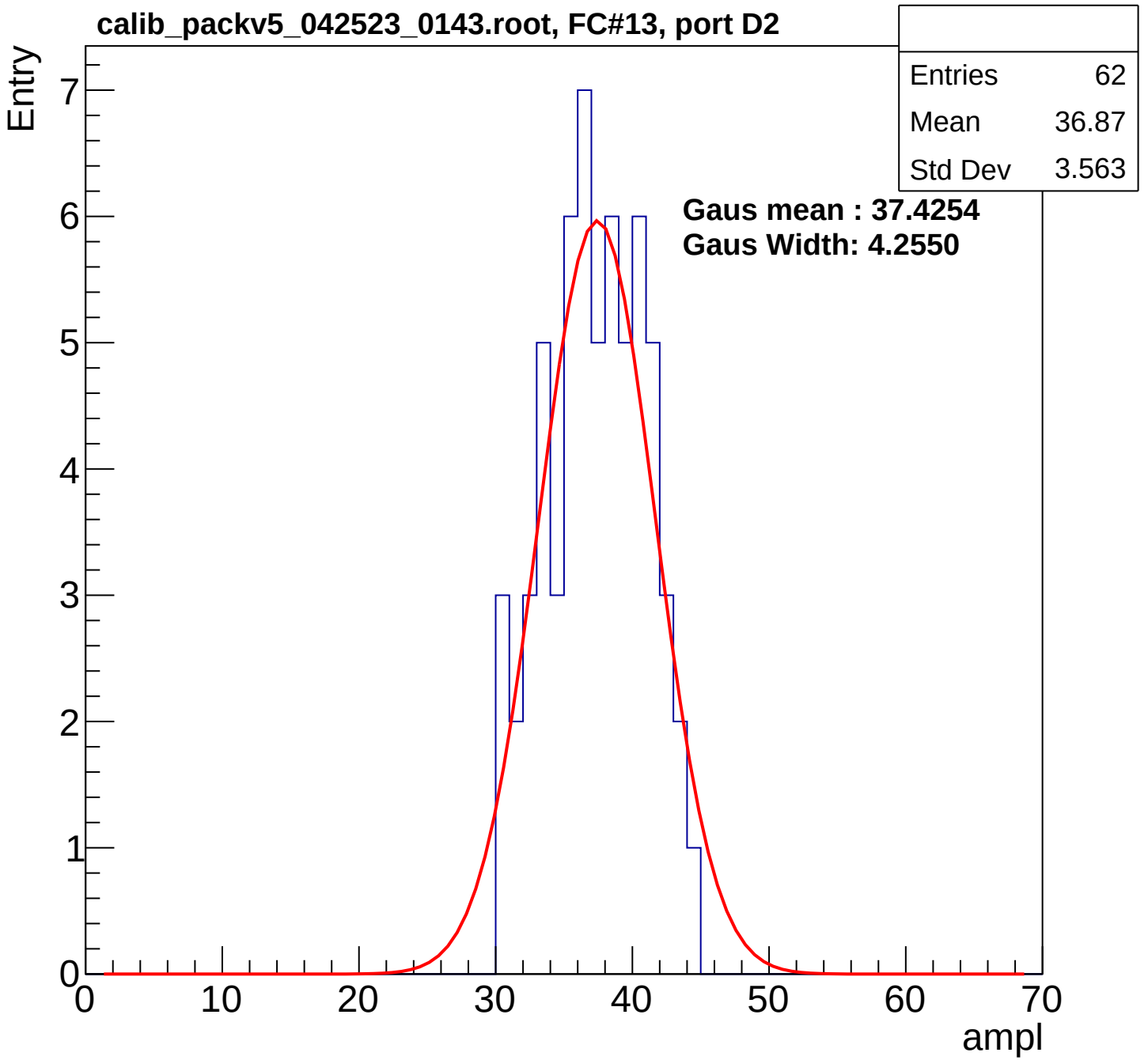
B1L003S, U15-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.87
Std Dev 3.563
Gaus mean : 37.4254
Gaus Width: 4.2550
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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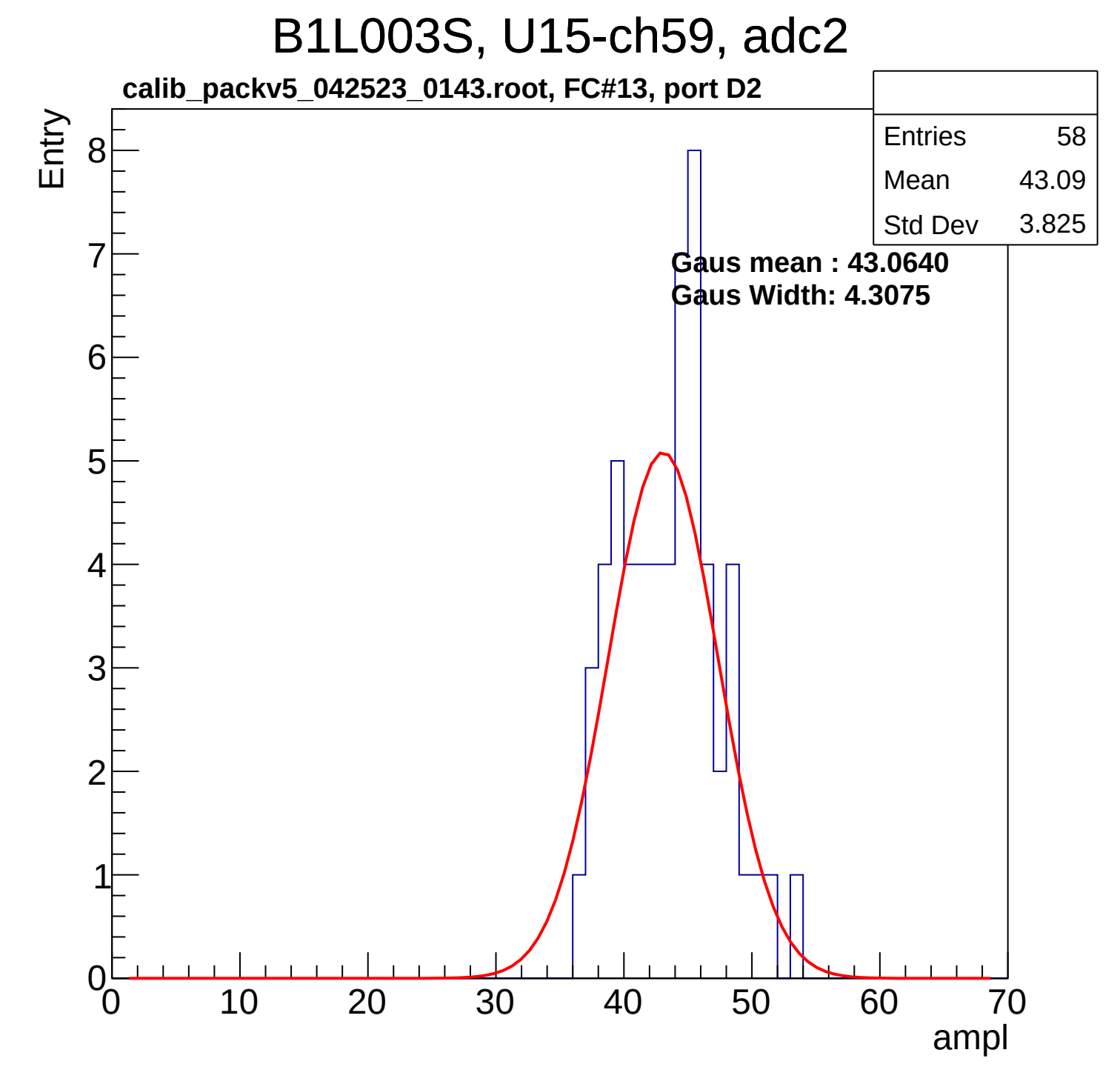
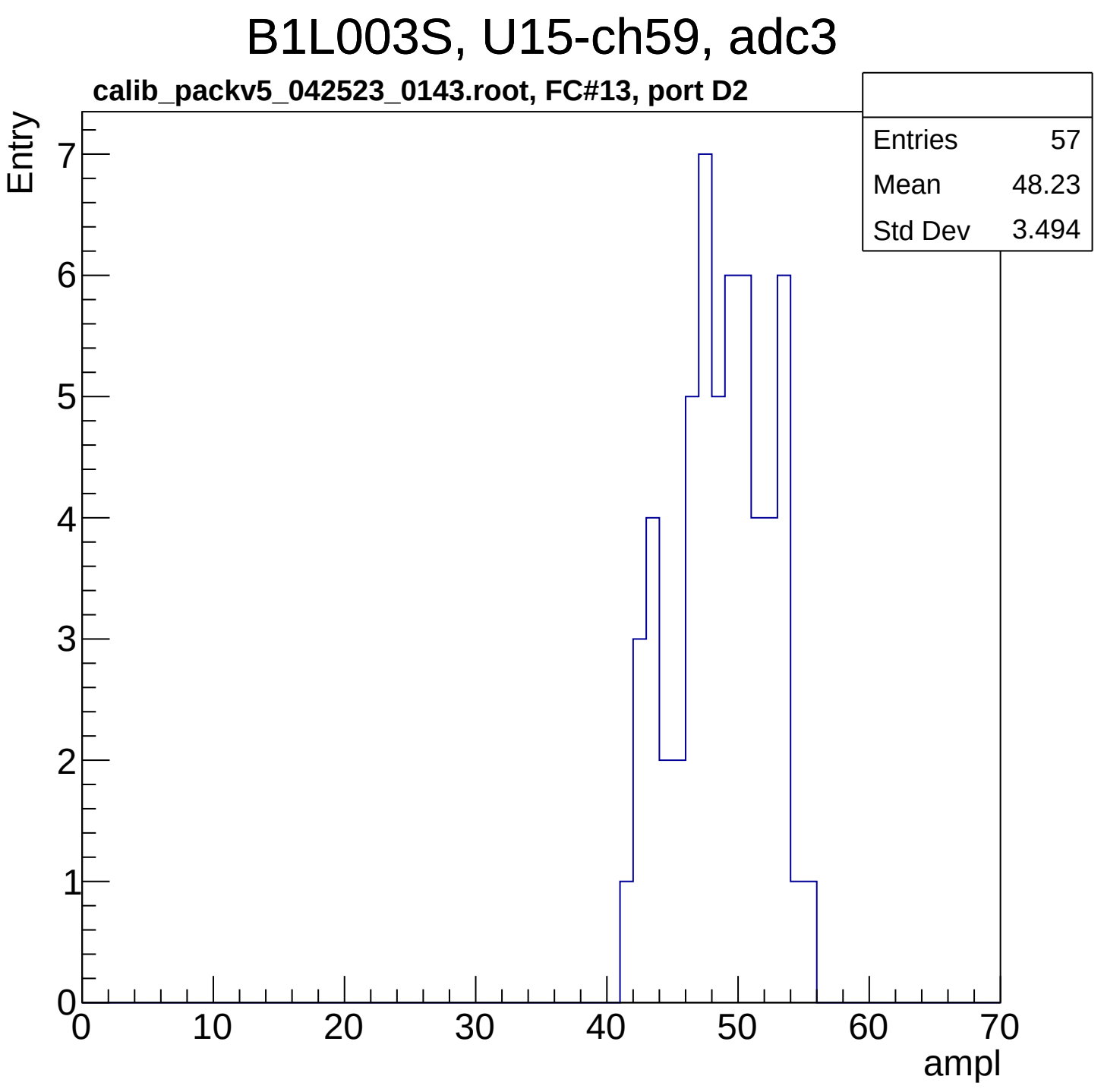
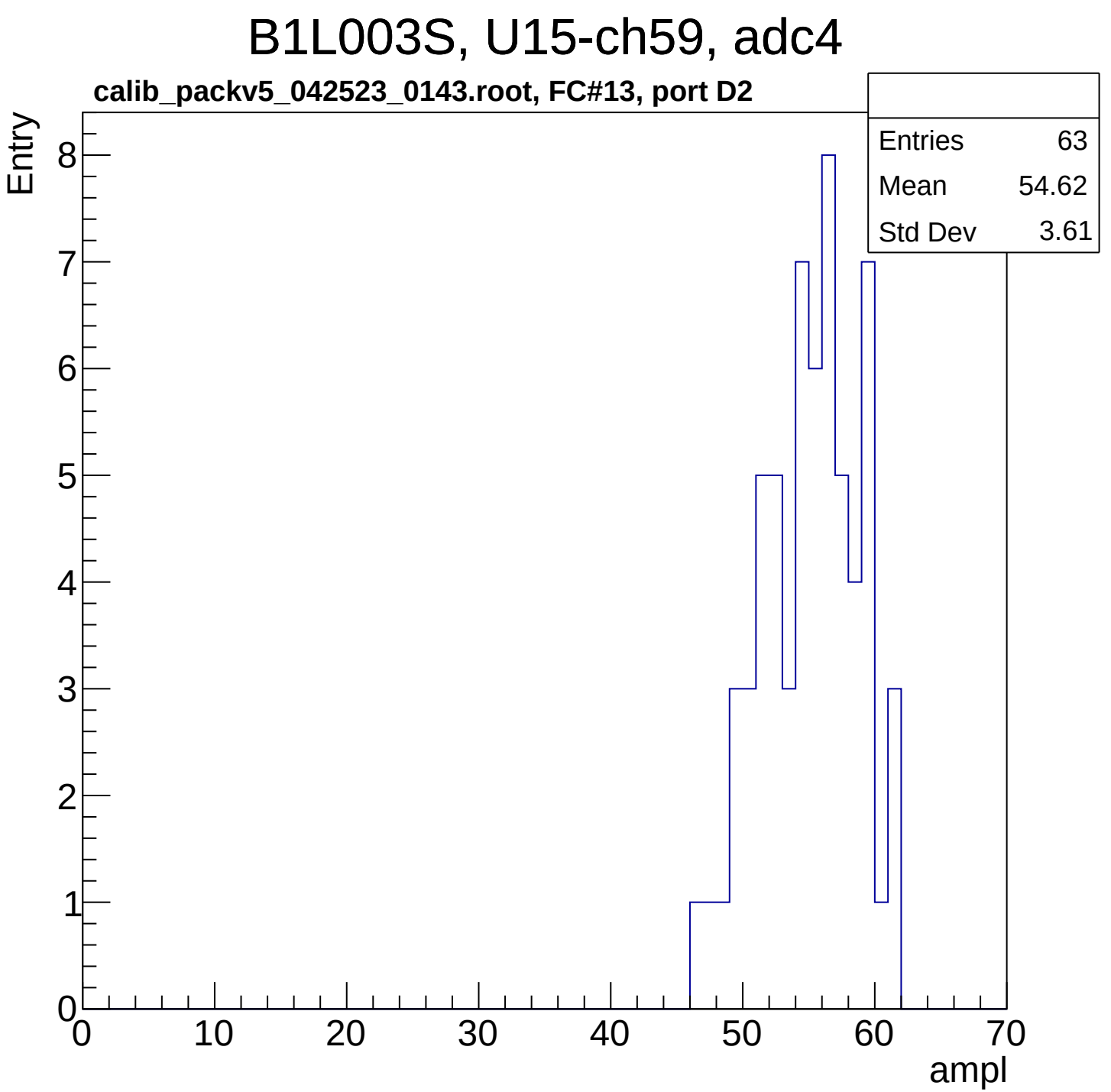

B1L003S, U15-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.09
Std Dev 3.825
Gaus mean : 43.0640
Gaus Width: 4.3075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.23
Std Dev 3.494
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.62
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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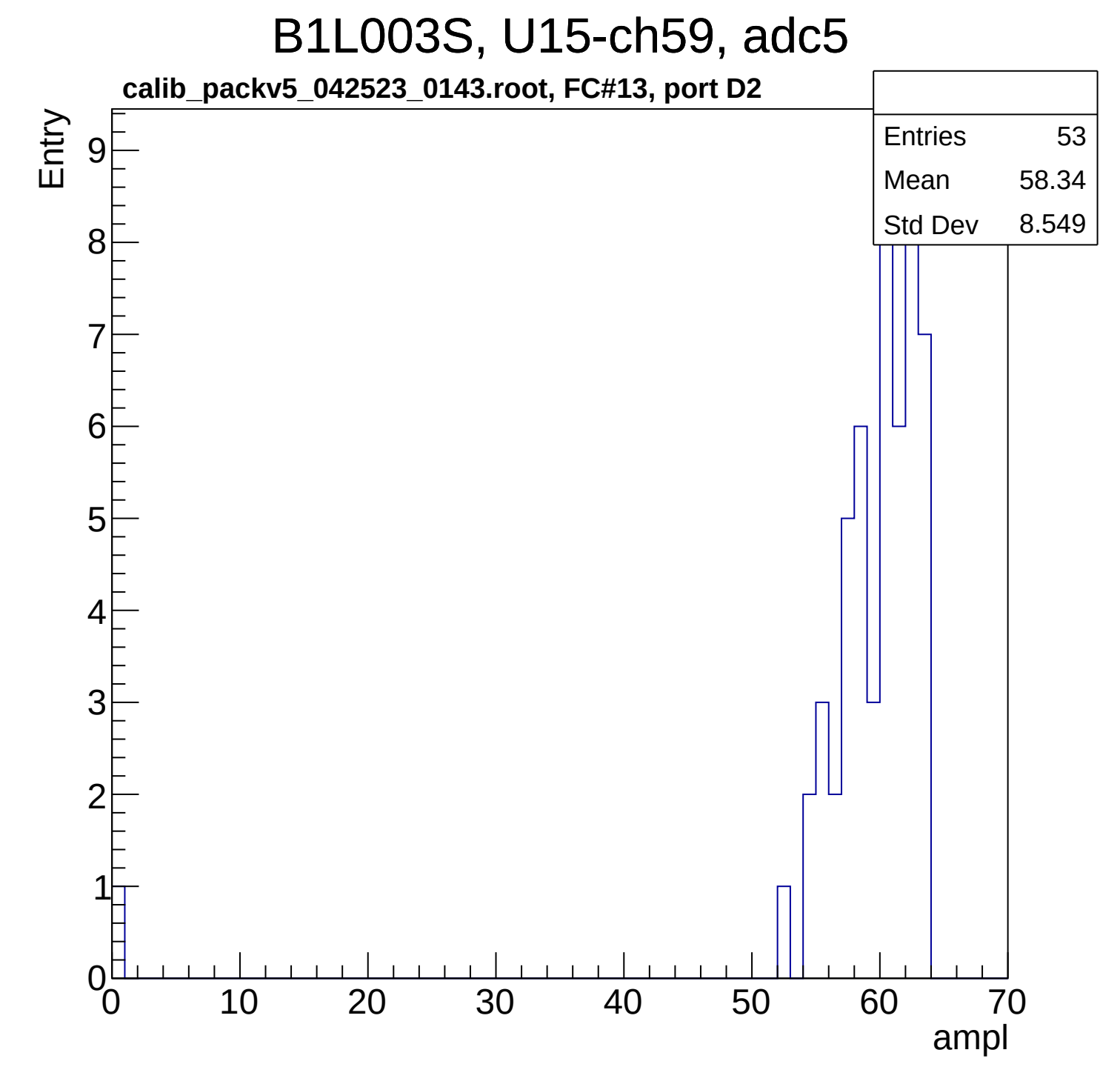
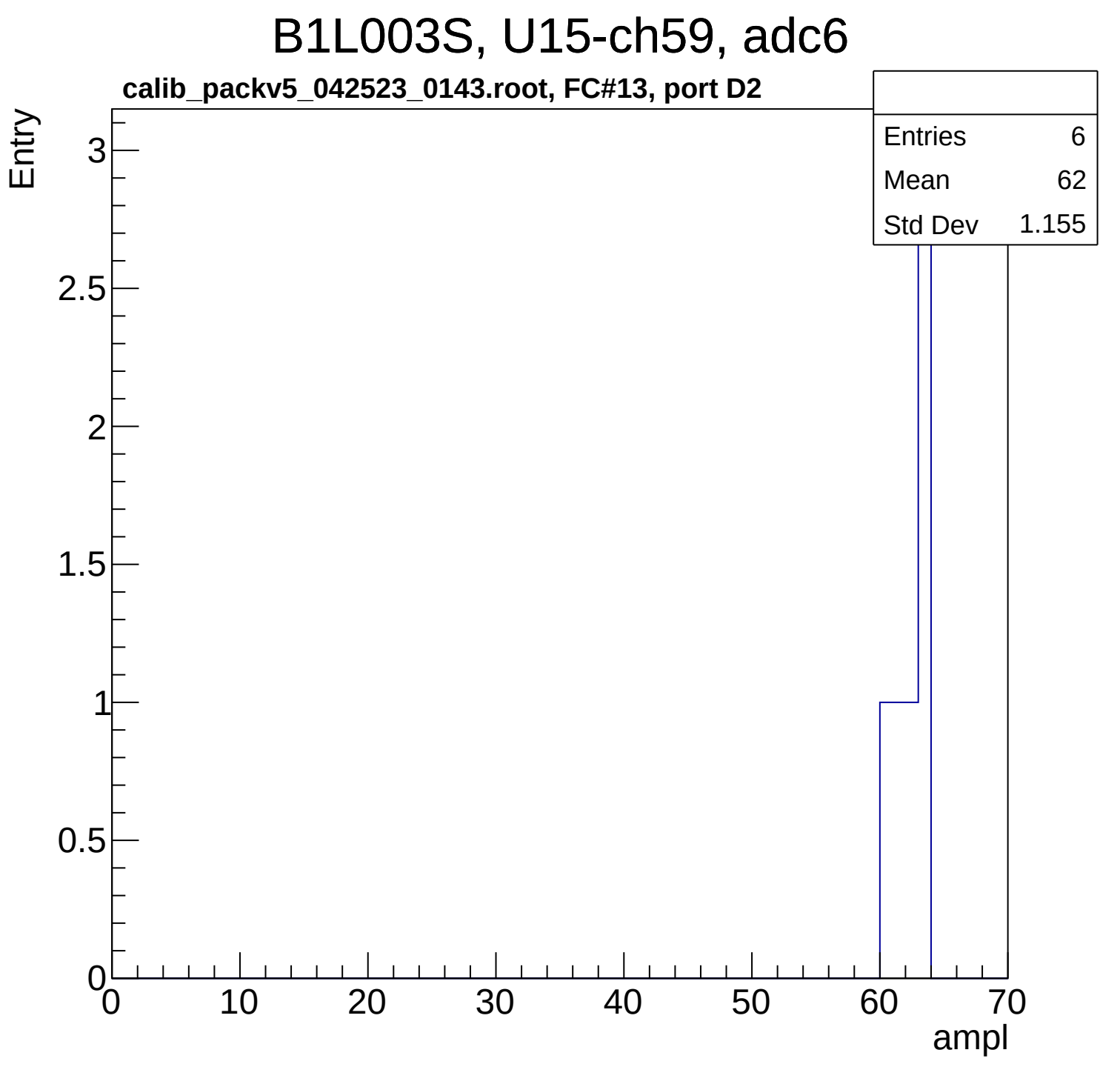
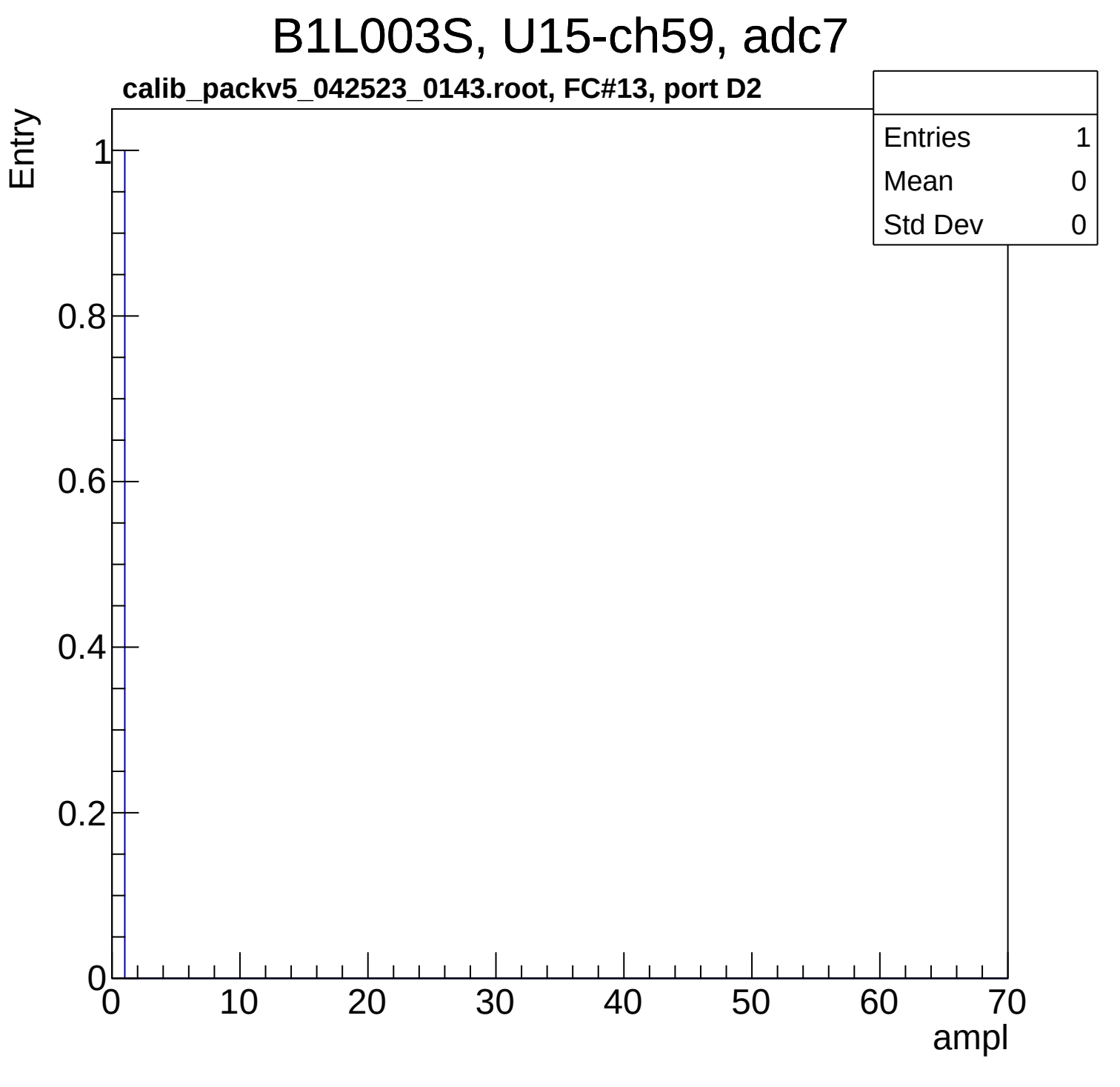
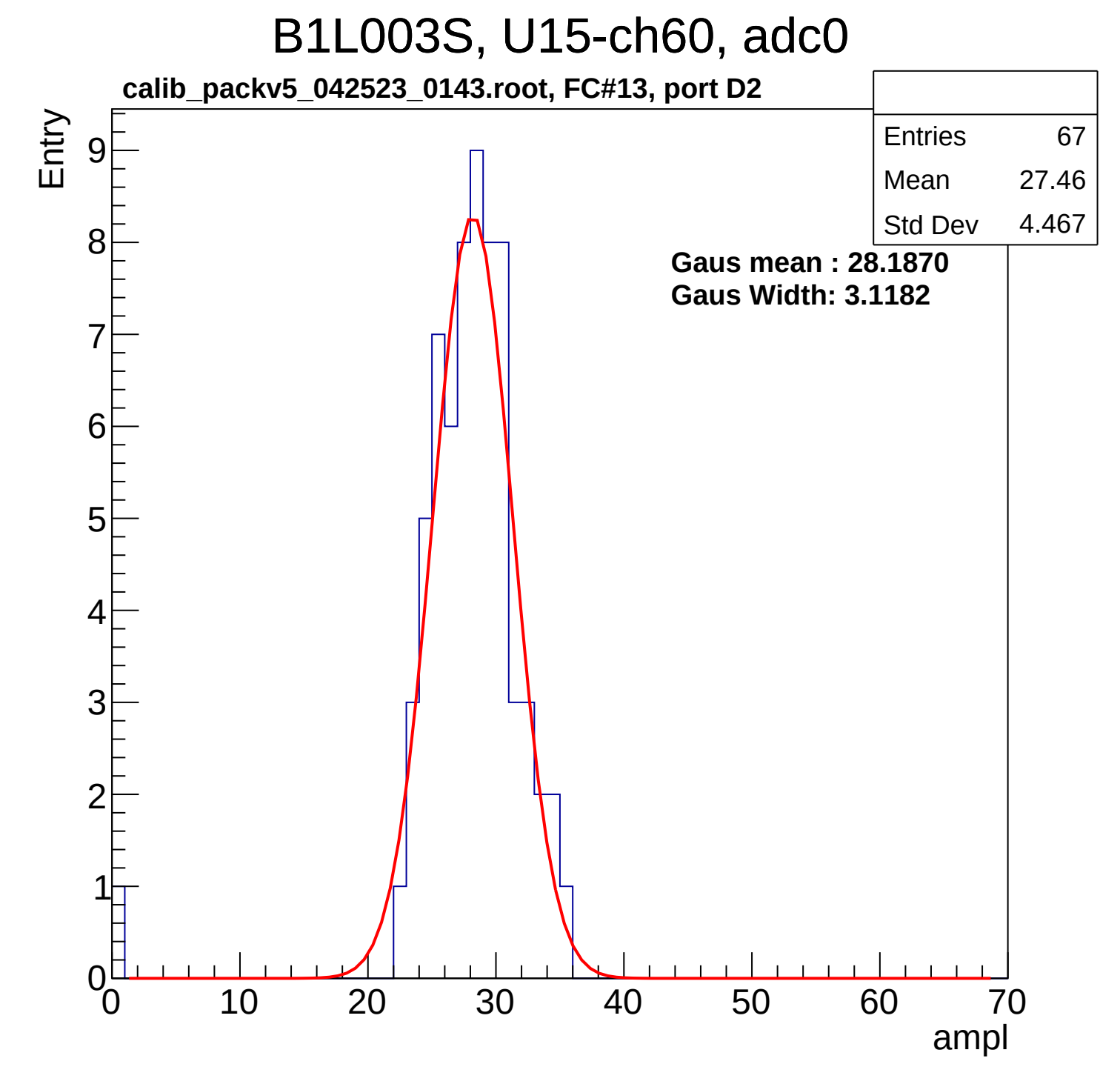
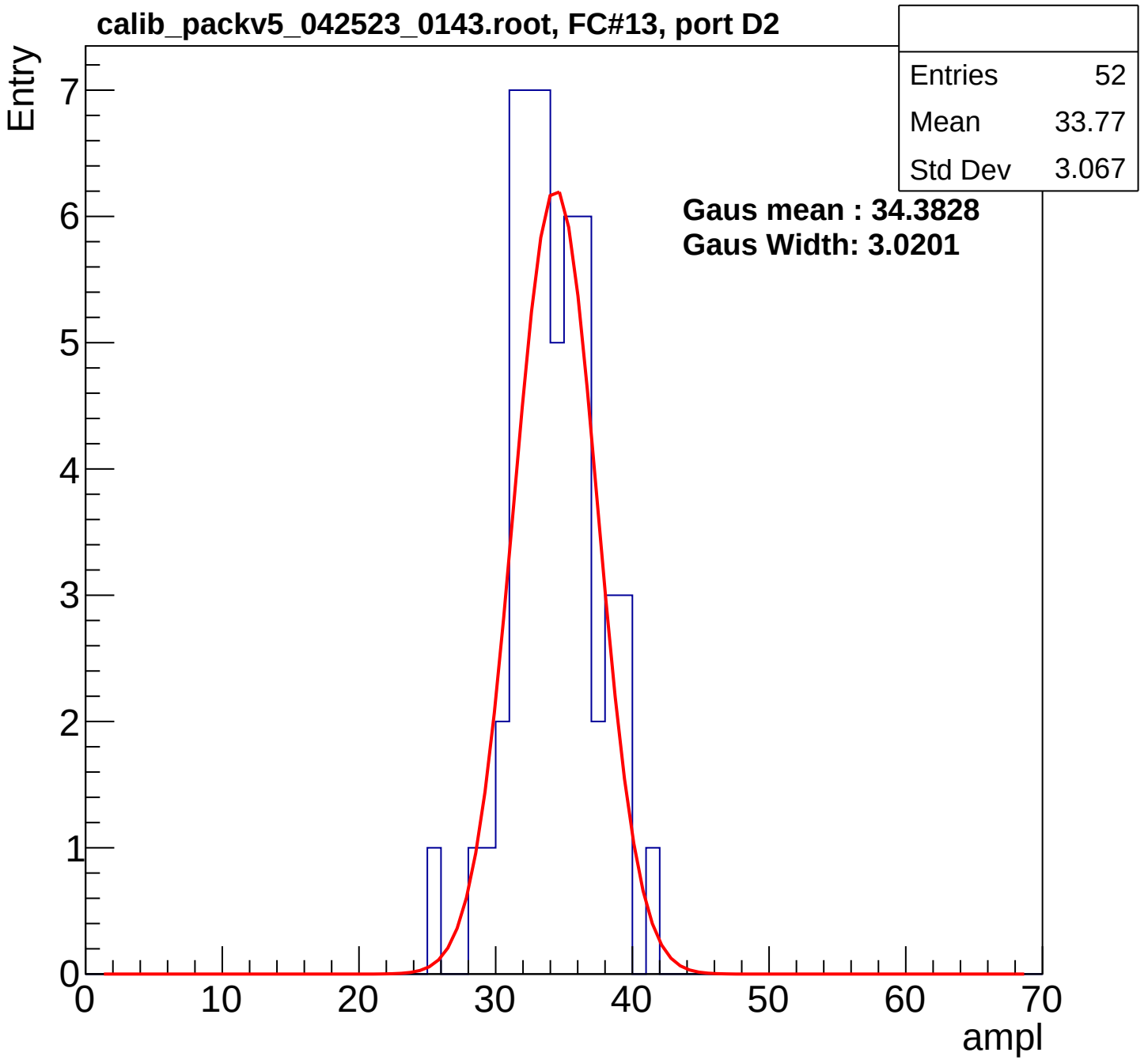

B1L003S, U15-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.34
Std Dev 8.549
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62
Std Dev 1.155
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 27.46
Std Dev 4.467
Gaus mean : 28.1870
Gaus Width: 3.1182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 33.77
Std Dev 3.067
Gaus mean : 34.3828
Gaus Width: 3.0201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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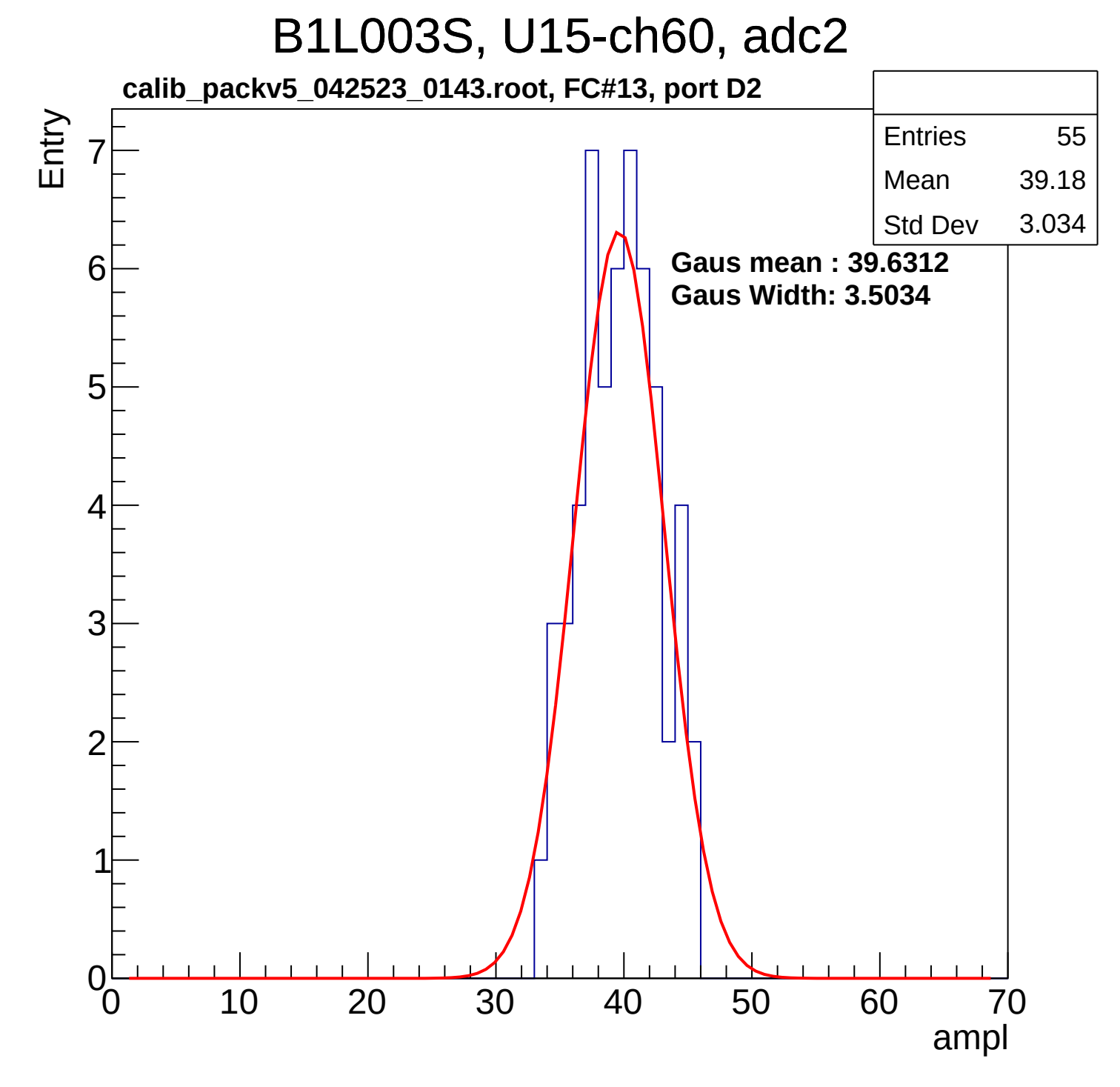
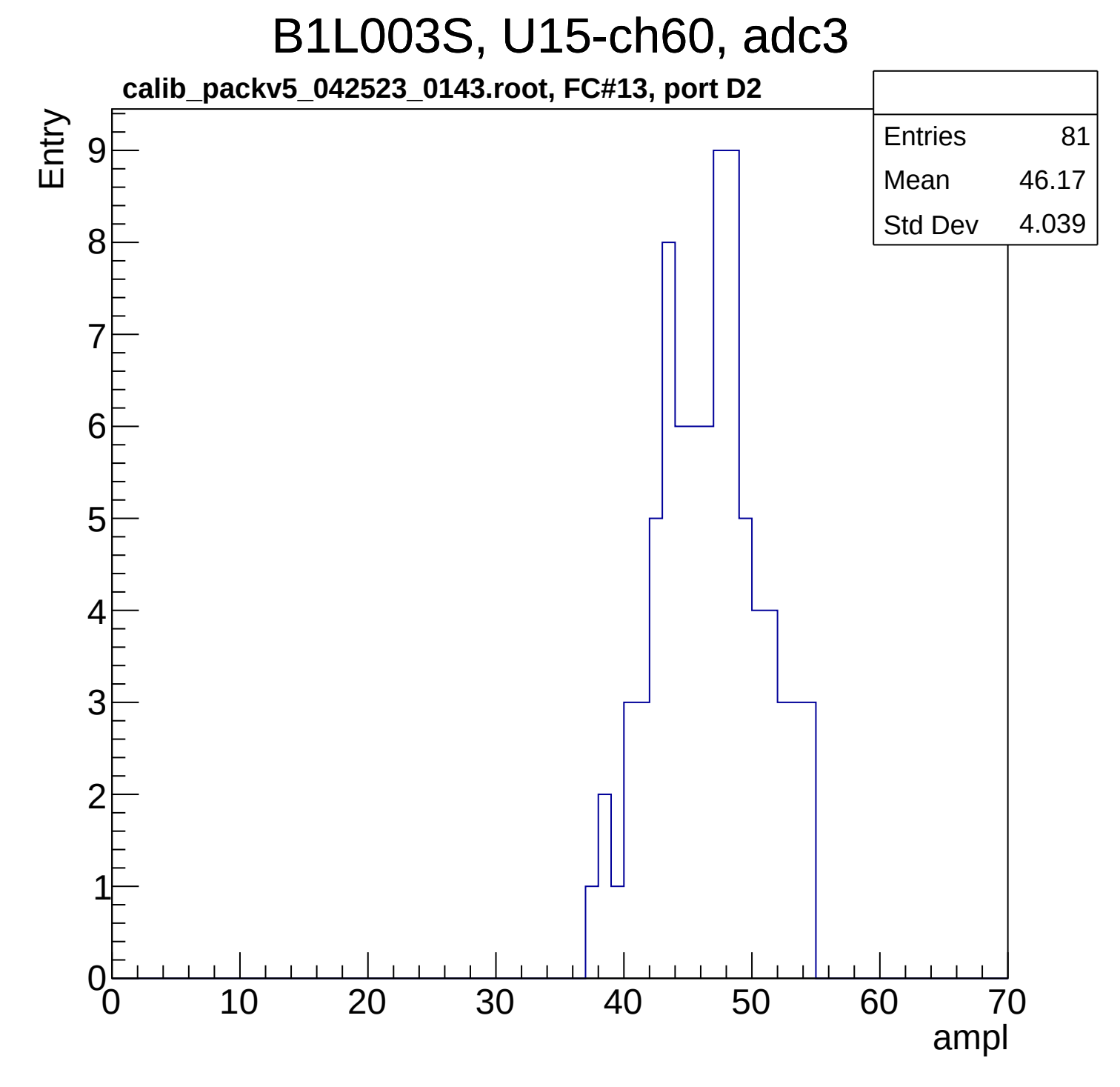

B1L003S, U15-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 39.18
Std Dev 3.034
Gaus mean : 39.6312
Gaus Width: 3.5034
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 46.17
Std Dev 4.039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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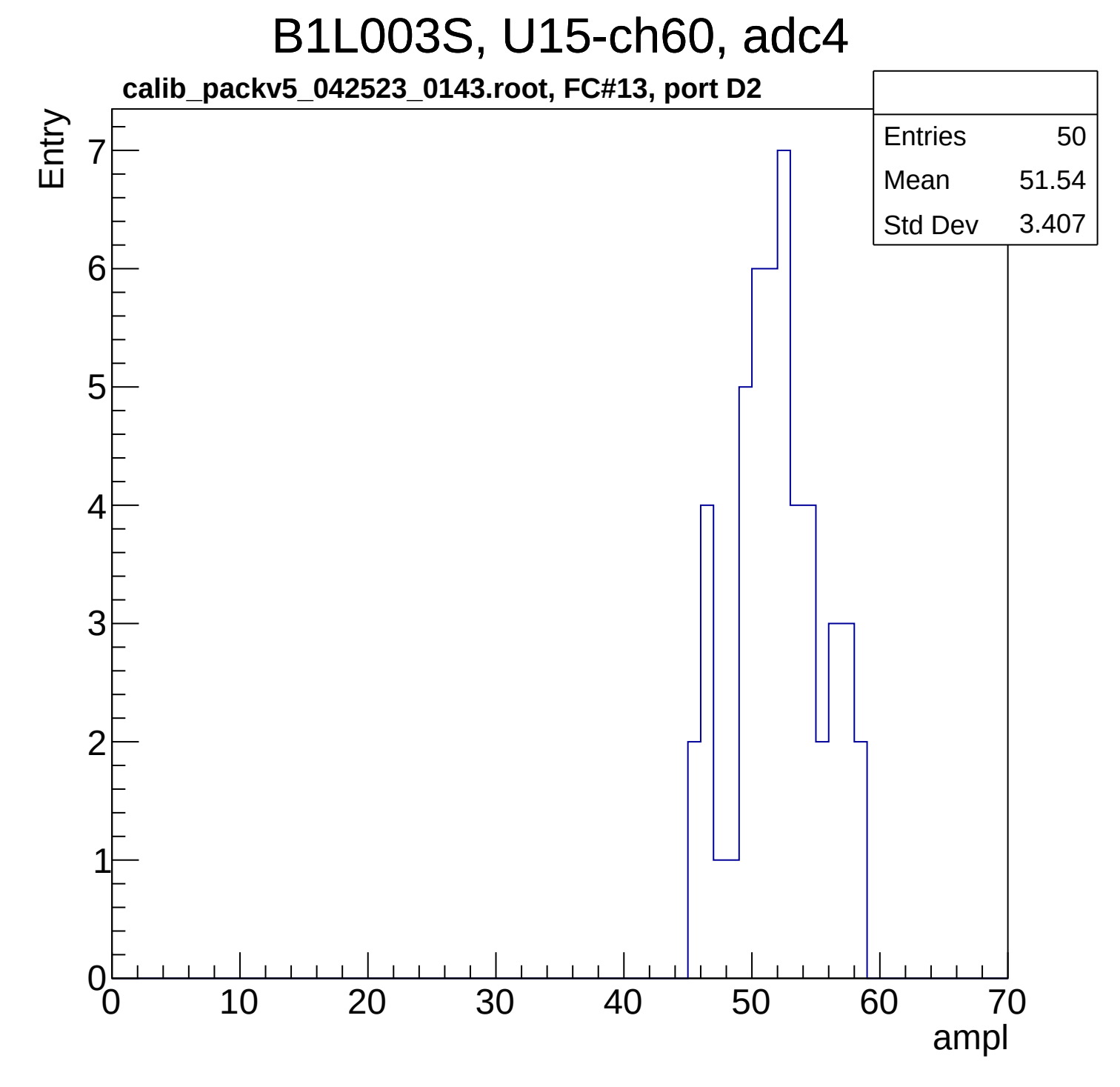
40
50
60
70


B1L003S, U15-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 51.54
Std Dev 3.407
Entry
ampl
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1
2
3
4
5
6
7
0
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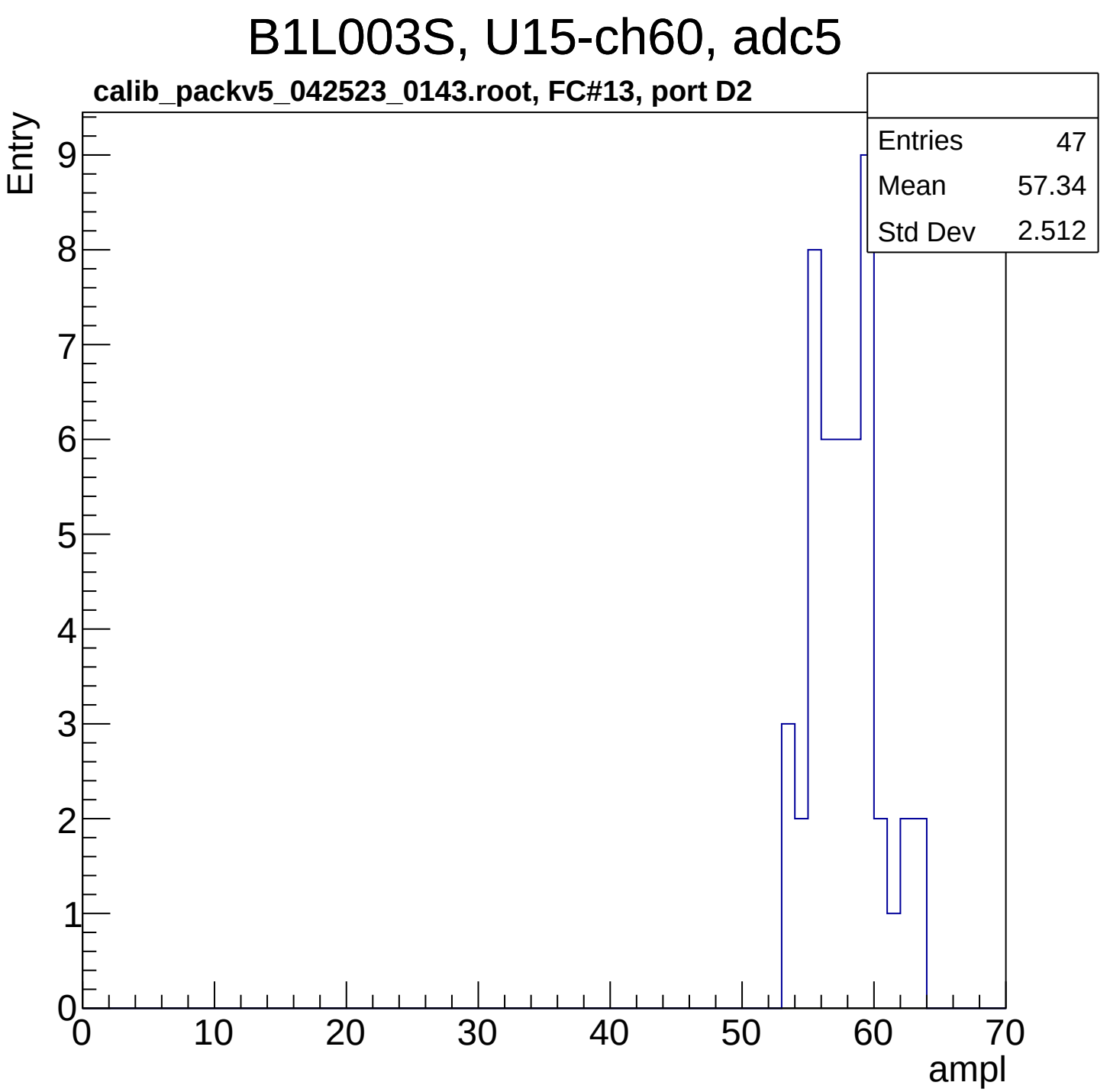
20
30
40
50
60
70


B1L003S, U15-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 57.34
Std Dev 2.512
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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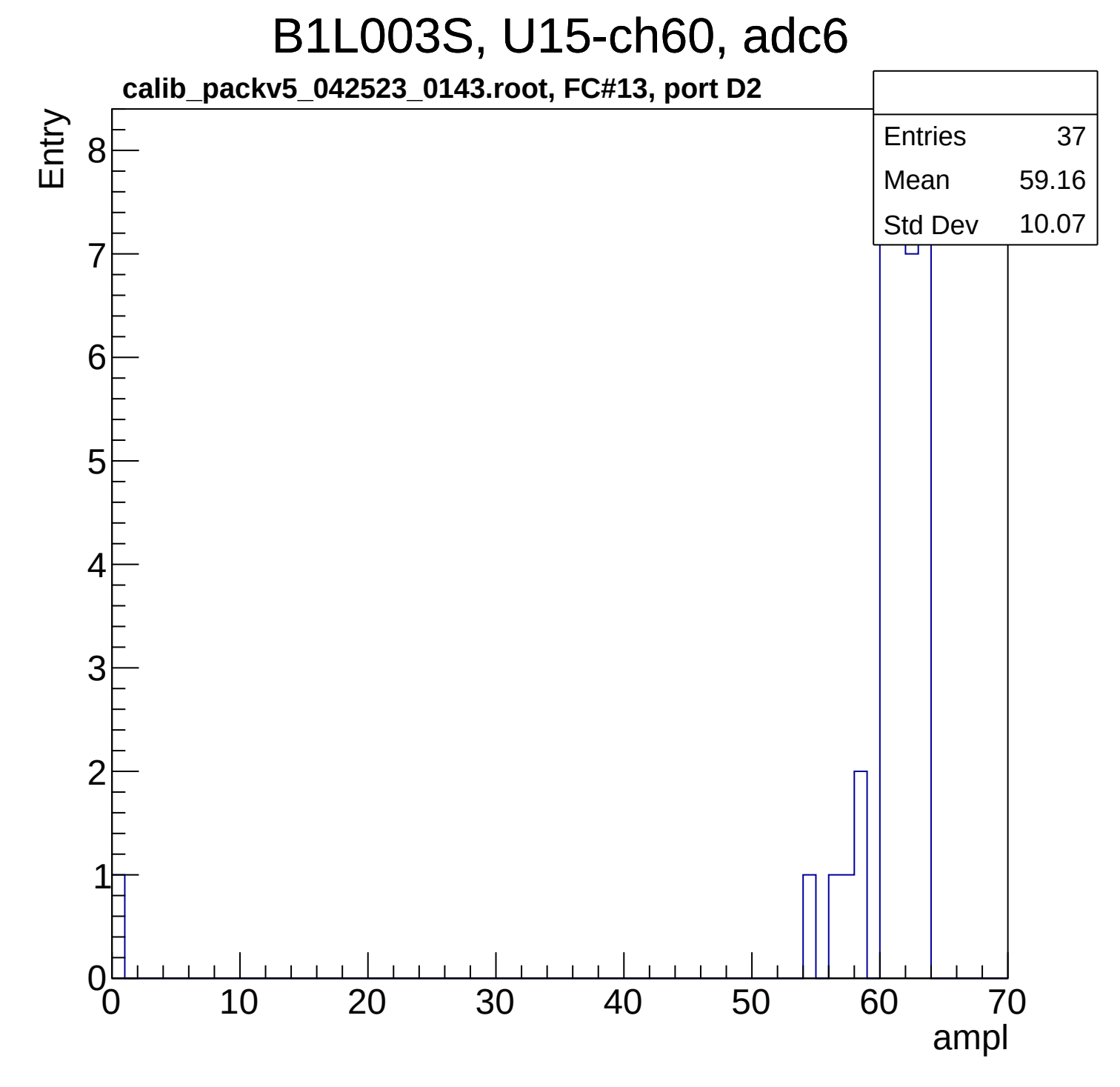
B1L003S, U15-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 59.16
Std Dev 10.07
Entry
ampl
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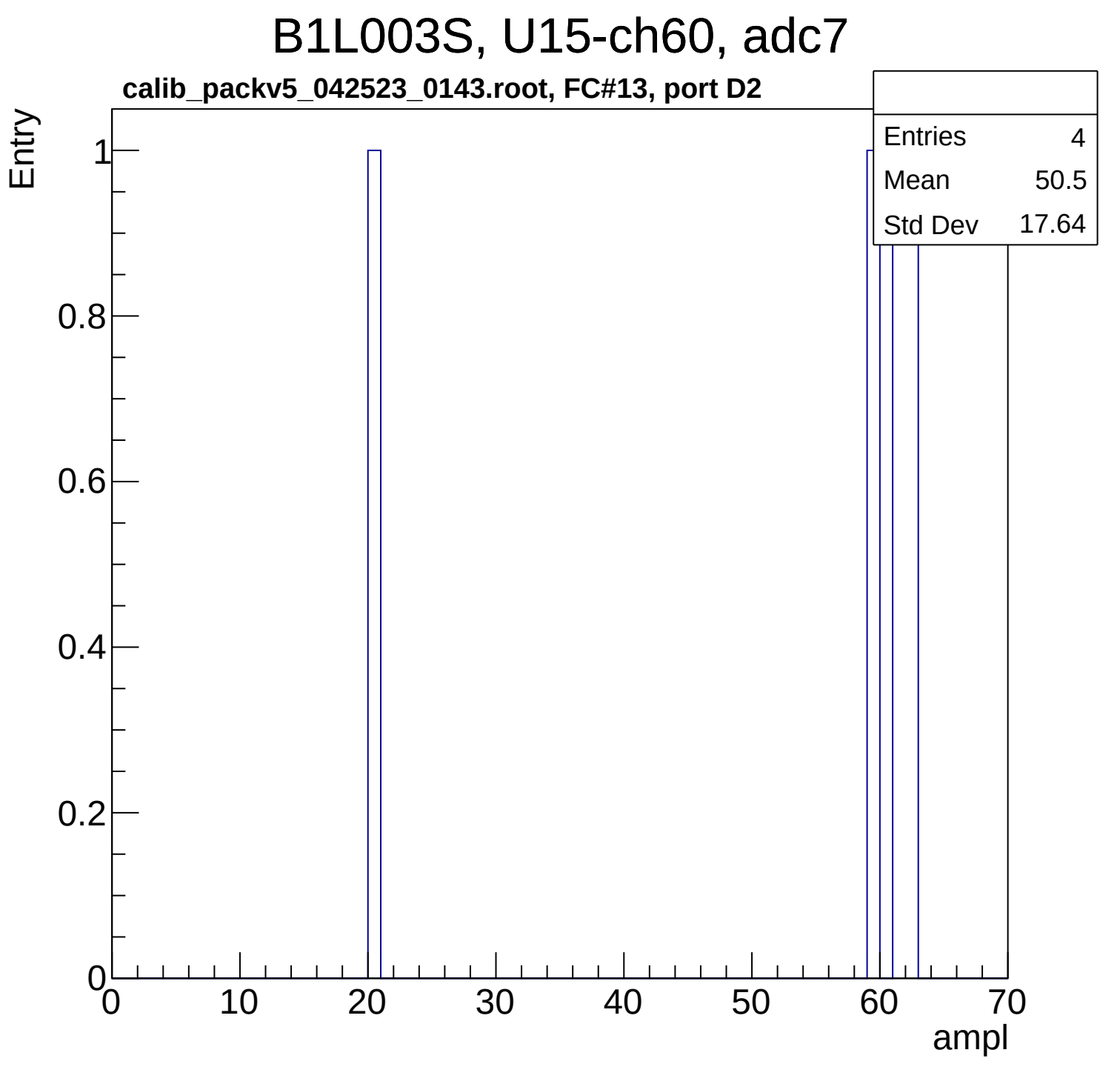
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 50.5
Std Dev 17.64
Entry
ampl
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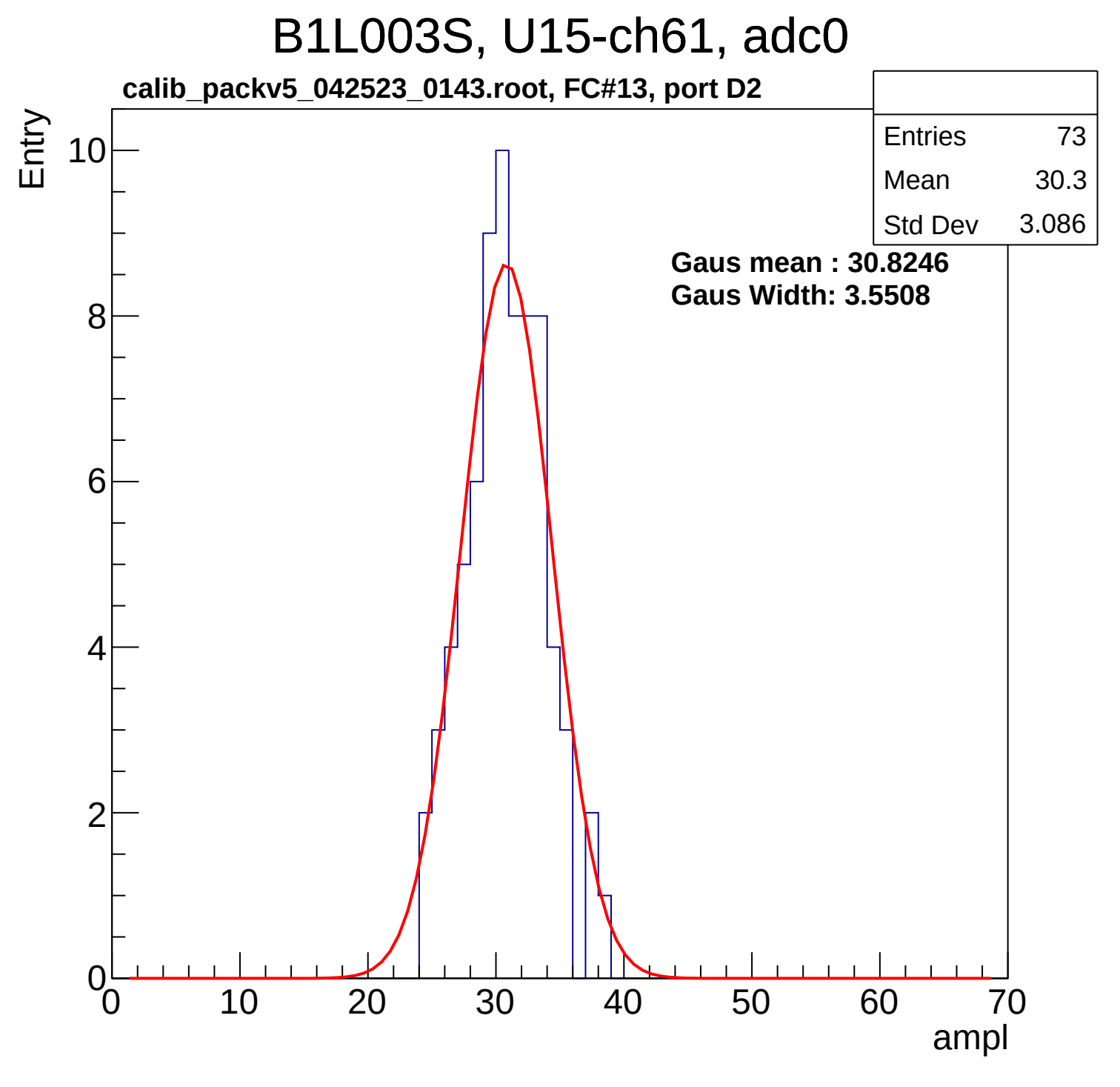
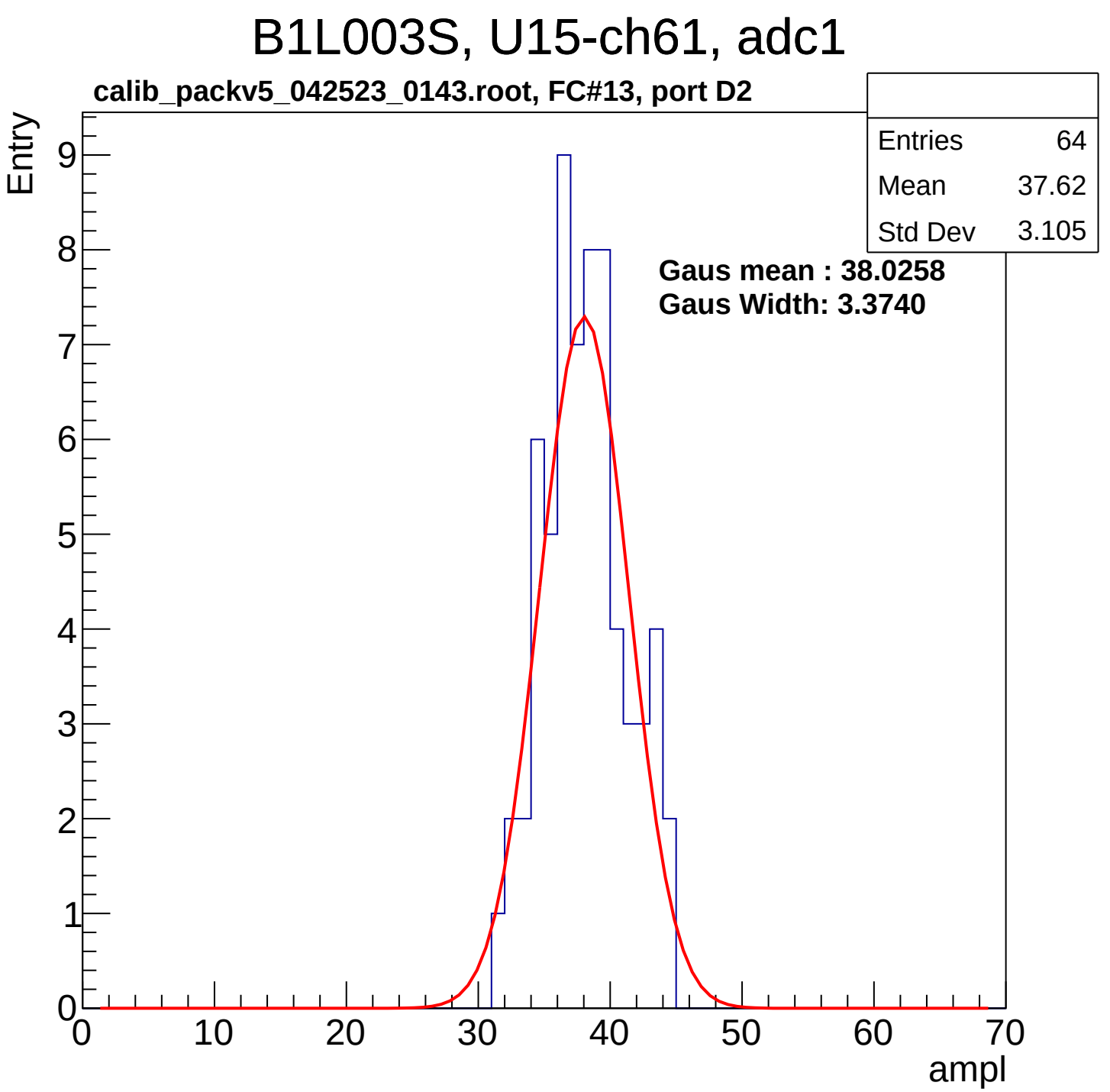
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L003S, U15-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.3
Std Dev 3.086
Gaus mean : 30.8246
Gaus Width: 3.5508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 37.62
Std Dev 3.105
Gaus mean : 38.0258
Gaus Width: 3.3740
Entry
ampl
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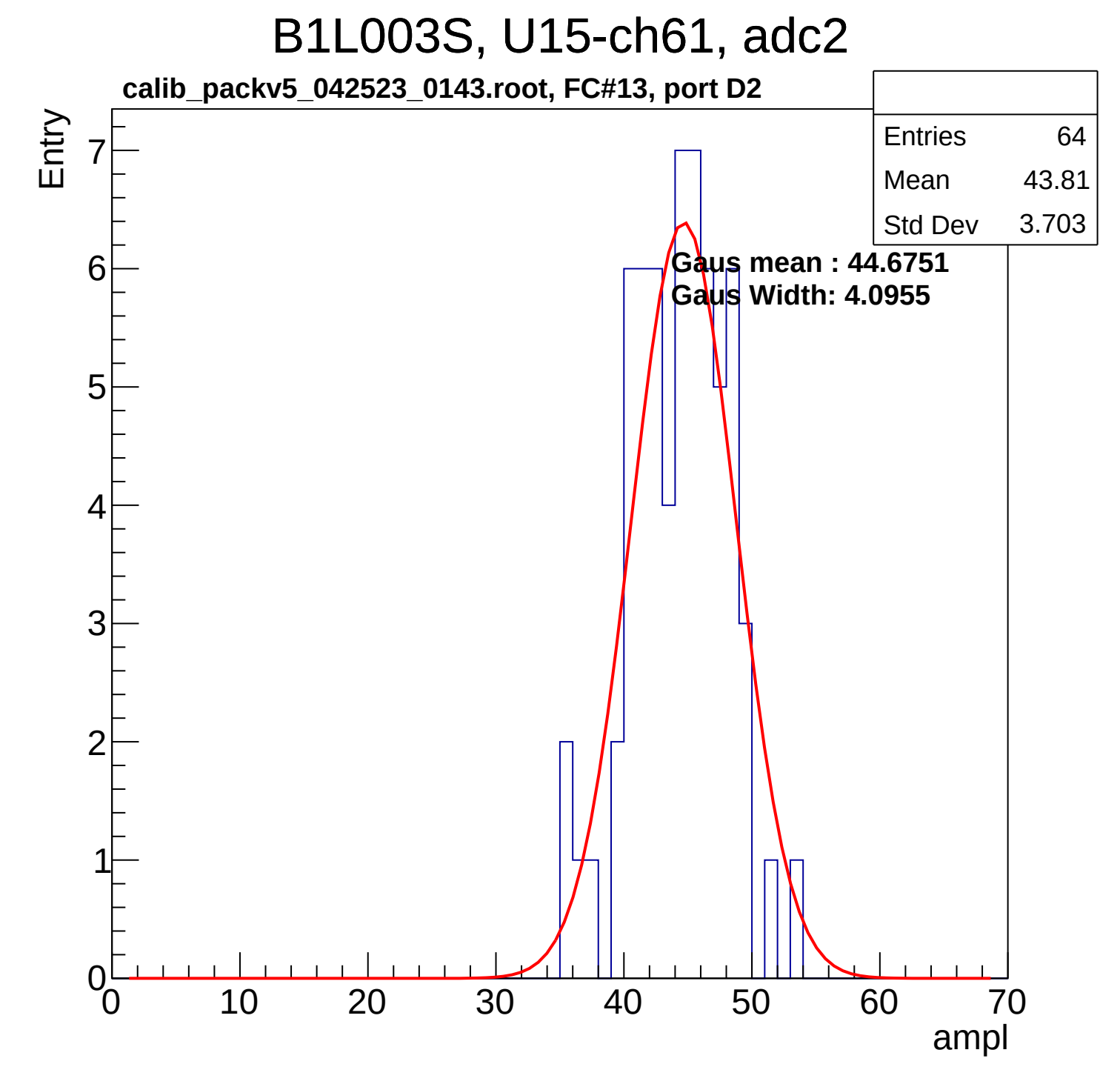
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.81
Std Dev 3.703
Gaus mean : 44.6751
Gaus Width: 4.0955
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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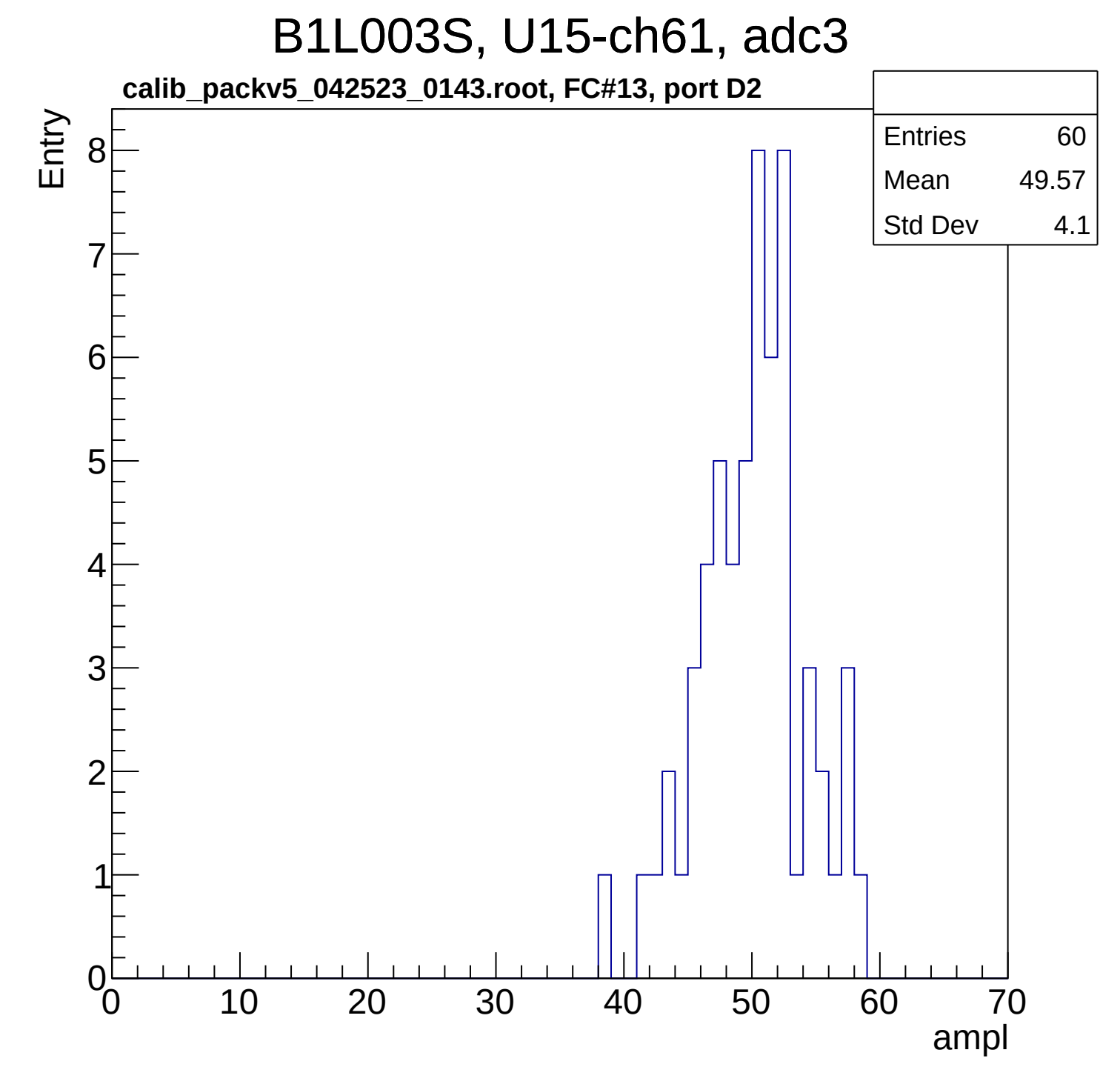
B1L003S, U15-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.57
Std Dev 4.1
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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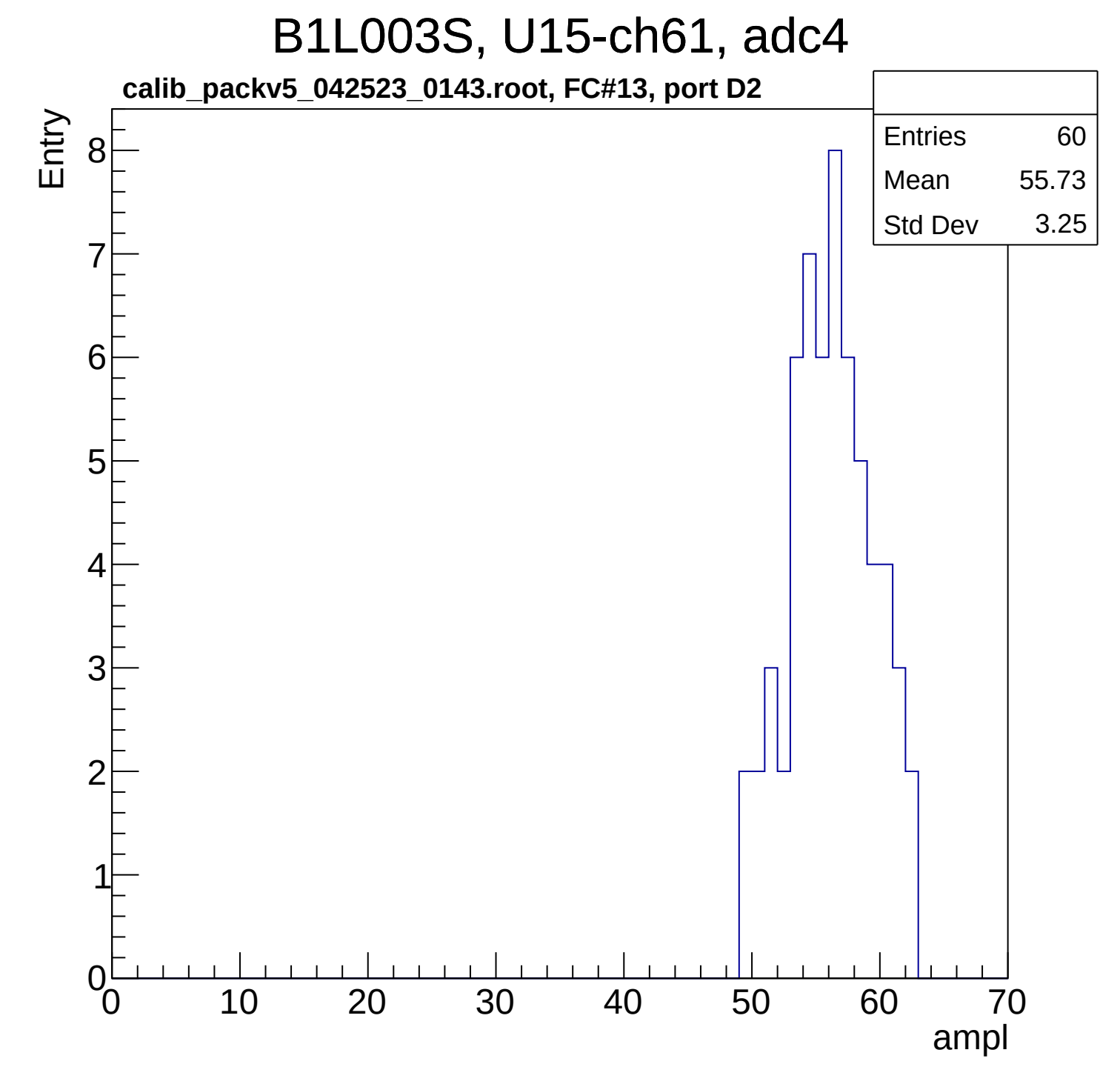
B1L003S, U15-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.73
Std Dev 3.25
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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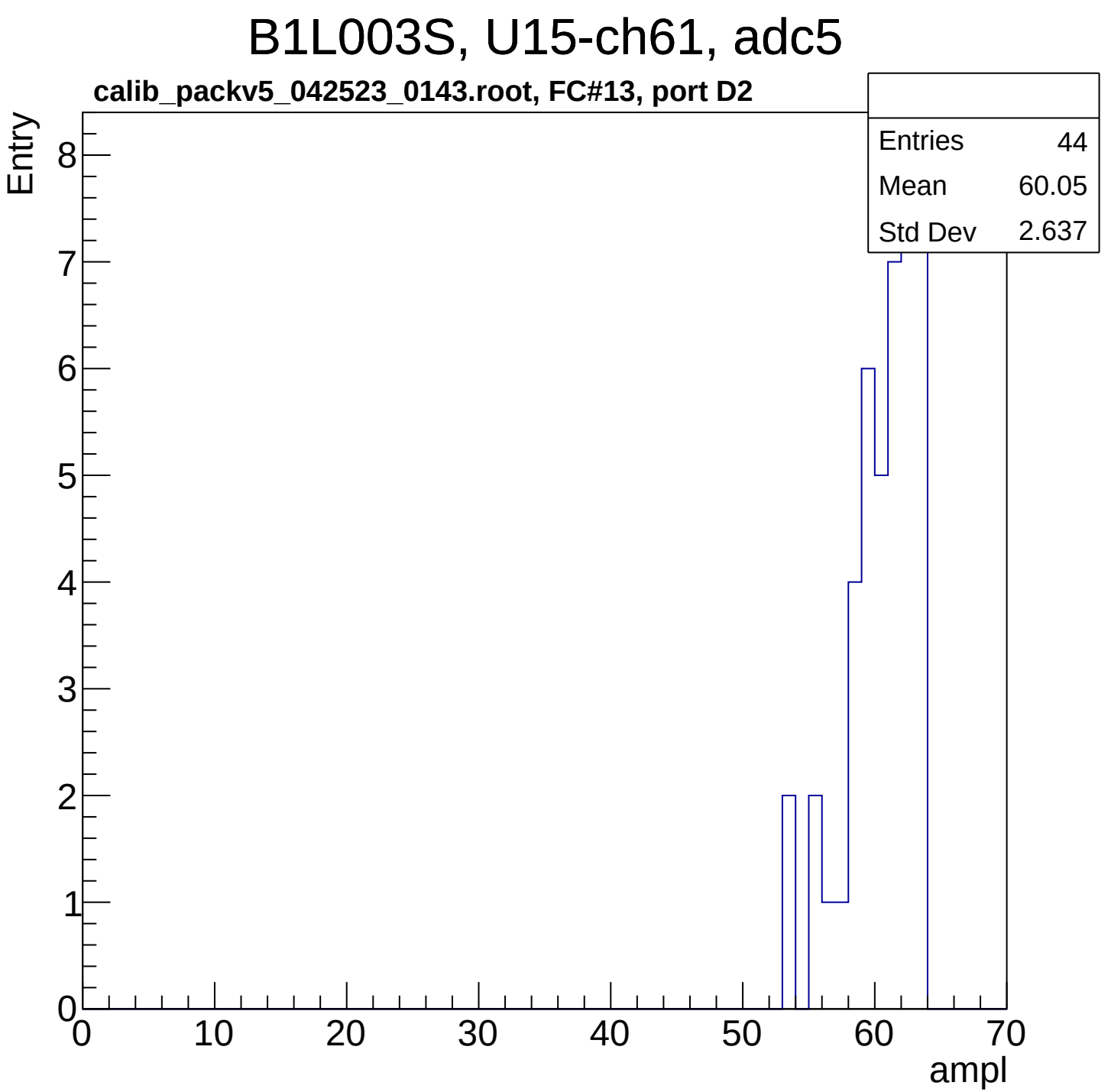
B1L003S, U15-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 60.05
Std Dev 2.637
Entry
0
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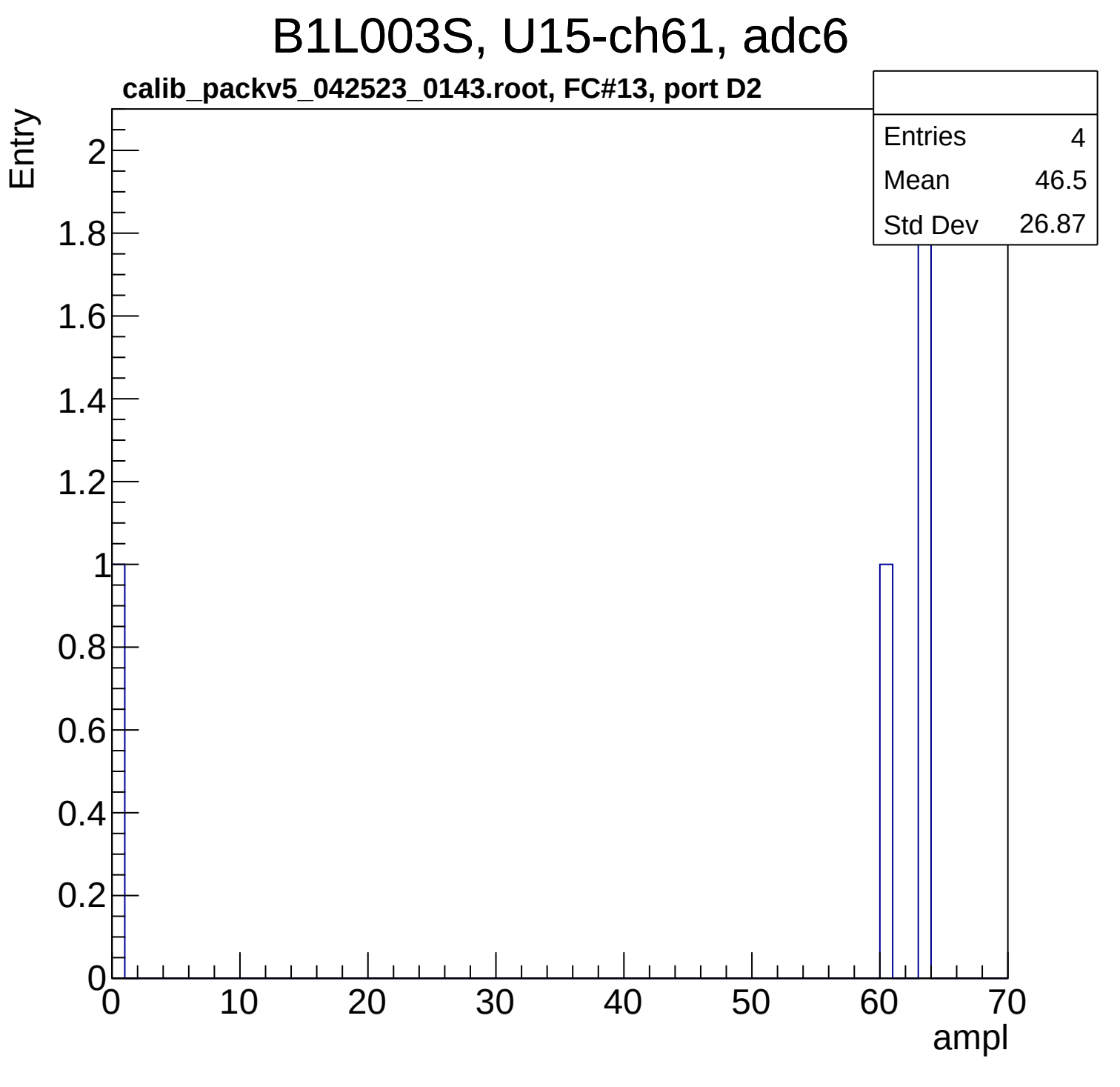
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 46.5
Std Dev 26.87
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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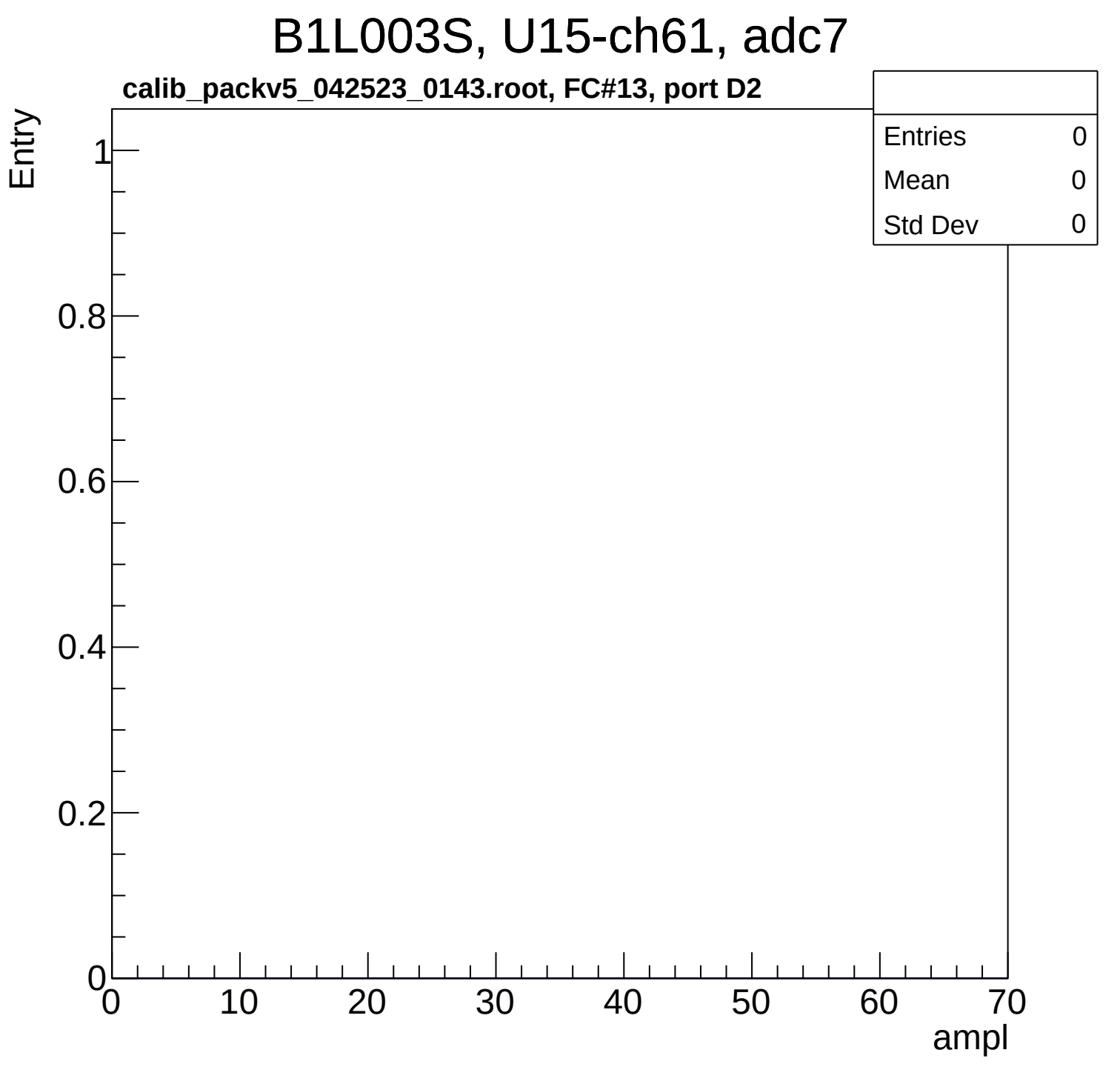
B1L003S, U15-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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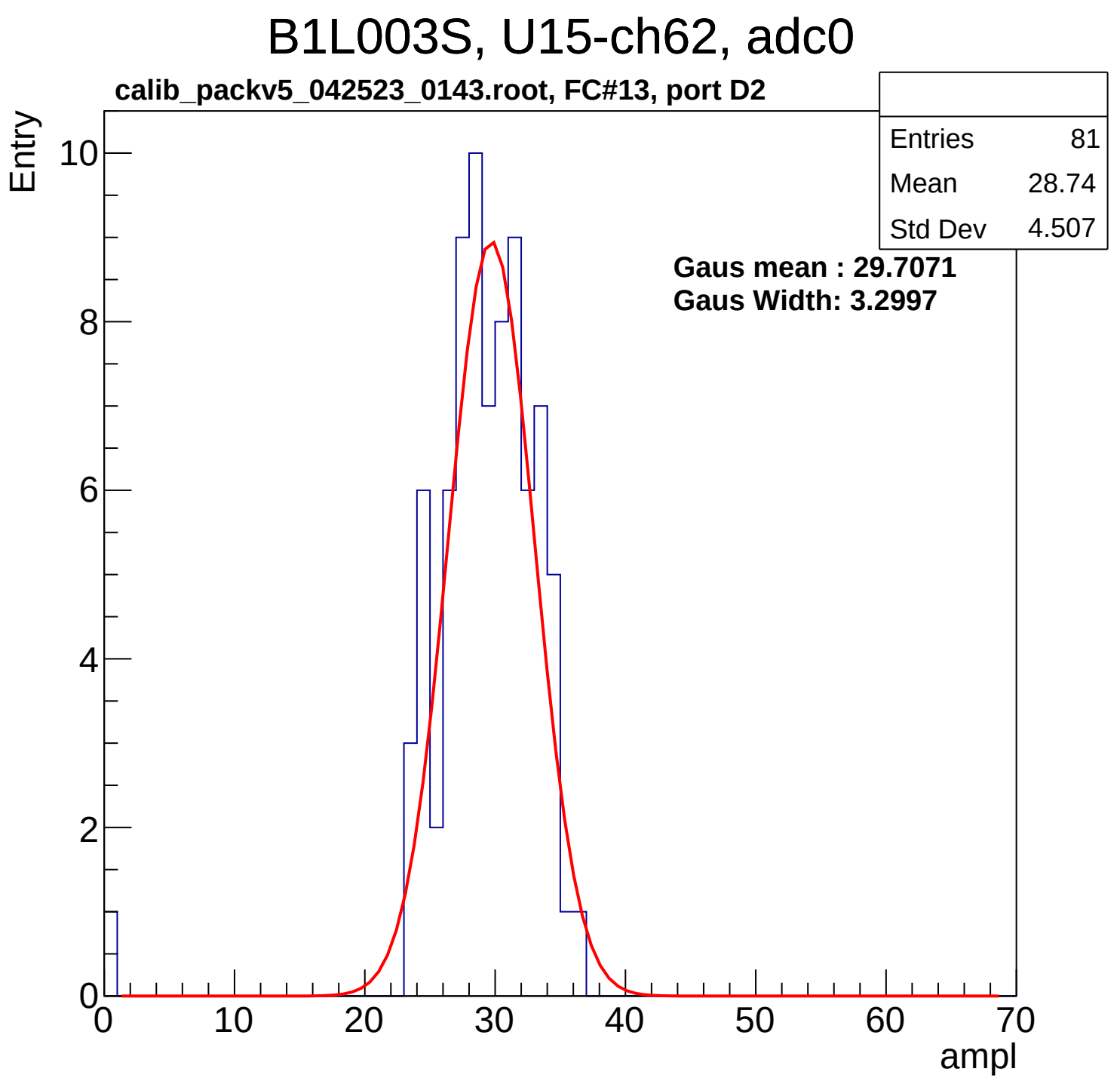
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.74
Std Dev 4.507
Gaus mean : 29.7071
Gaus Width: 3.2997
Entry
ampl
0
2
4
6
8
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0
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20
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60
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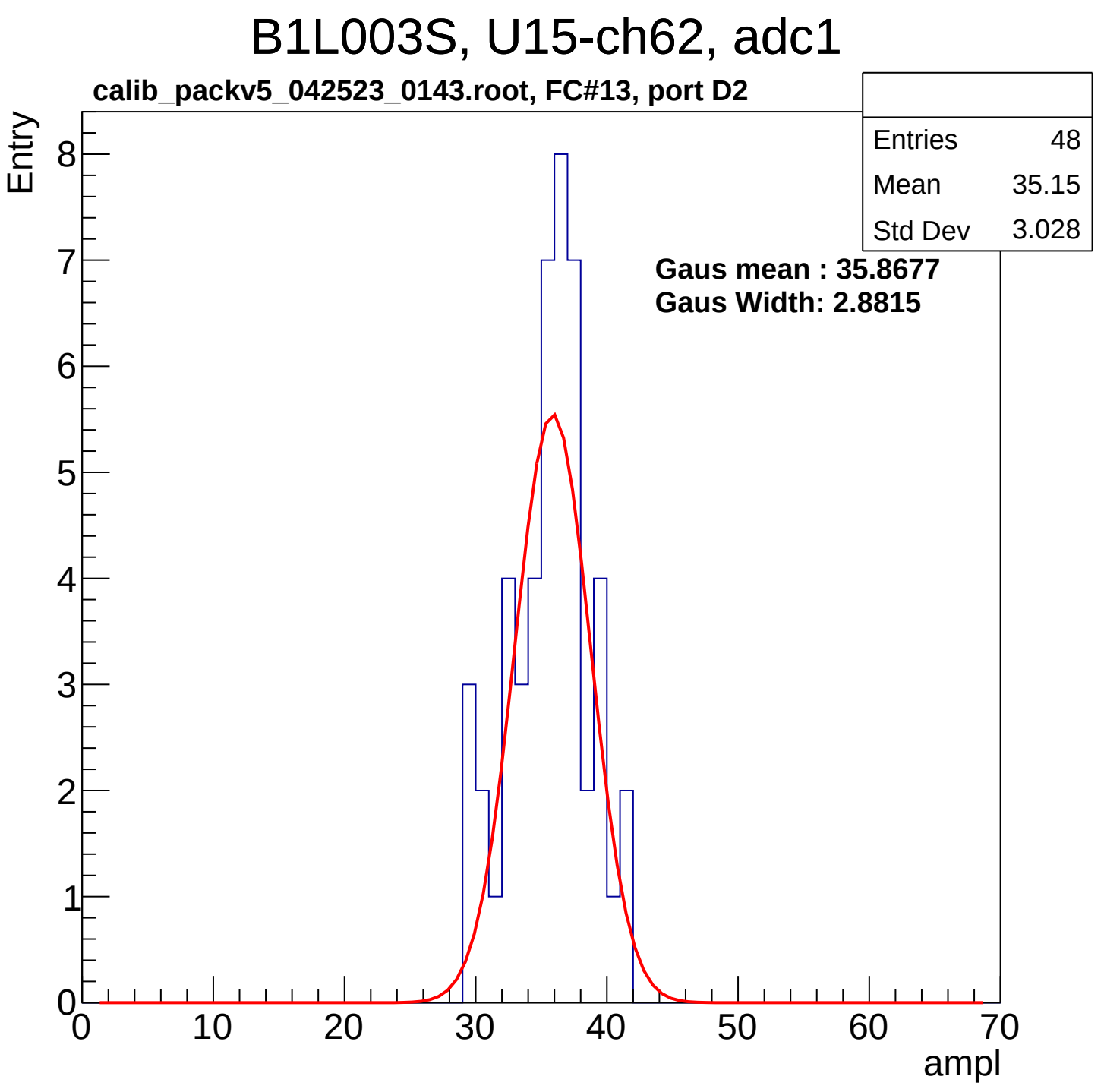
B1L003S, U15-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 35.15
Std Dev 3.028
Gaus mean : 35.8677
Gaus Width: 2.8815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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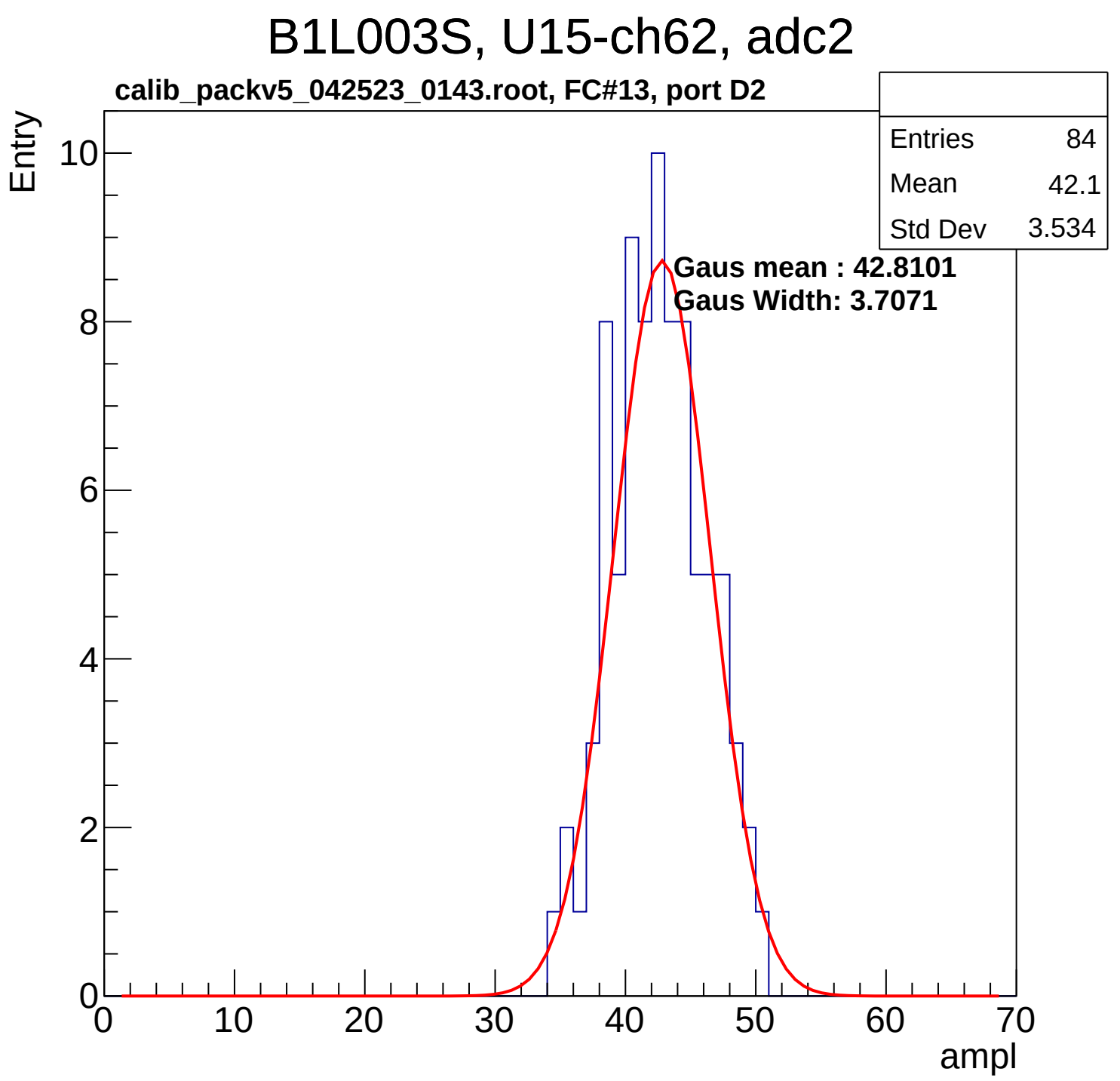
B1L003S, U15-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 42.1
Std Dev 3.534
Gaus mean : 42.8101
Gaus Width: 3.7071
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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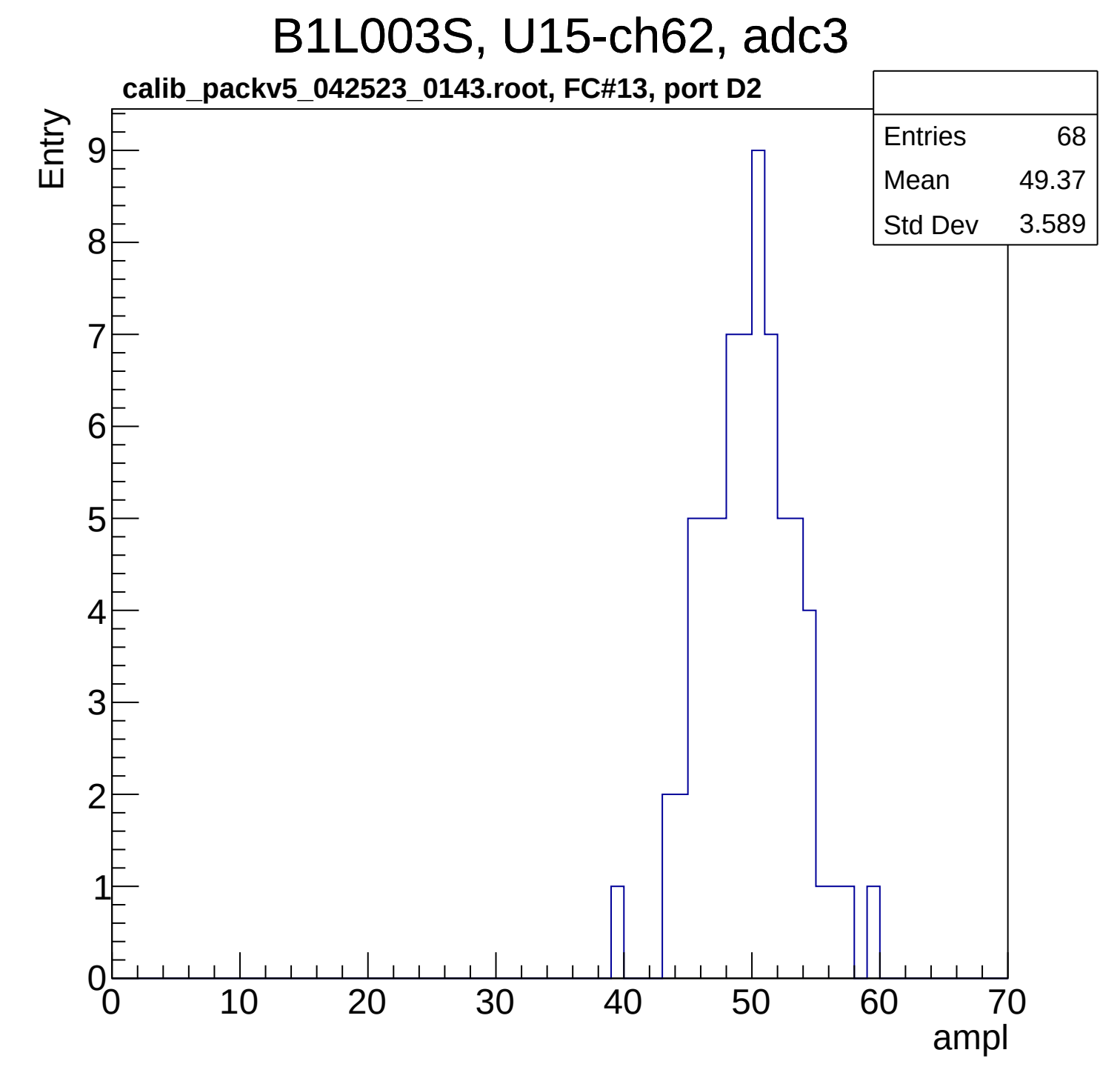
B1L003S, U15-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.37
Std Dev 3.589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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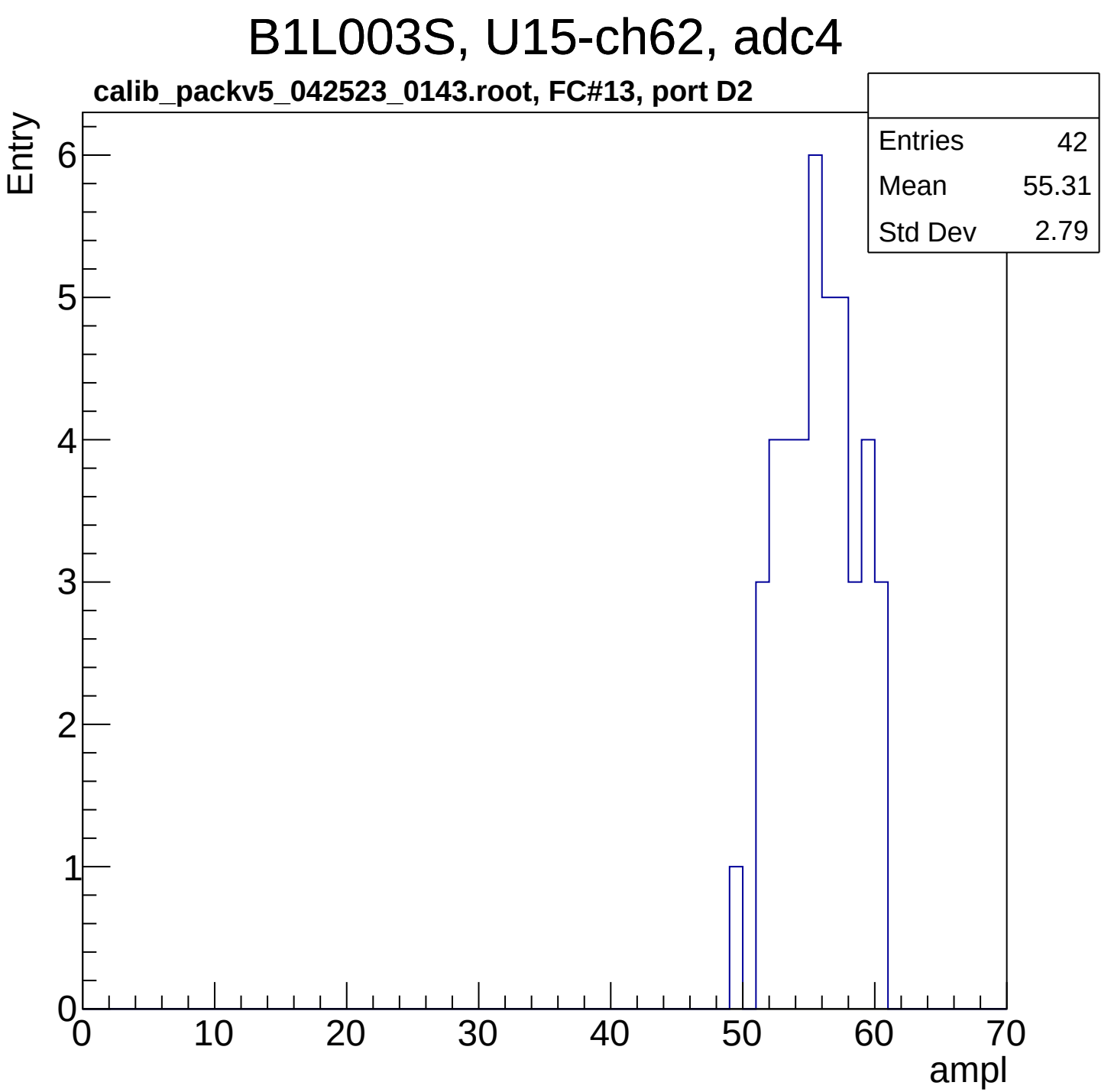
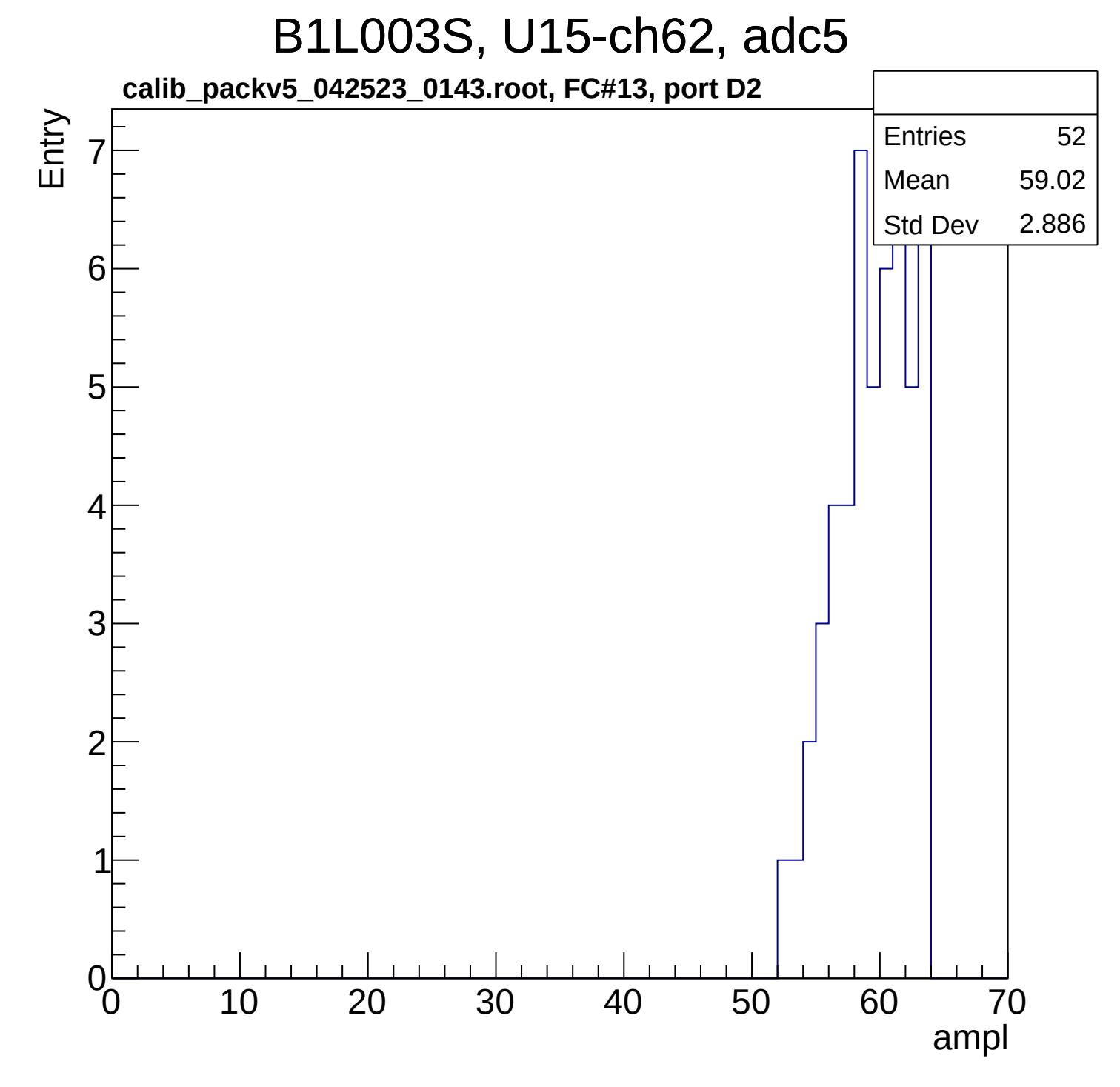

B1L003S, U15-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 55.31
Std Dev 2.79
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.02
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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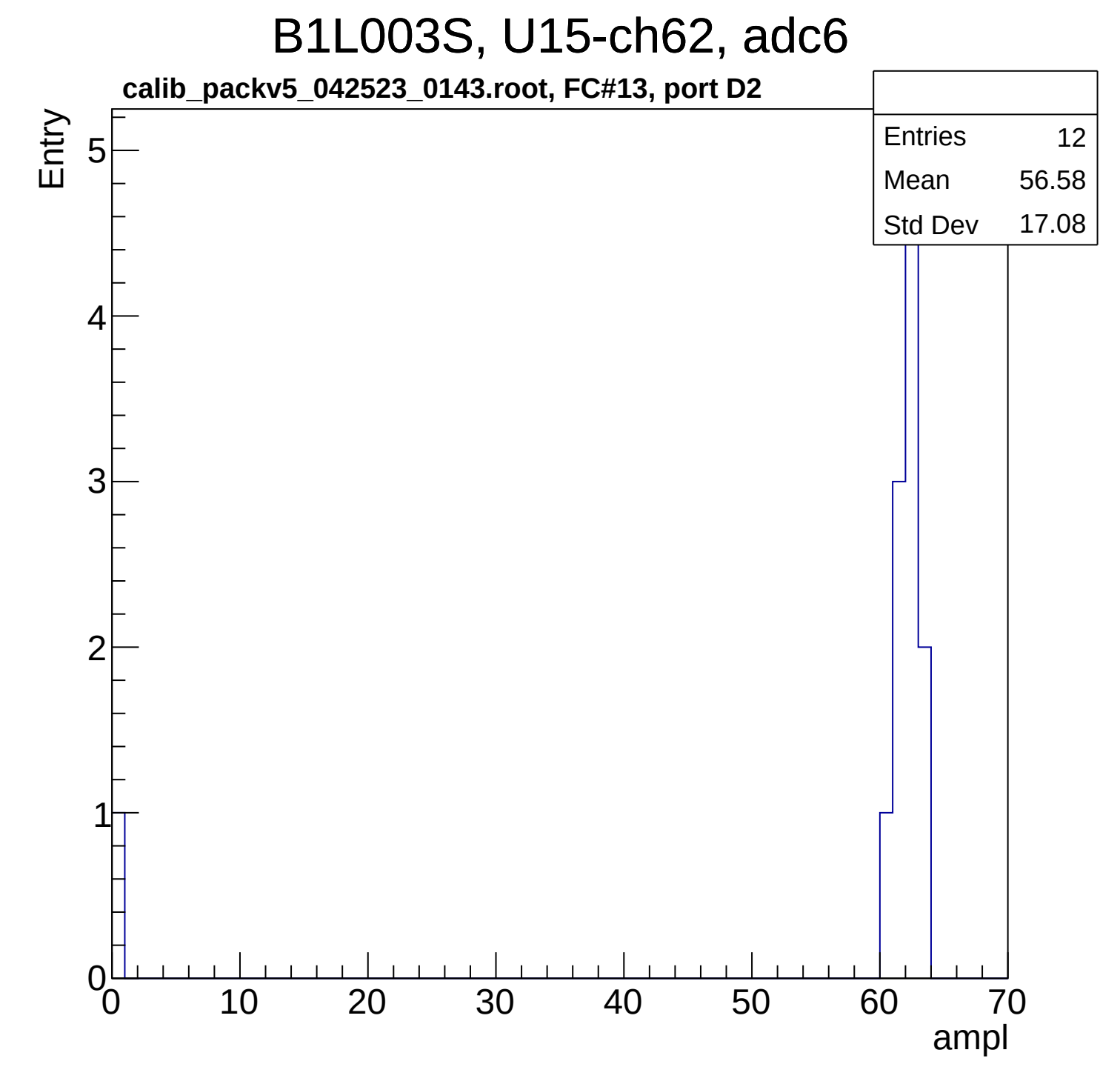
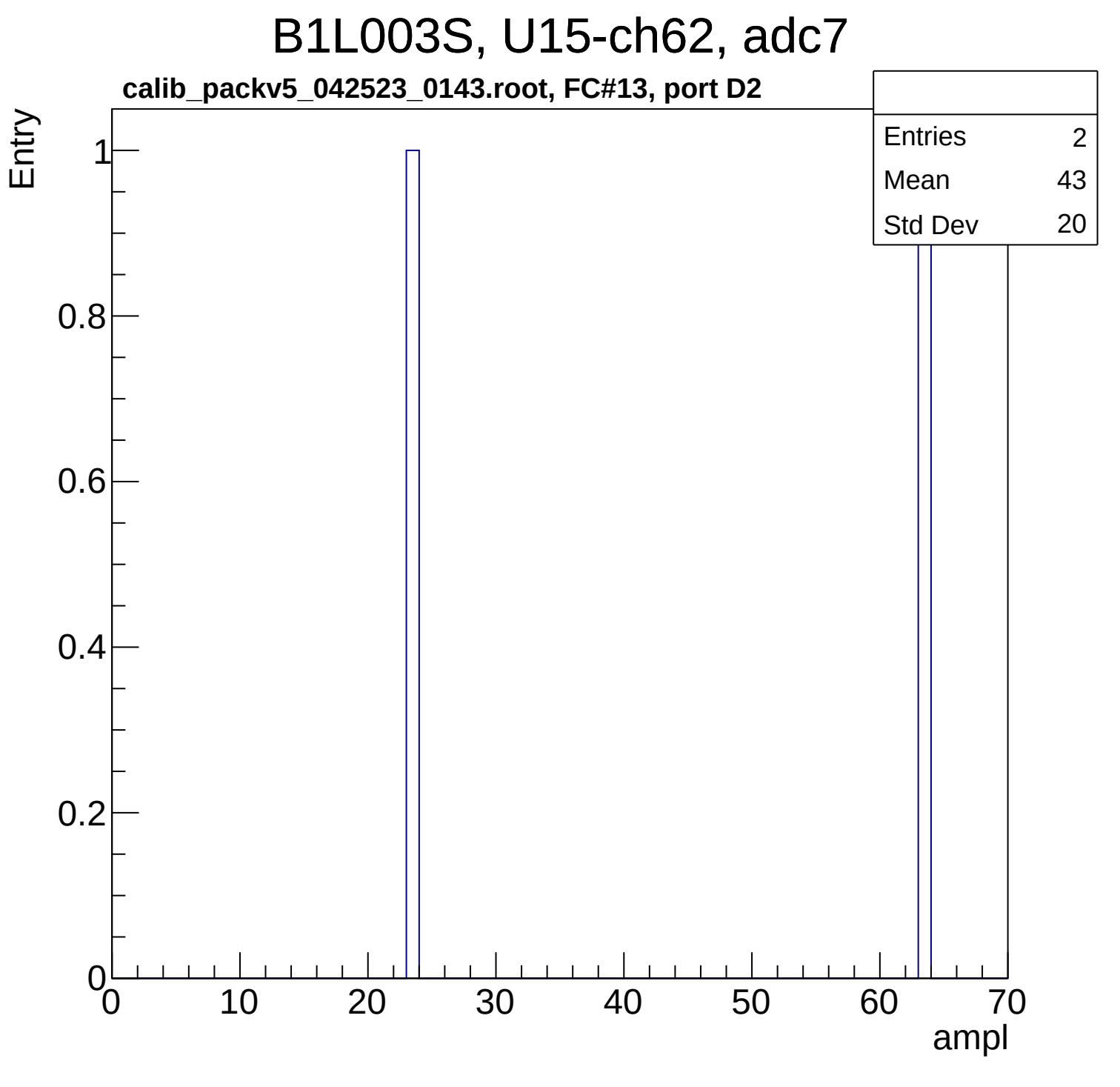
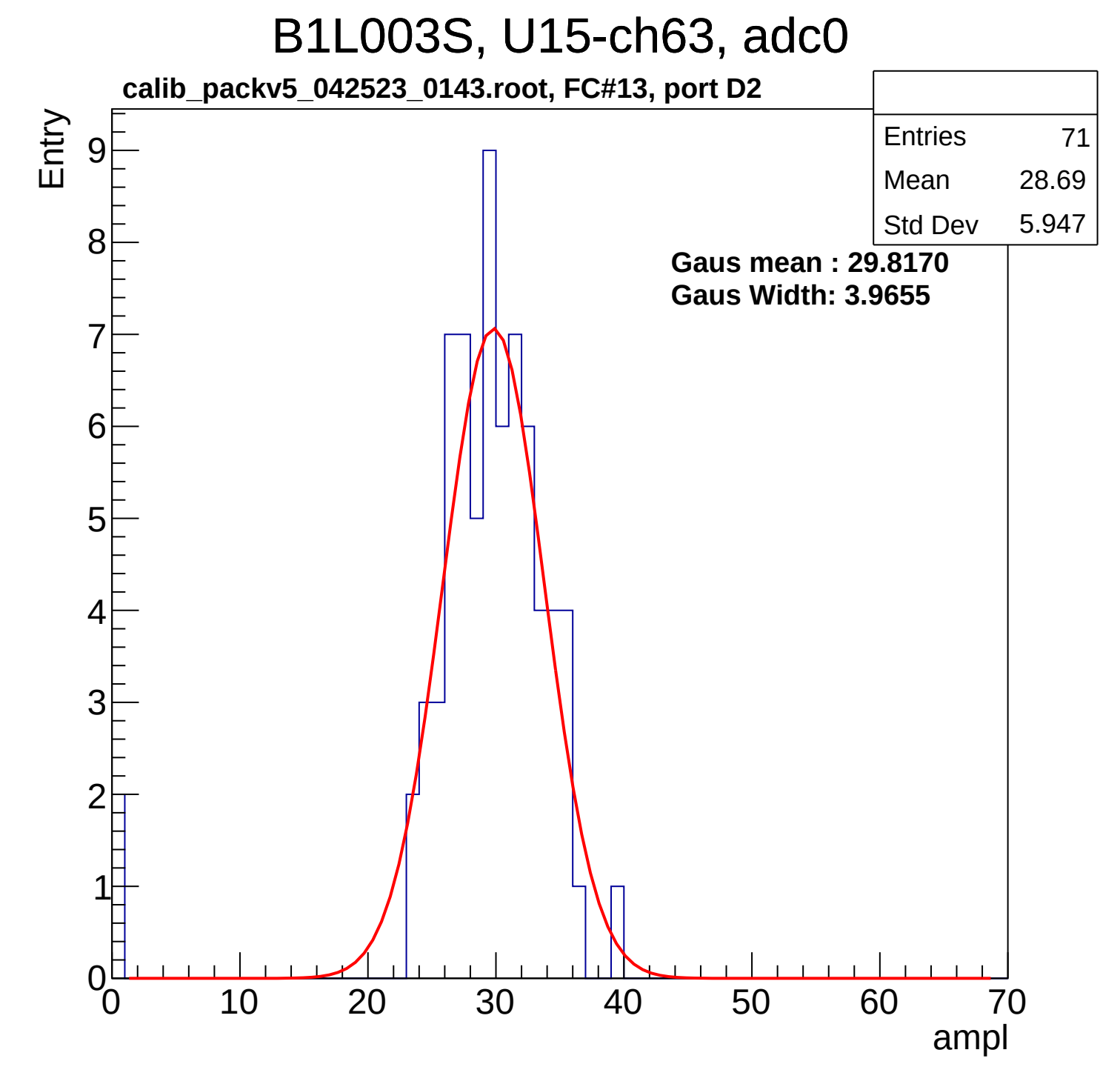

B1L003S, U15-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.58
Std Dev 17.08
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 43
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 28.69
Std Dev 5.947
Gaus mean : 29.8170
Gaus Width: 3.9655
Entry
ampl
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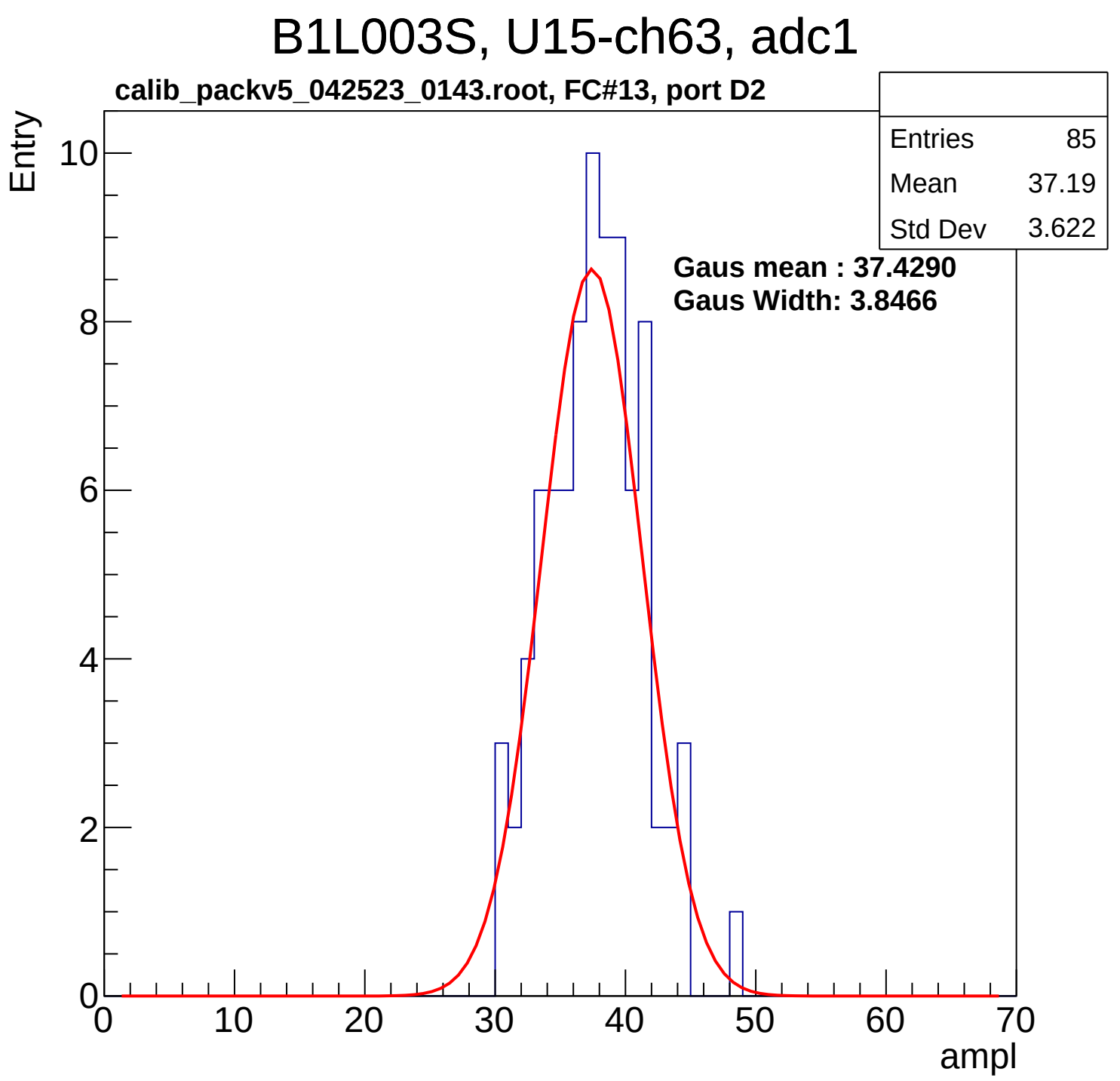
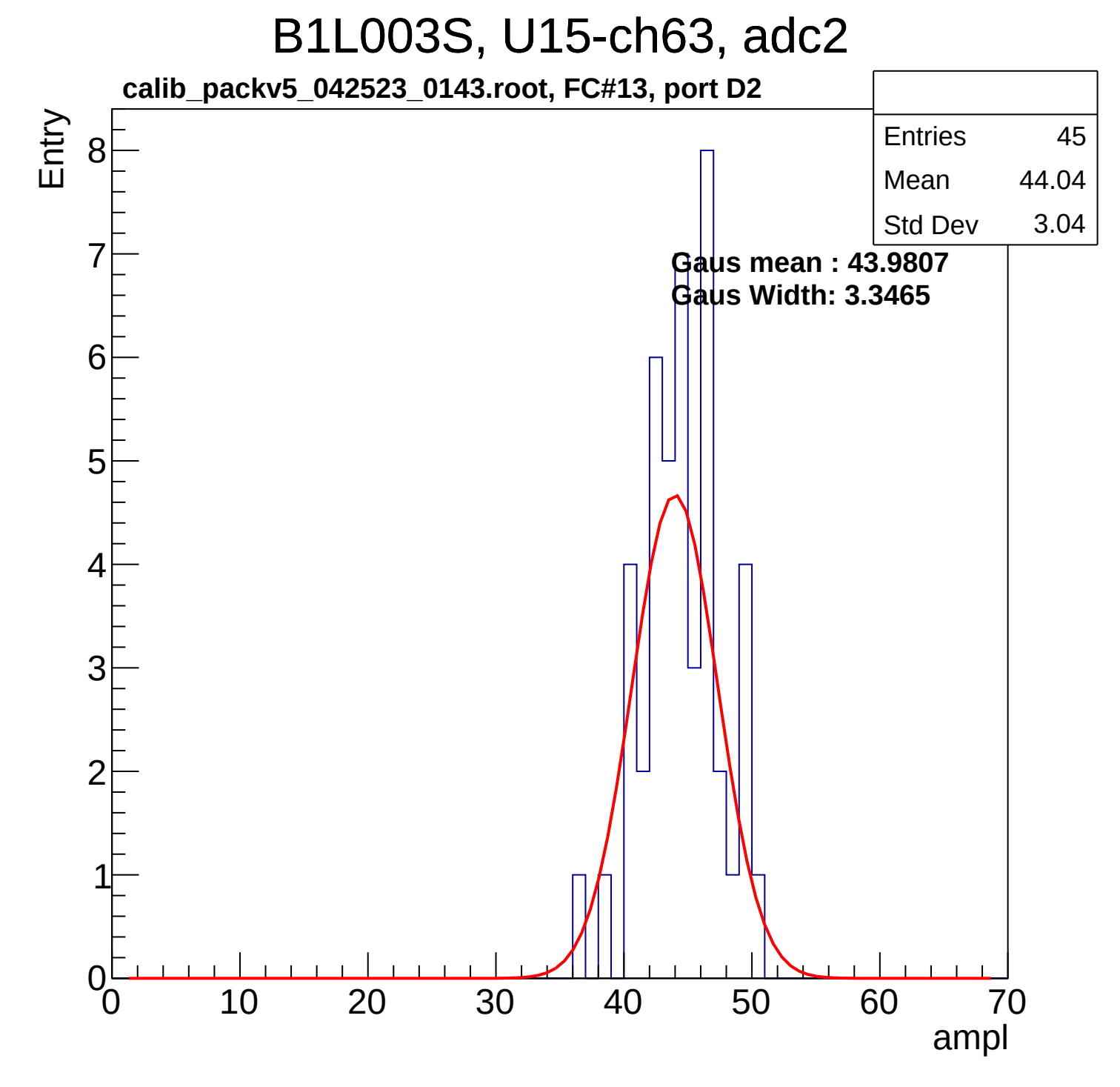
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 37.19
Std Dev 3.622
Gaus mean : 37.4290
Gaus Width: 3.8466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 44.04
Std Dev 3.04
Gaus mean : 43.9807
Gaus Width: 3.3465
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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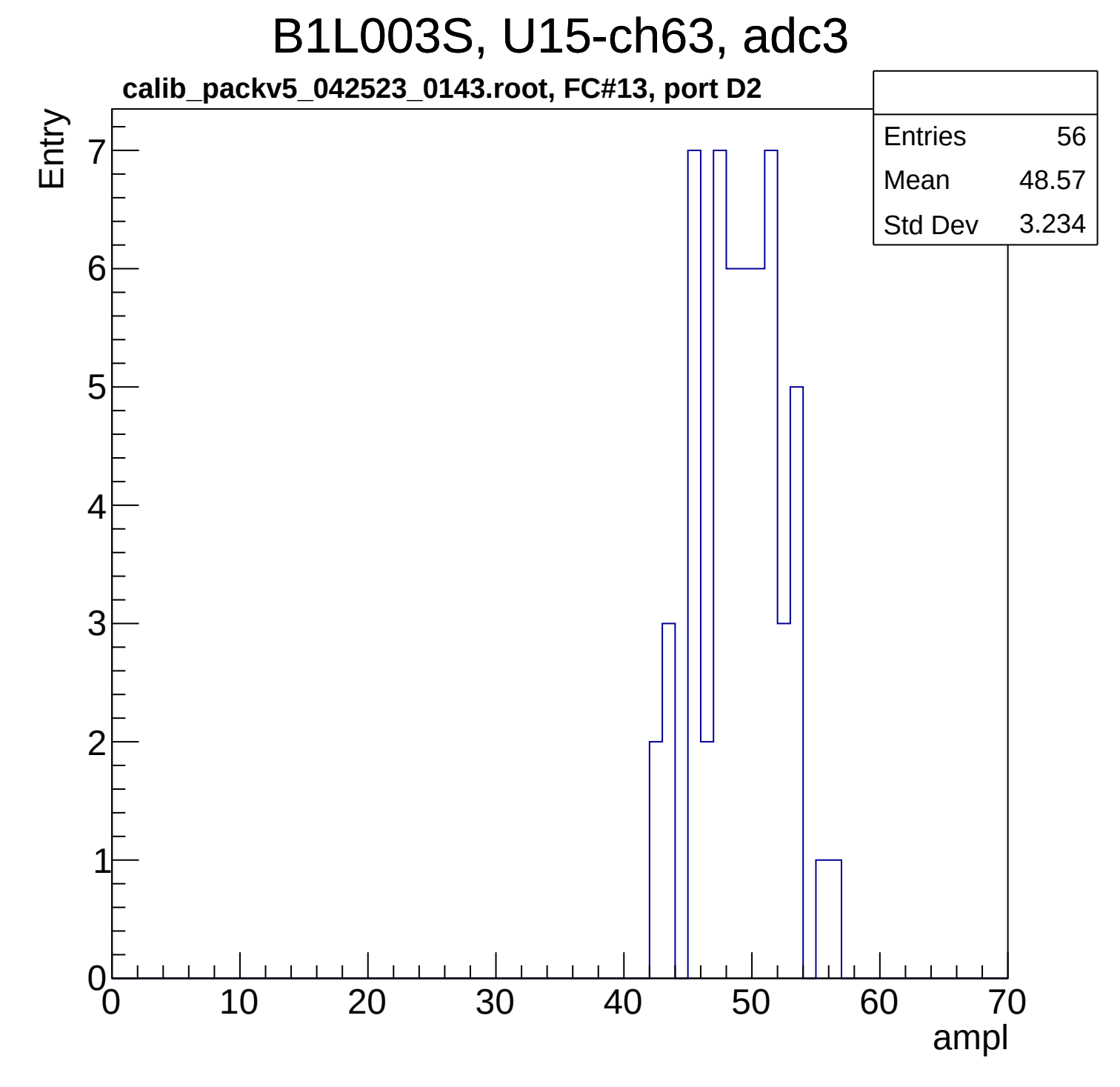
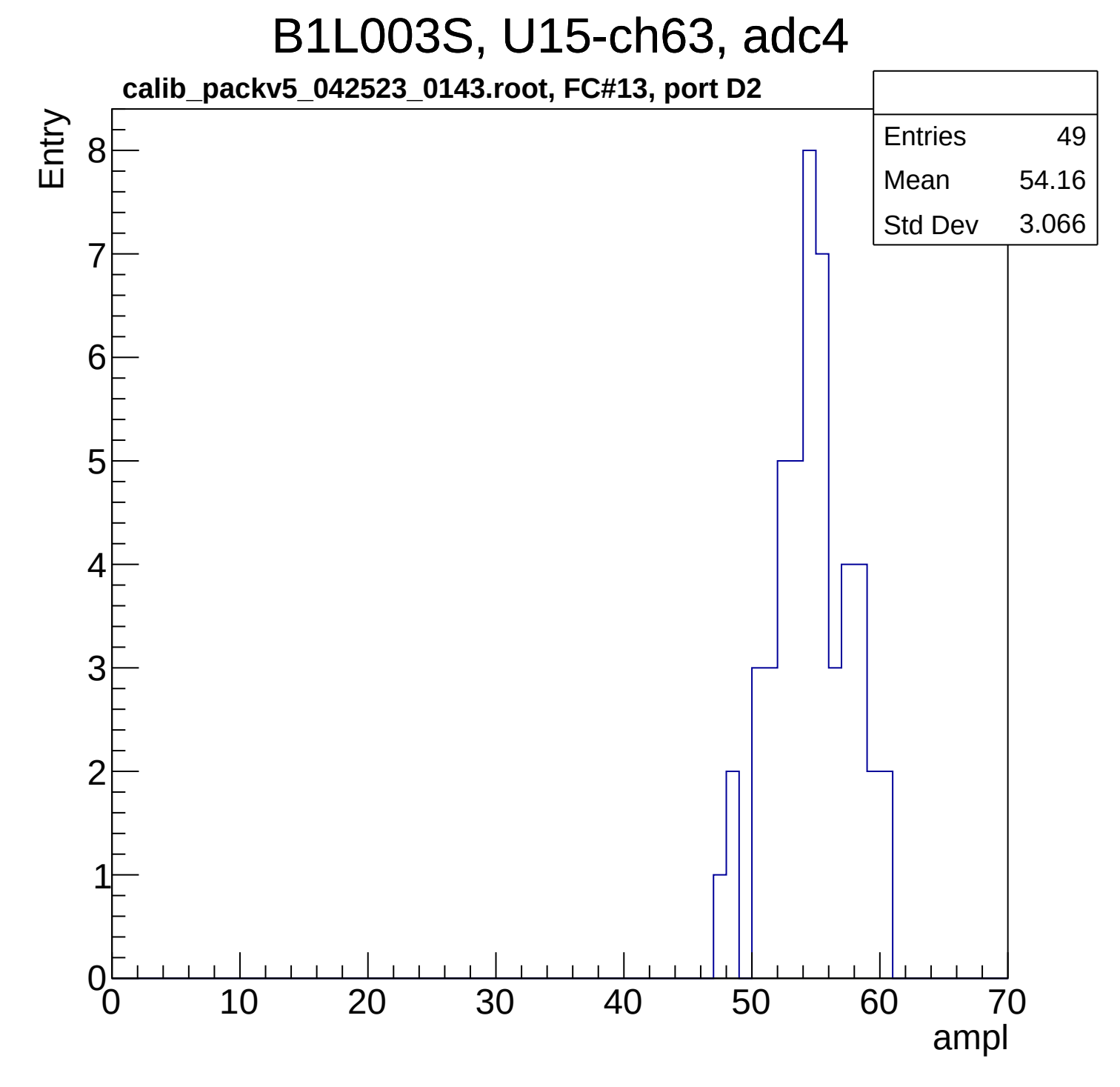

B1L003S, U15-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.57
Std Dev 3.234
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.16
Std Dev 3.066
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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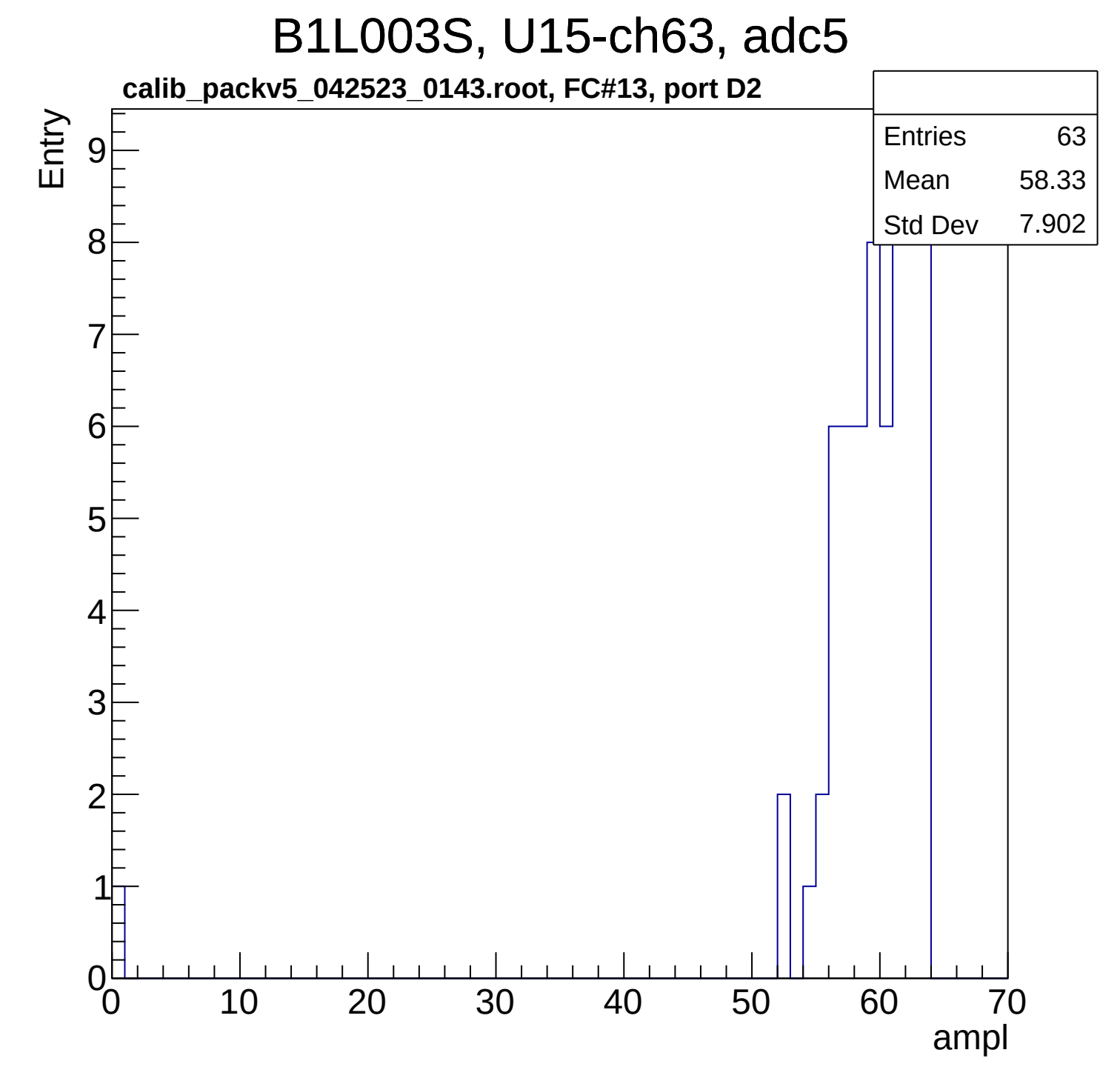
B1L003S, U15-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.33
Std Dev 7.902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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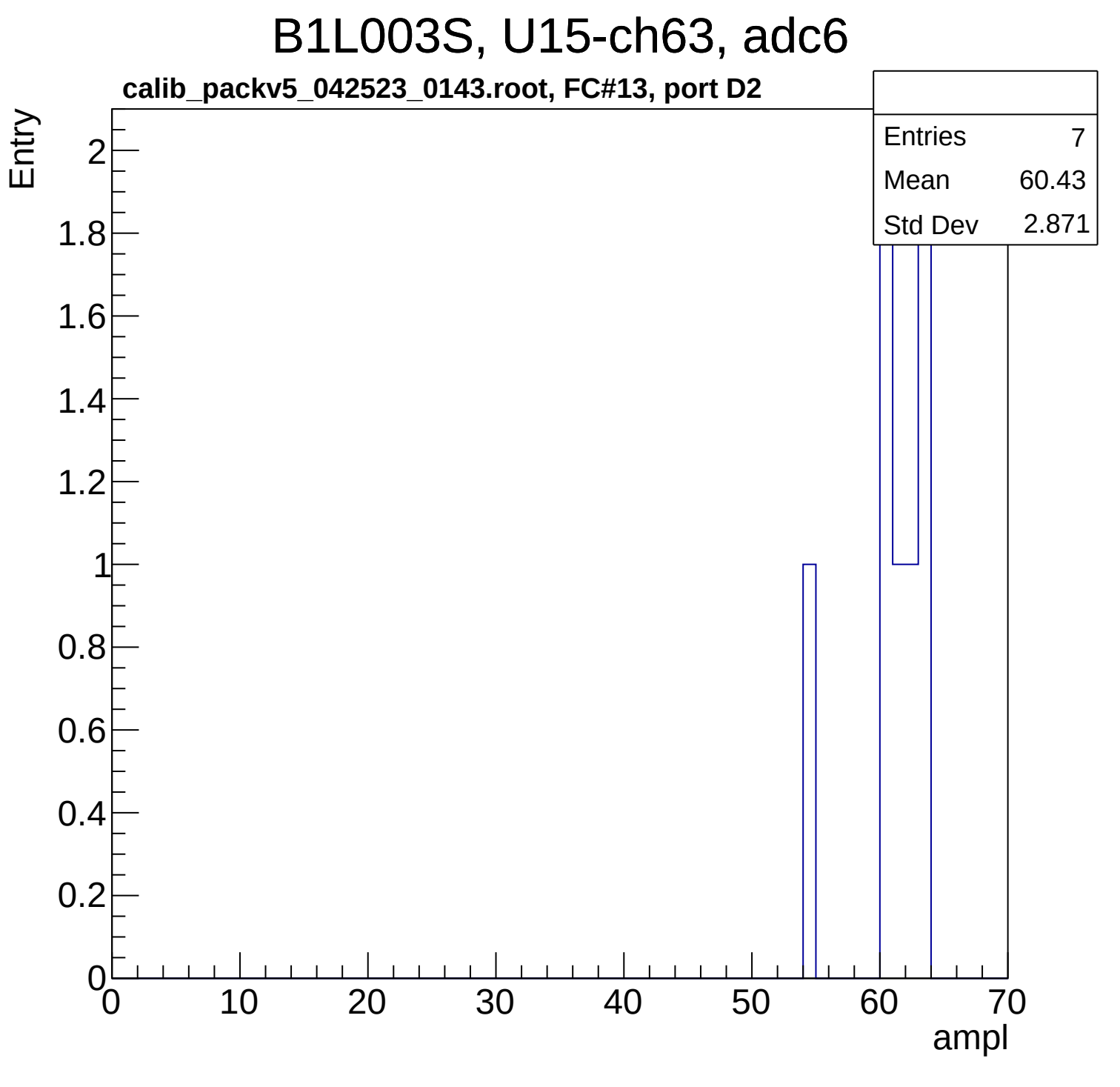
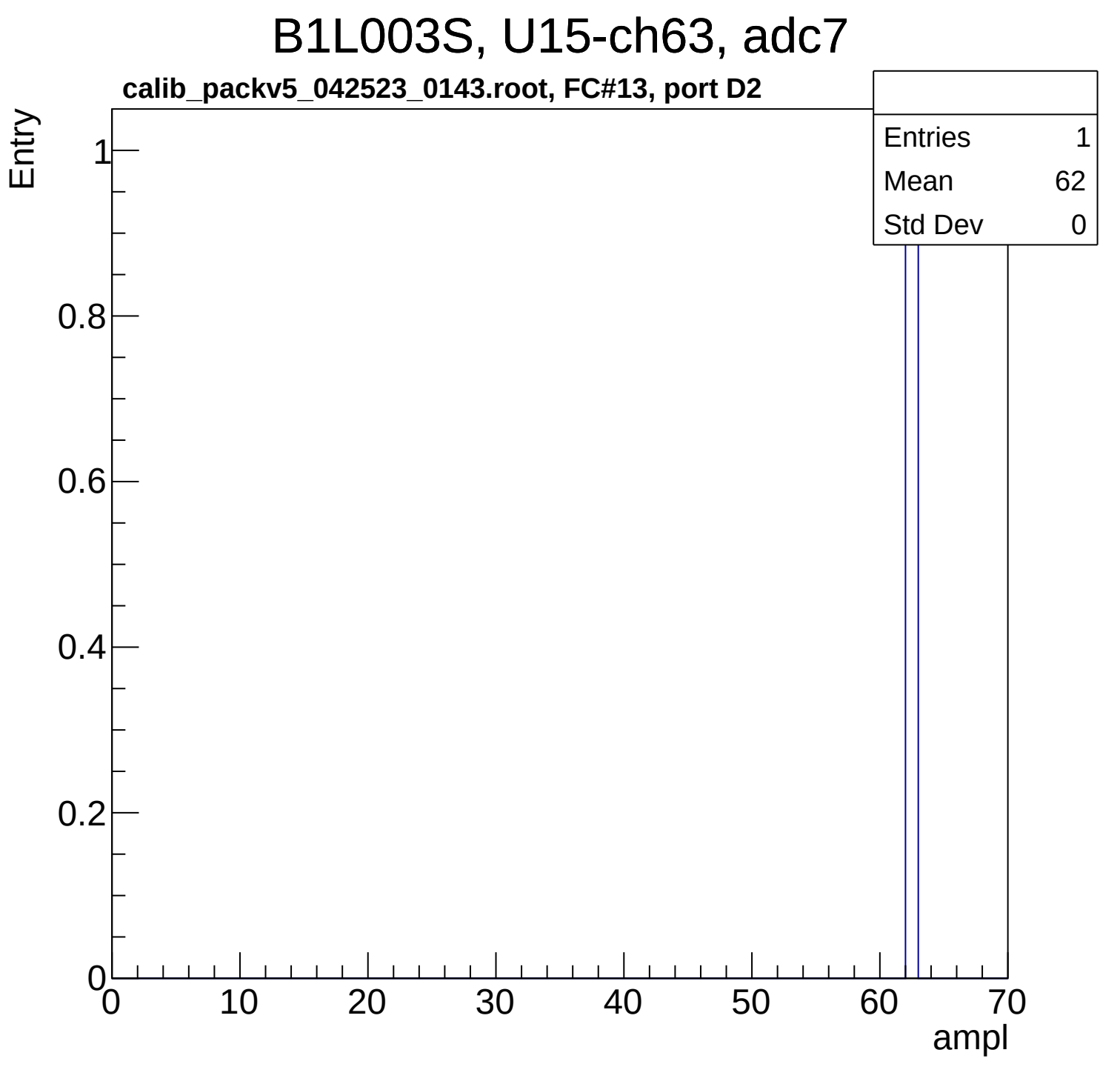

B1L003S, U15-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 60.43
Std Dev 2.871
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 62
Std Dev 0
Entry
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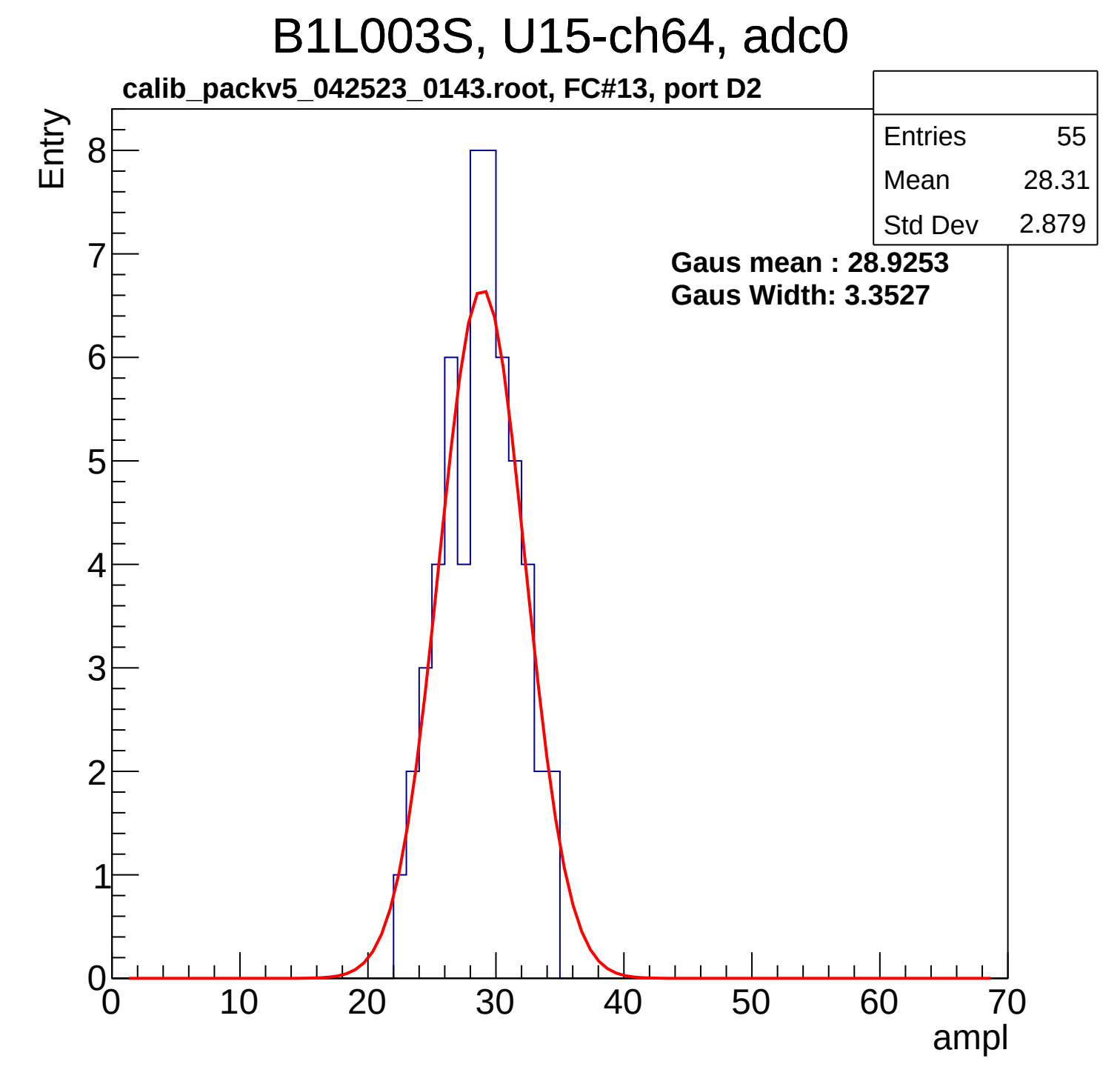
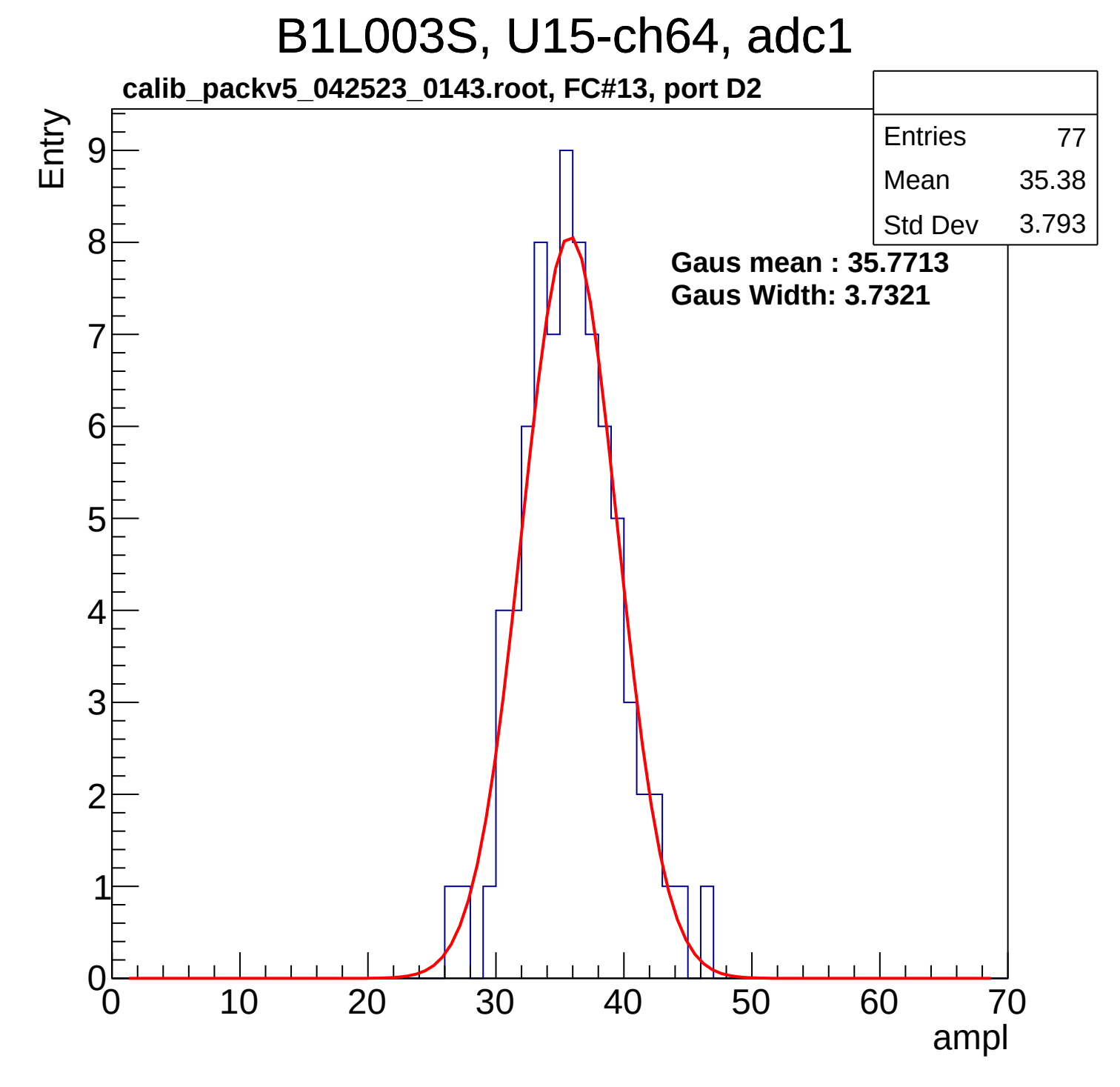
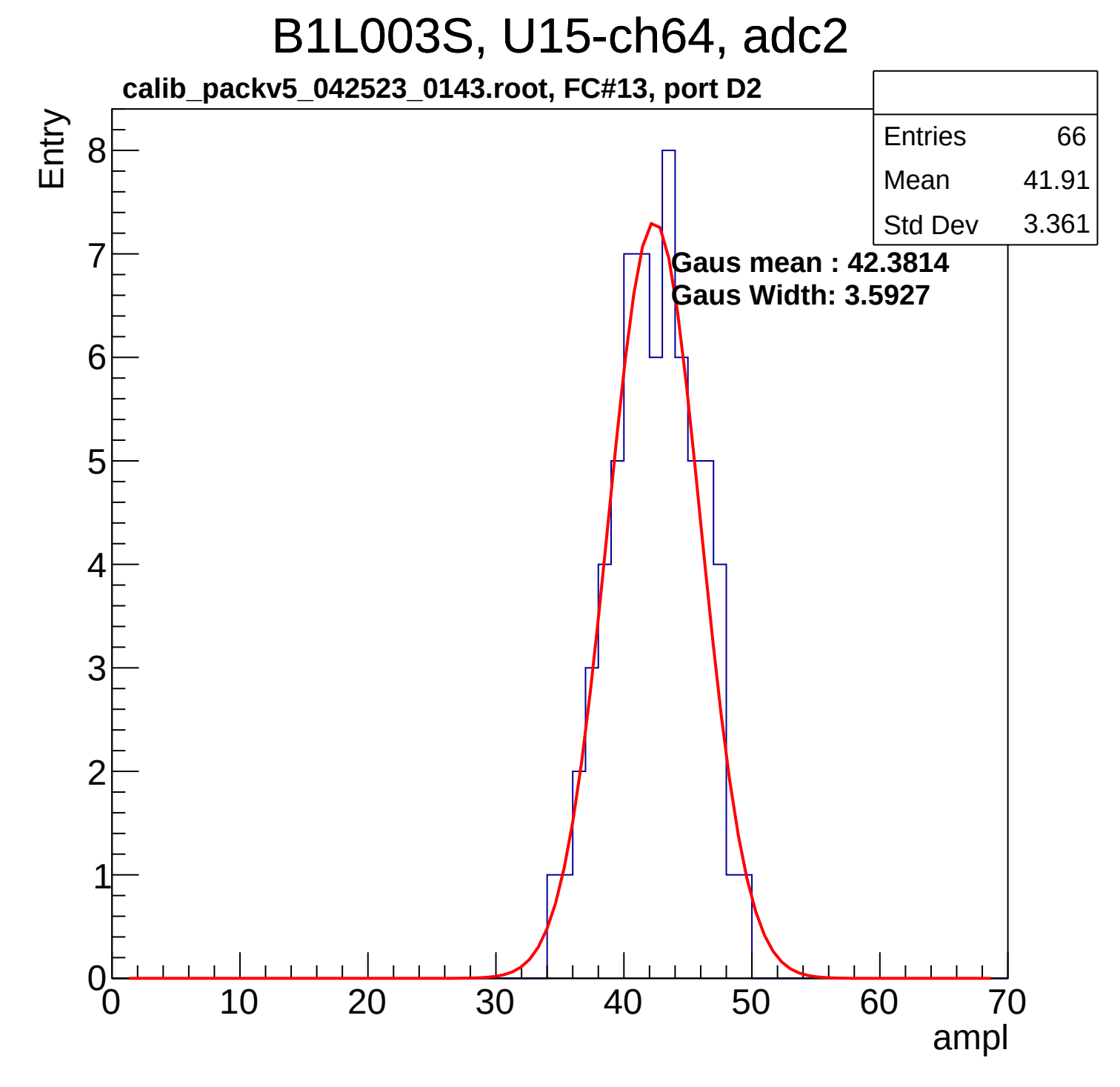
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 28.31
Std Dev 2.879
Gaus mean : 28.9253
Gaus Width: 3.3527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.38
Std Dev 3.793
Gaus mean : 35.7713
Gaus Width: 3.7321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 41.91
Std Dev 3.361
Gaus mean : 42.3814
Gaus Width: 3.5927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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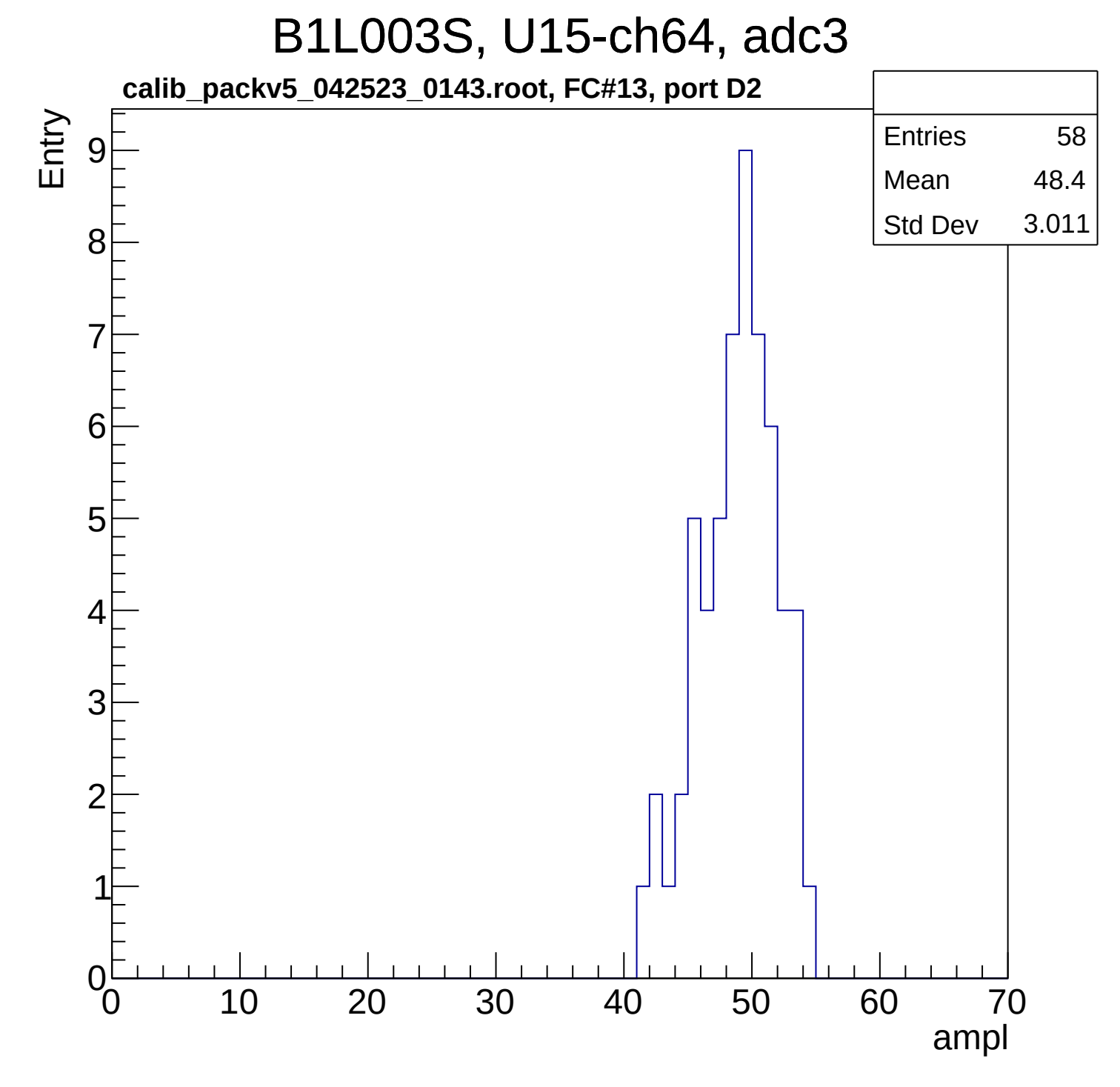
B1L003S, U15-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.4
Std Dev 3.011
Entry
0
1
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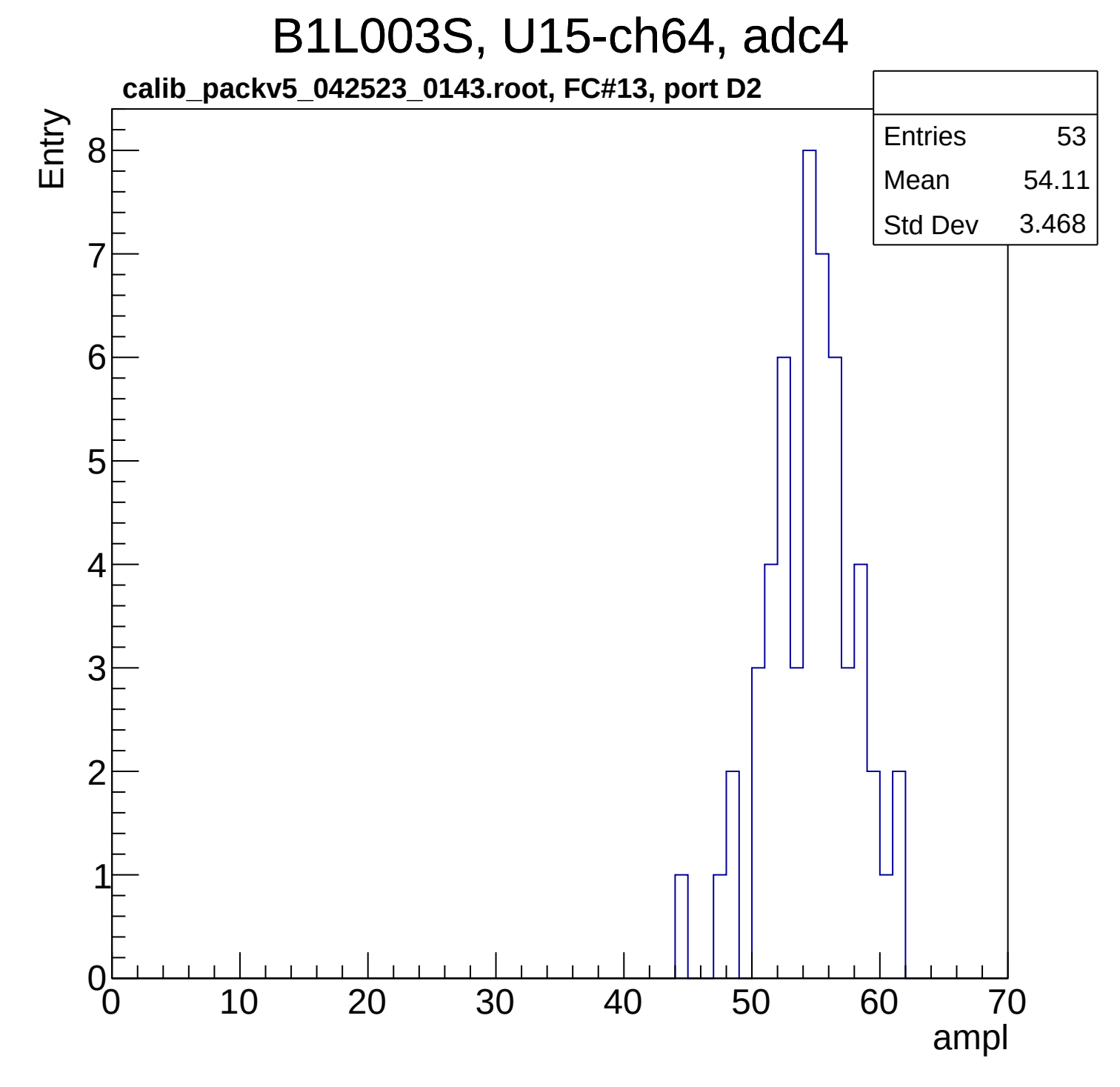
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.11
Std Dev 3.468
Entry
ampl
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1
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3
4
5
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0
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60
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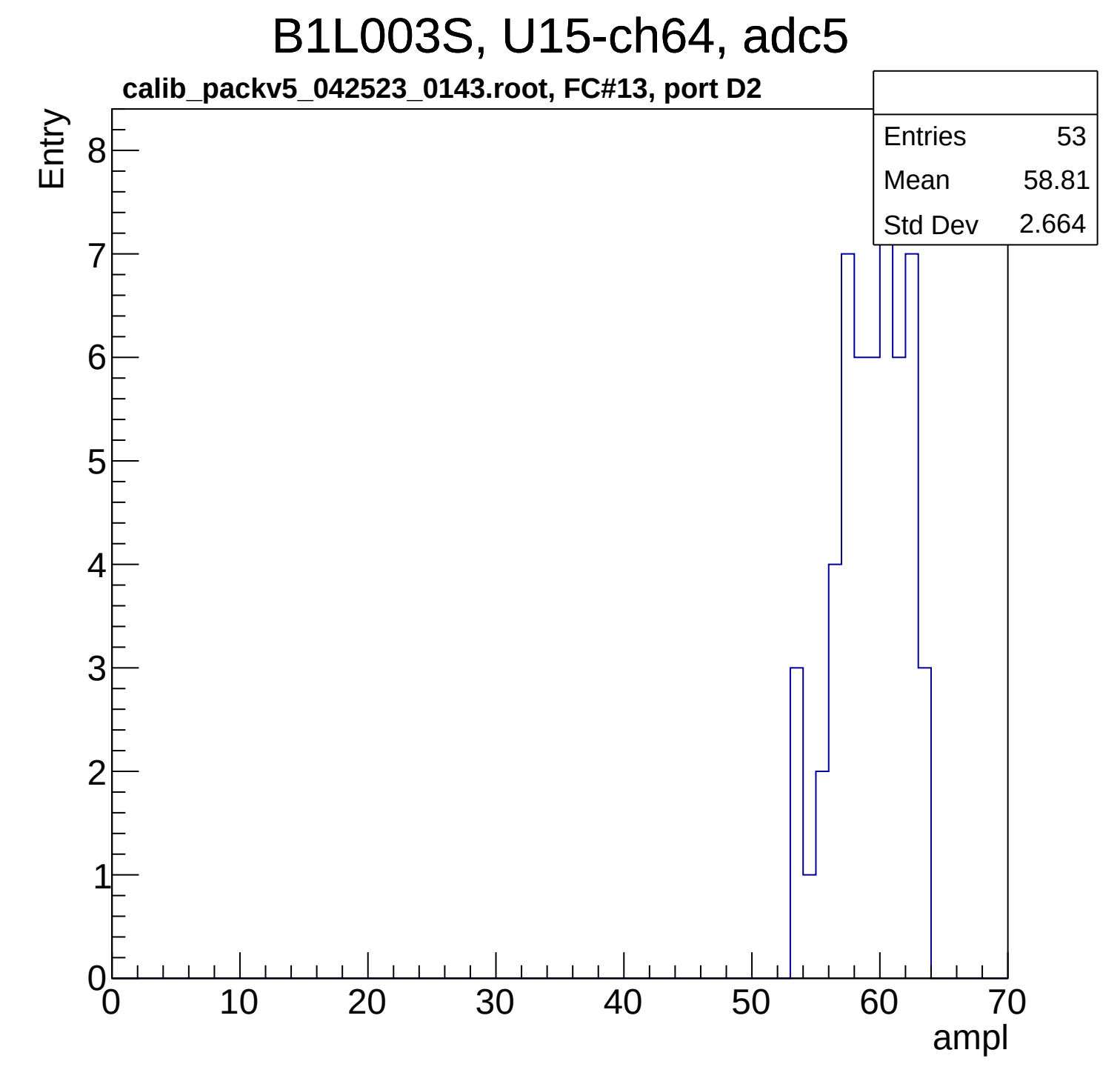
B1L003S, U15-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.81
Std Dev 2.664
Entry
ampl
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1
2
3
4
5
6
7
8
0
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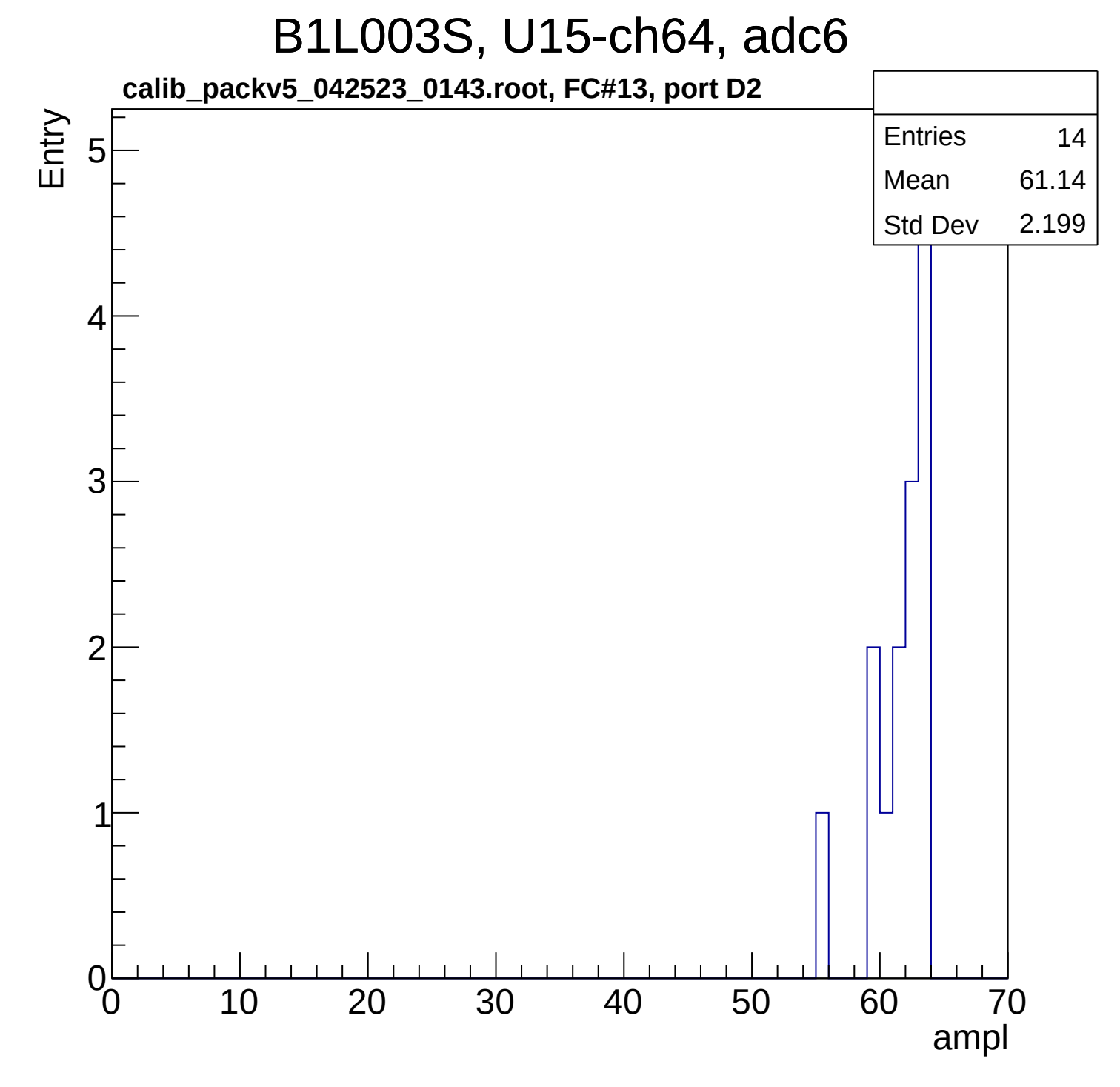
B1L003S, U15-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.14
Std Dev 2.199
Entry
0
1
2
3
4
5
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10
20
30
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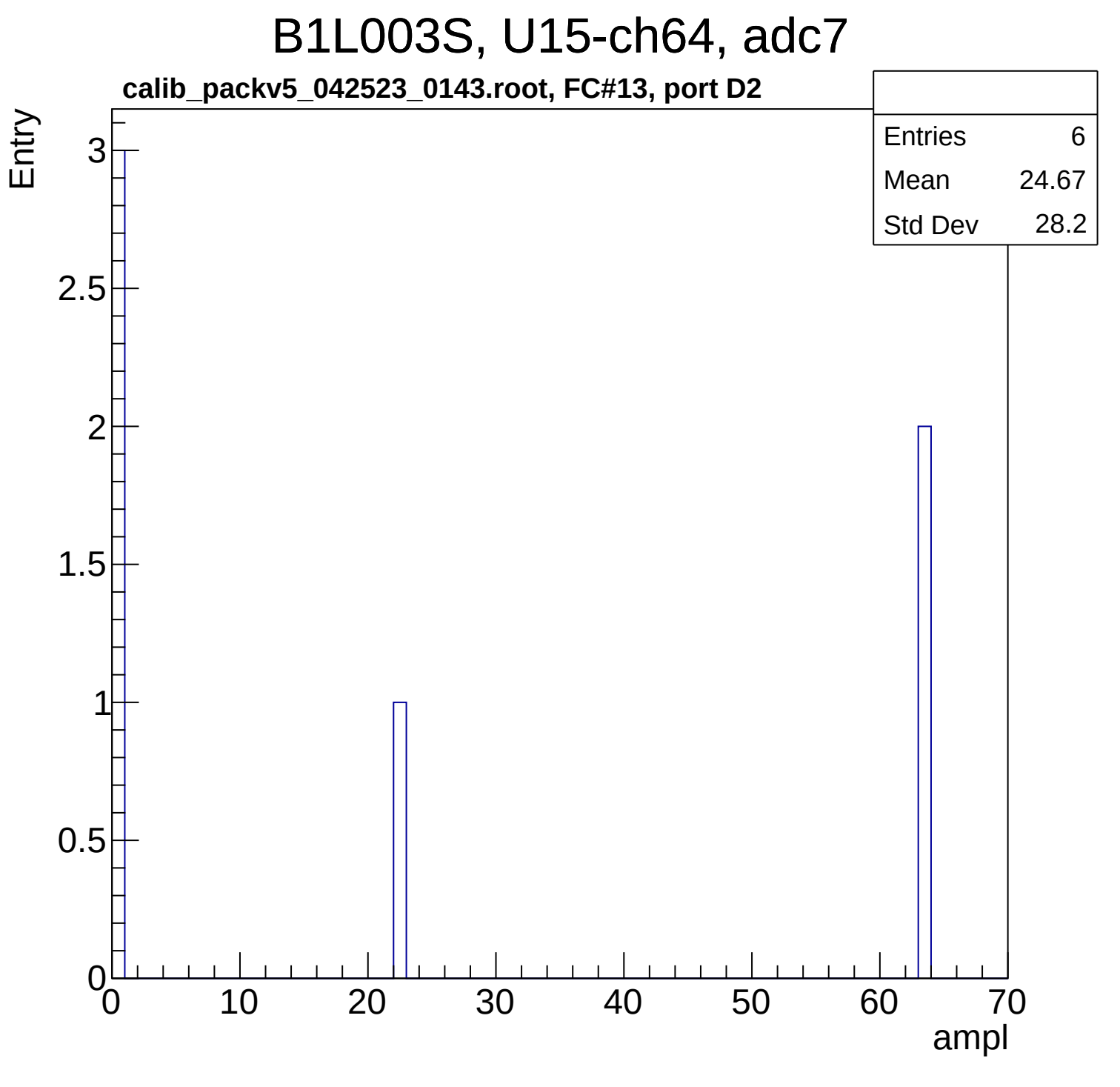
50
60
70
ampl


B1L003S, U15-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 24.67
Std Dev 28.2
Entry
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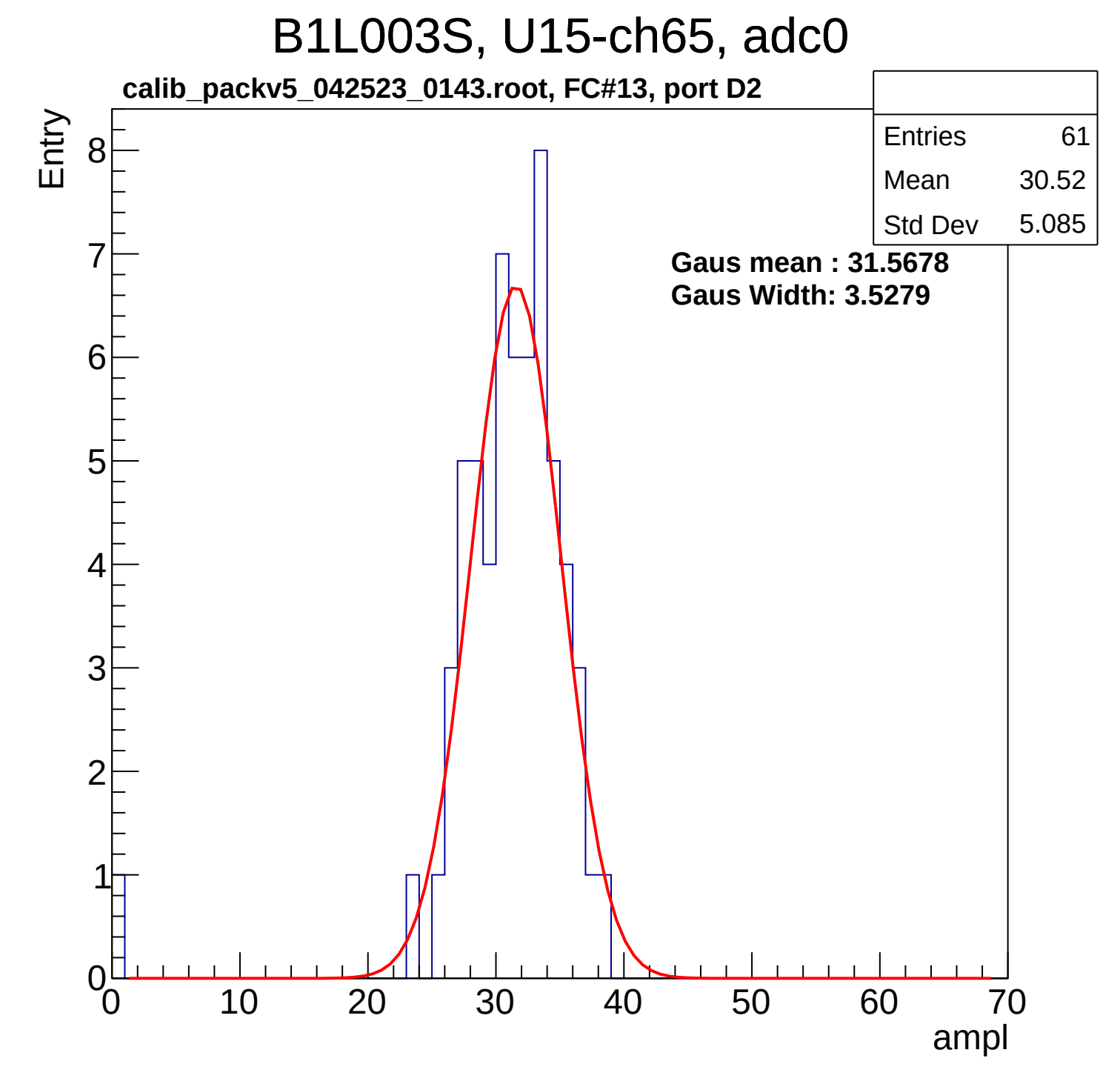
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 30.52
Std Dev 5.085
Gaus mean : 31.5678
Gaus Width: 3.5279
Entry
ampl
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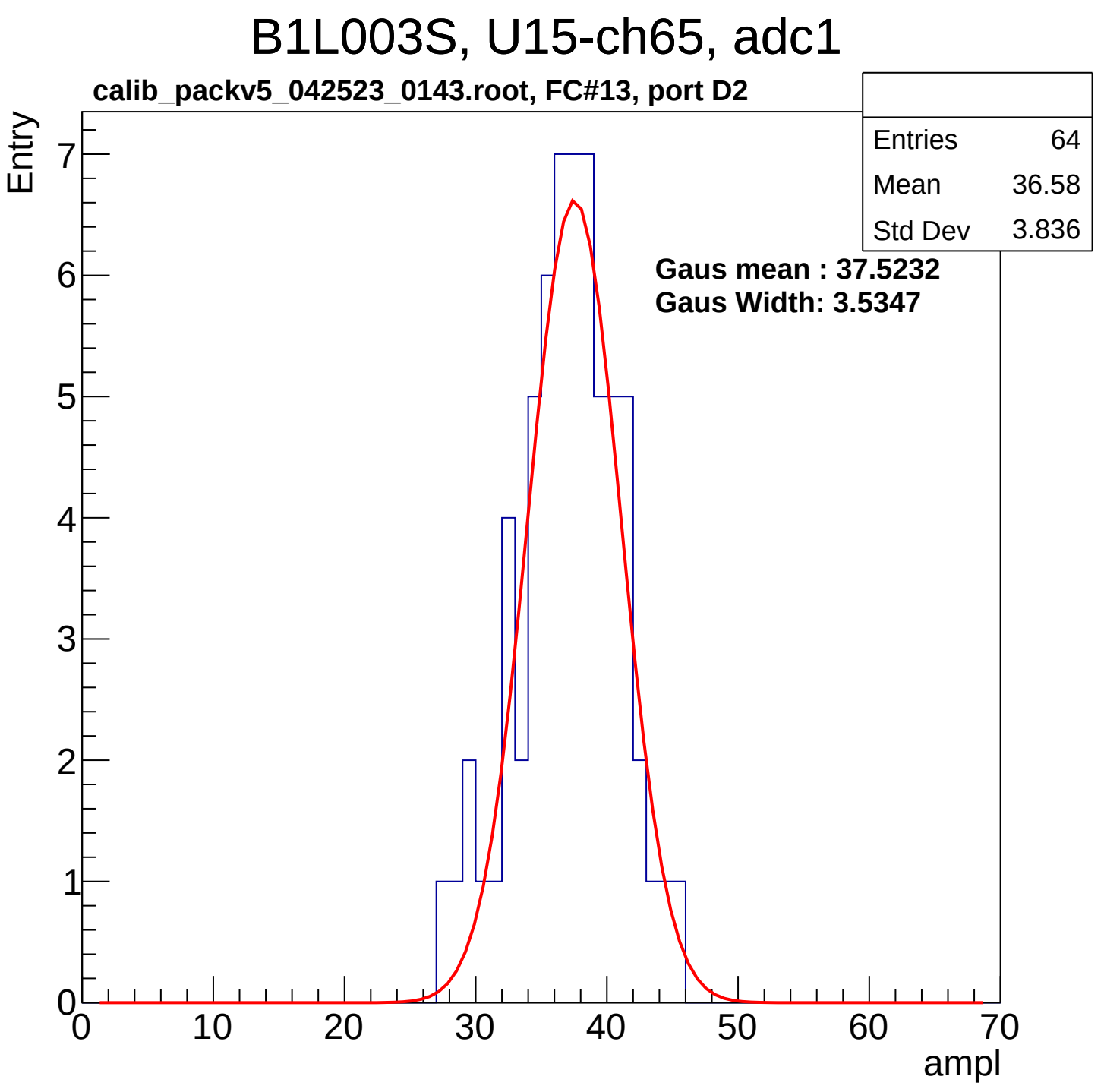
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.58
Std Dev 3.836
Gaus mean : 37.5232
Gaus Width: 3.5347
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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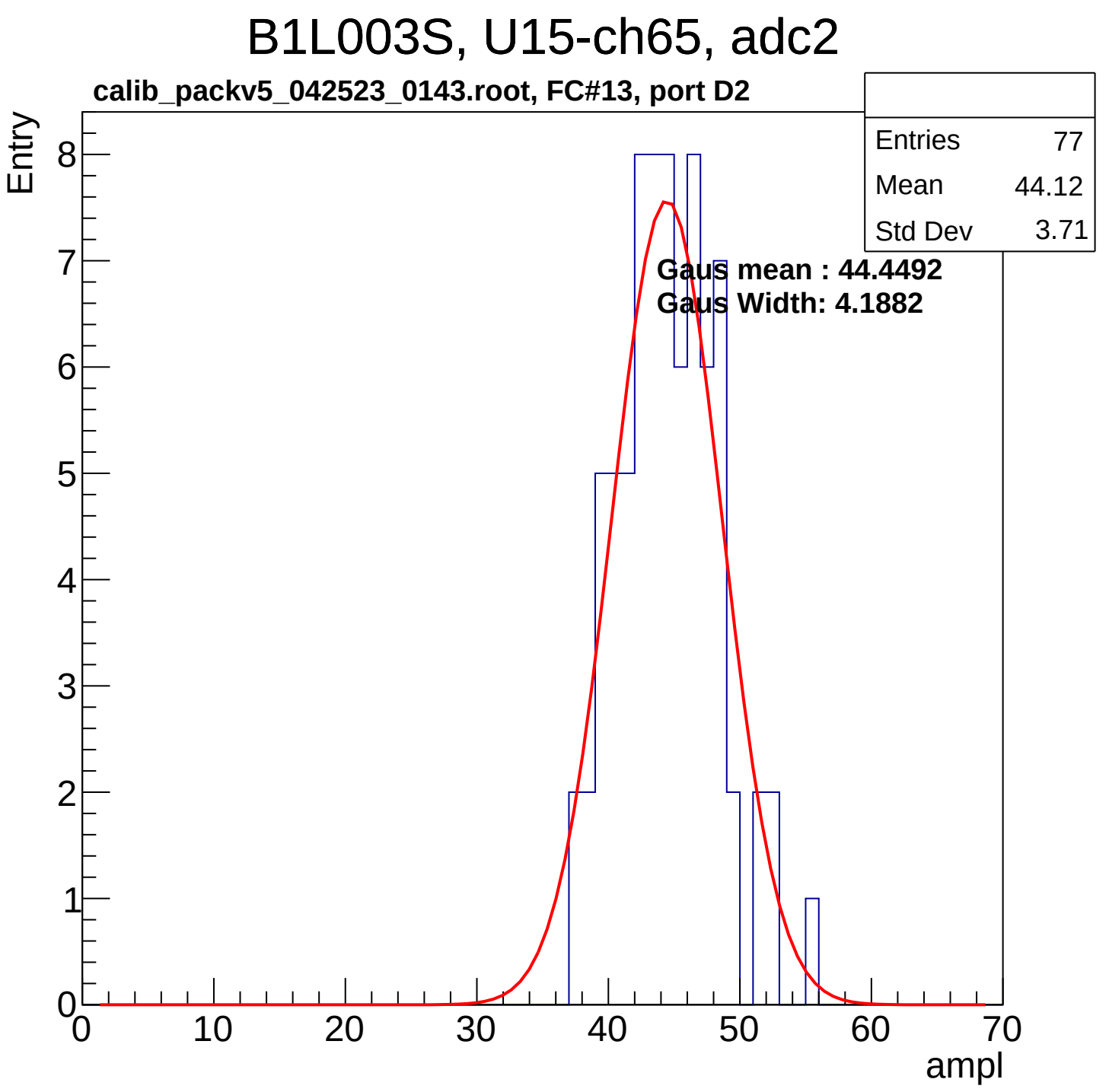
B1L003S, U15-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 44.12
Std Dev 3.71
Gaus mean : 44.4492
Gaus Width: 4.1882
Entry
ampl
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2
3
4
5
6
7
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0
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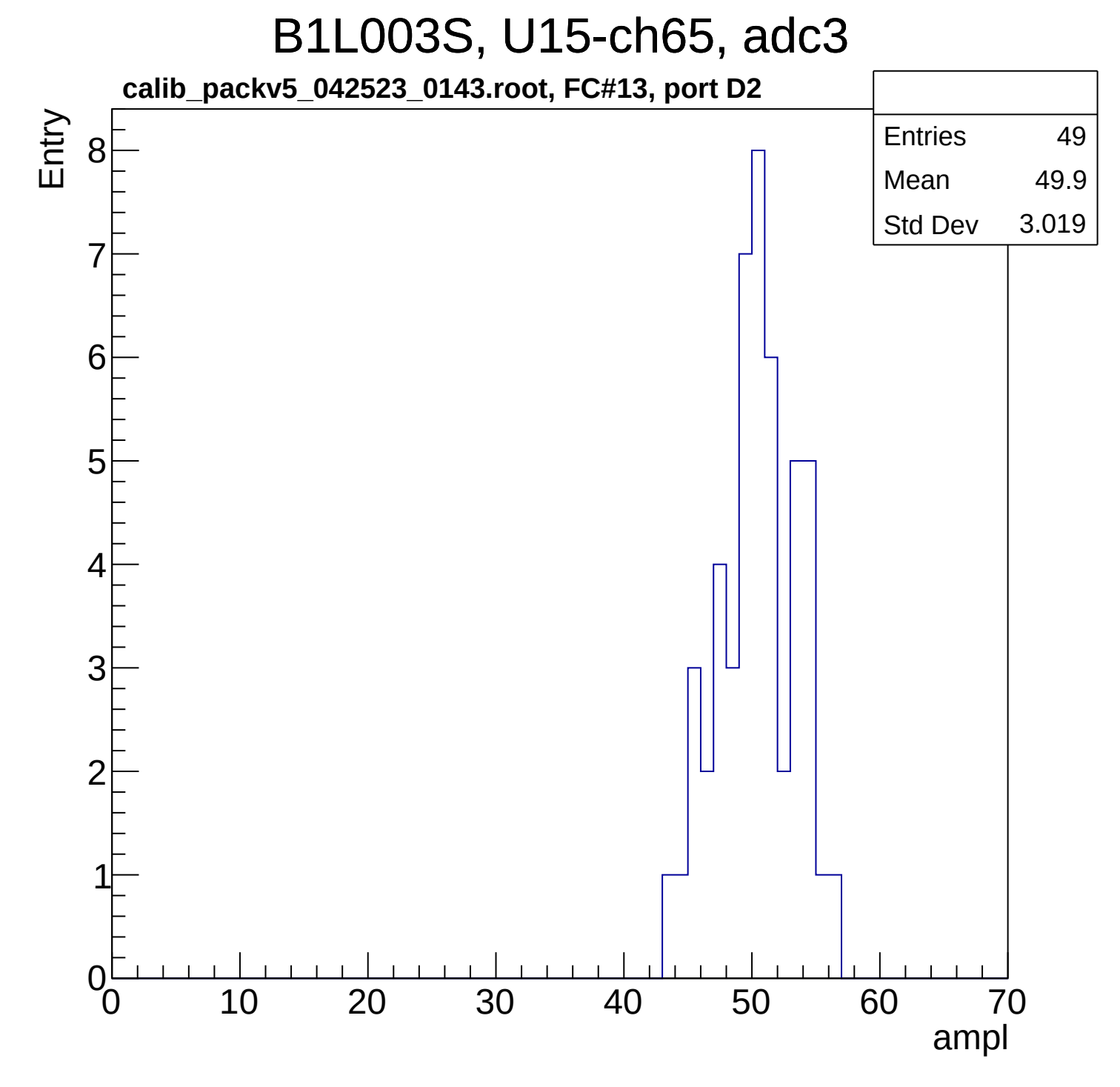
B1L003S, U15-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 49.9
Std Dev 3.019
Entry
ampl
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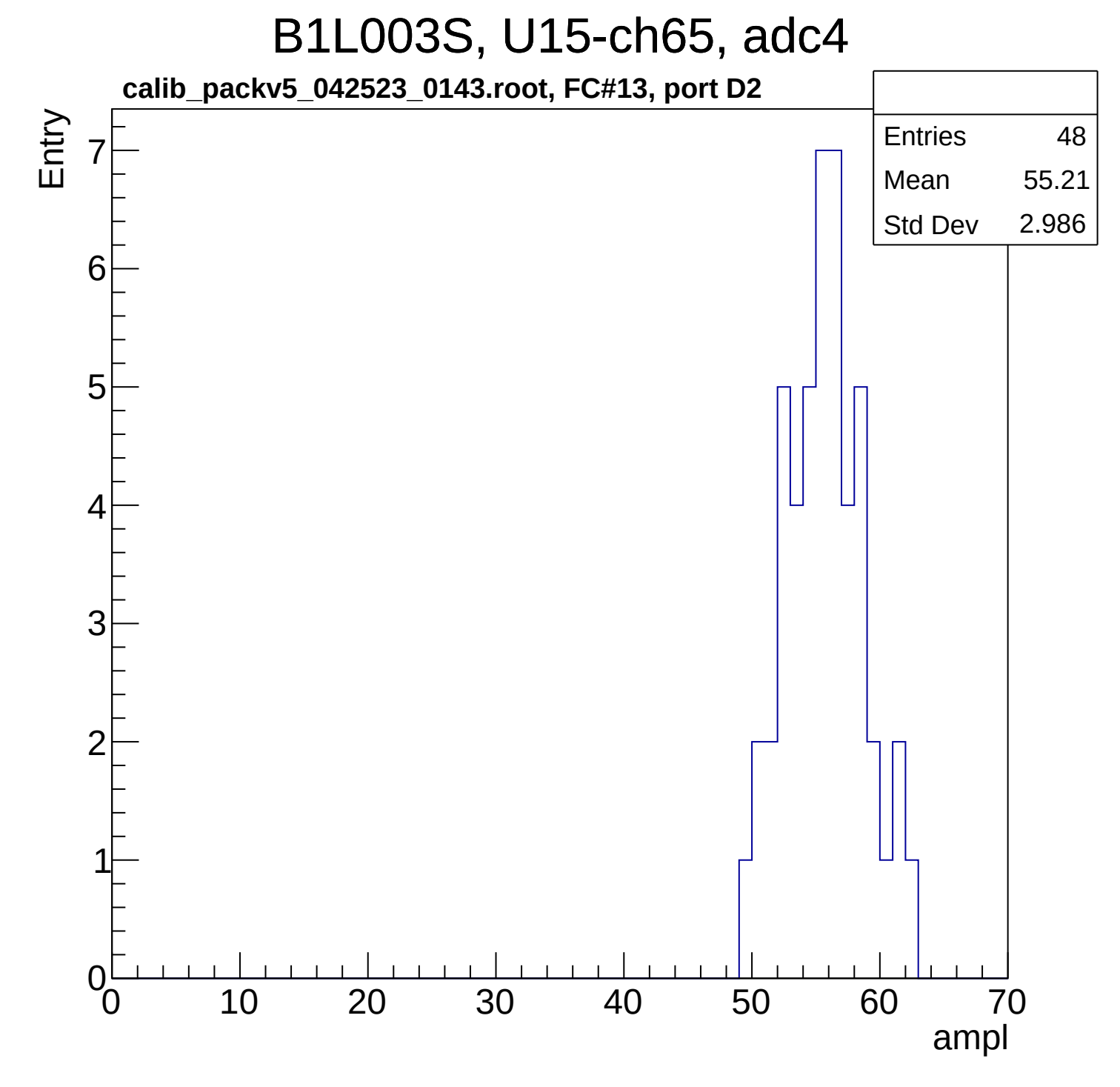
B1L003S, U15-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 55.21
Std Dev 2.986
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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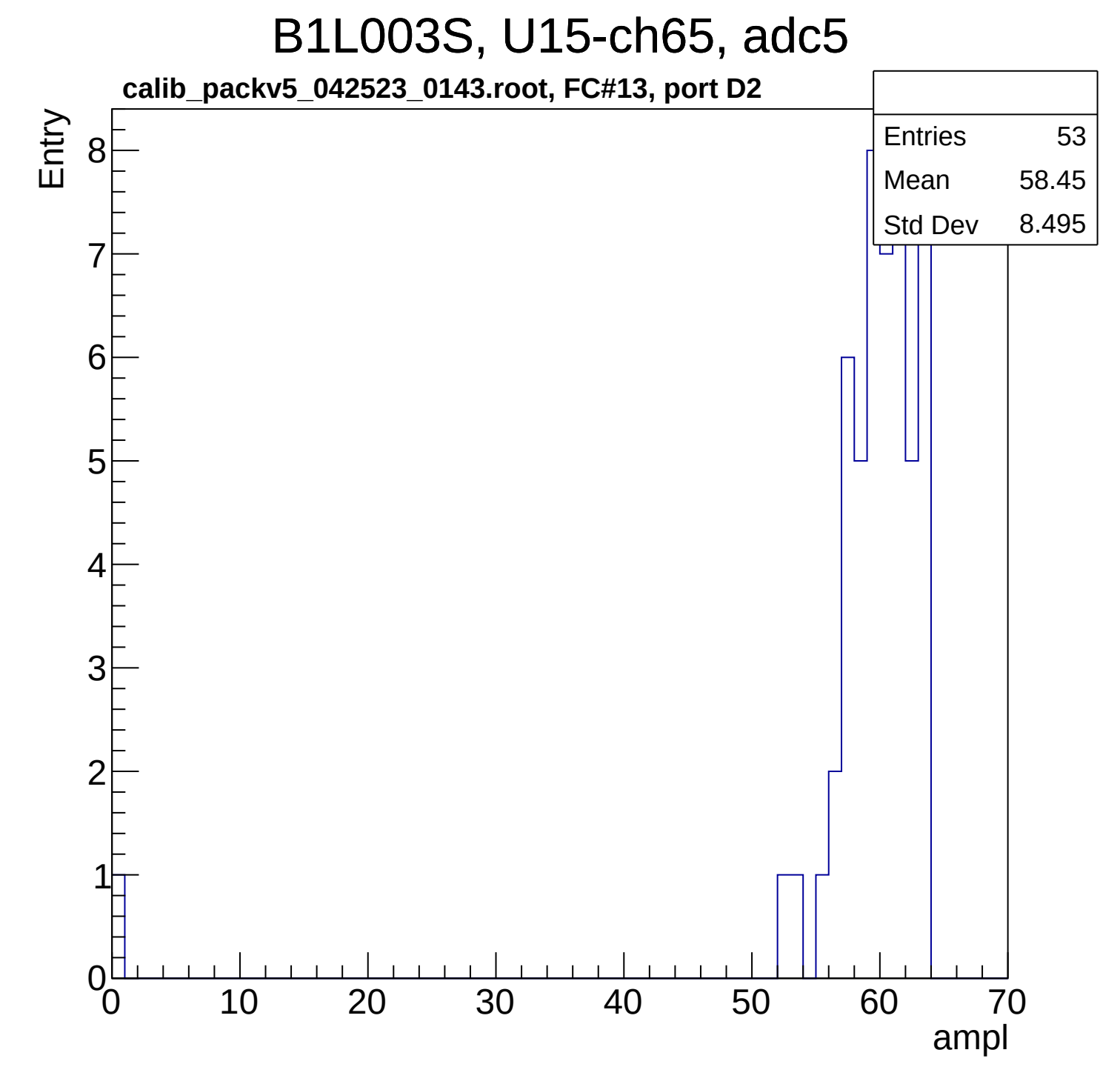
B1L003S, U15-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.45
Std Dev 8.495
Entry
ampl
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2
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4
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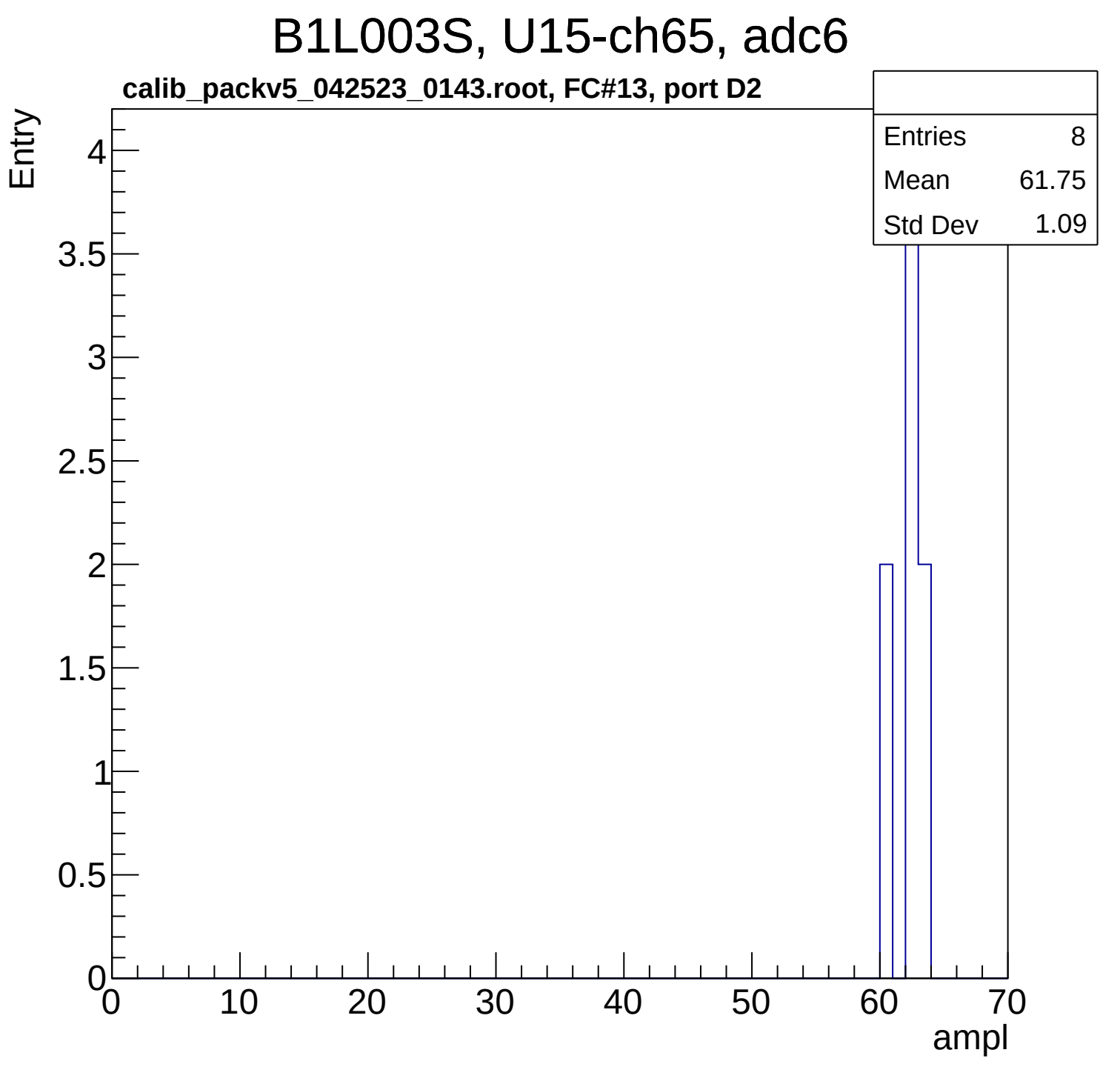
B1L003S, U15-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.75
Std Dev 1.09
Entry
ampl
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1.5
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2.5
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3.5
4
0
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60
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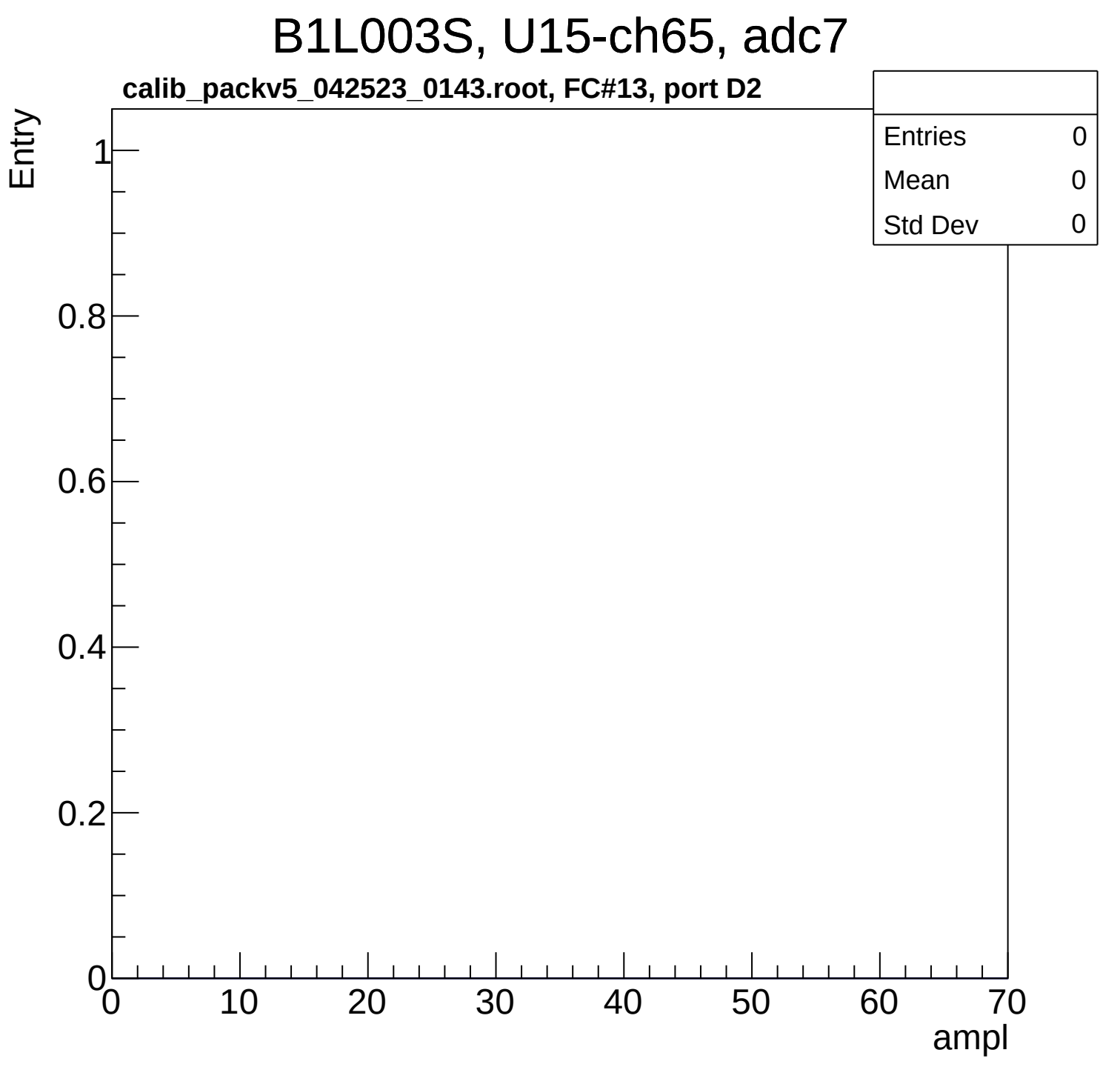
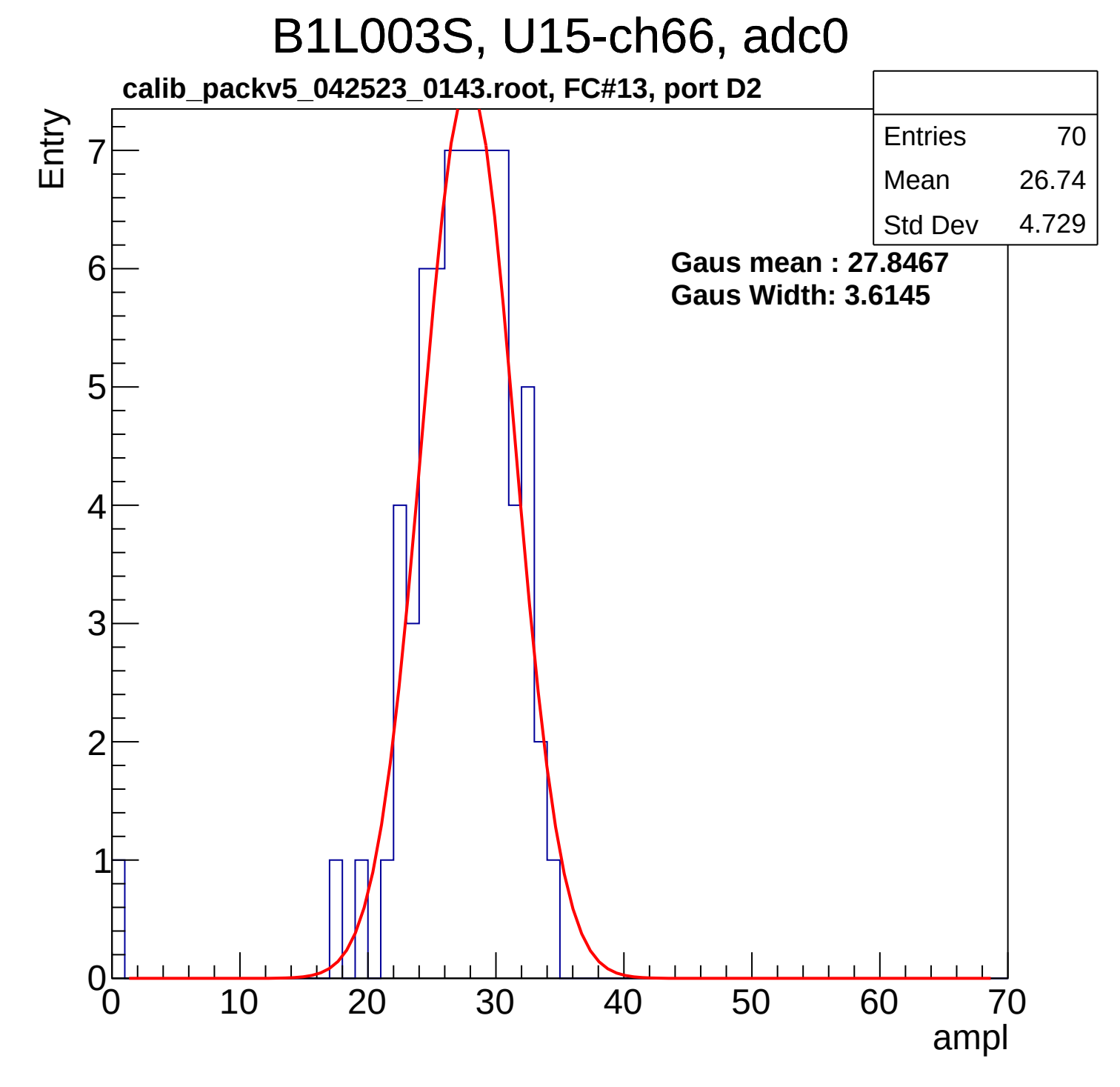

B1L003S, U15-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 26.74
Std Dev 4.729
Gaus mean : 27.8467
Gaus Width: 3.6145
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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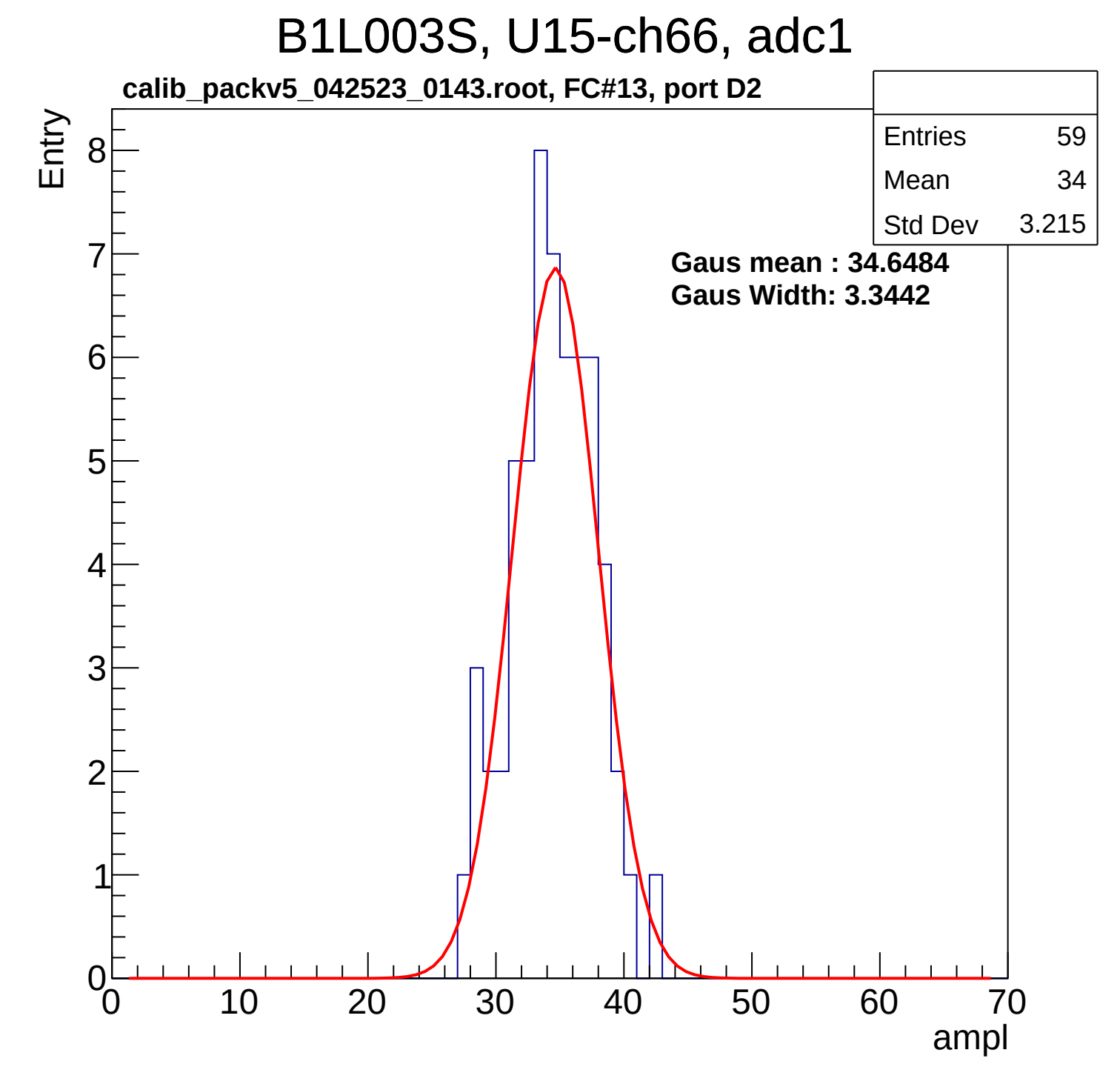
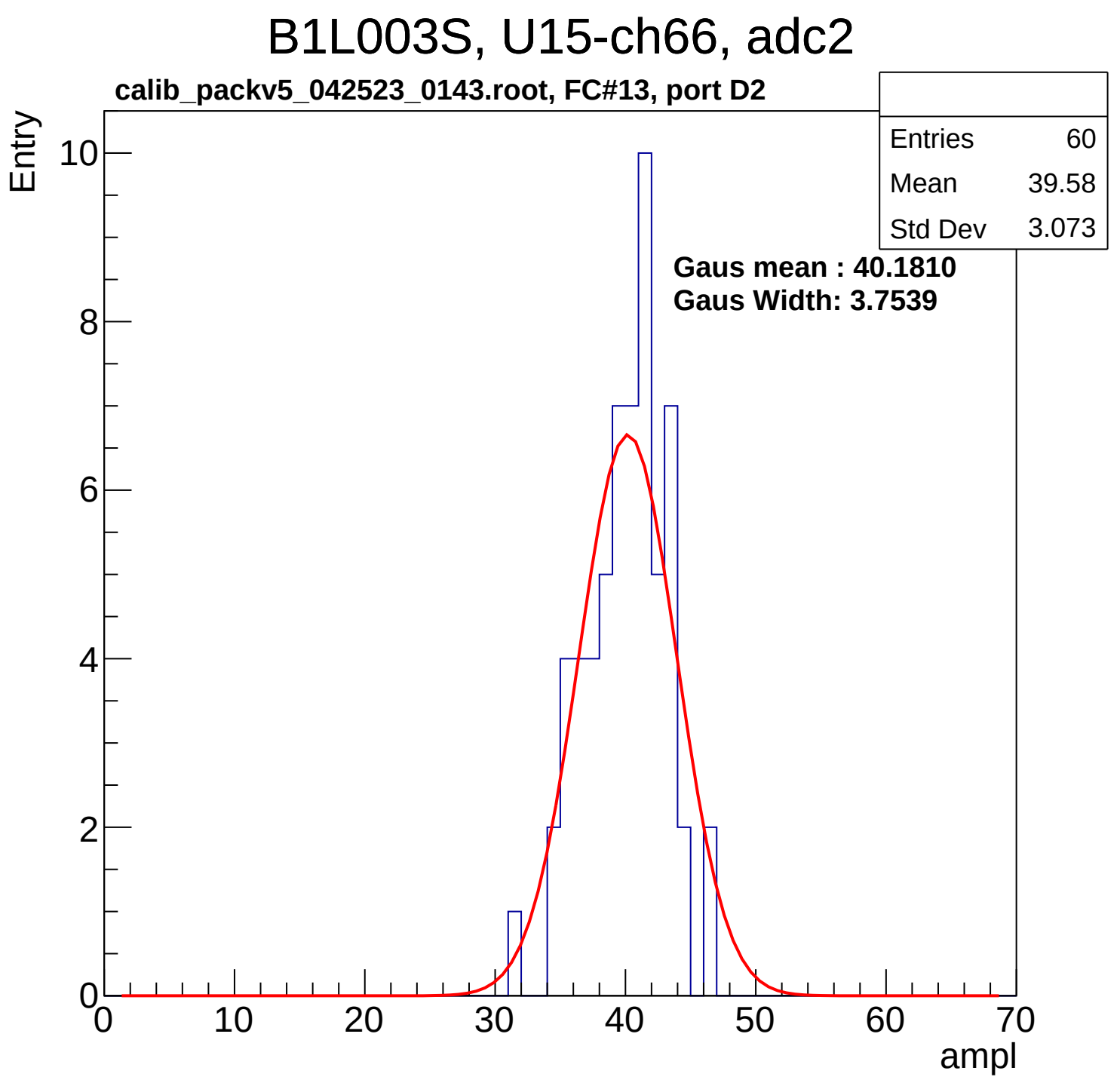

B1L003S, U15-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 34
Std Dev 3.215
Gaus mean : 34.6484
Gaus Width: 3.3442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 39.58
Std Dev 3.073
Gaus mean : 40.1810
Gaus Width: 3.7539
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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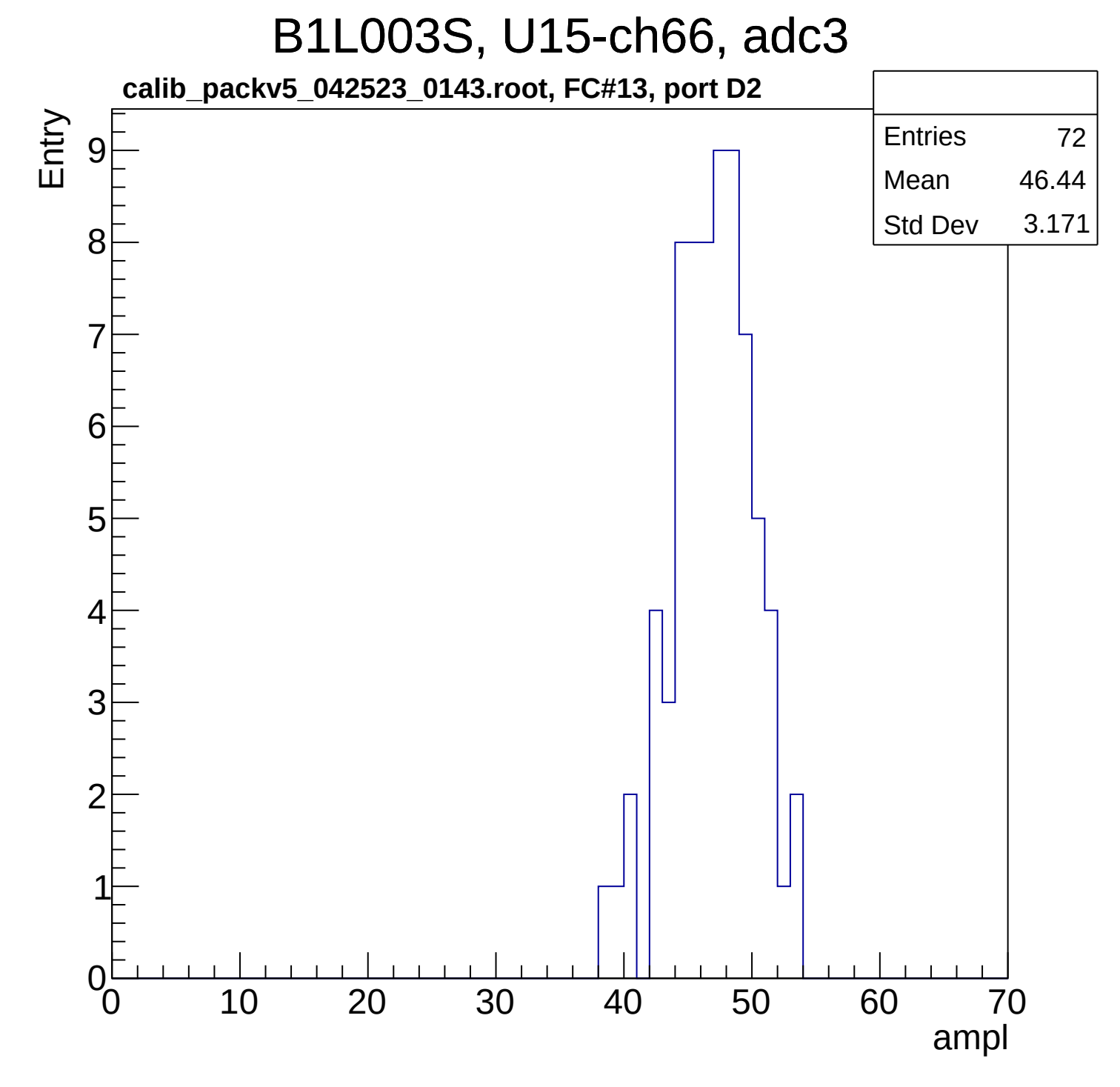
B1L003S, U15-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 46.44
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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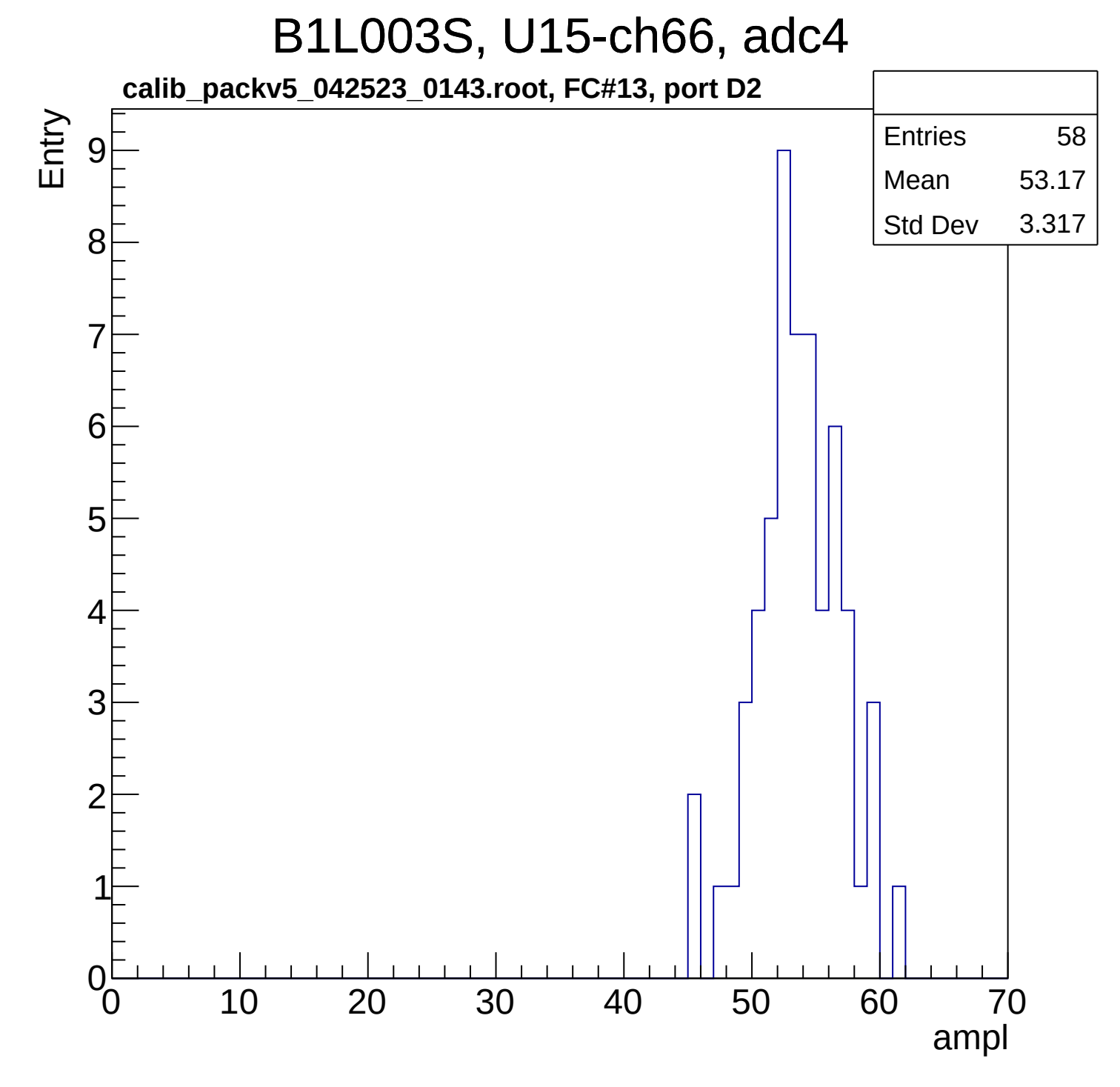
B1L003S, U15-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.17
Std Dev 3.317
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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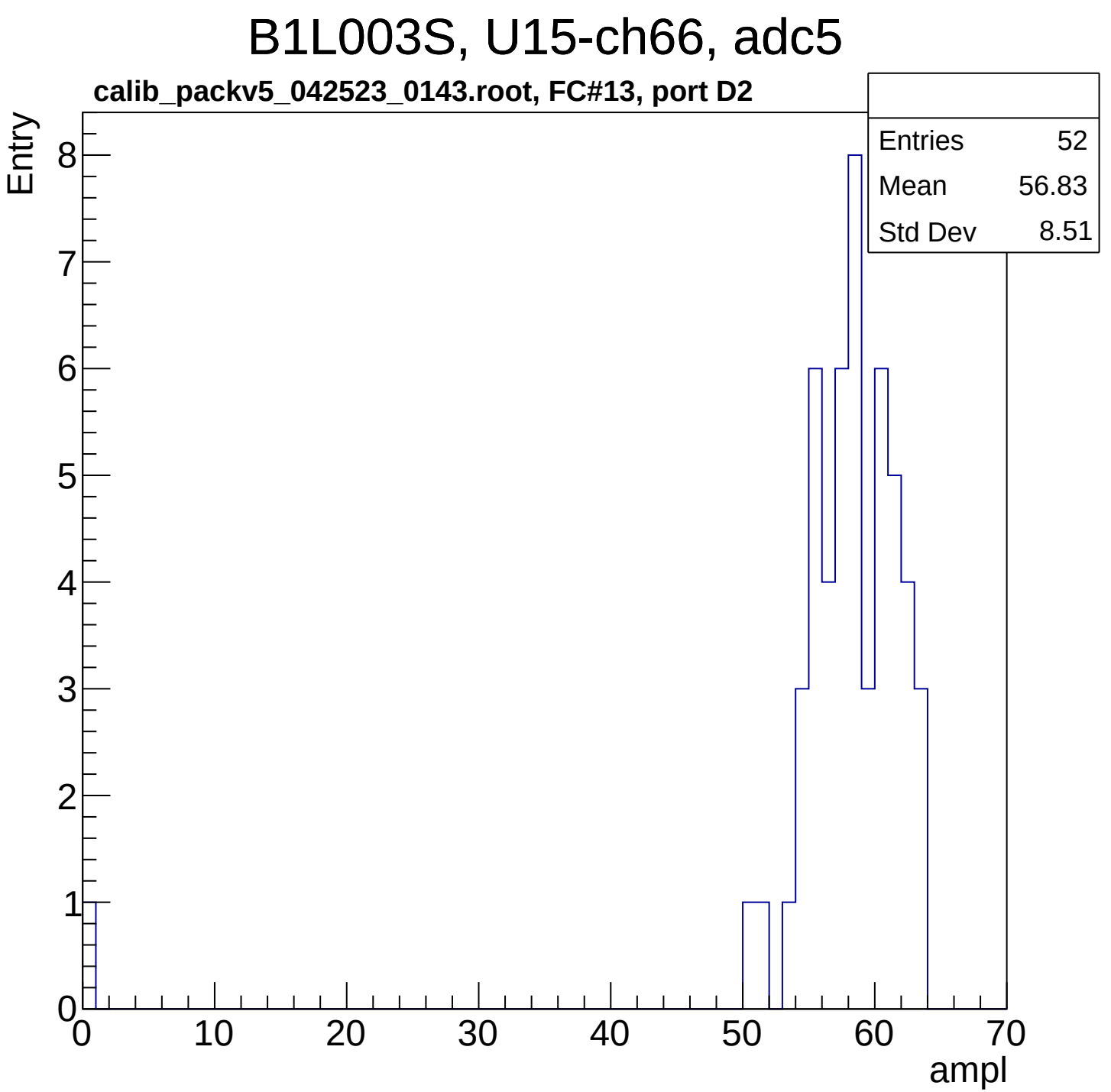
B1L003S, U15-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 56.83
Std Dev 8.51
Entry
ampl
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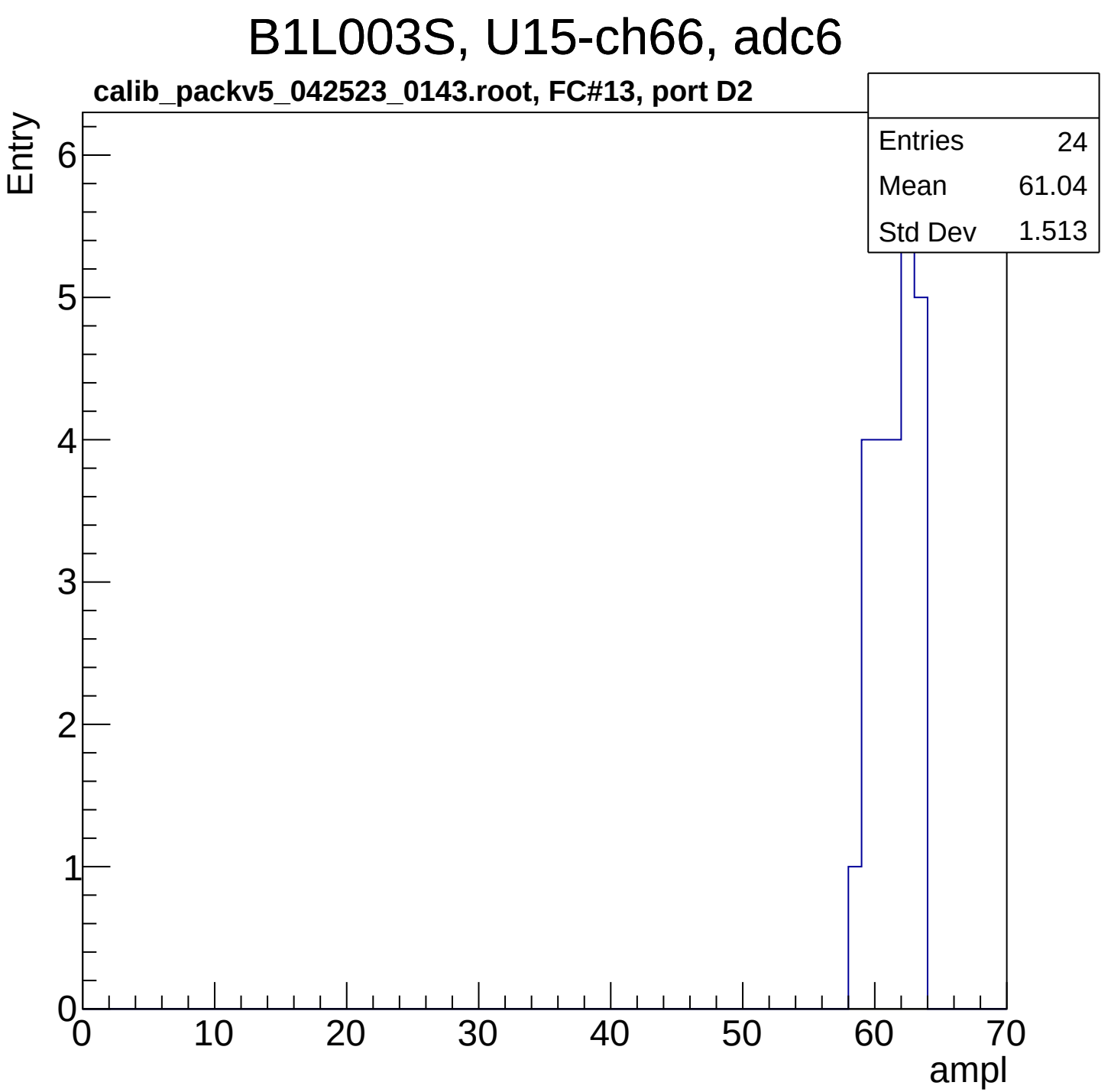
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 24
Mean 61.04
Std Dev 1.513
Entry
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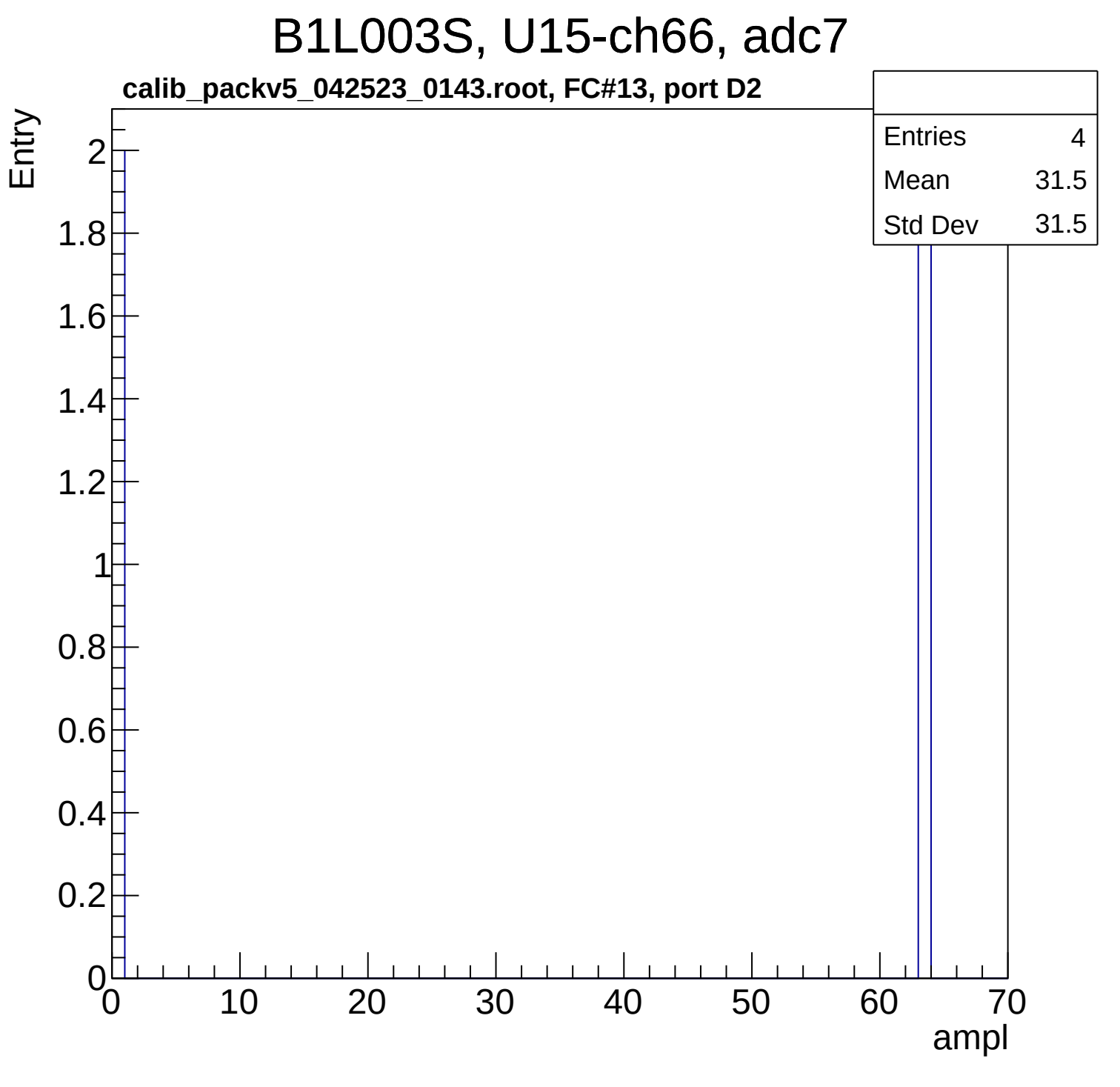
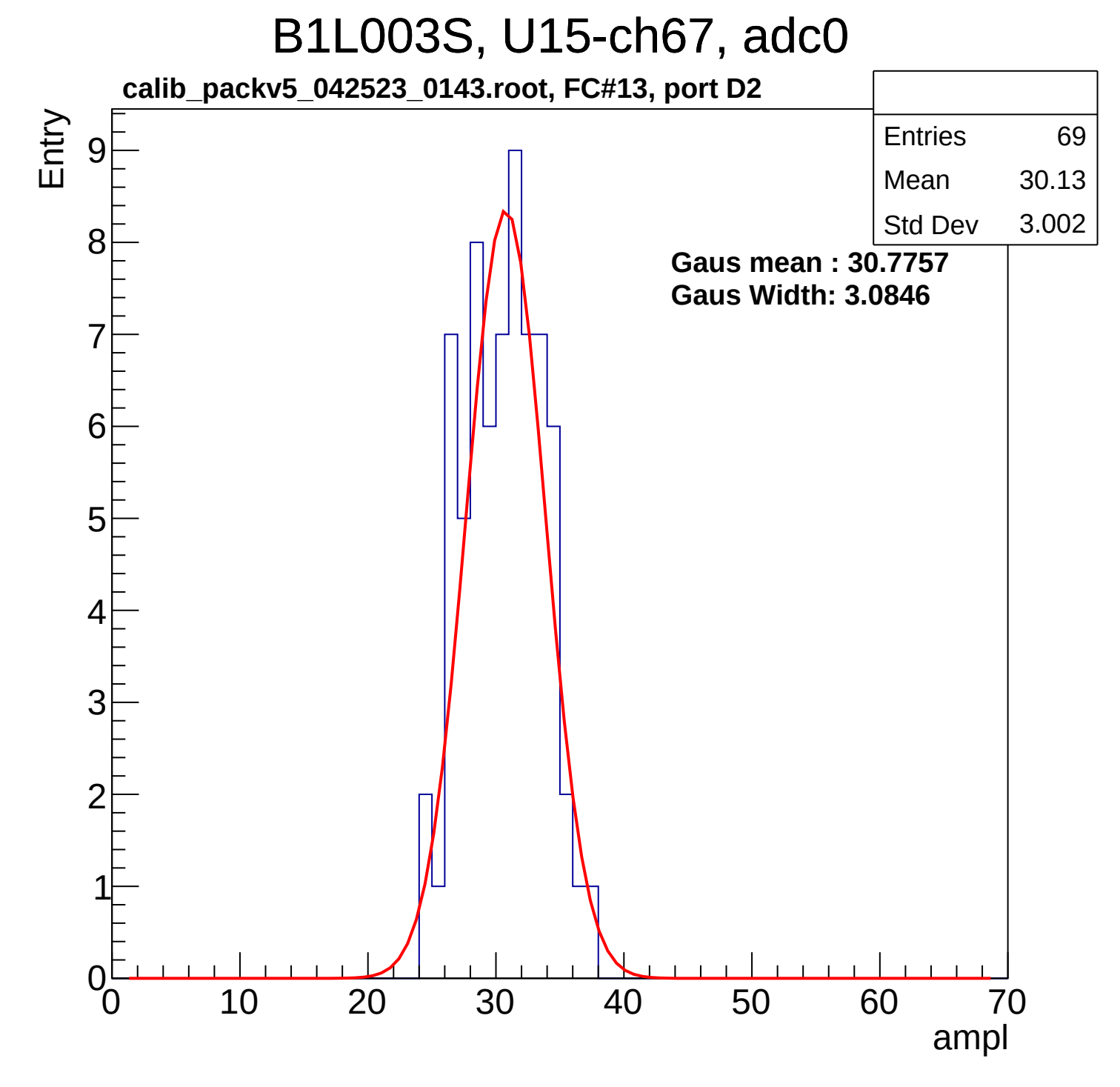
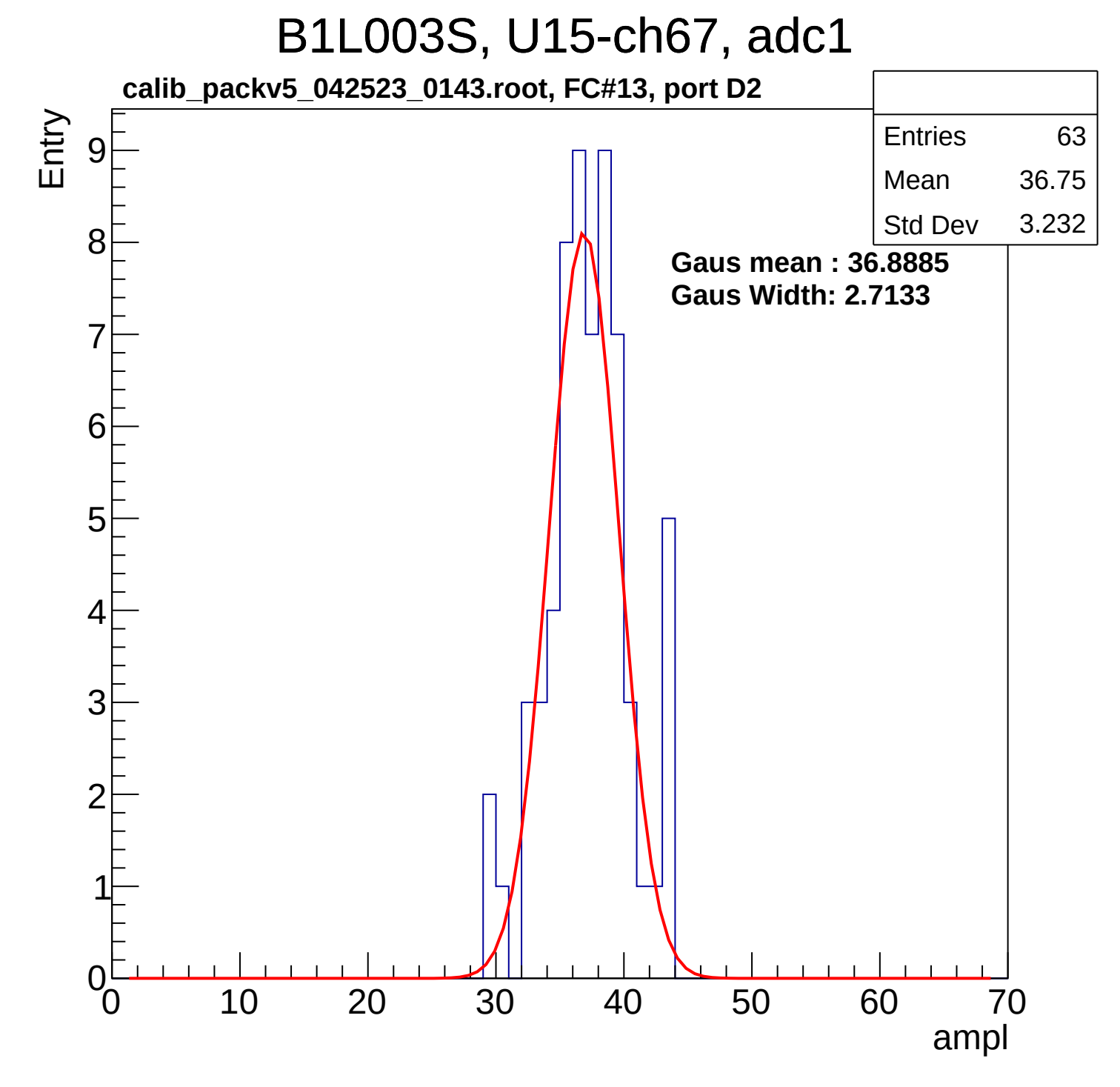
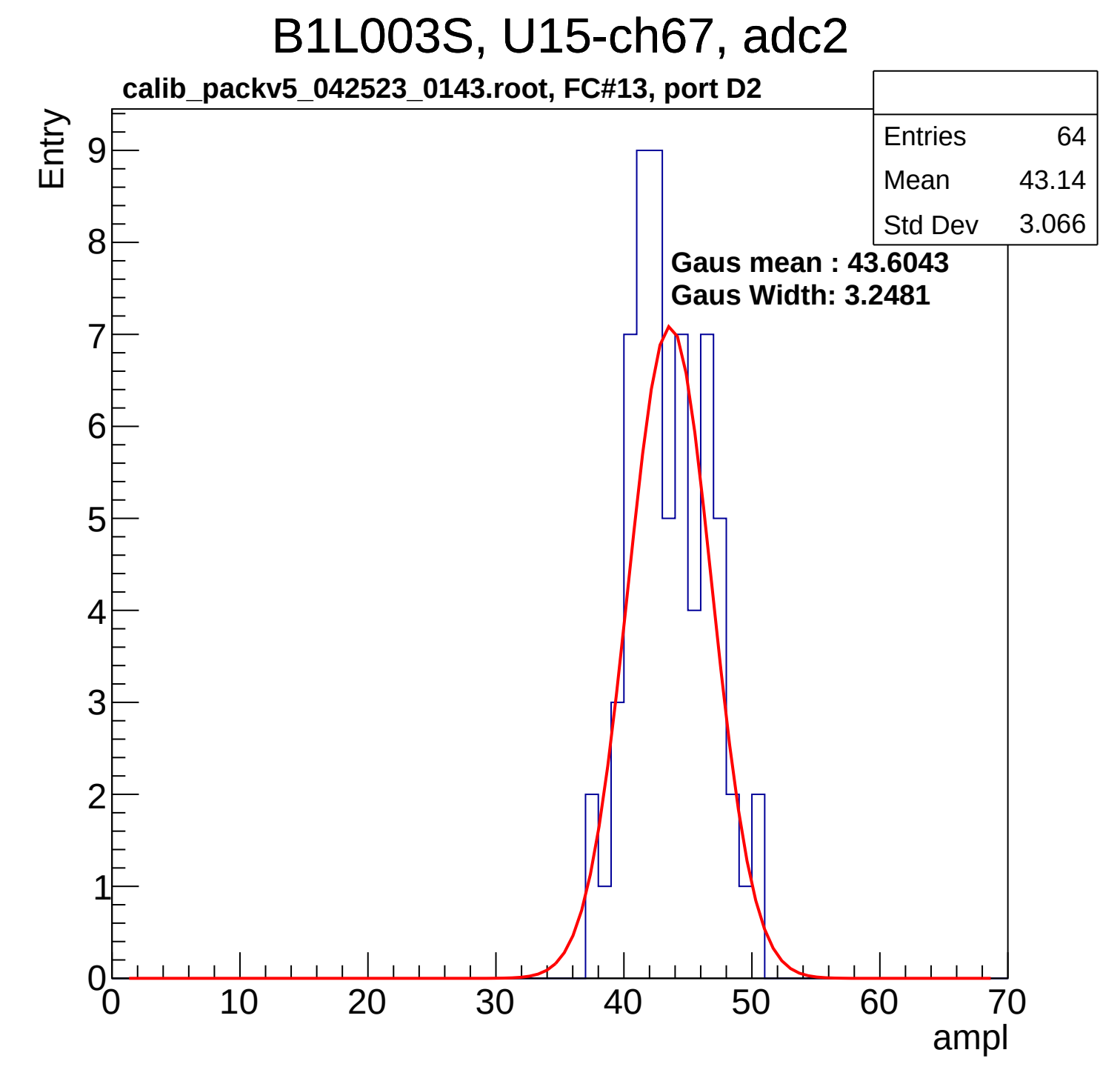
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 30.13
Std Dev 3.002
Gaus mean : 30.7757
Gaus Width: 3.0846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.75
Std Dev 3.232
Gaus mean : 36.8885
Gaus Width: 2.7133
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.14
Std Dev 3.066
Gaus mean : 43.6043
Gaus Width: 3.2481
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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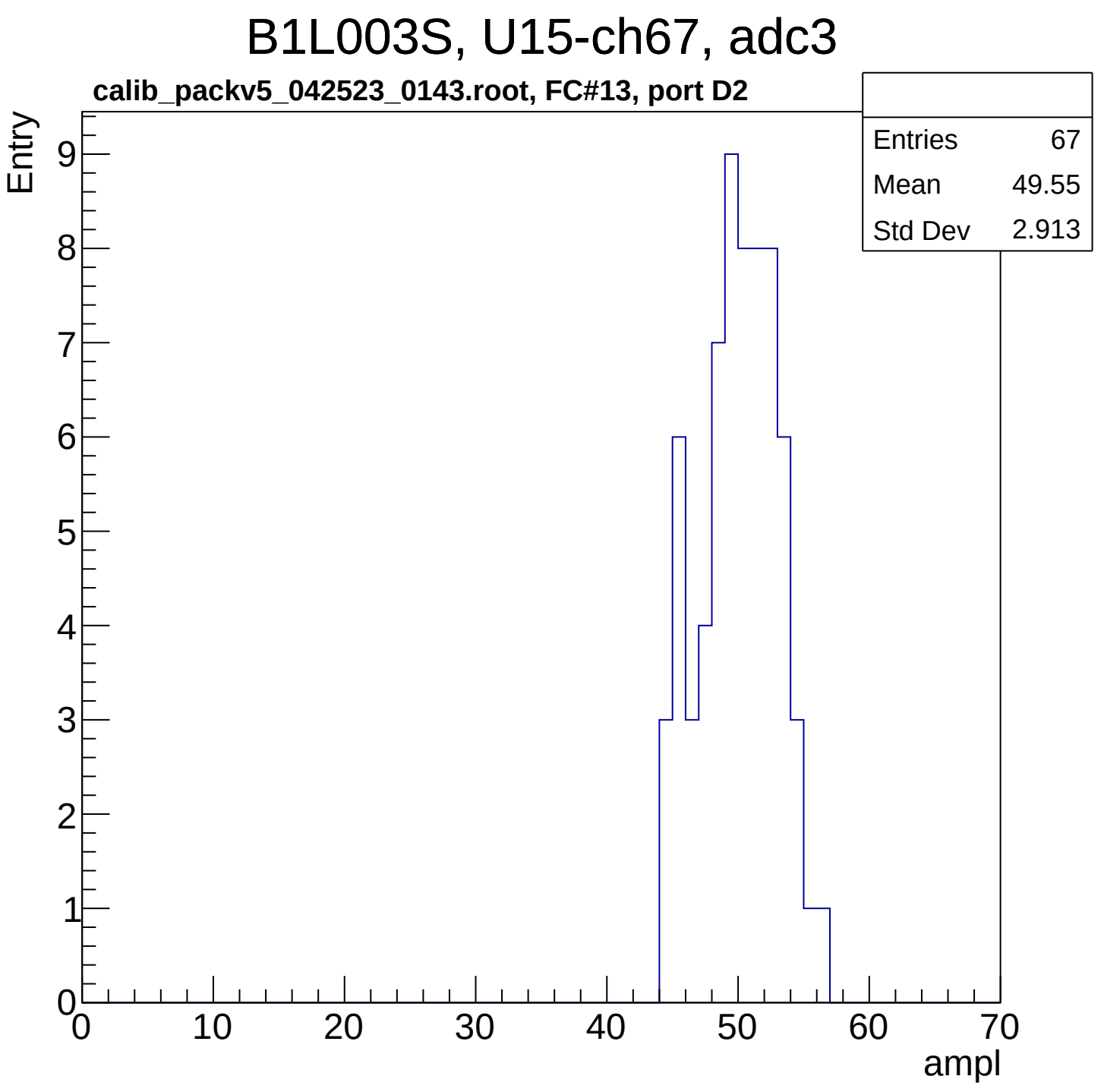
B1L003S, U15-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.55
Std Dev 2.913
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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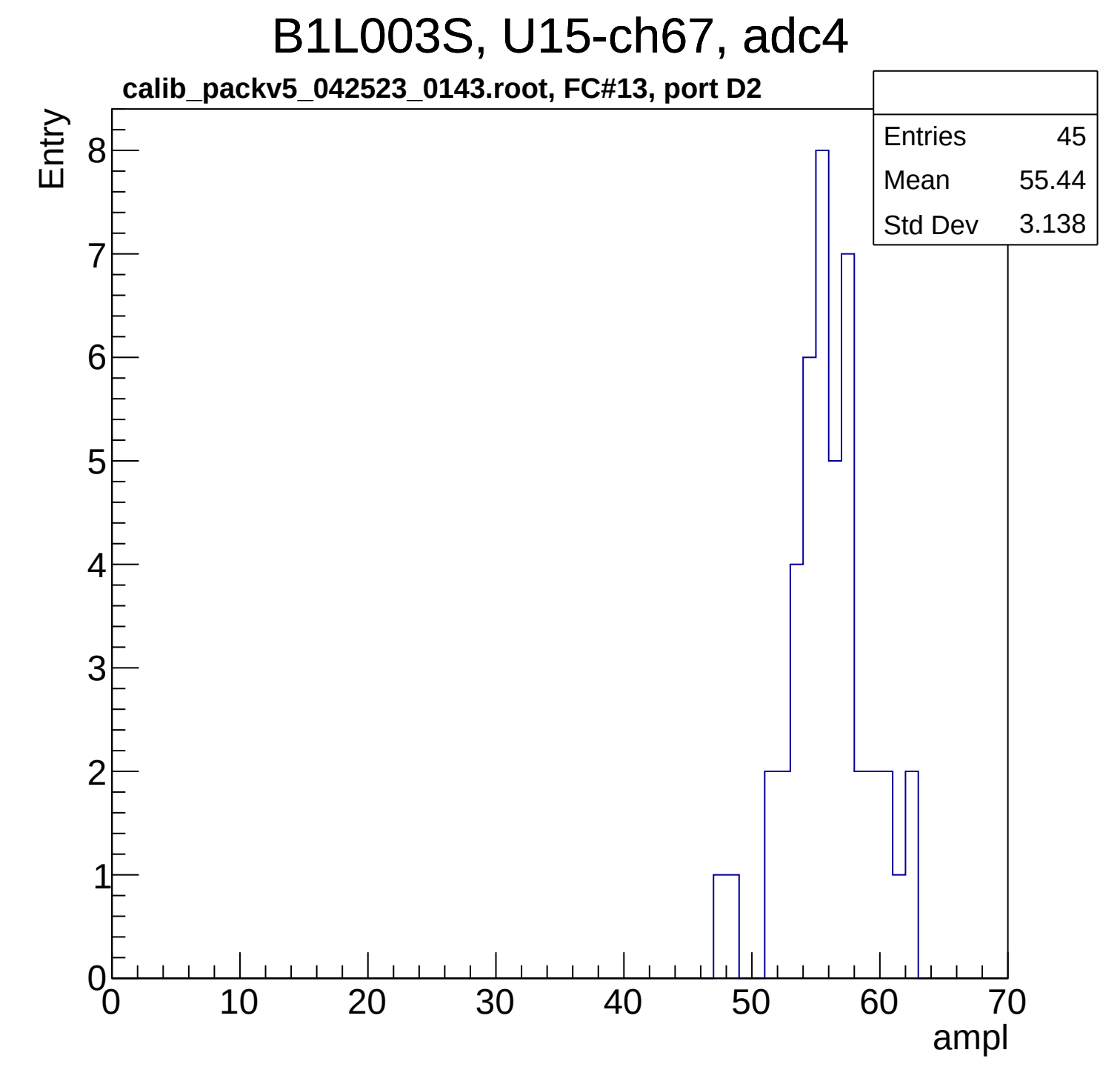
B1L003S, U15-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 55.44
Std Dev 3.138
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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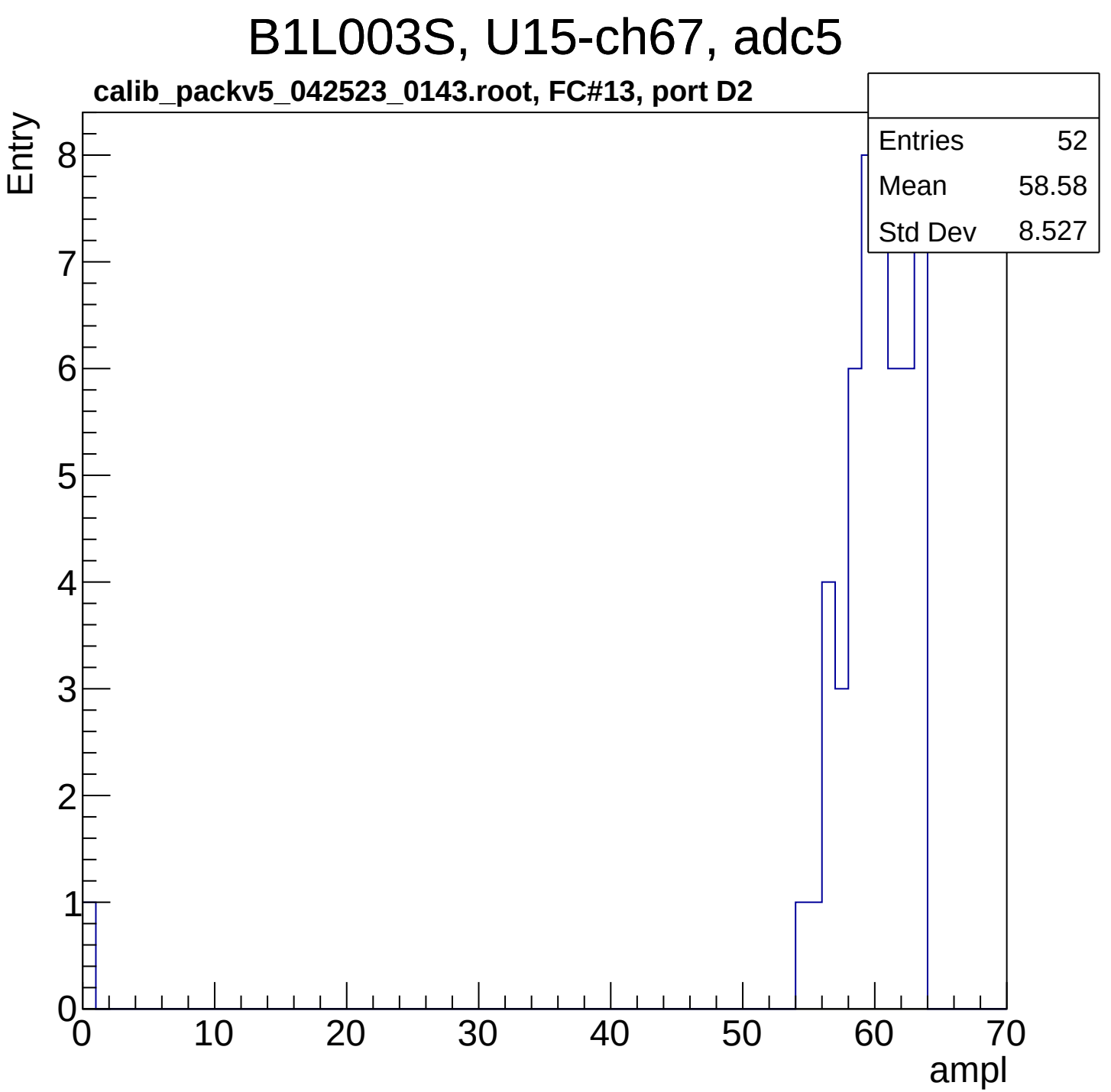
B1L003S, U15-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.58
Std Dev 8.527
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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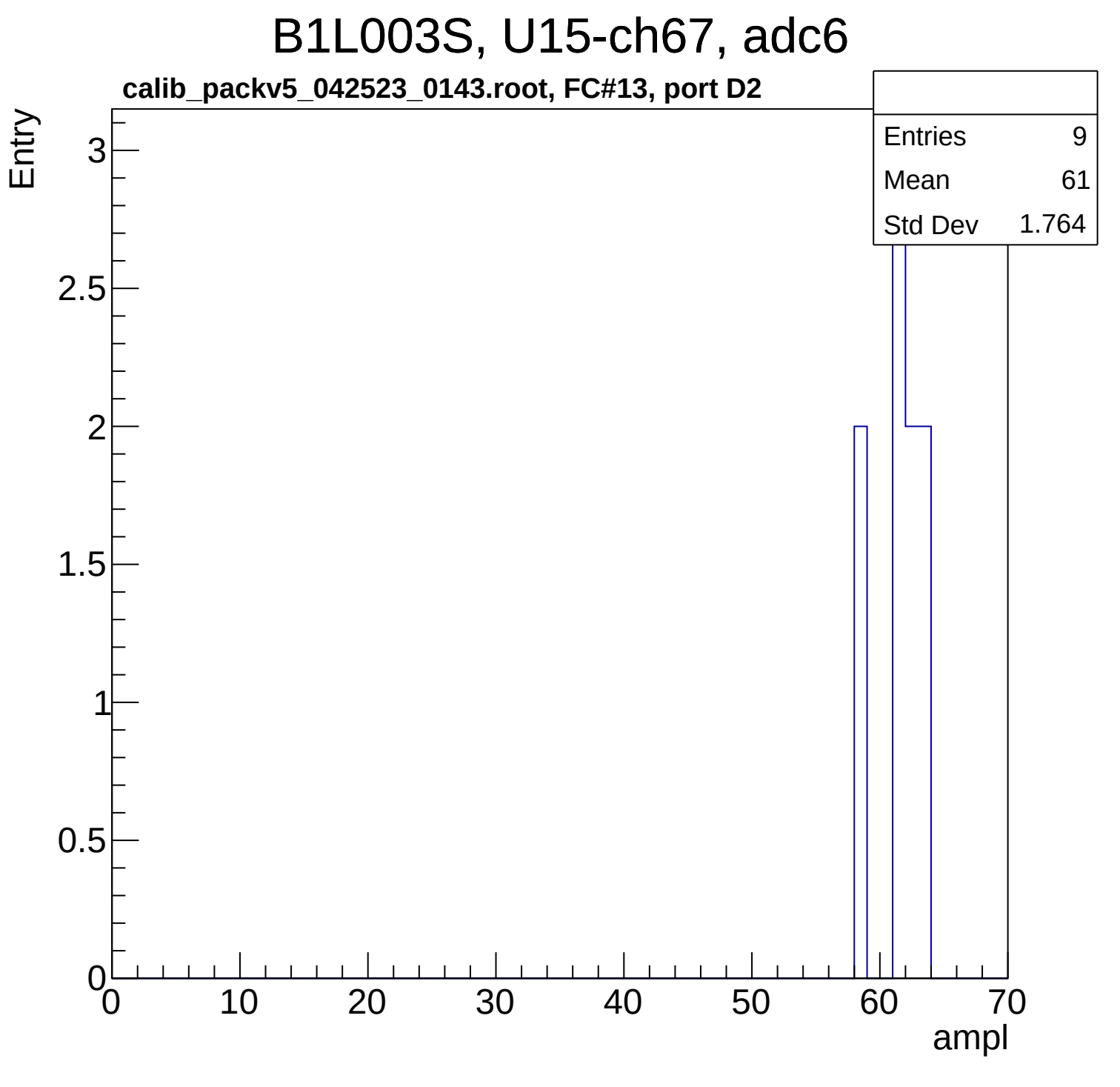
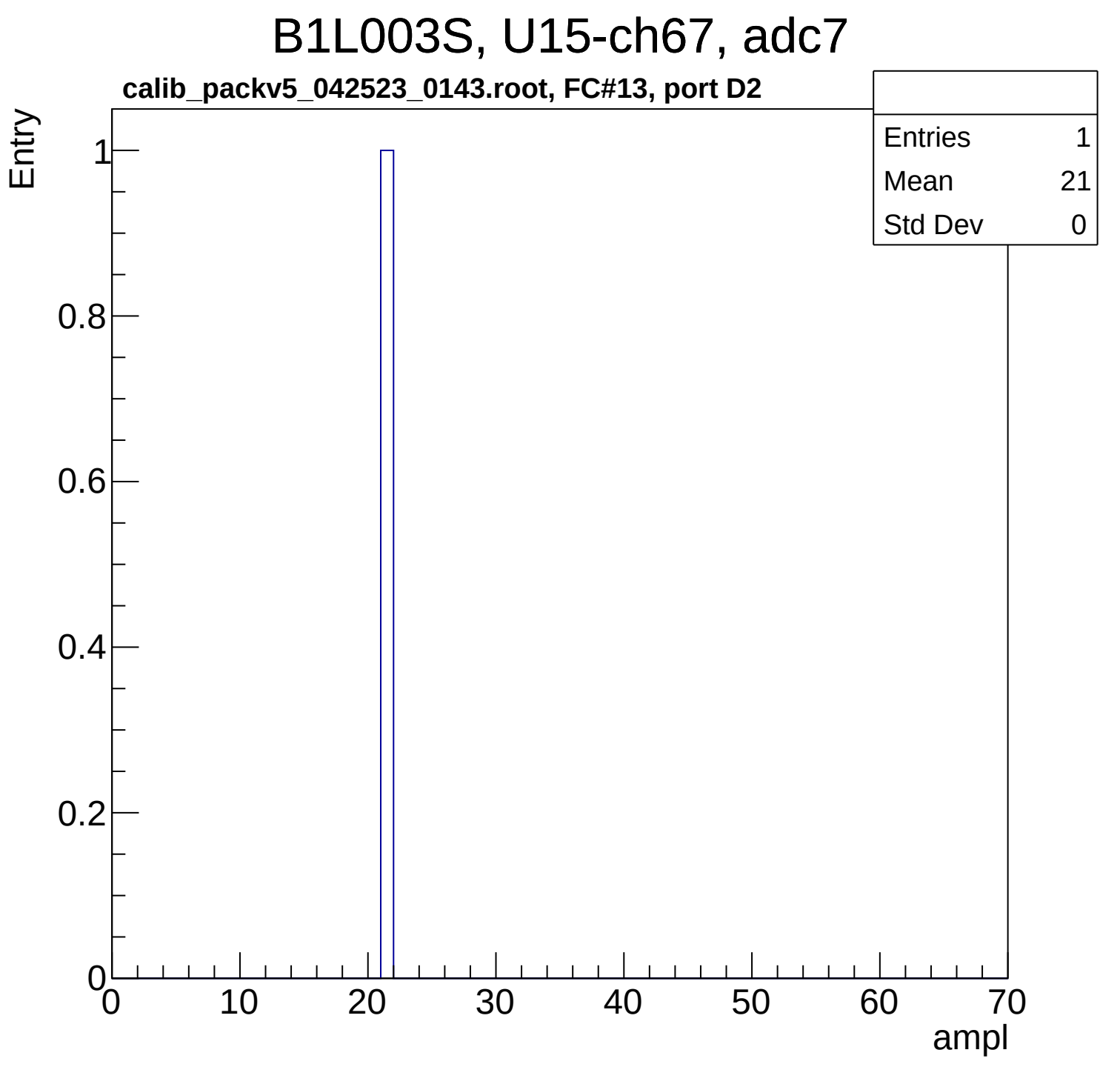
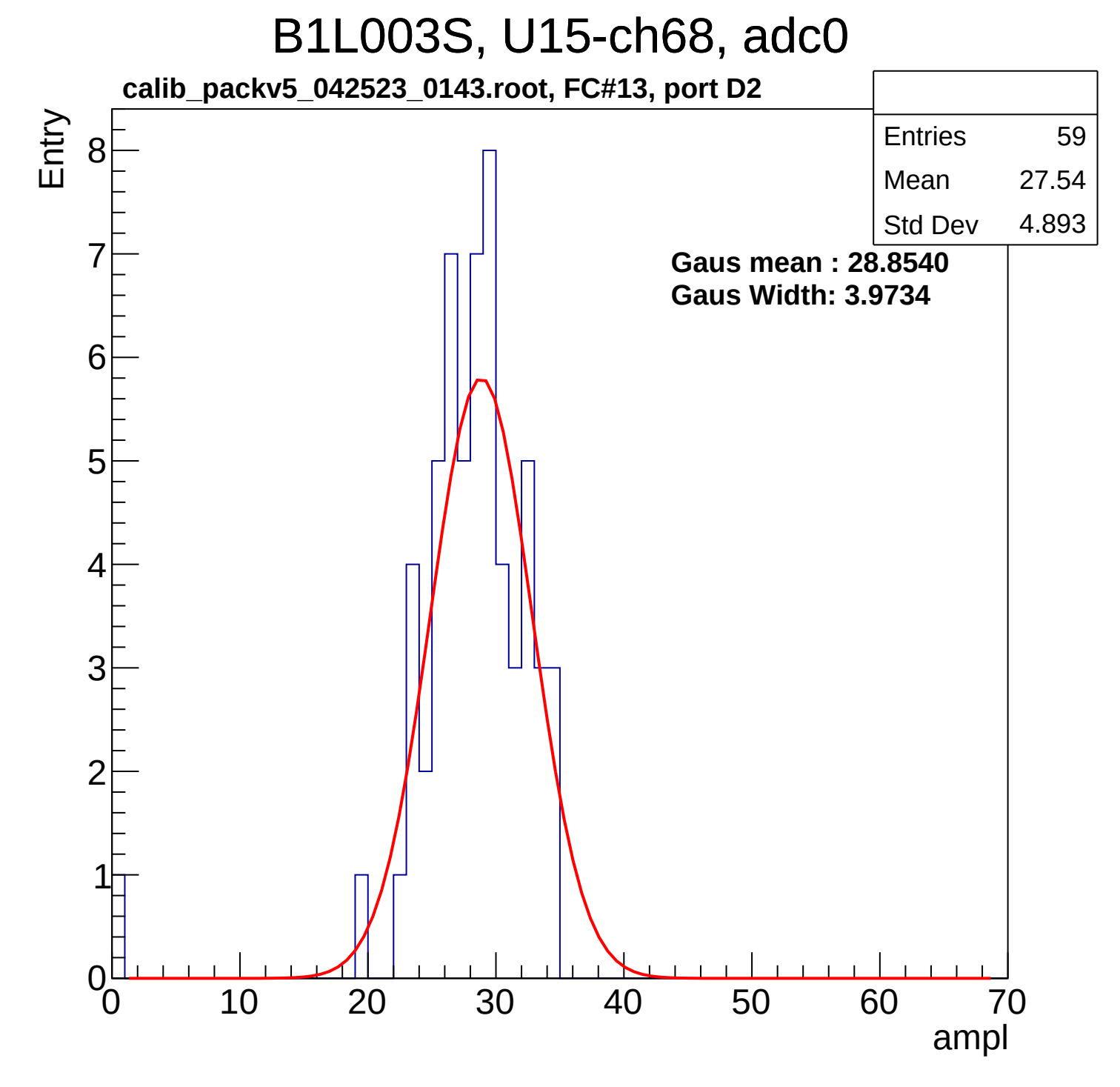
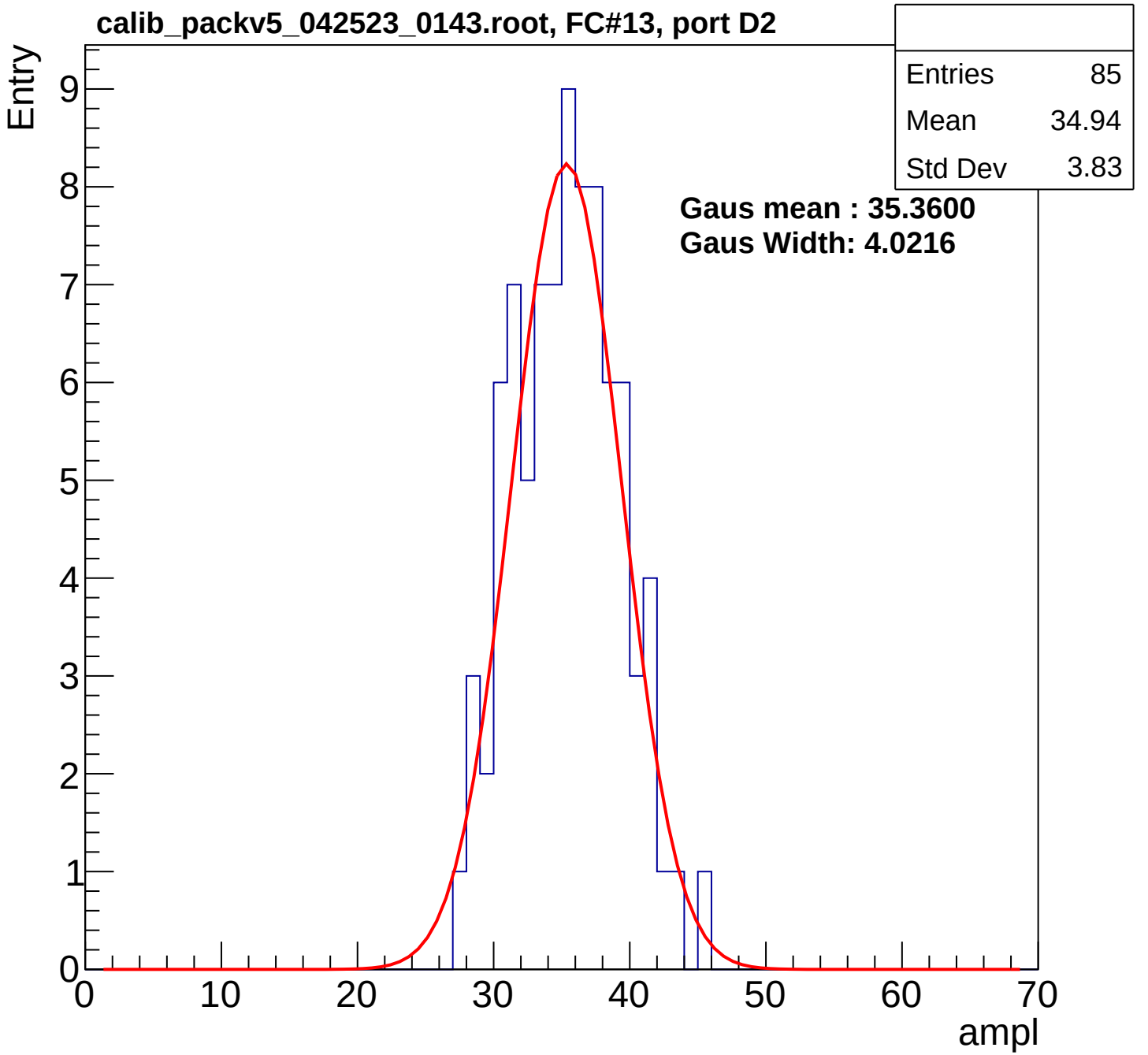

B1L003S, U15-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61
Std Dev 1.764
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 27.54
Std Dev 4.893
Gaus mean : 28.8540
Gaus Width: 3.9734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 34.94
Std Dev 3.83
Gaus mean : 35.3600
Gaus Width: 4.0216
Entry
ampl
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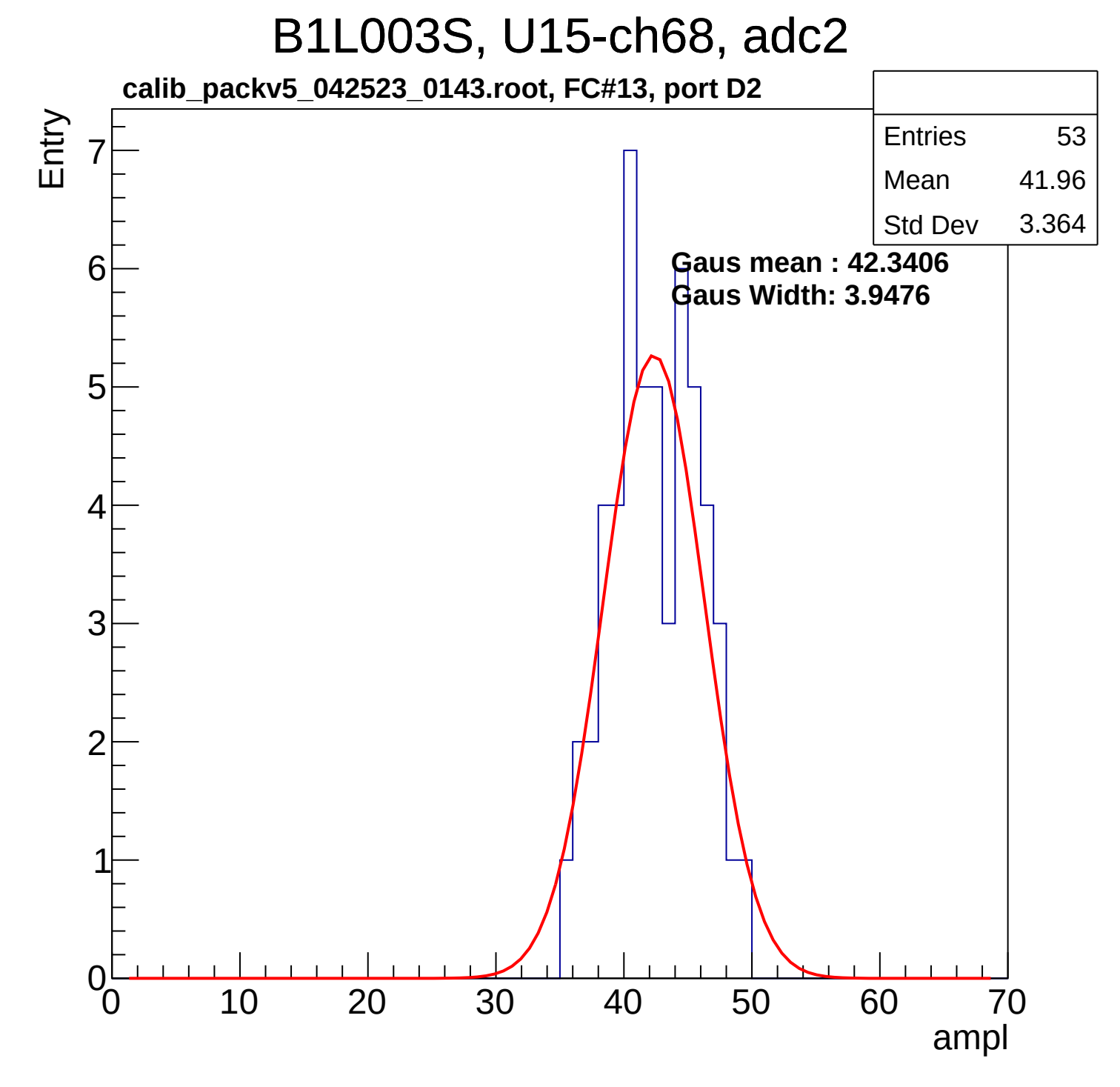
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 41.96
Std Dev 3.364
Gaus mean : 42.3406
Gaus Width: 3.9476
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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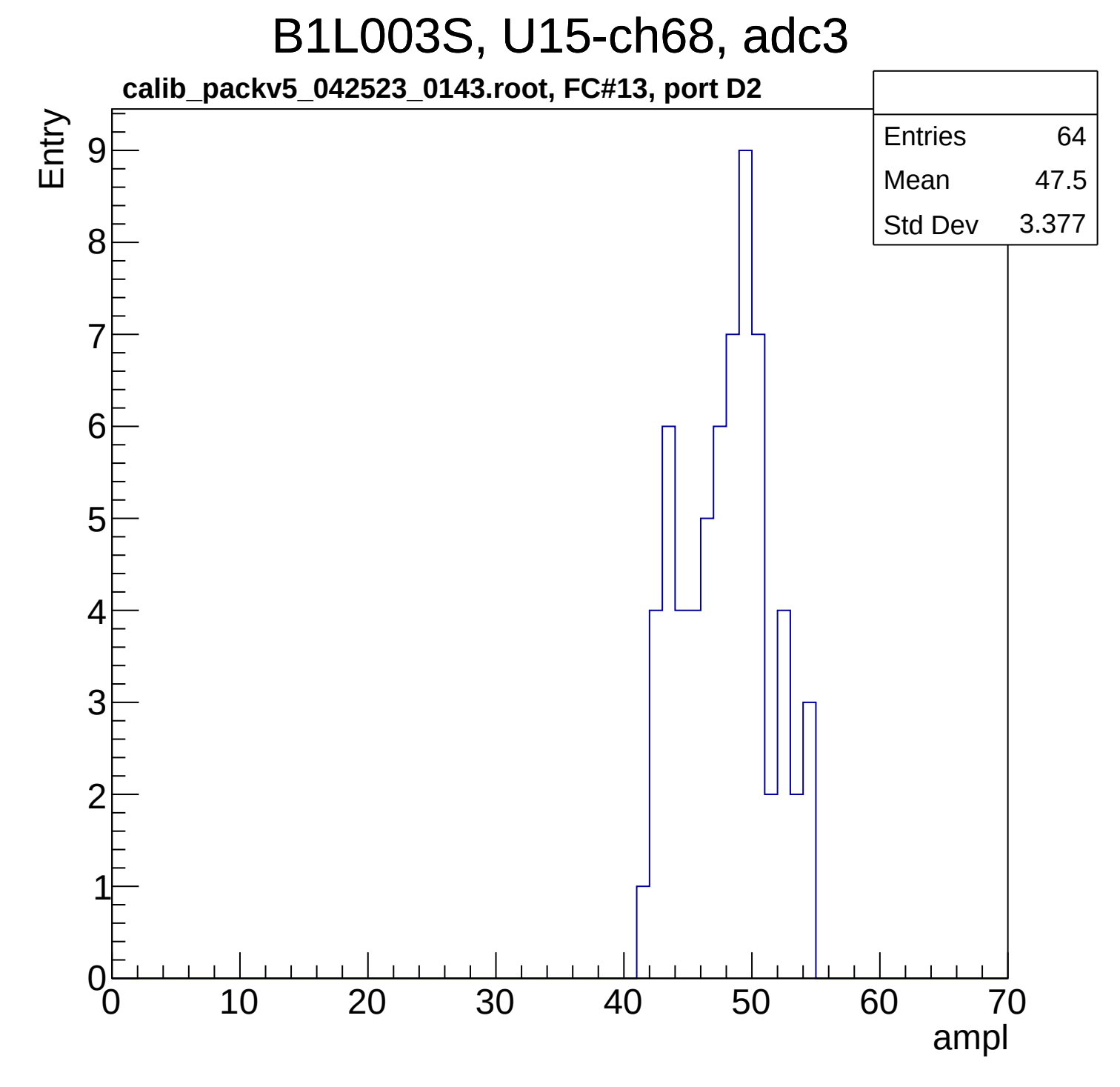
B1L003S, U15-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.5
Std Dev 3.377
Entry
ampl
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4
5
6
7
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20
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60
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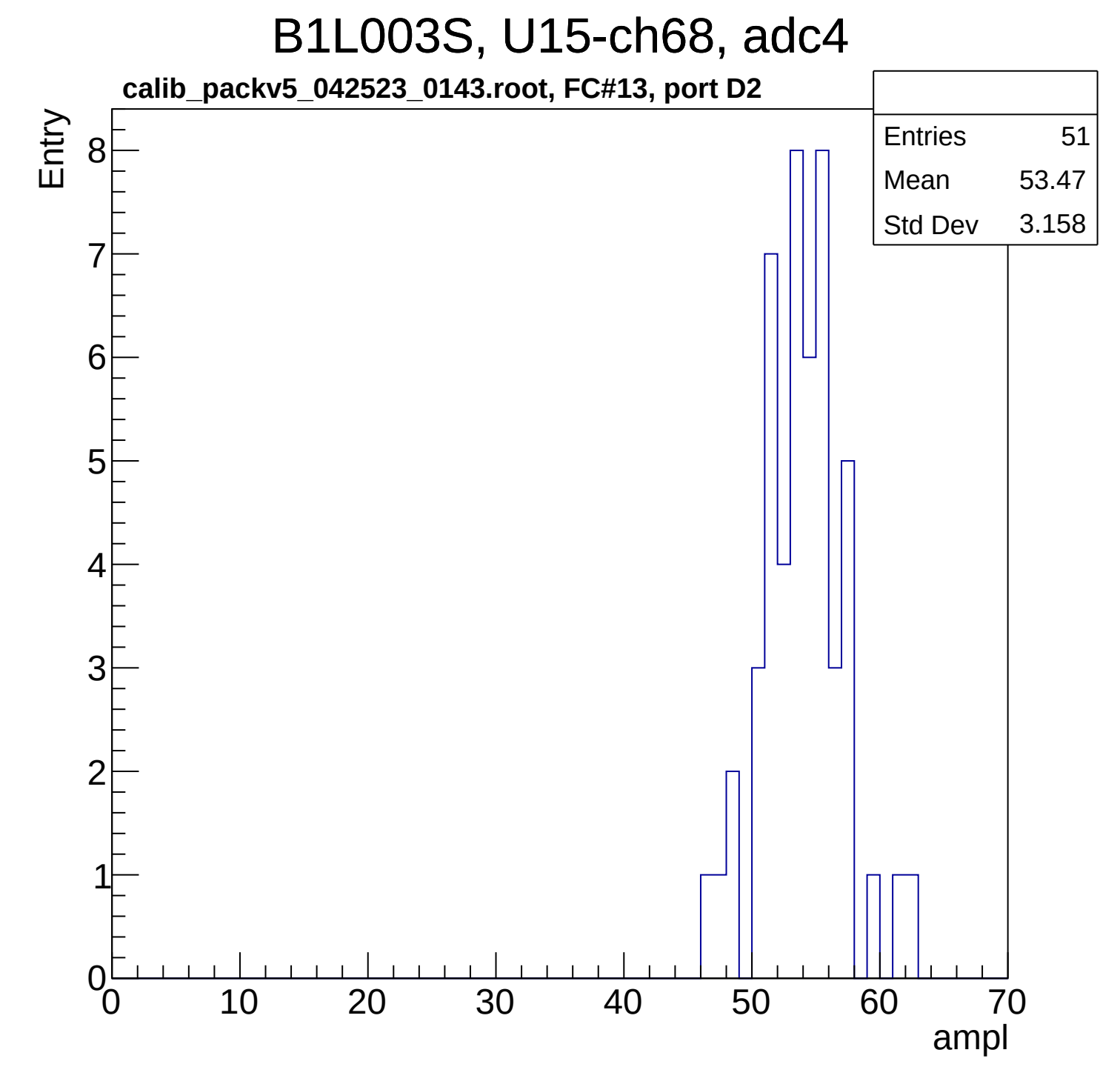
B1L003S, U15-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.47
Std Dev 3.158
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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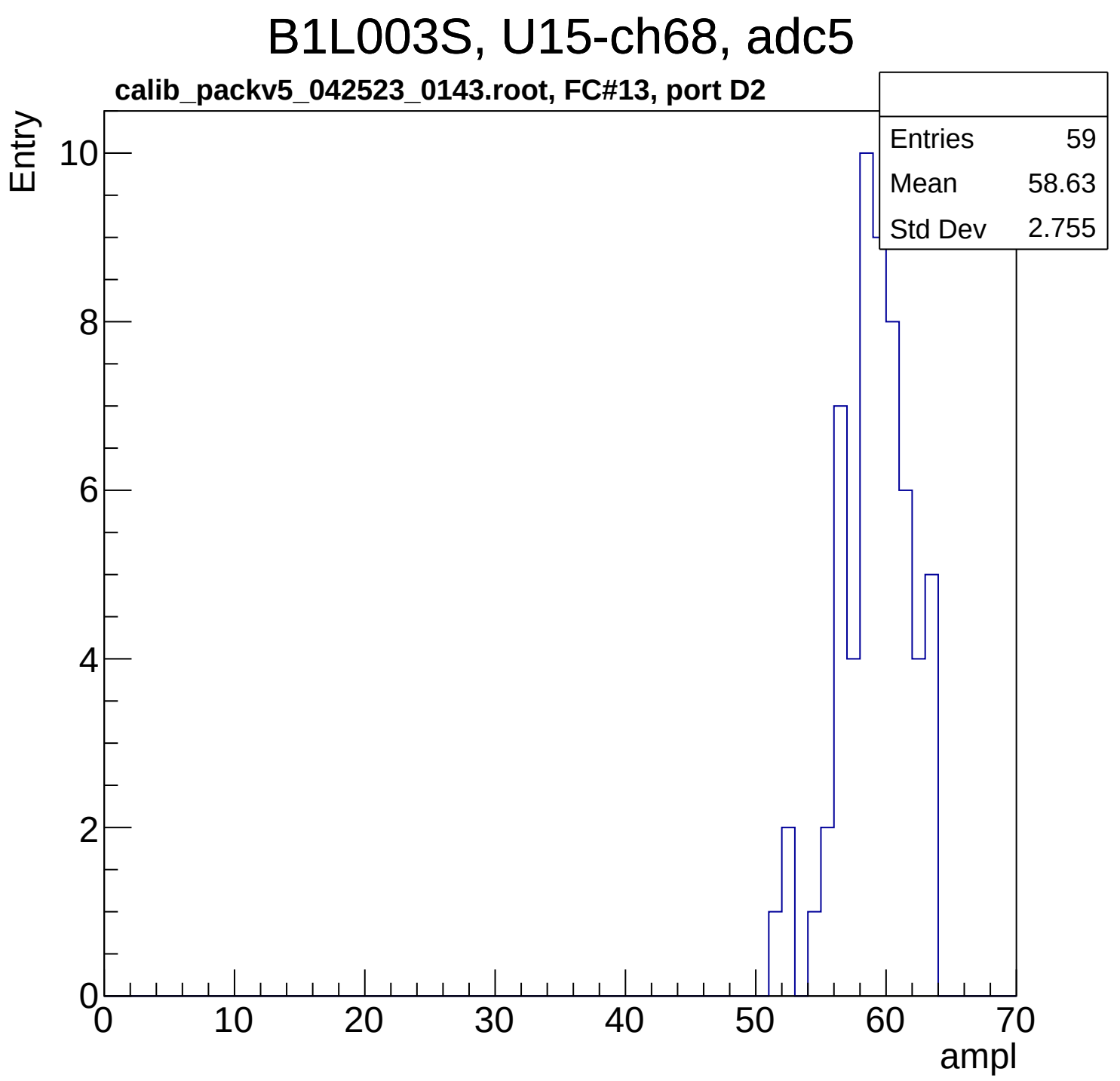
B1L003S, U15-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.63
Std Dev 2.755
Entry
0
2
4
6
8
10
0
10
20
30
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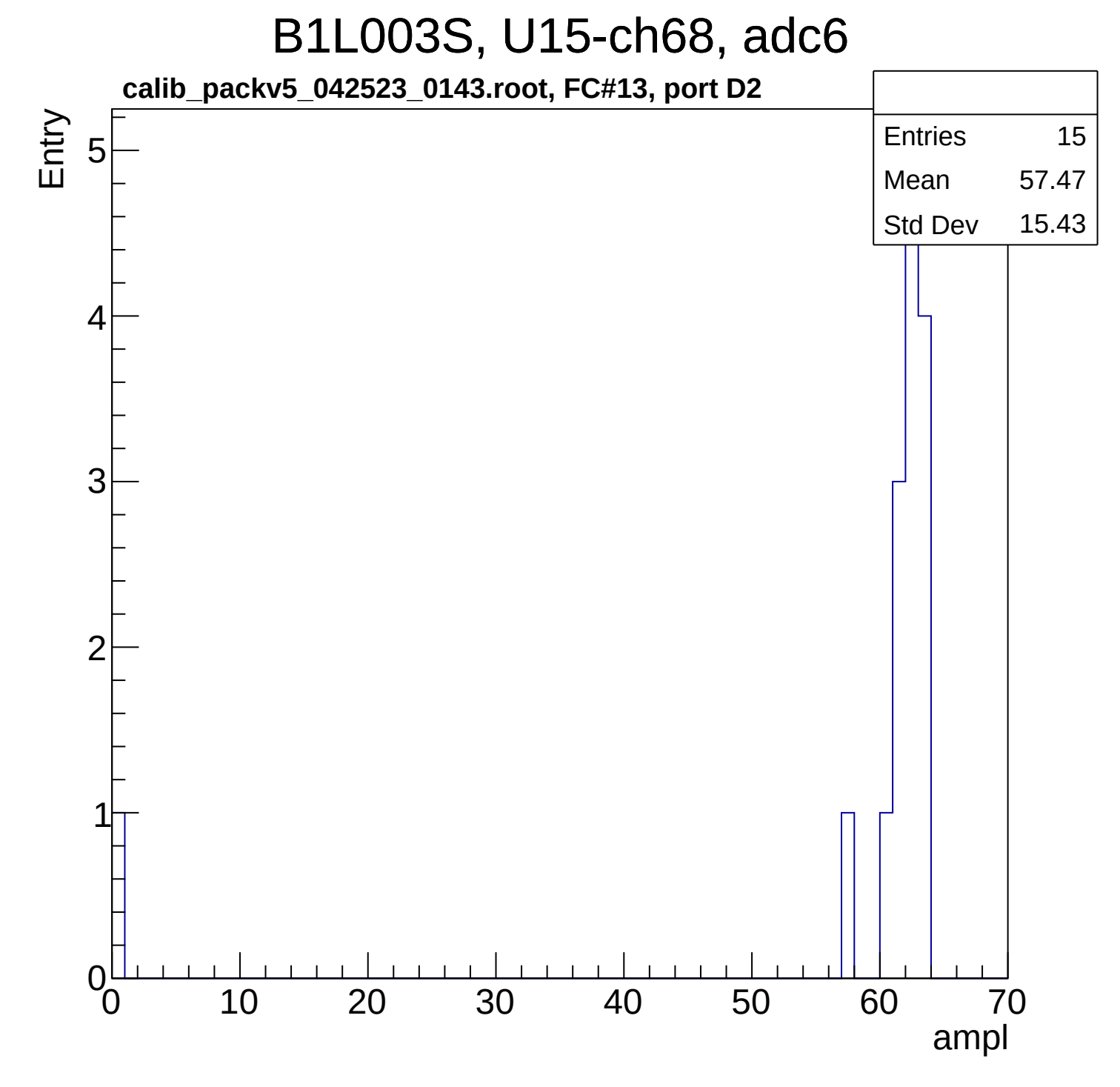
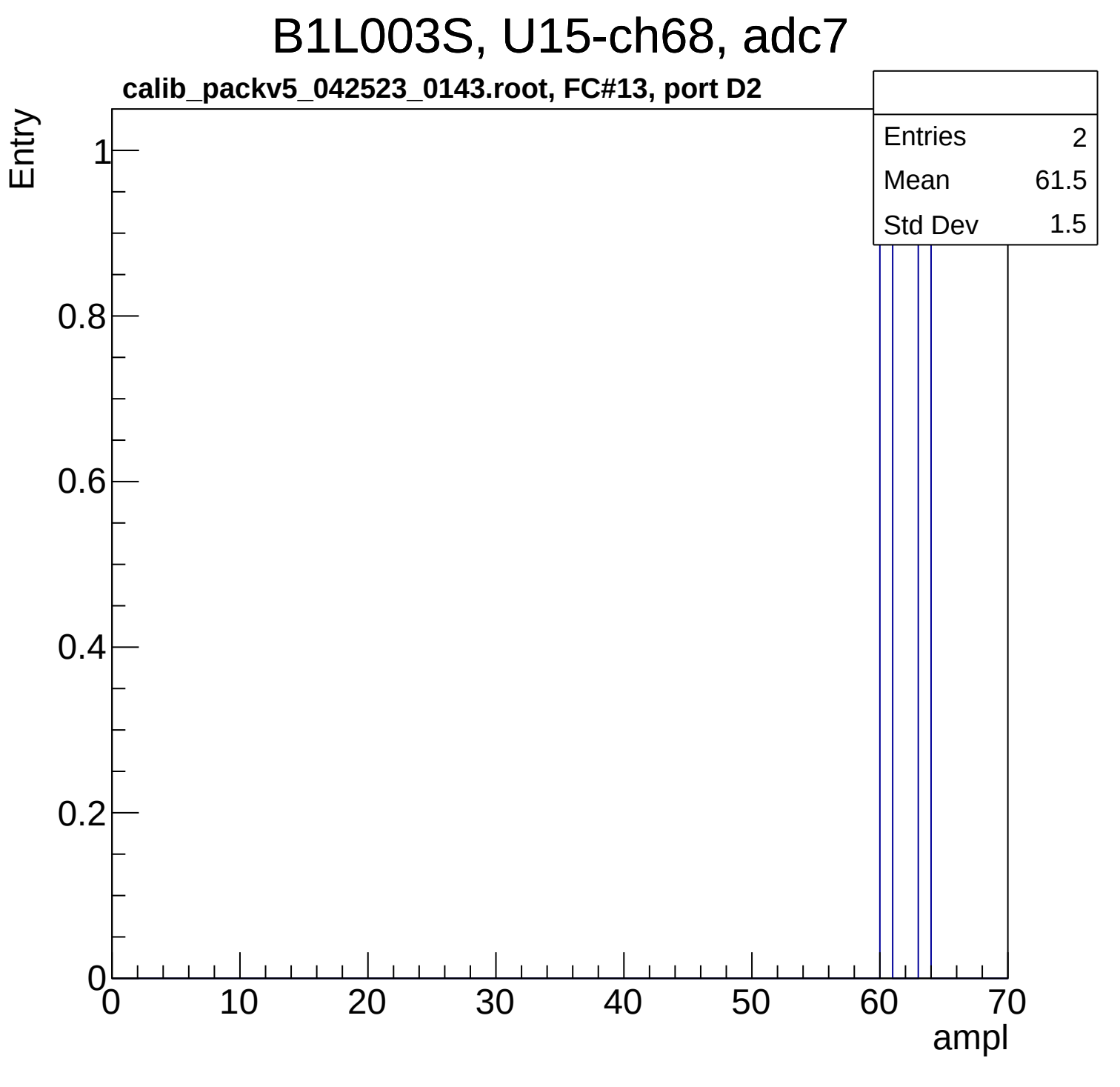
50
60
70
ampl


B1L003S, U15-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.47
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
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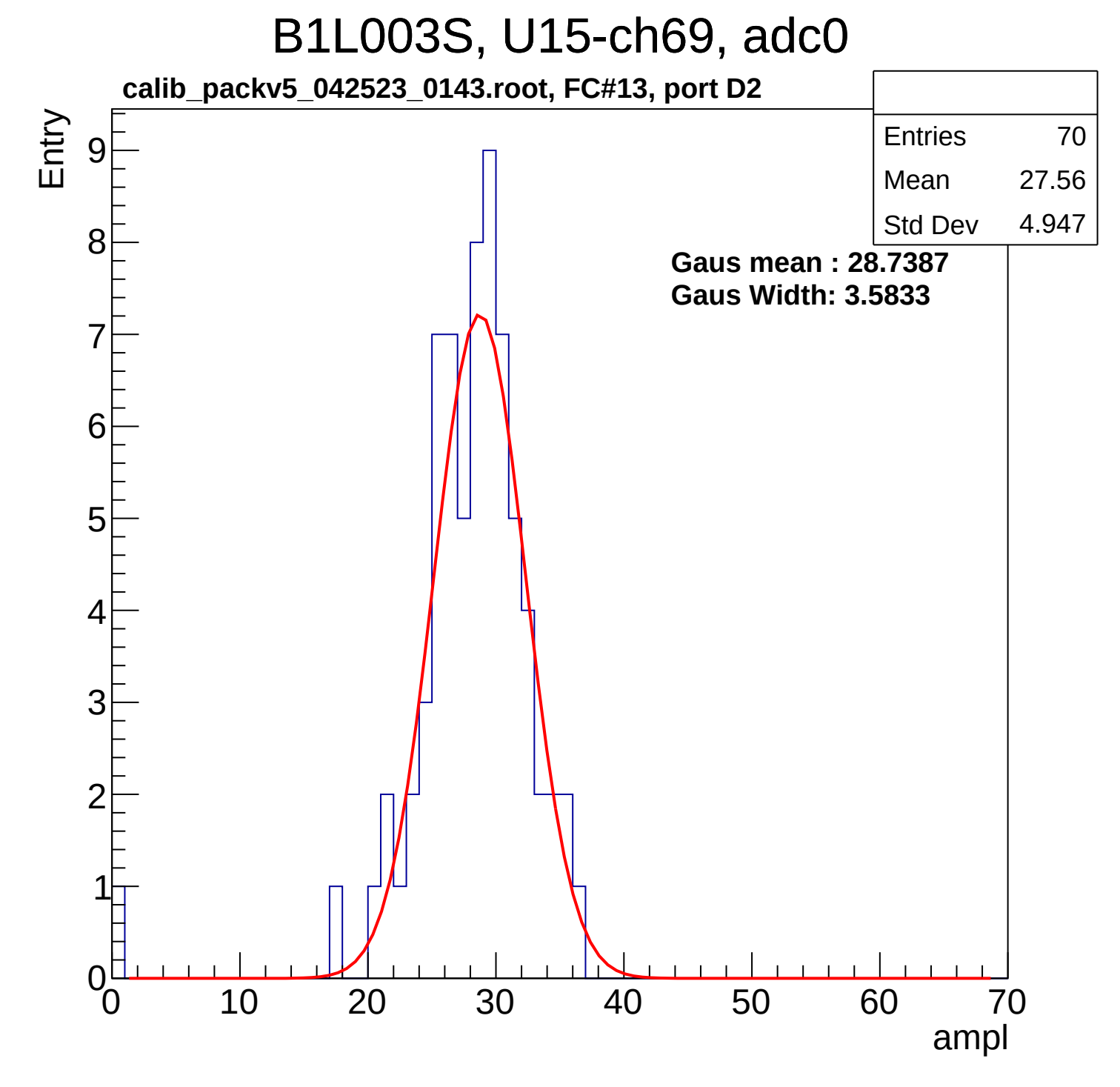
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.56
Std Dev 4.947
Gaus mean : 28.7387
Gaus Width: 3.5833
Entry
ampl
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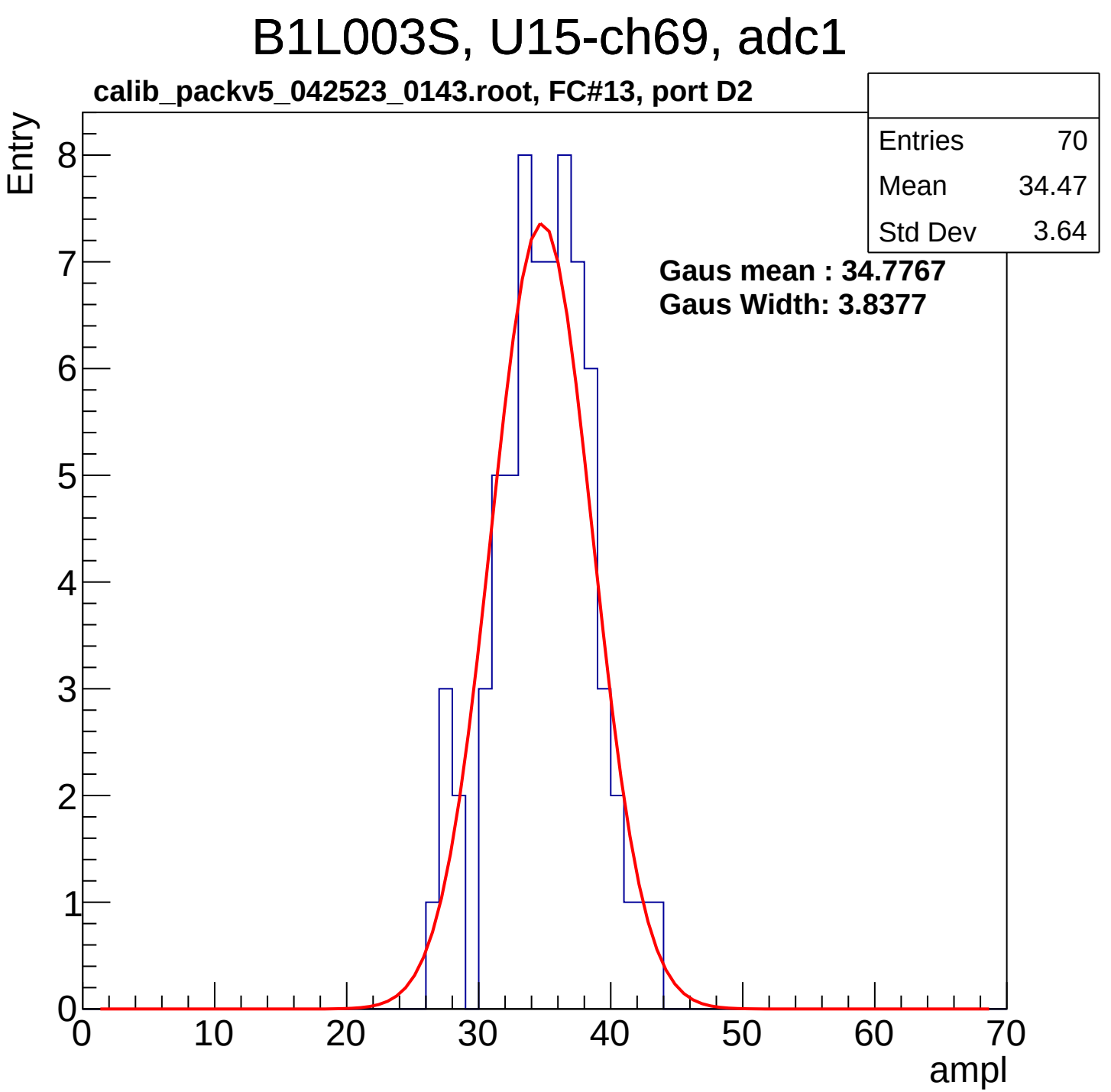
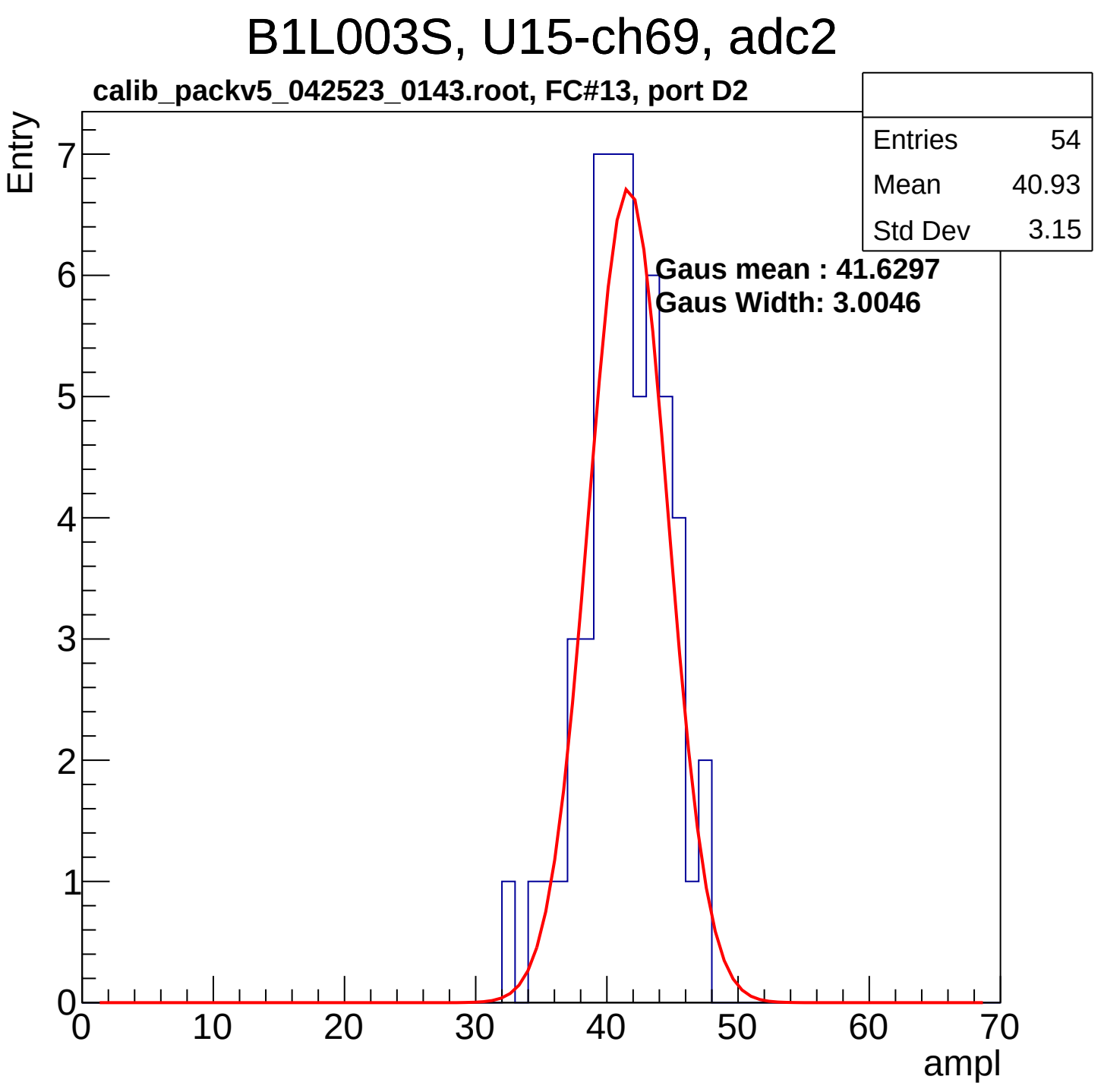
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 34.47
Std Dev 3.64
Gaus mean : 34.7767
Gaus Width: 3.8377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.93
Std Dev 3.15
Gaus mean : 41.6297
Gaus Width: 3.0046
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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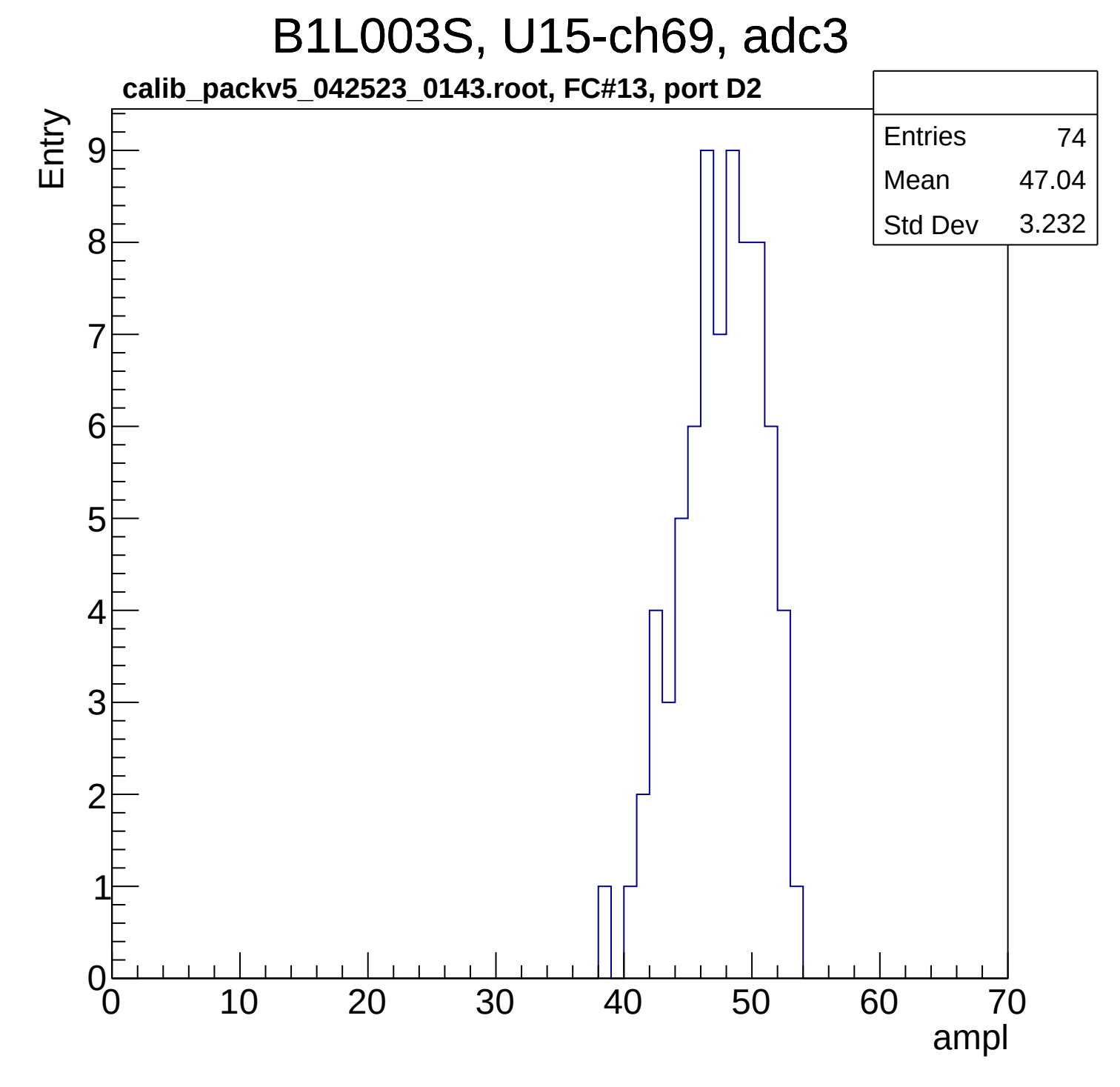
B1L003S, U15-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 47.04
Std Dev 3.232
Entry
ampl
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2
3
4
5
6
7
8
9
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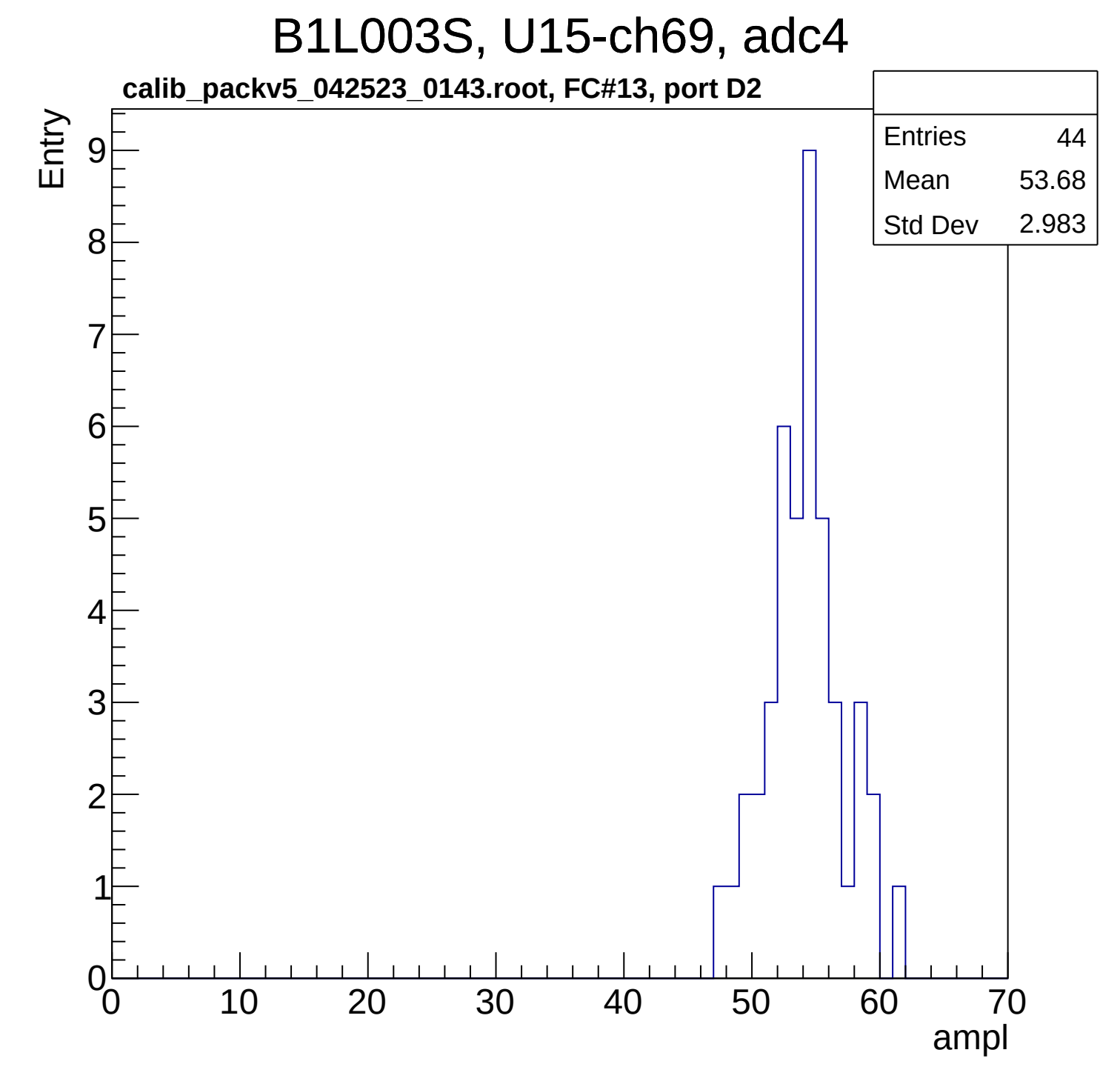
B1L003S, U15-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 53.68
Std Dev 2.983
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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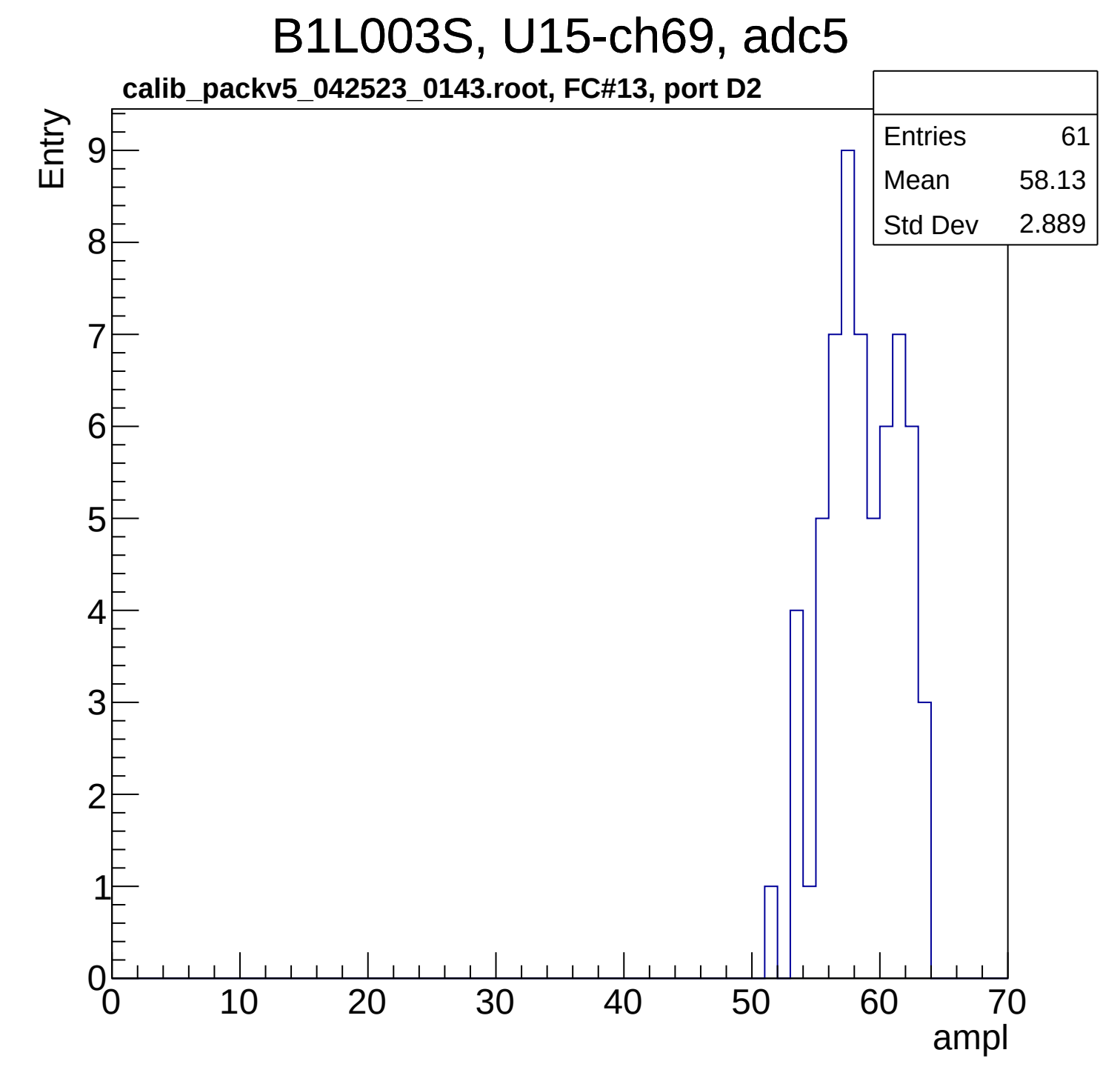
B1L003S, U15-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.13
Std Dev 2.889
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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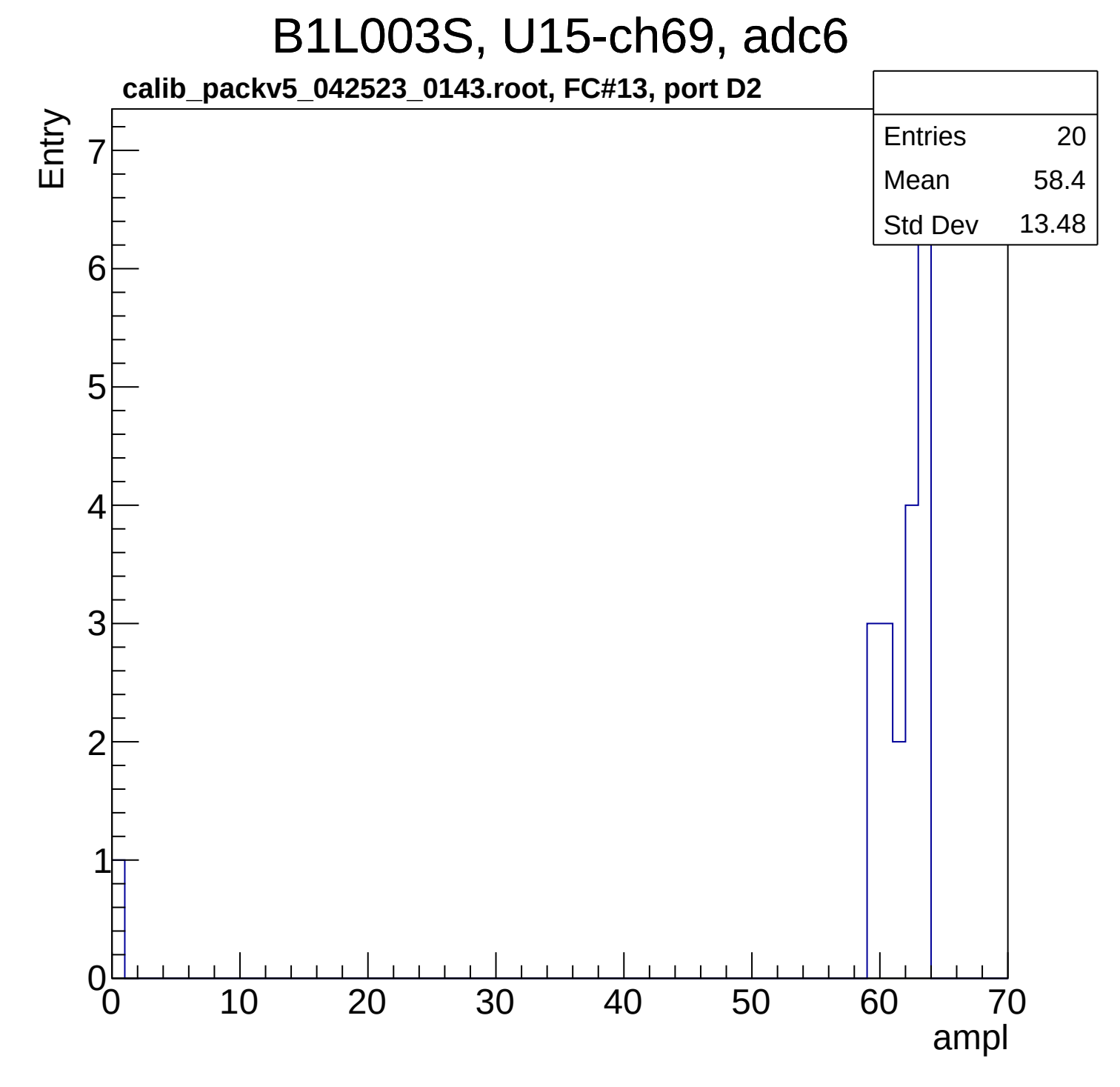
B1L003S, U15-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.4
Std Dev 13.48
Entry
0
1
2
3
4
5
6
7
0
10
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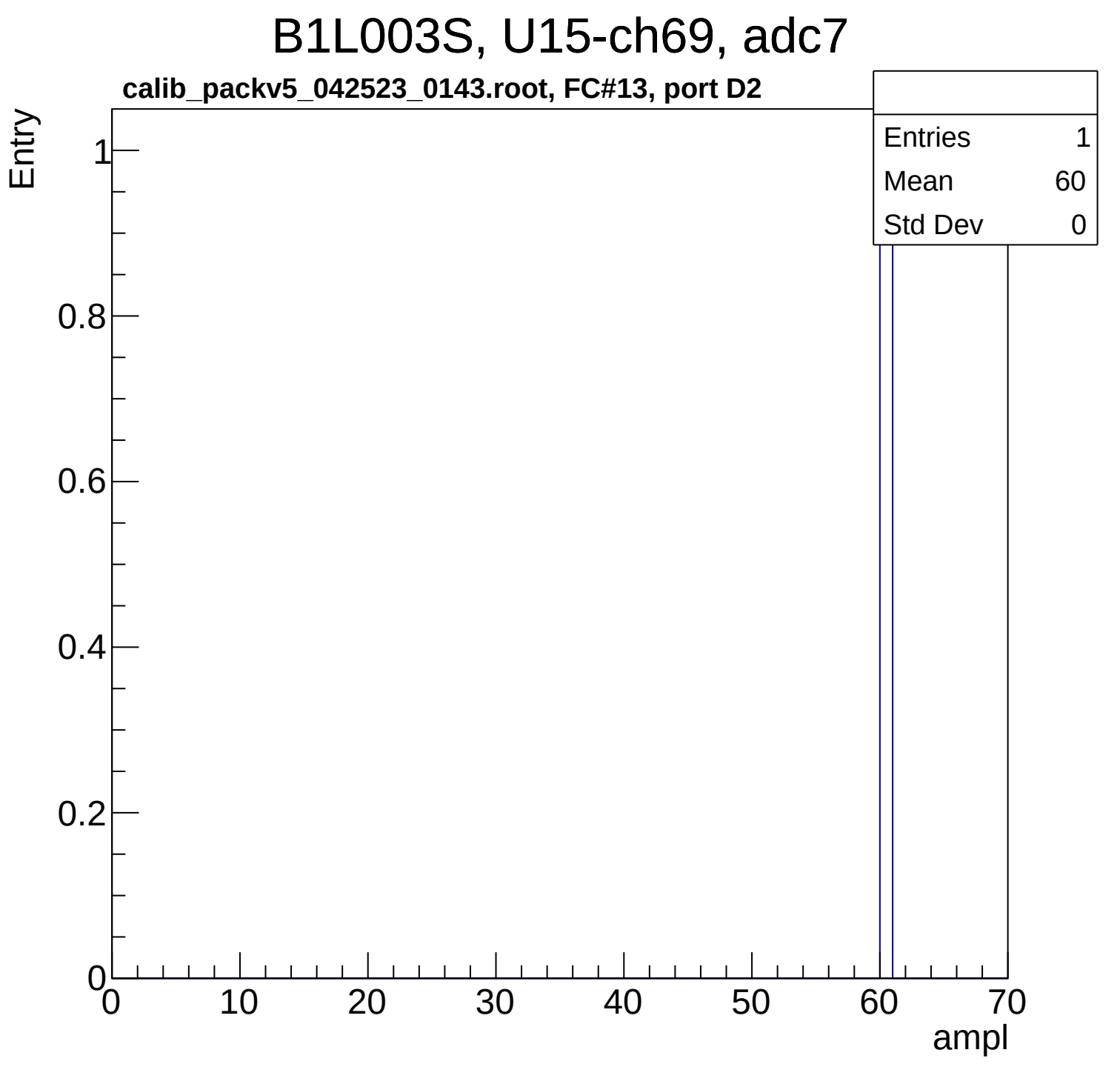
50
60
70
ampl


B1L003S, U15-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 60
Std Dev 0
Entry
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0.6
0.4
0.2
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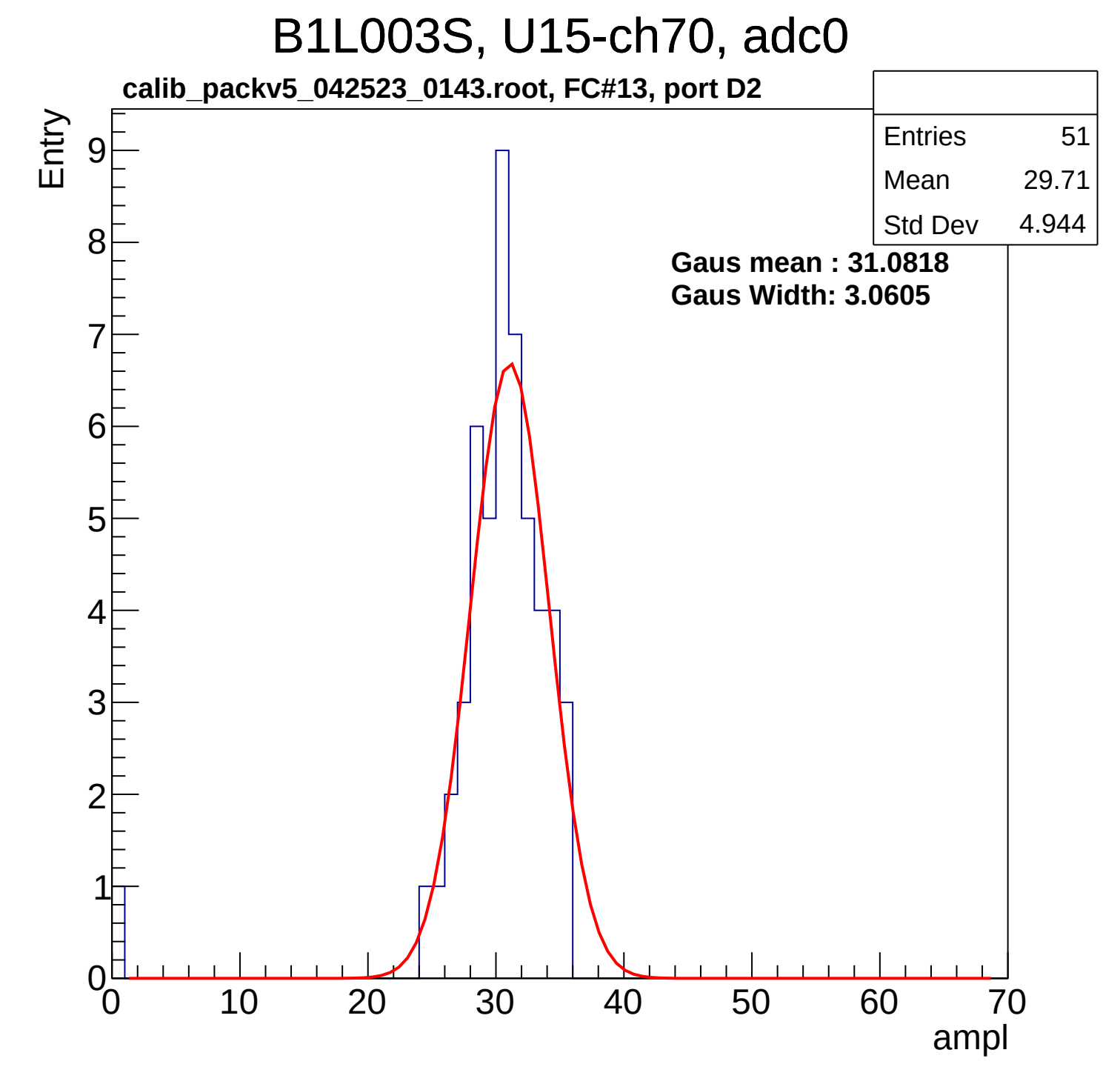
70
ampl


B1L003S, U15-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 29.71
Std Dev 4.944
Gaus mean : 31.0818
Gaus Width: 3.0605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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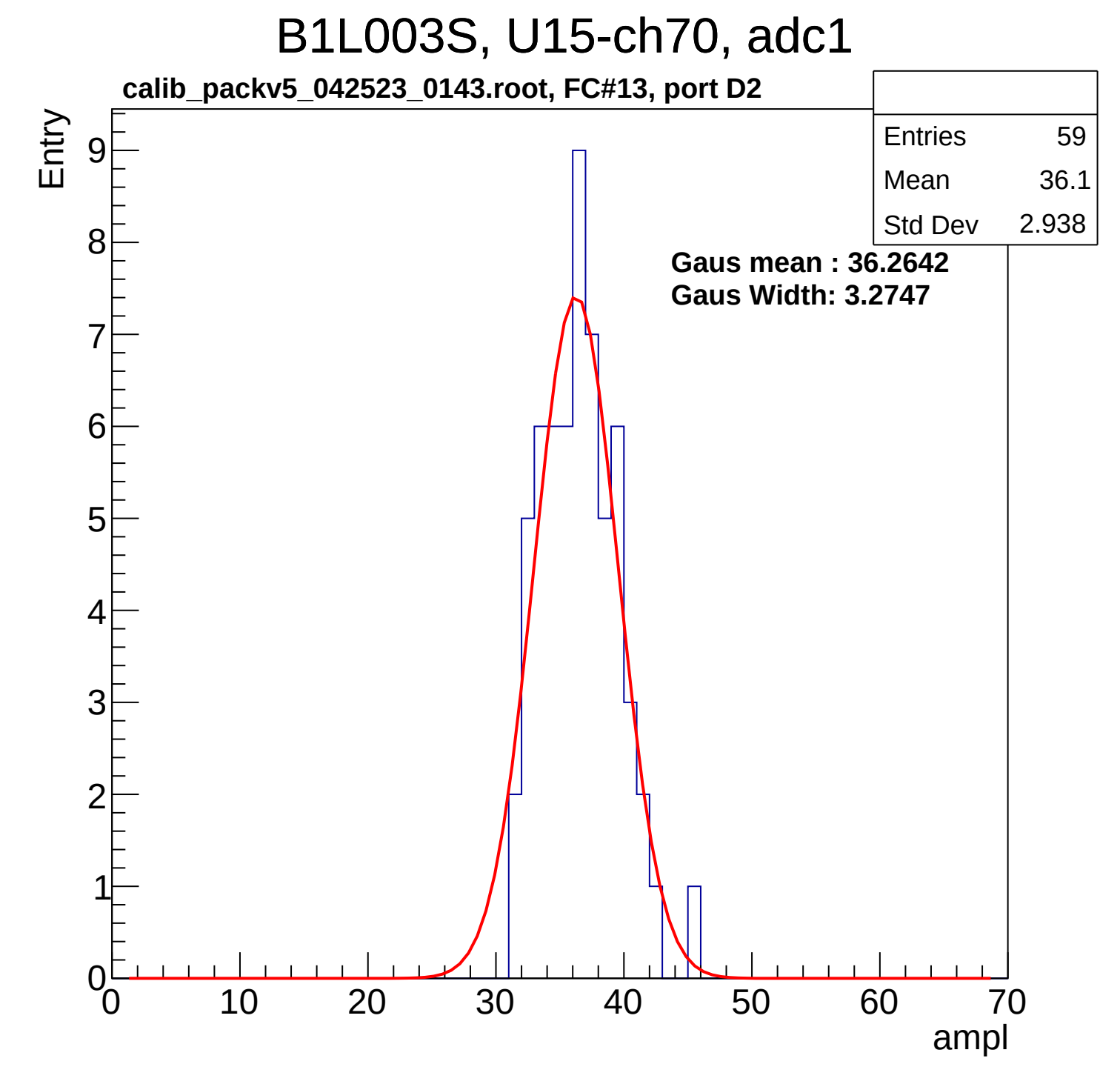
B1L003S, U15-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.1
Std Dev 2.938
Gaus mean : 36.2642
Gaus Width: 3.2747
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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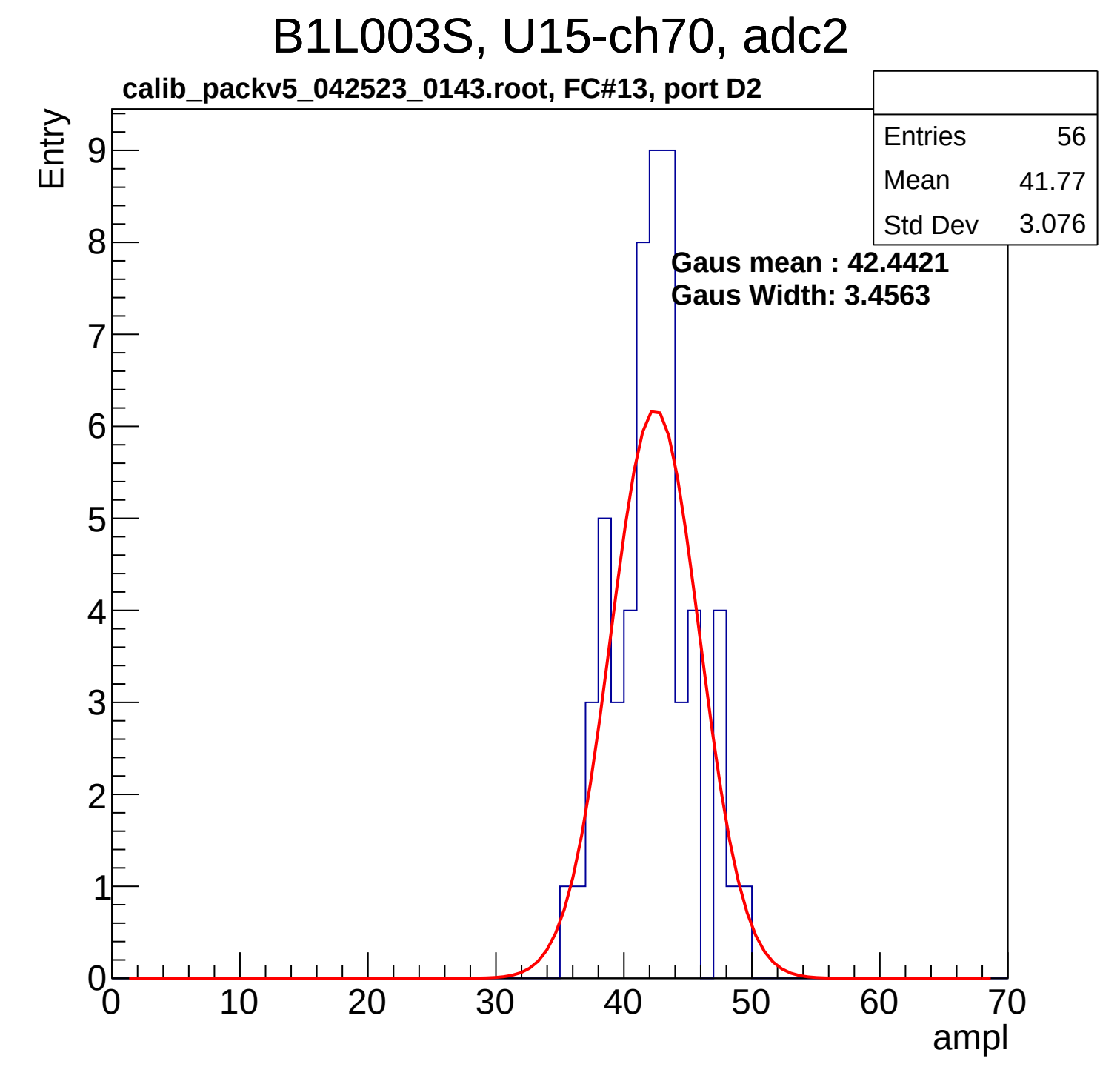
B1L003S, U15-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 41.77
Std Dev 3.076
Gaus mean : 42.4421
Gaus Width: 3.4563
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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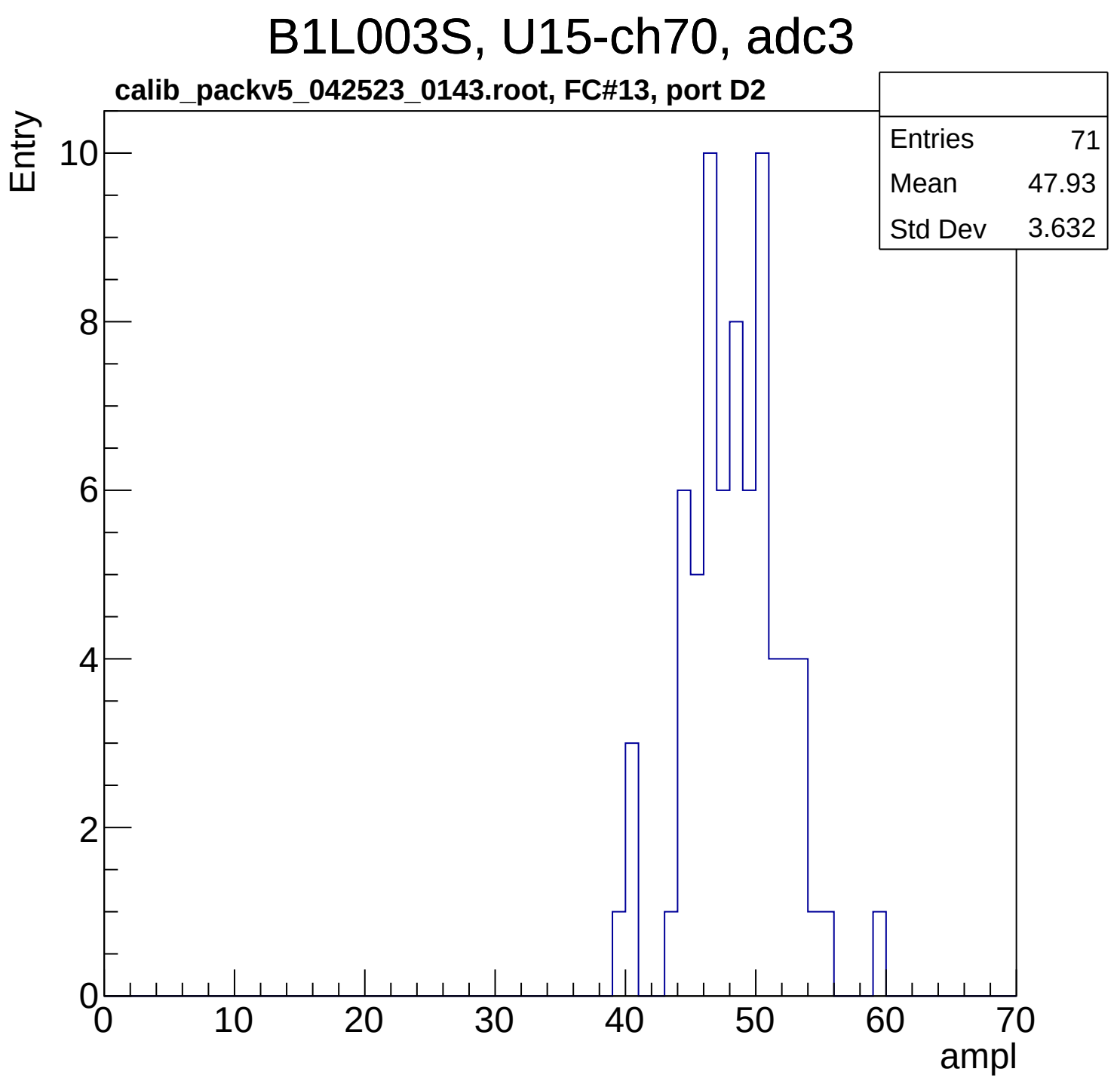
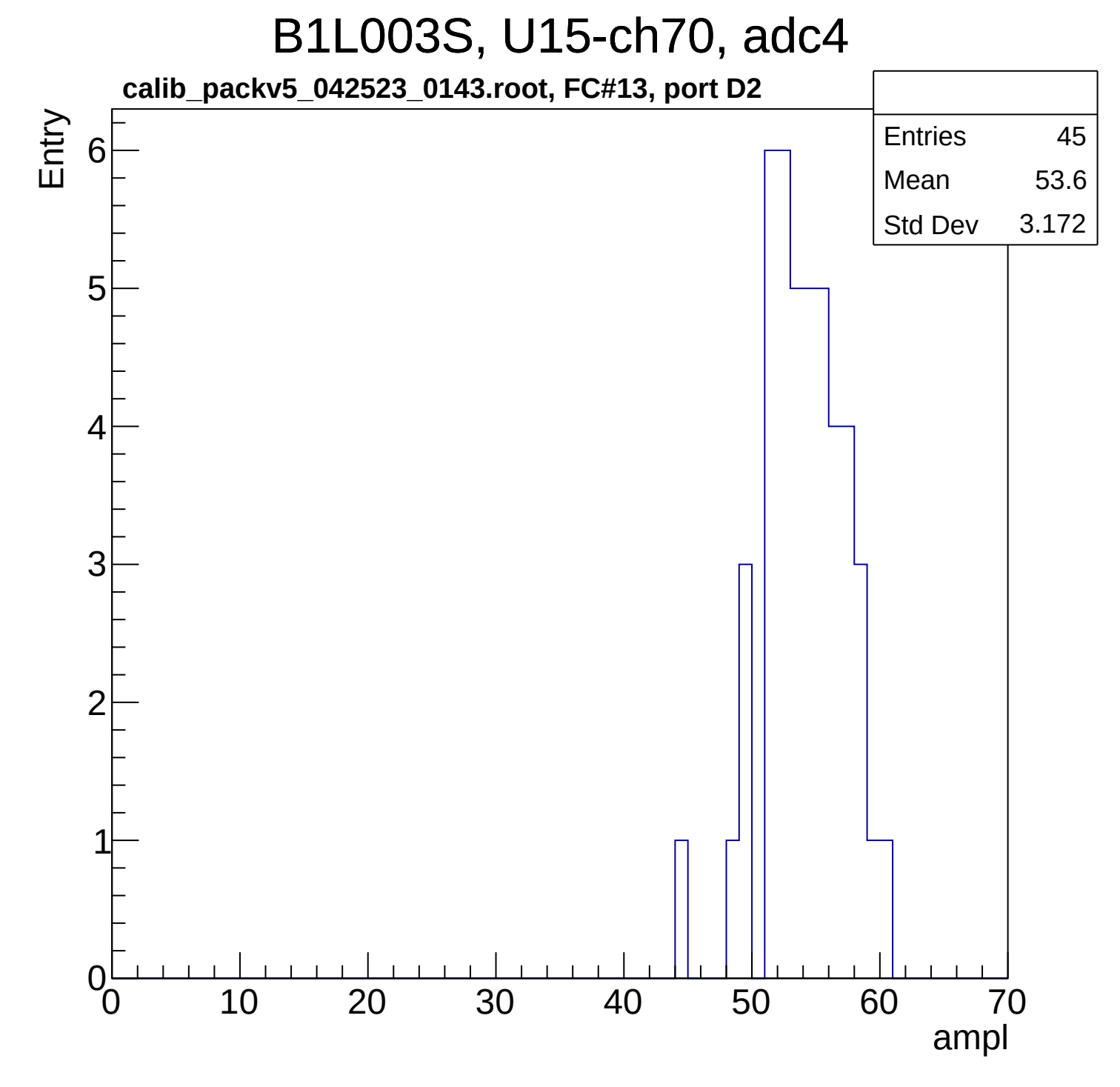
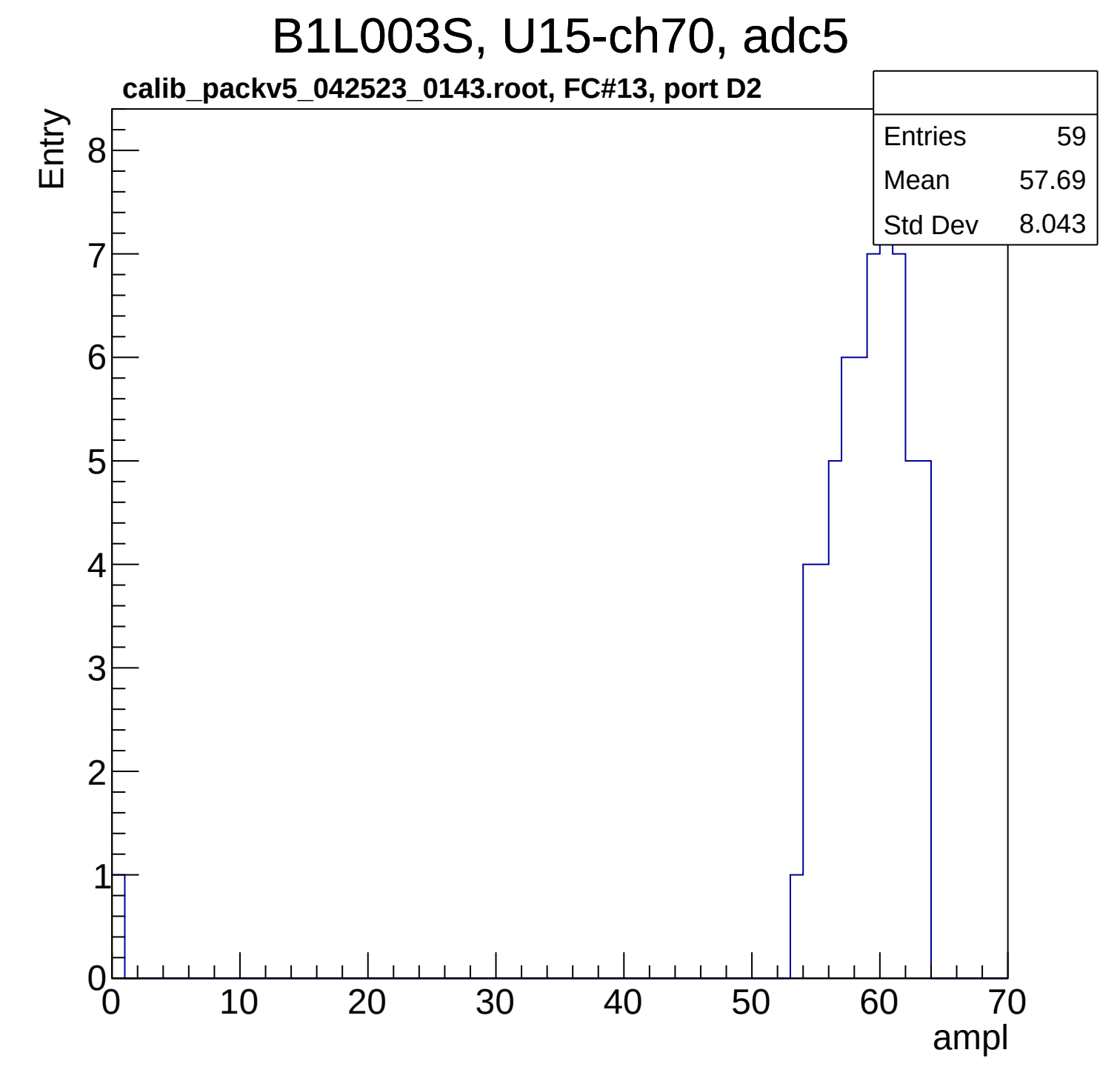

B1L003S, U15-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.93
Std Dev 3.632
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 53.6
Std Dev 3.172
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.69
Std Dev 8.043
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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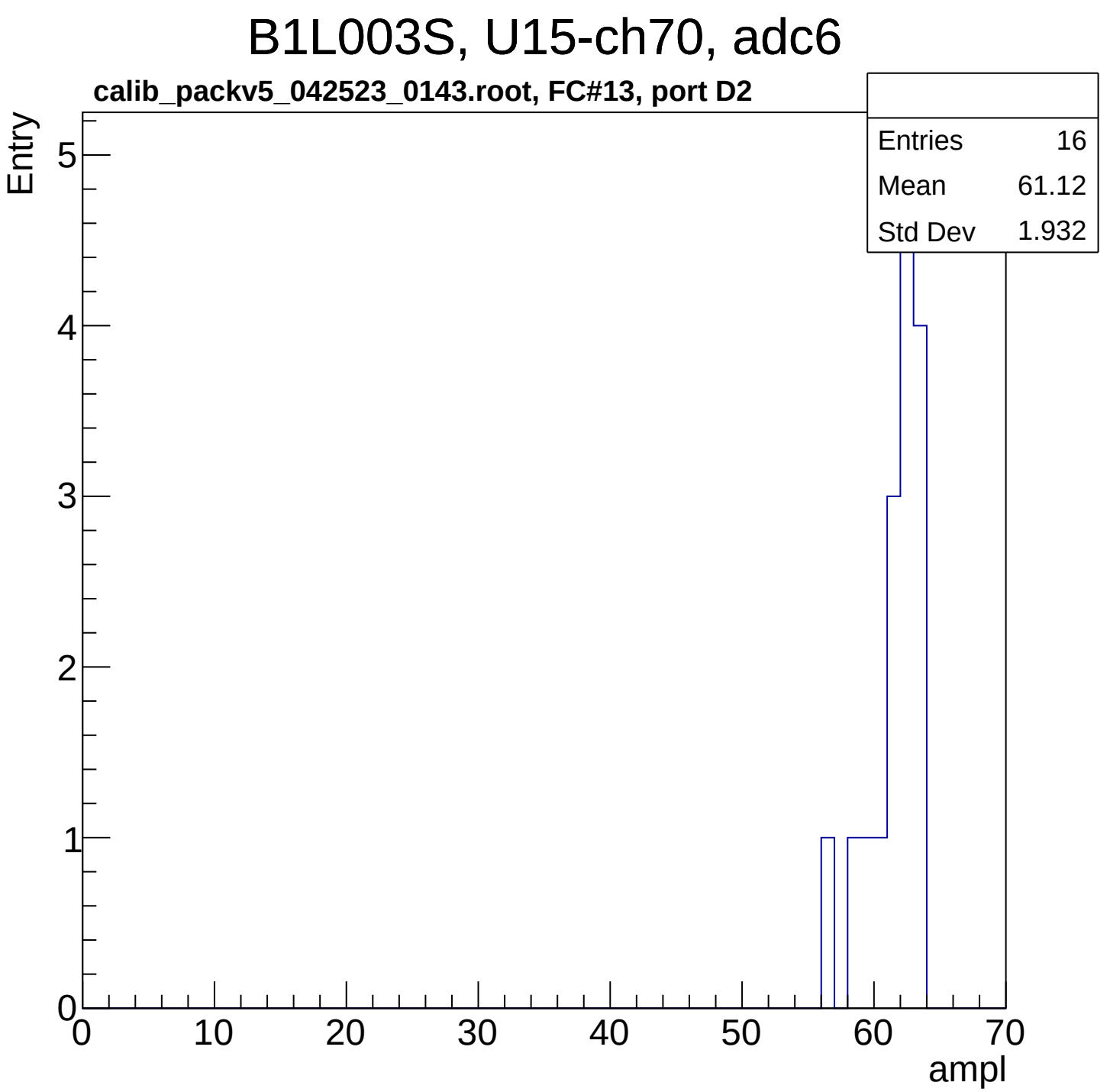
B1L003S, U15-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.12
Std Dev 1.932
Entry
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1
2
3
4
5
0
10
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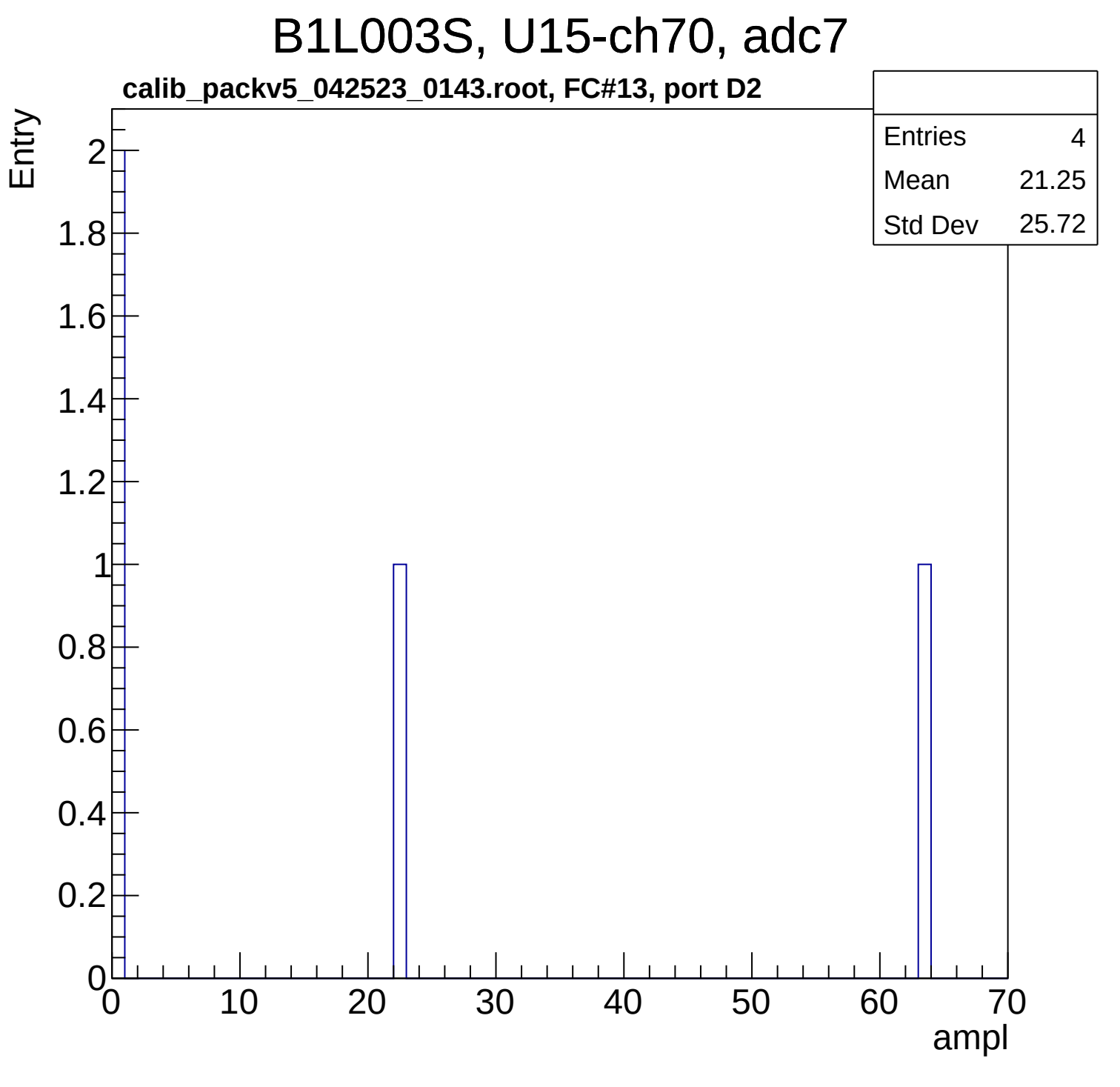
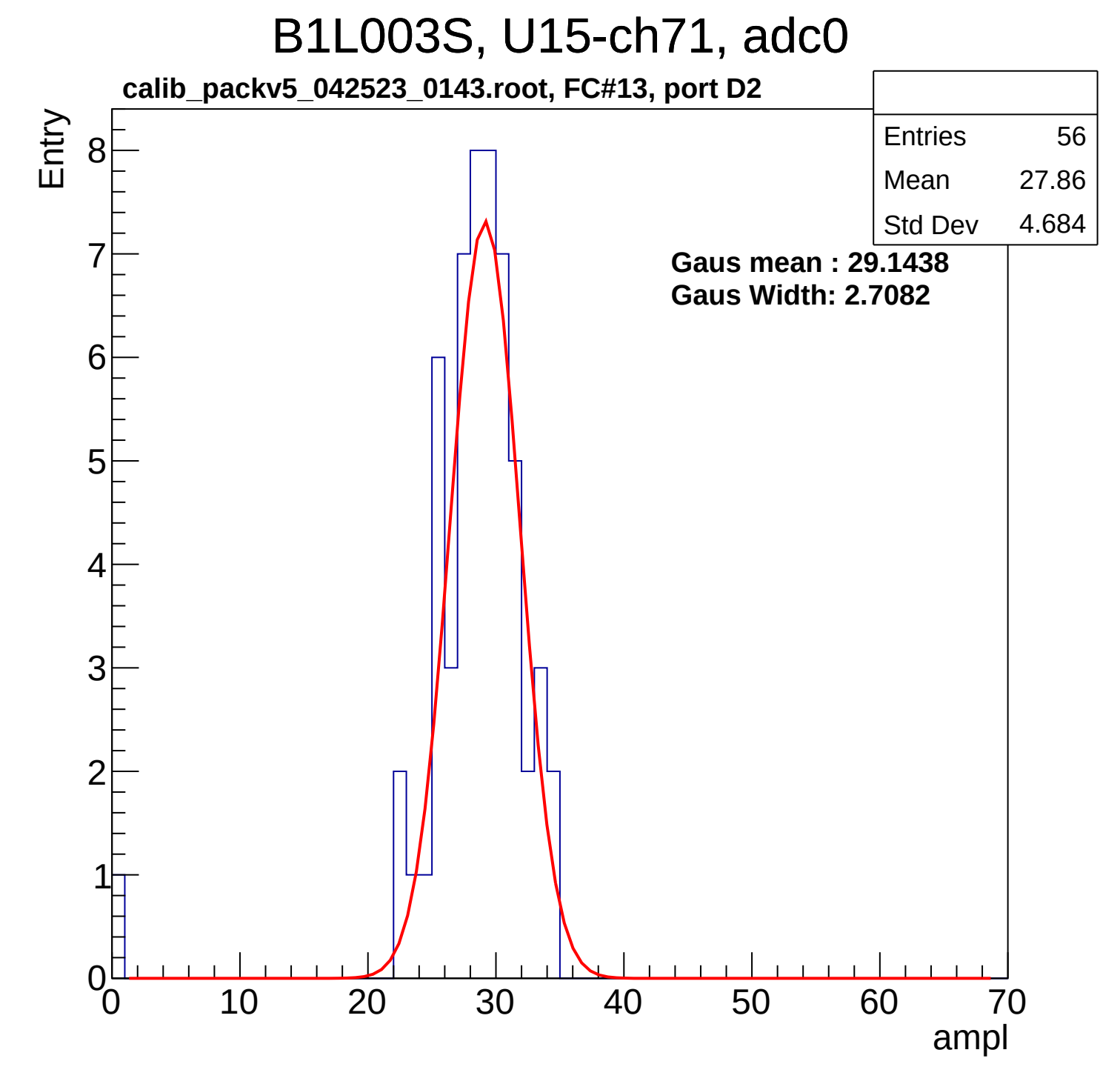
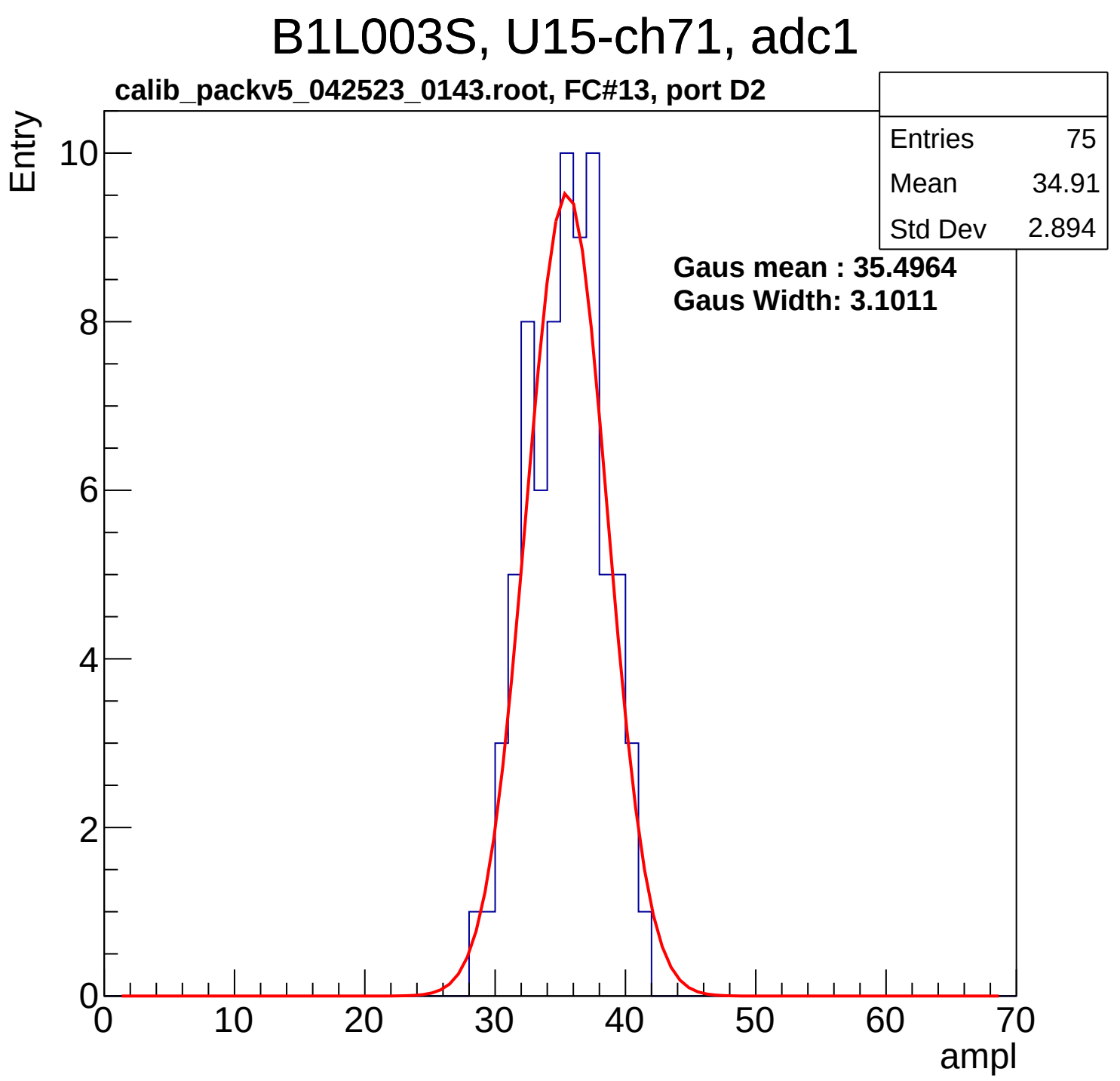
30
40
50
60
70
ampl


B1L003S, U15-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 21.25
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 27.86
Std Dev 4.684
Gaus mean : 29.1438
Gaus Width: 2.7082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 34.91
Std Dev 2.894
Gaus mean : 35.4964
Gaus Width: 3.1011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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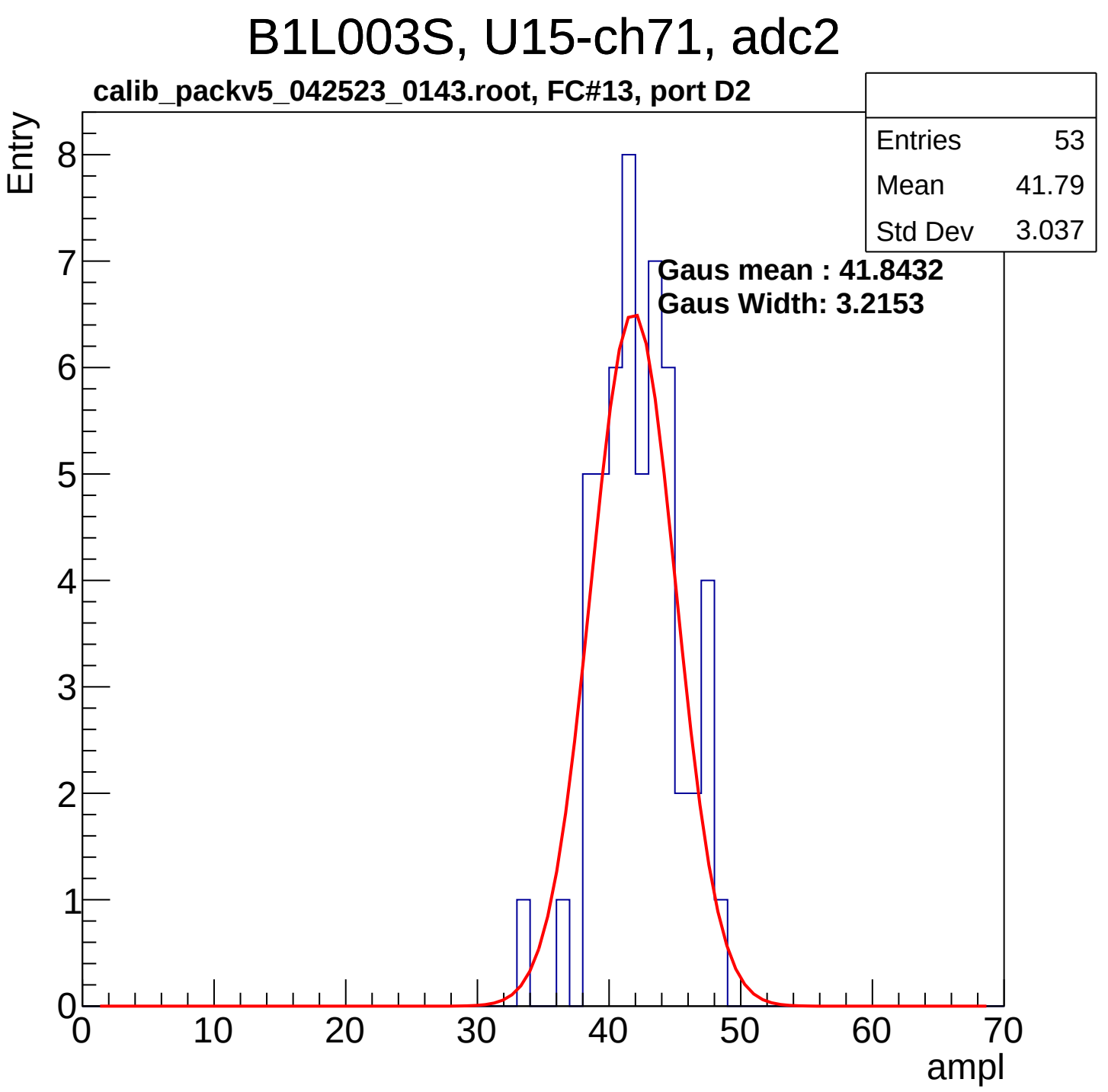
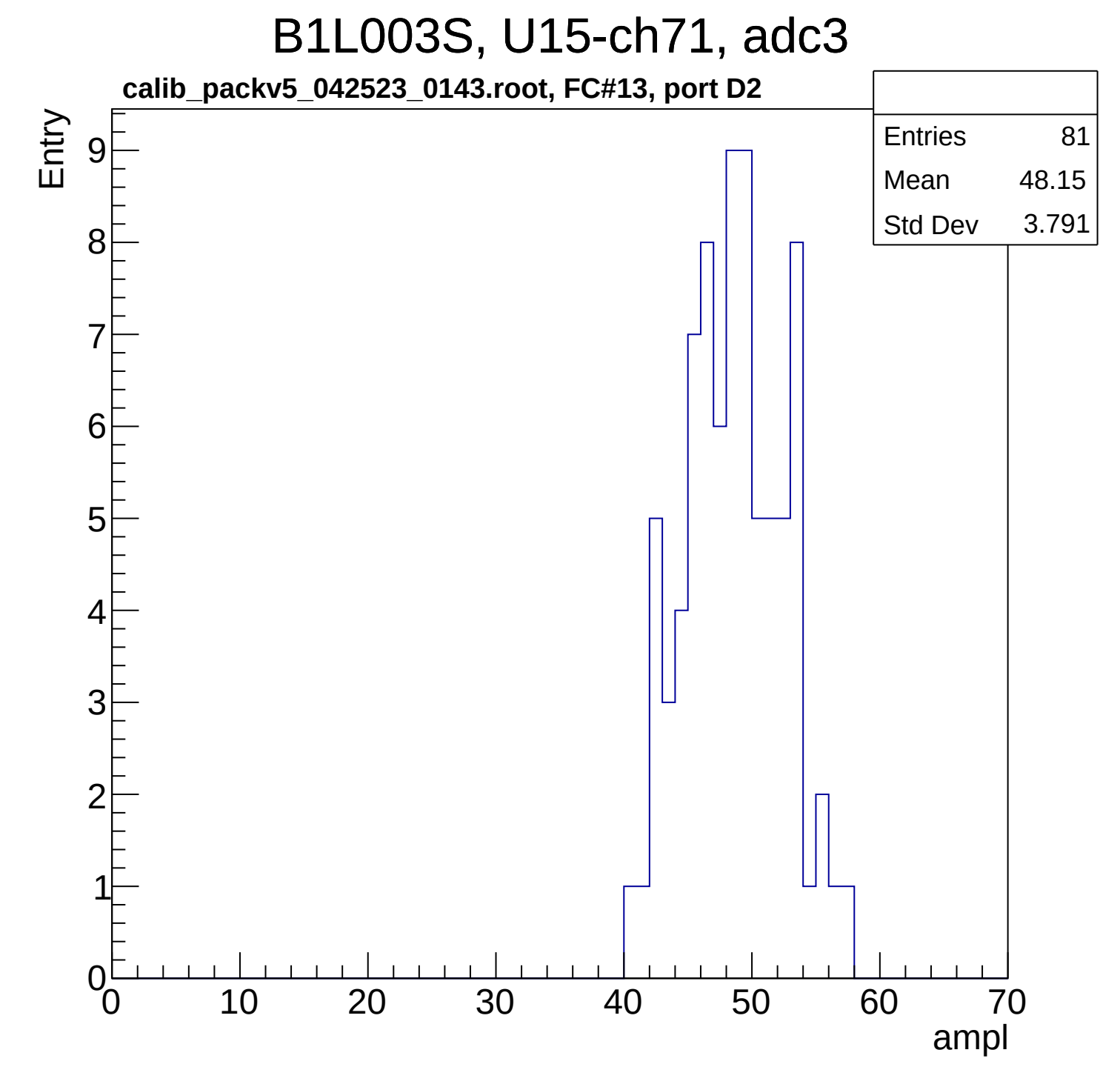

B1L003S, U15-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 41.79
Std Dev 3.037
Gaus mean : 41.8432
Gaus Width: 3.2153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 48.15
Std Dev 3.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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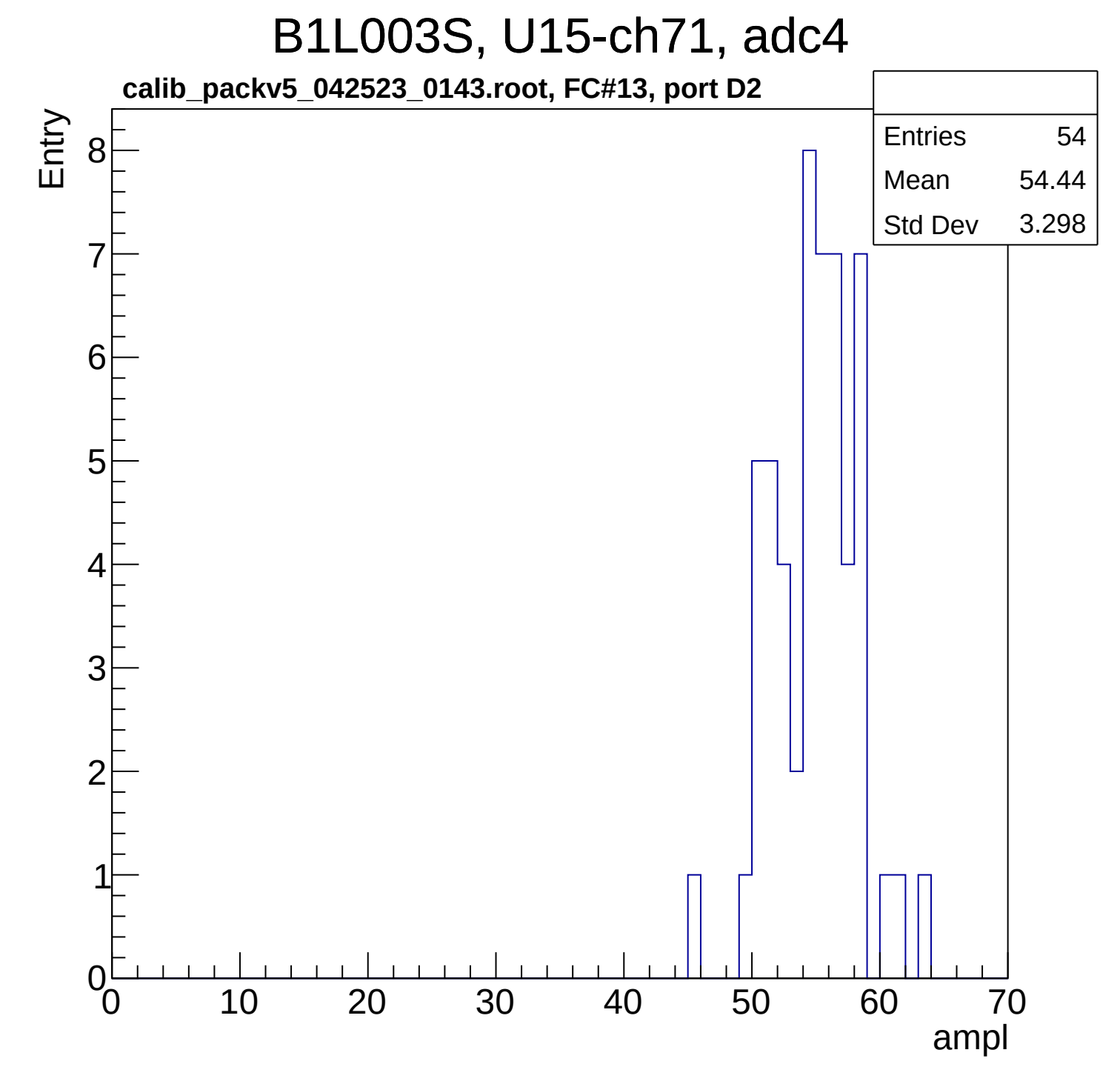
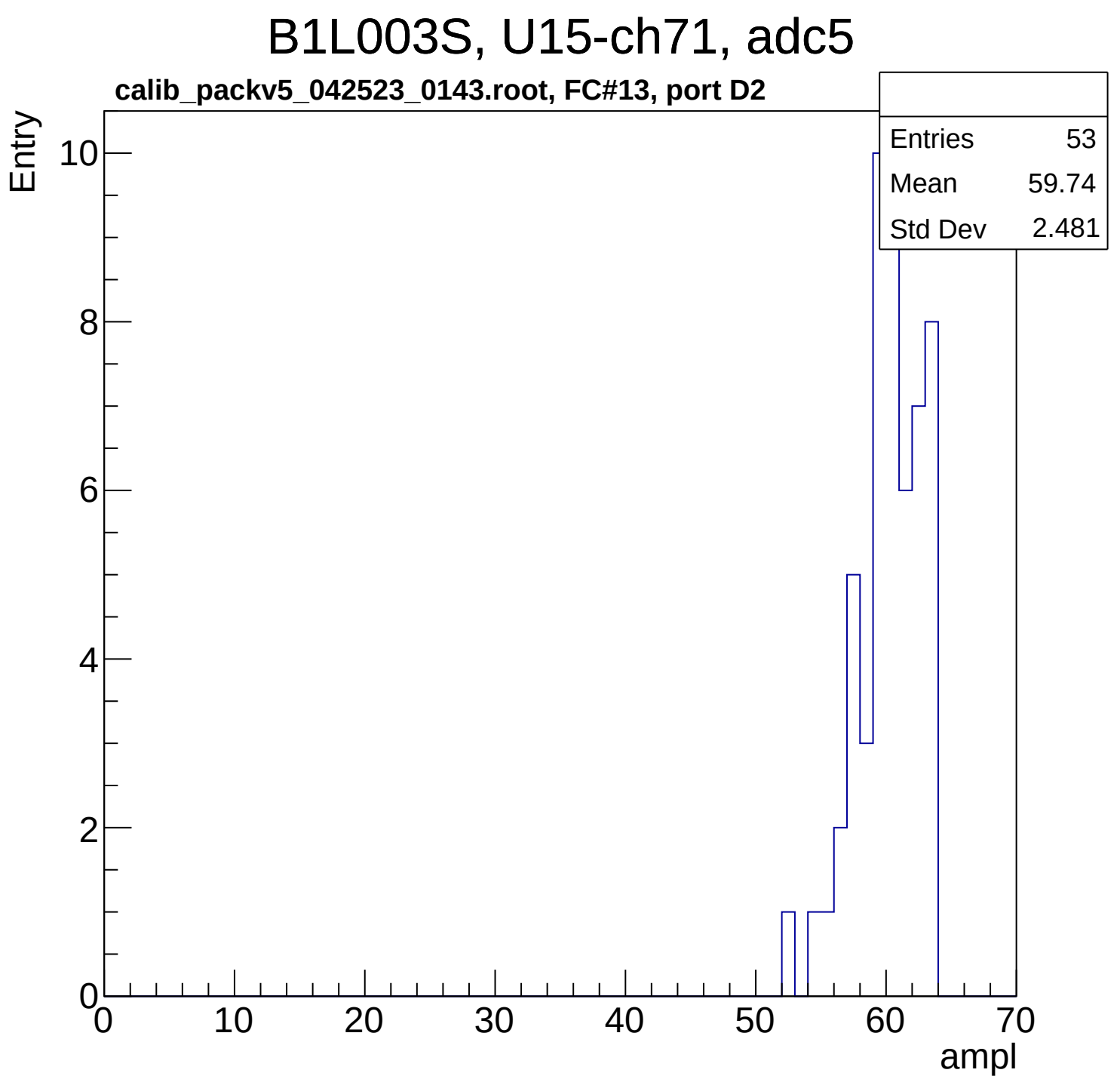
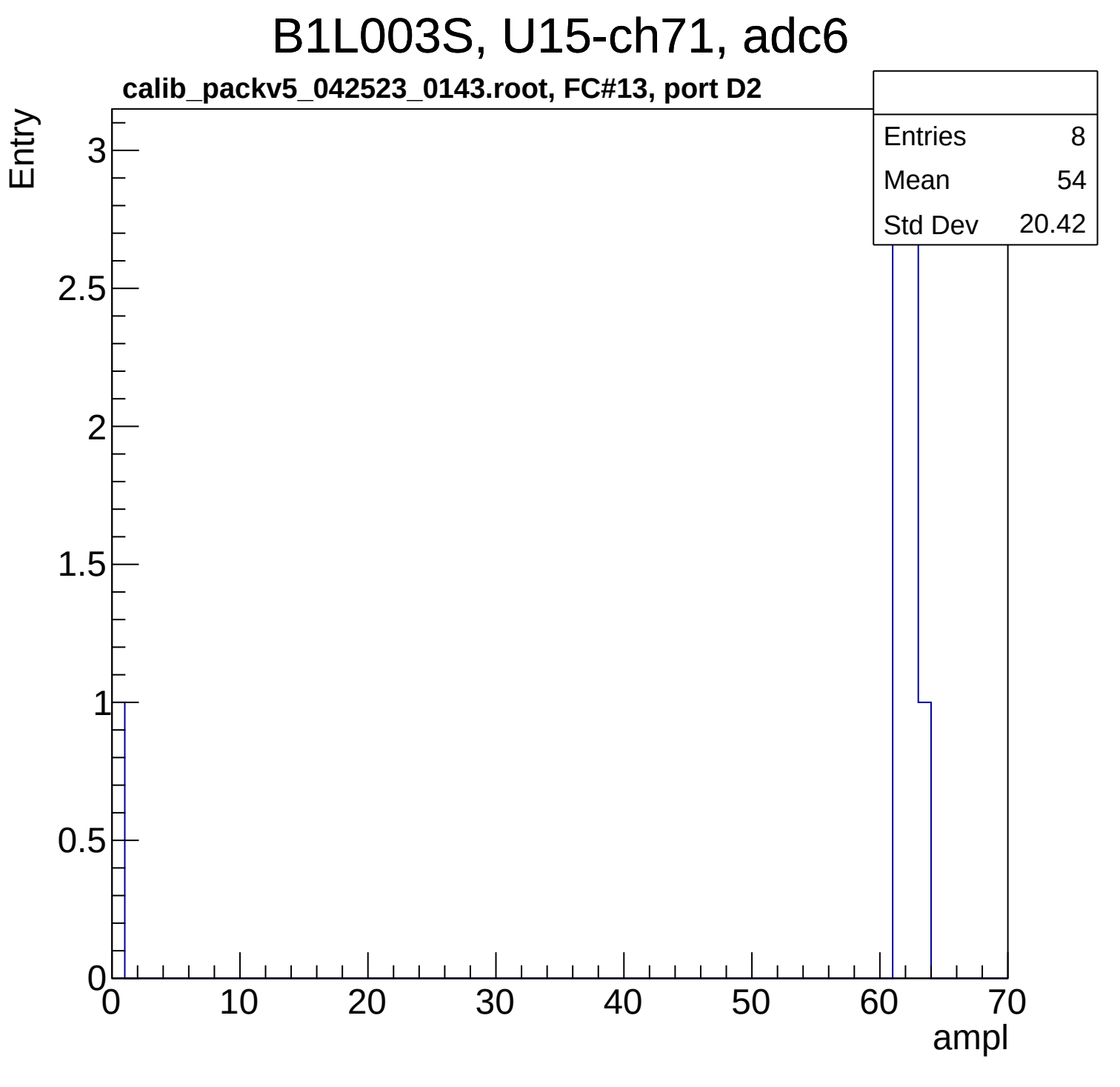
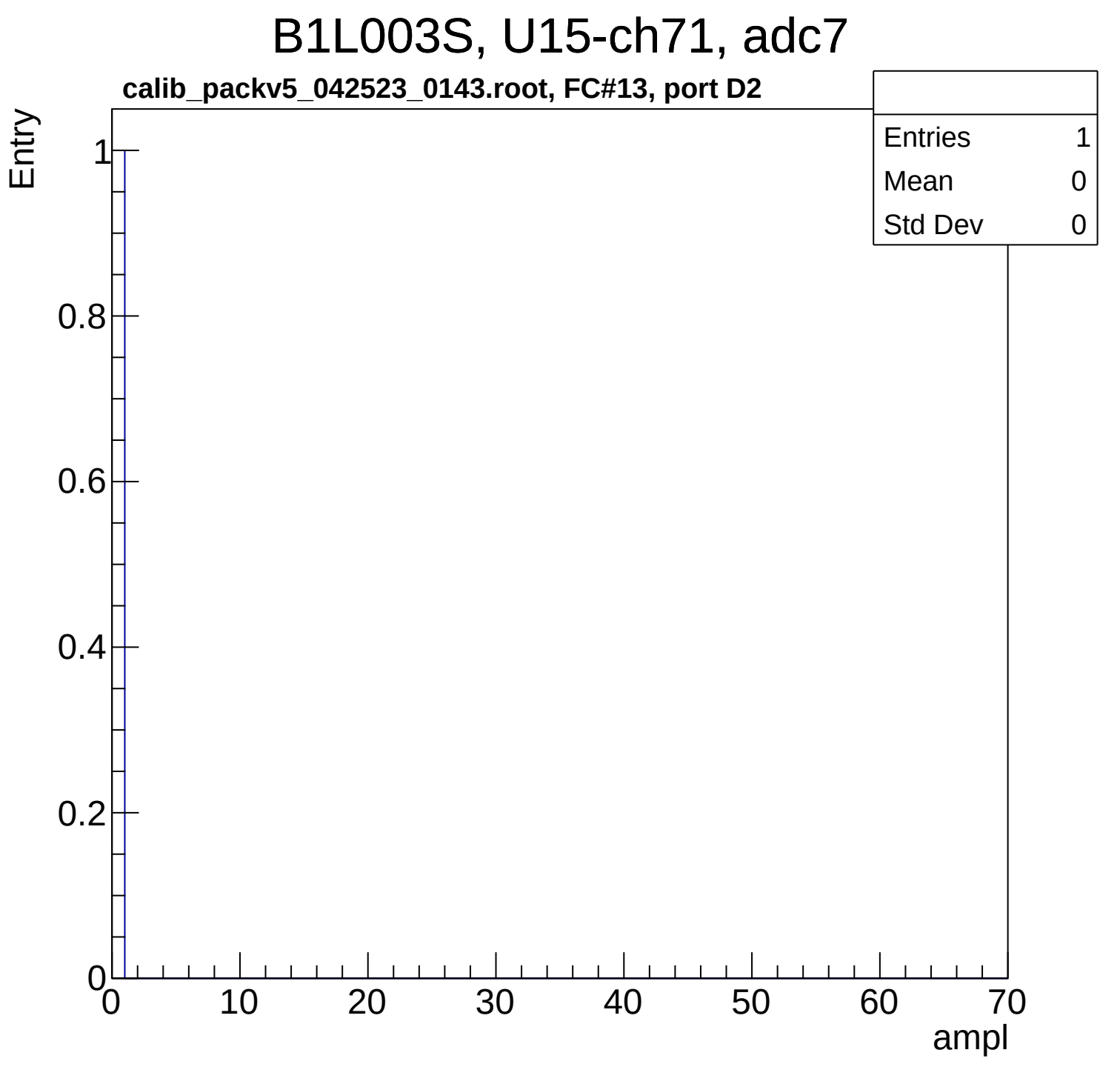

B1L003S, U15-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.44
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.74
Std Dev 2.481
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54
Std Dev 20.42
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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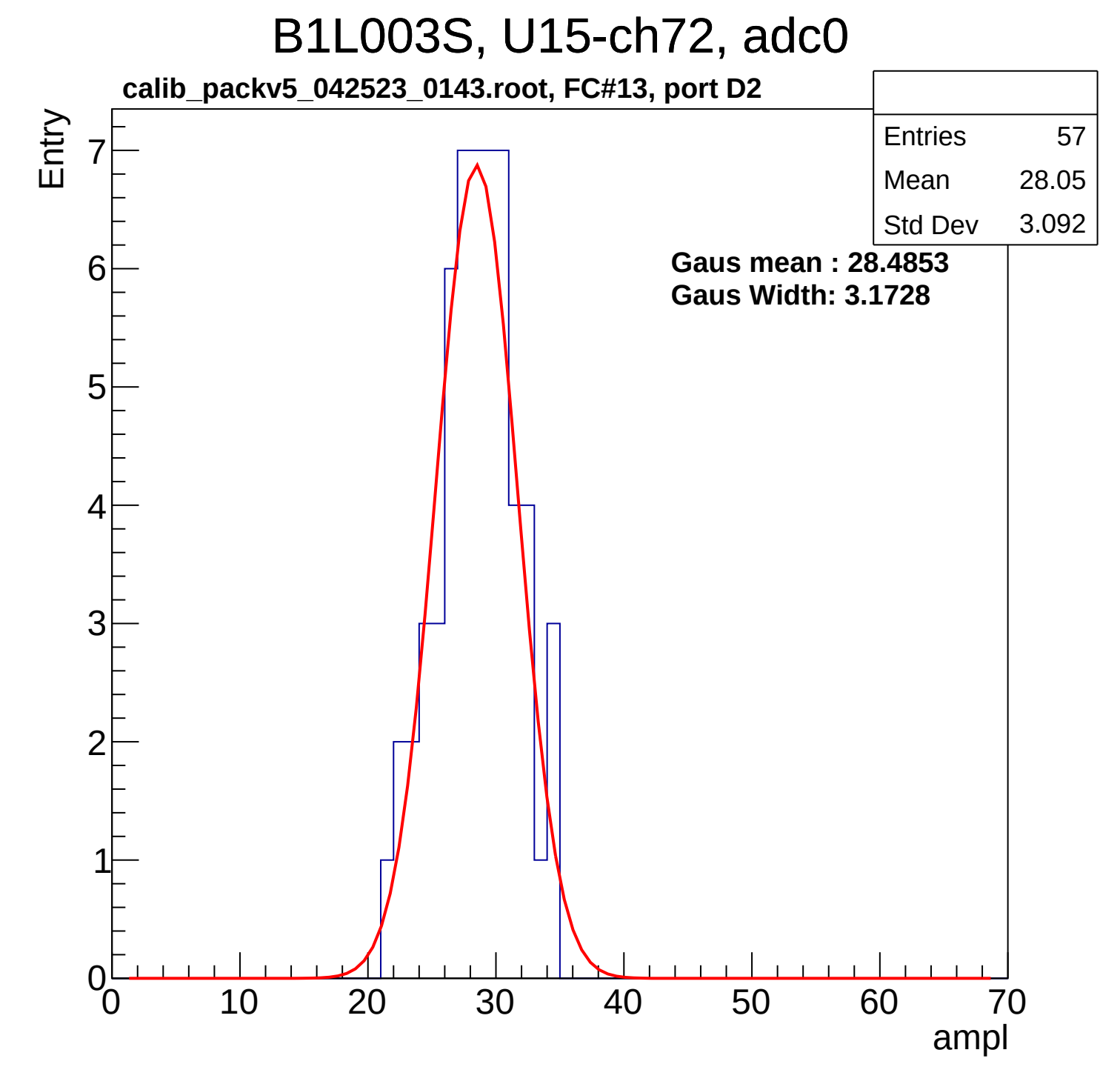
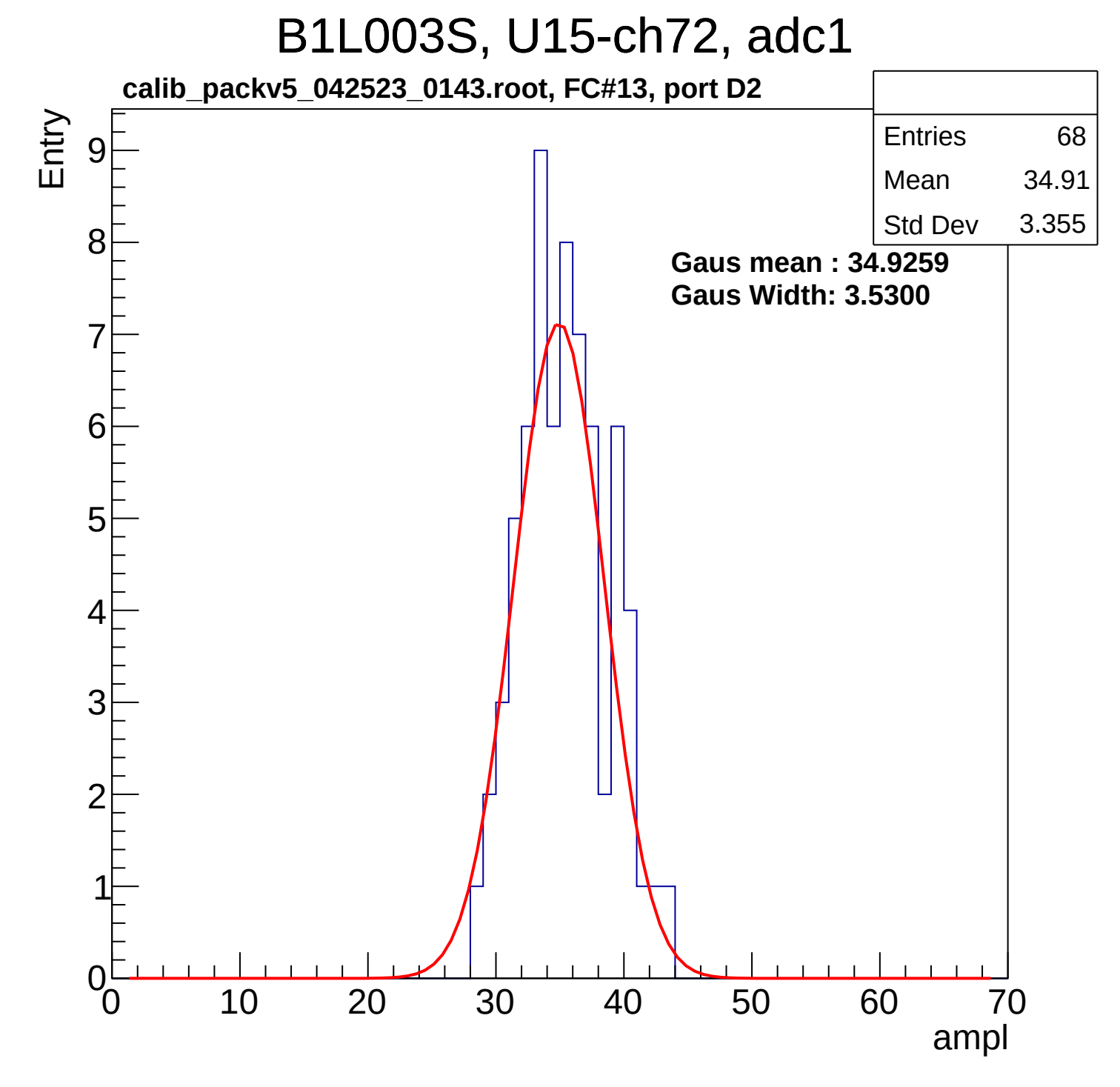
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.05
Std Dev 3.092
Gaus mean : 28.4853
Gaus Width: 3.1728
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 34.91
Std Dev 3.355
Gaus mean : 34.9259
Gaus Width: 3.5300
Entry
ampl
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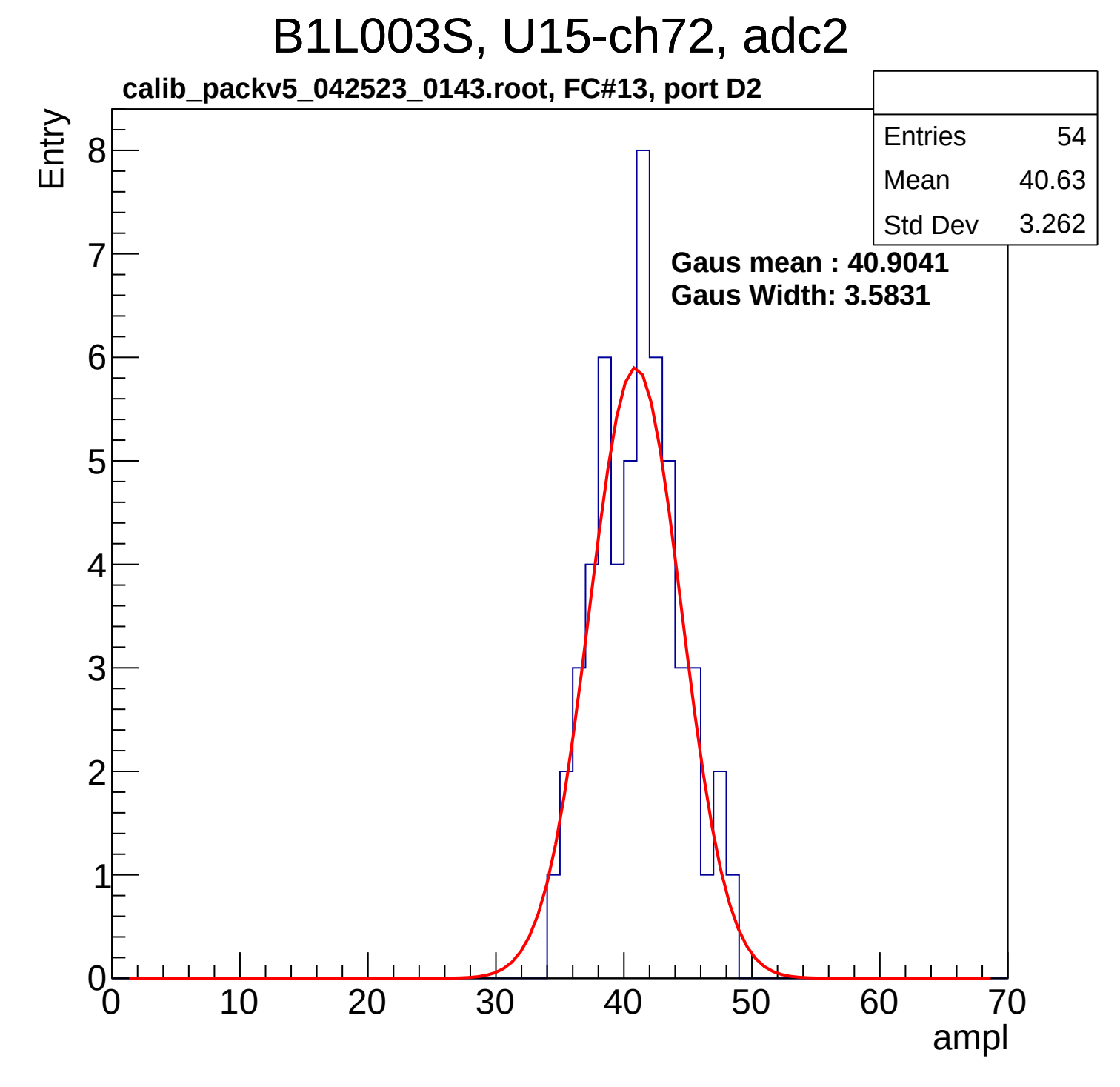
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.63
Std Dev 3.262
Gaus mean : 40.9041
Gaus Width: 3.5831
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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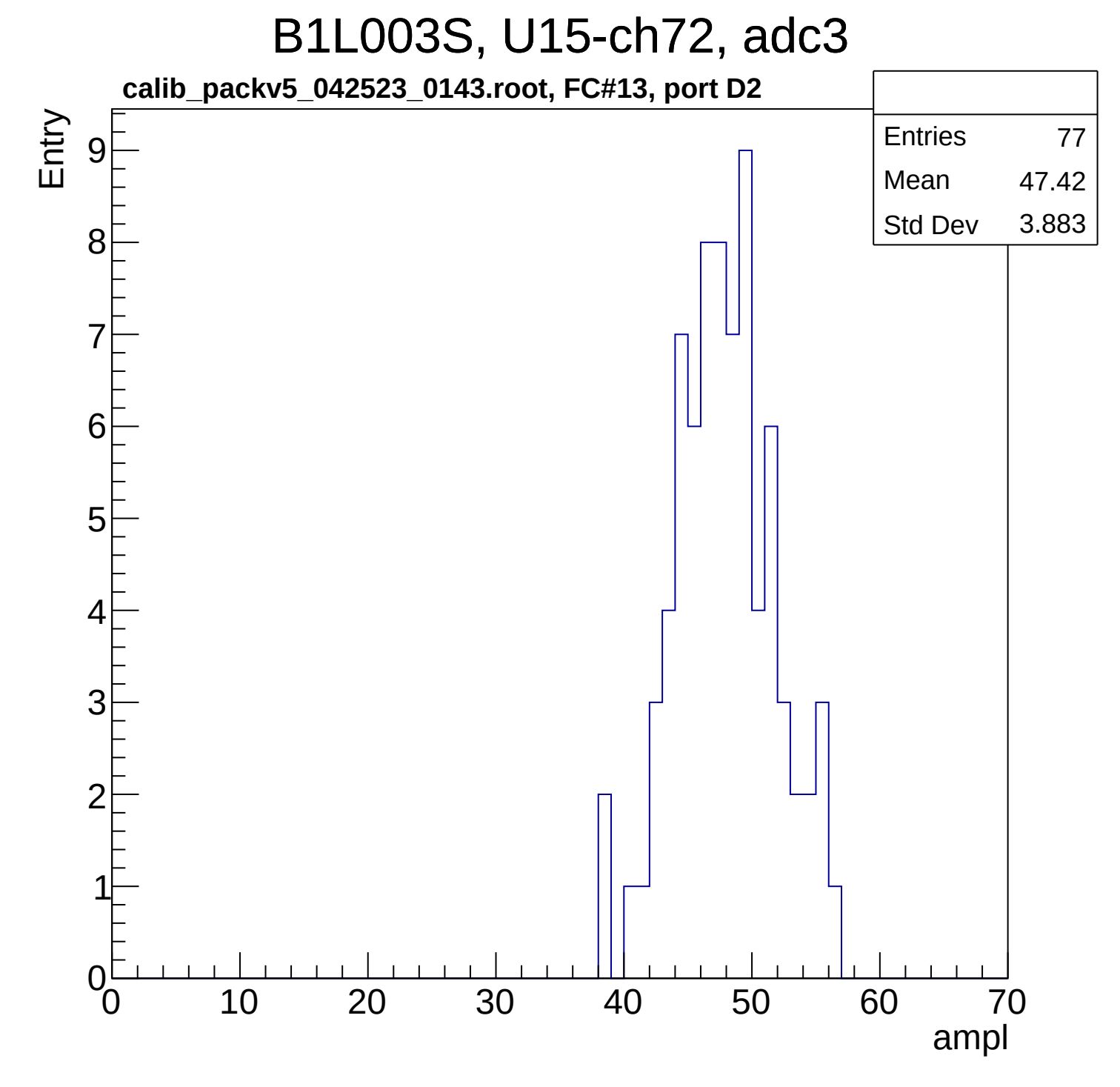
B1L003S, U15-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 47.42
Std Dev 3.883
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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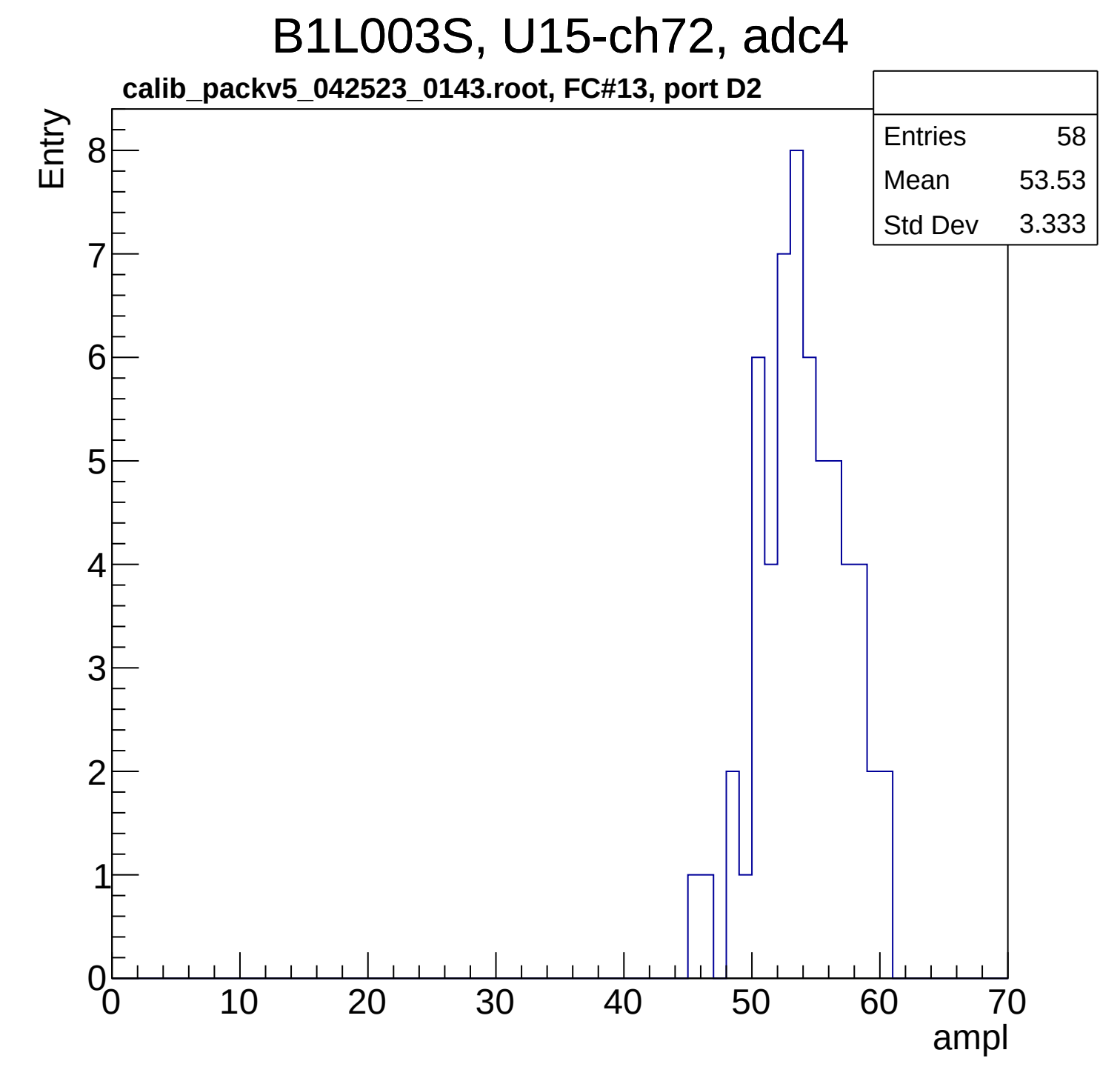
60
70


B1L003S, U15-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 53.53
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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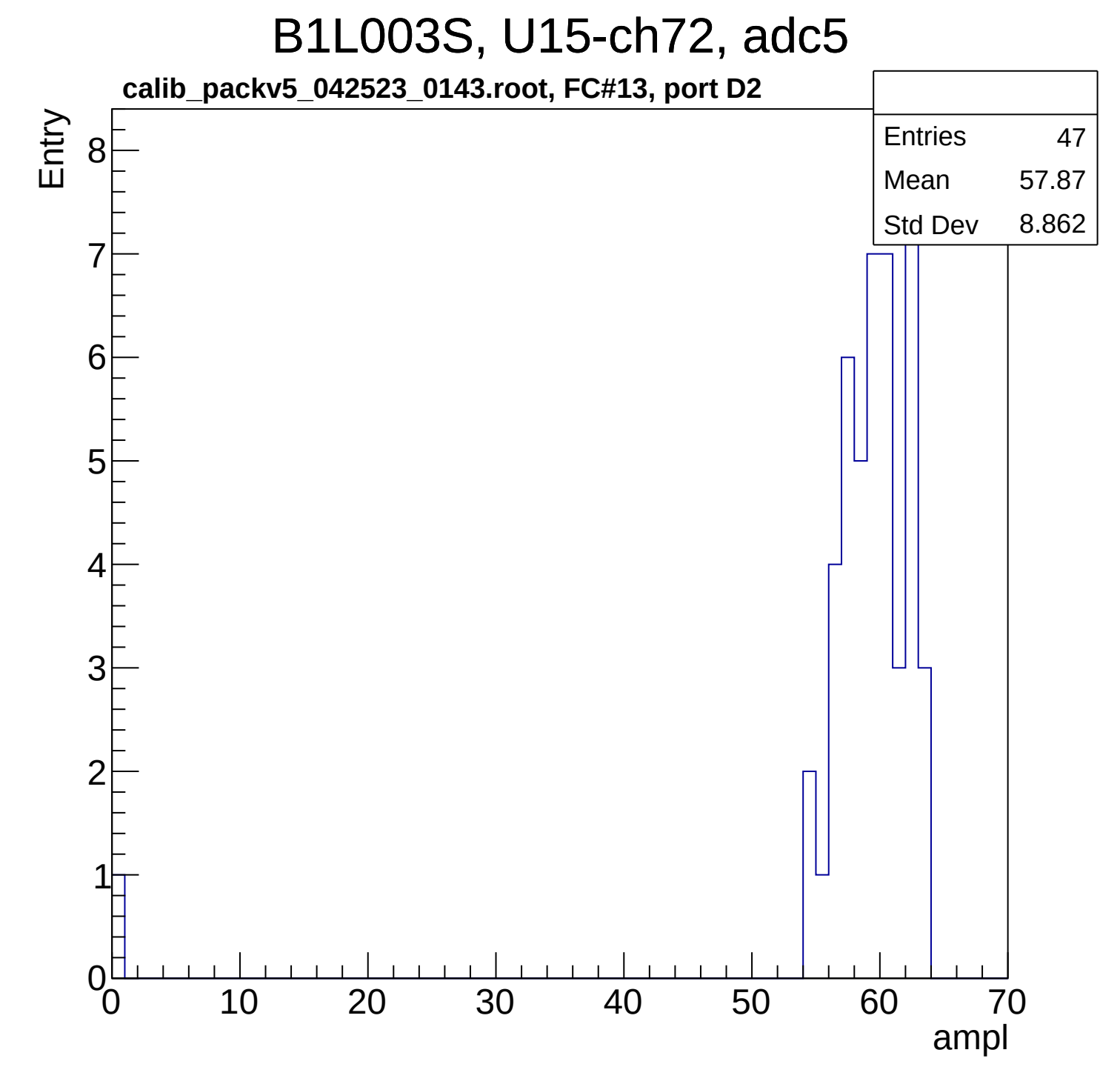
B1L003S, U15-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 57.87
Std Dev 8.862
Entry
ampl
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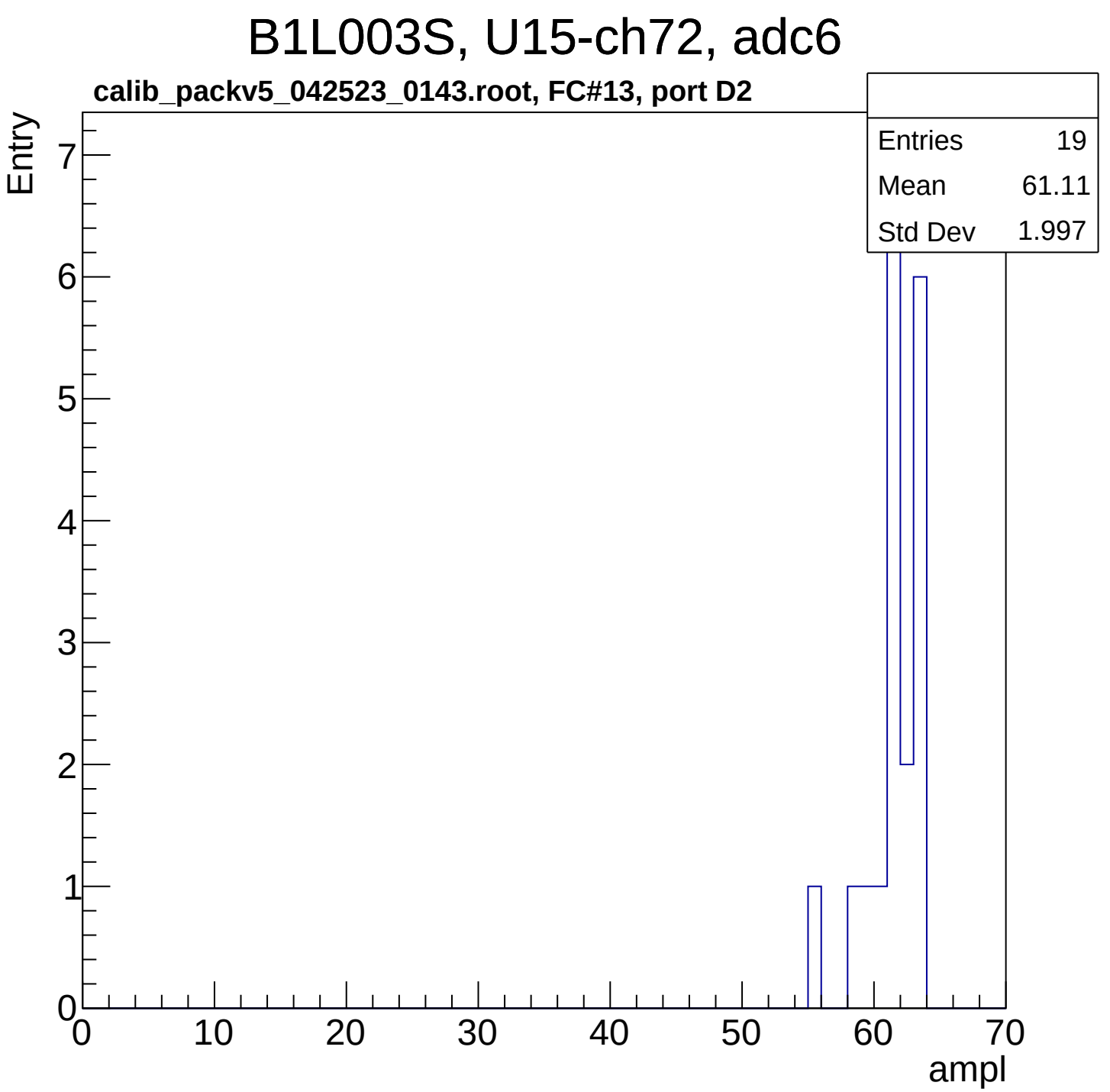
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 61.11
Std Dev 1.997
Entry
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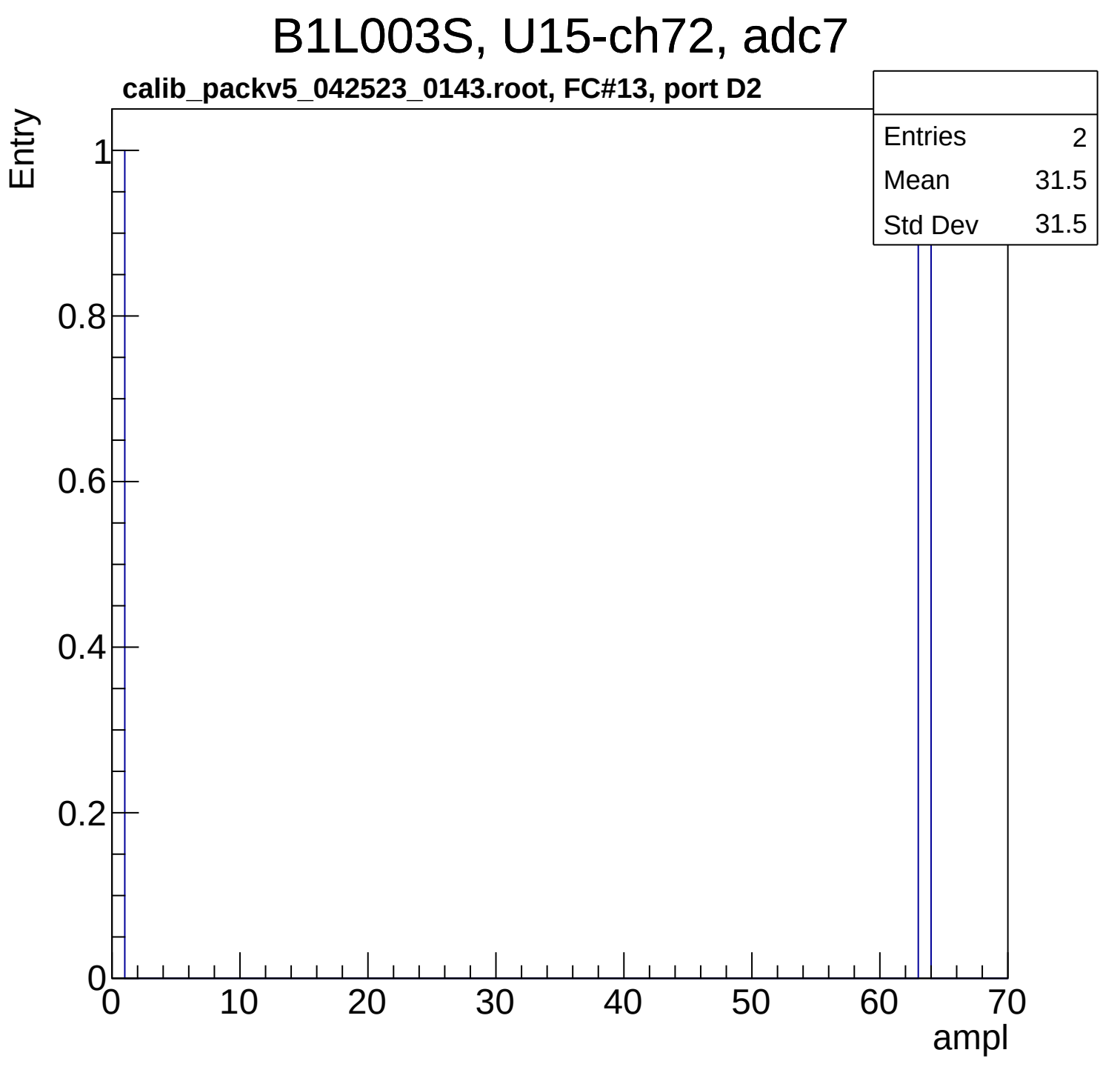
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
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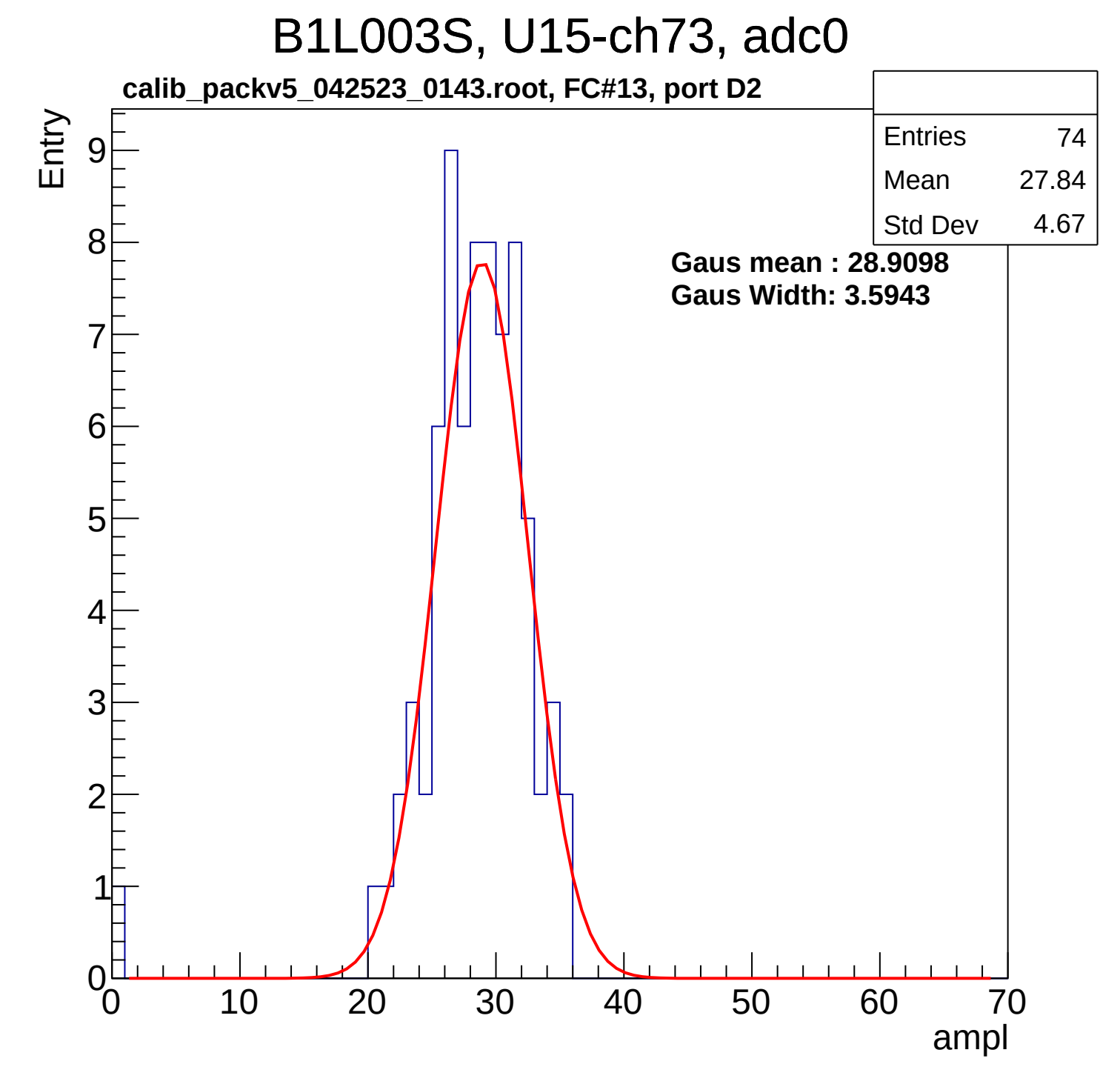
30
40
50
60
70
ampl


B1L003S, U15-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.84
Std Dev 4.67
Gaus mean : 28.9098
Gaus Width: 3.5943
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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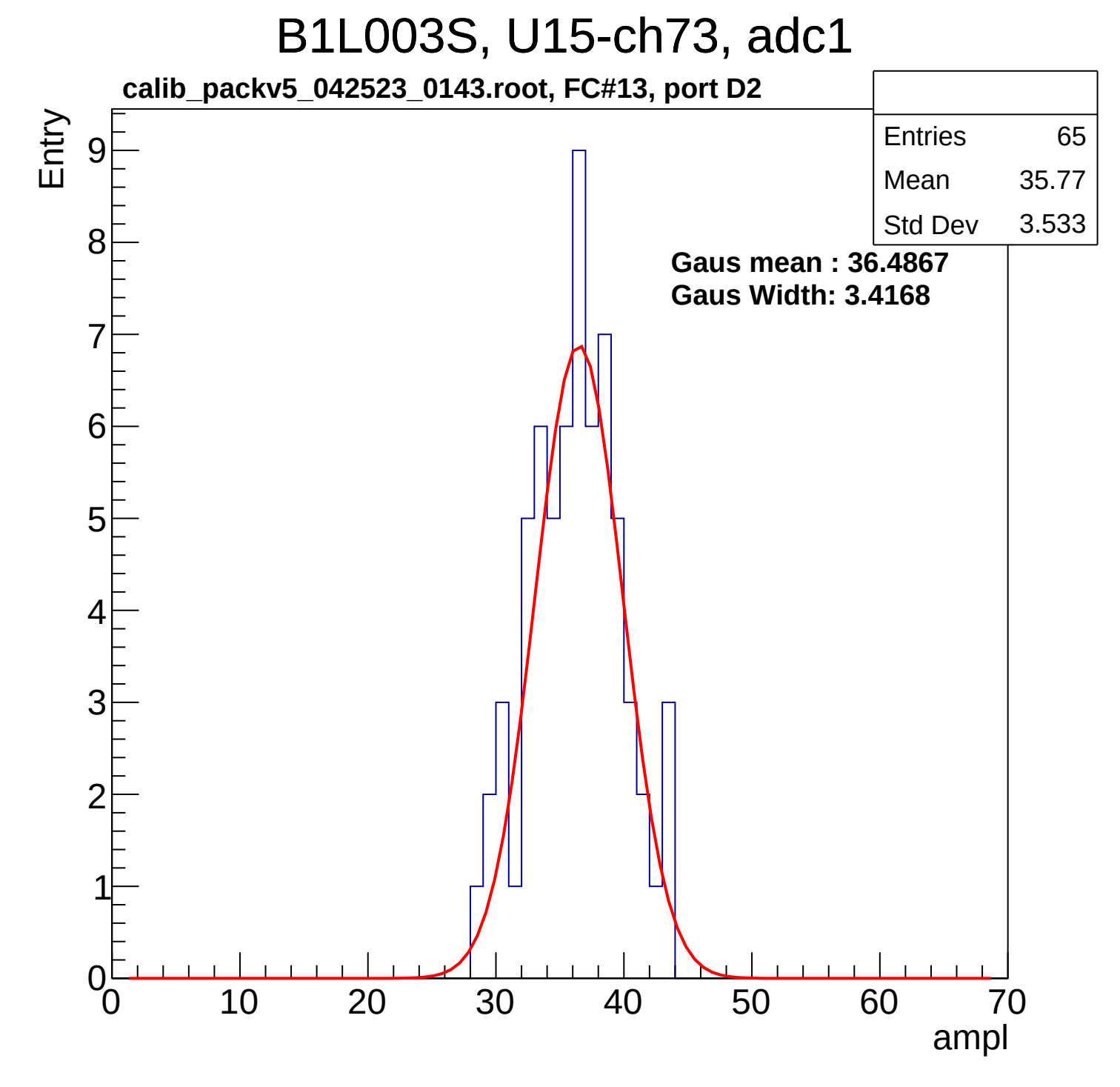
B1L003S, U15-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.77
Std Dev 3.533
Gaus mean : 36.4867
Gaus Width: 3.4168
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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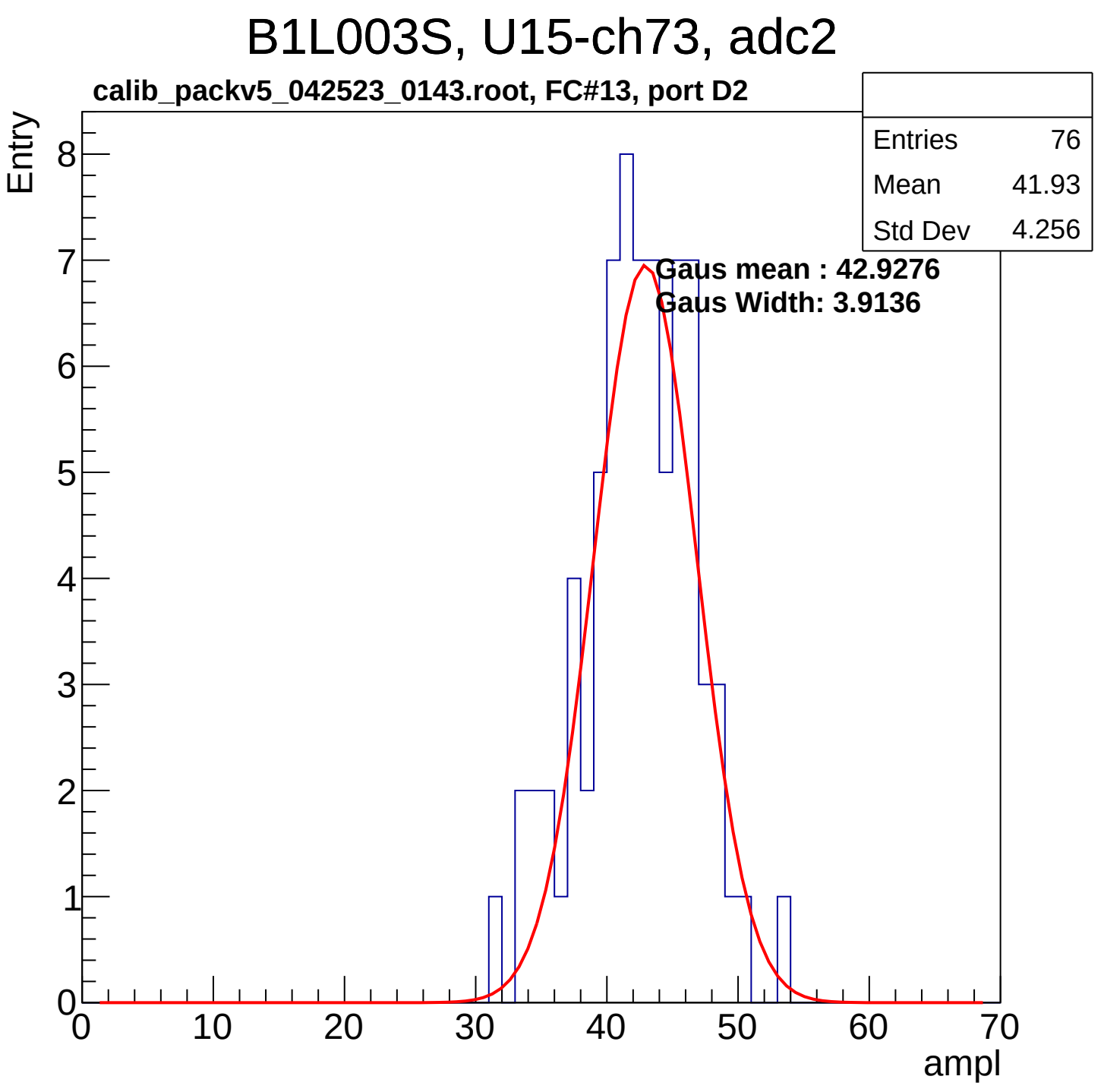
B1L003S, U15-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 41.93
Std Dev 4.256
Gaus mean : 42.9276
Gaus Width: 3.9136
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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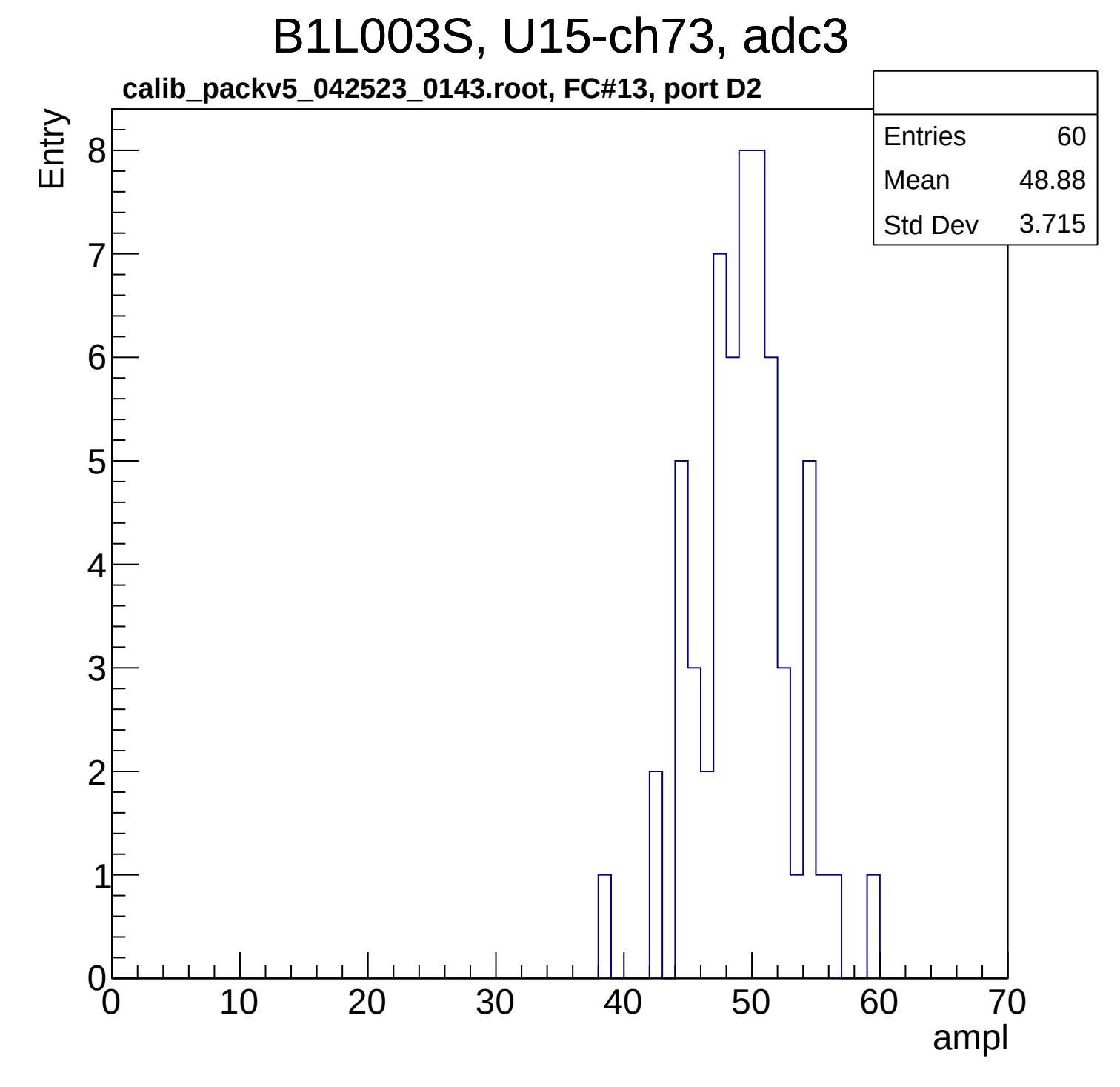
B1L003S, U15-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.88
Std Dev 3.715
Entry
ampl
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1
2
3
4
5
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7
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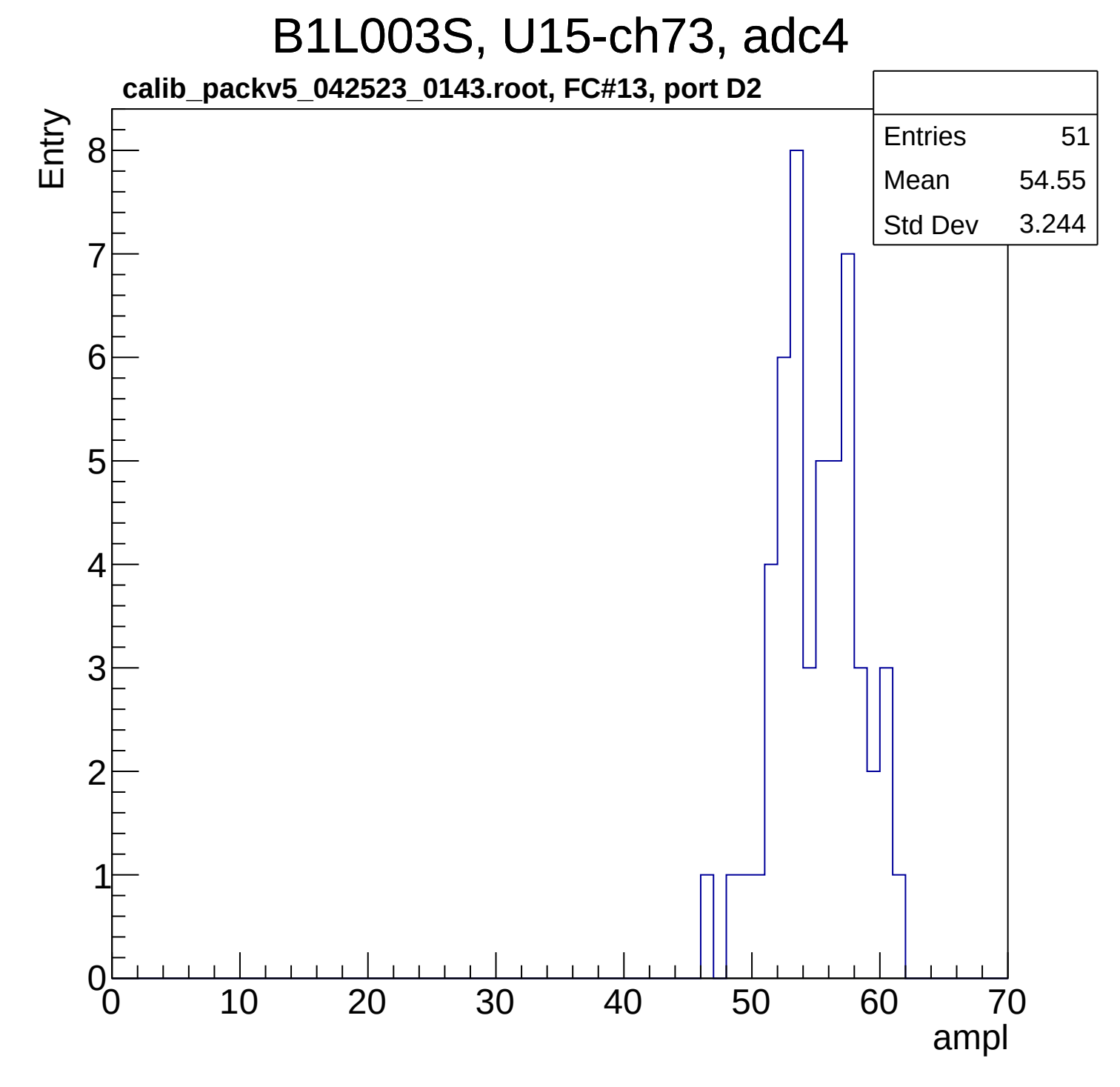
B1L003S, U15-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.55
Std Dev 3.244
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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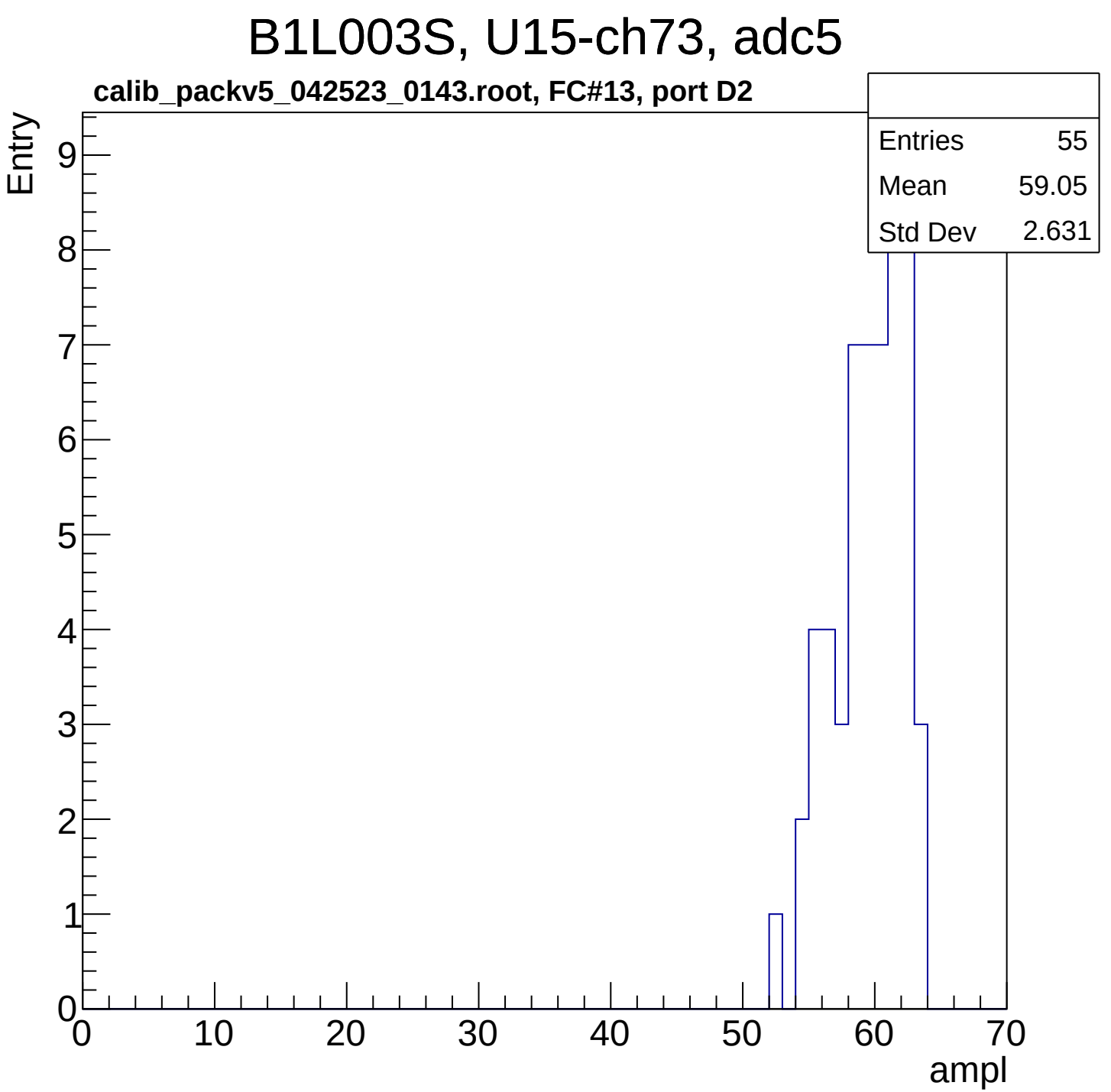
B1L003S, U15-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.05
Std Dev 2.631
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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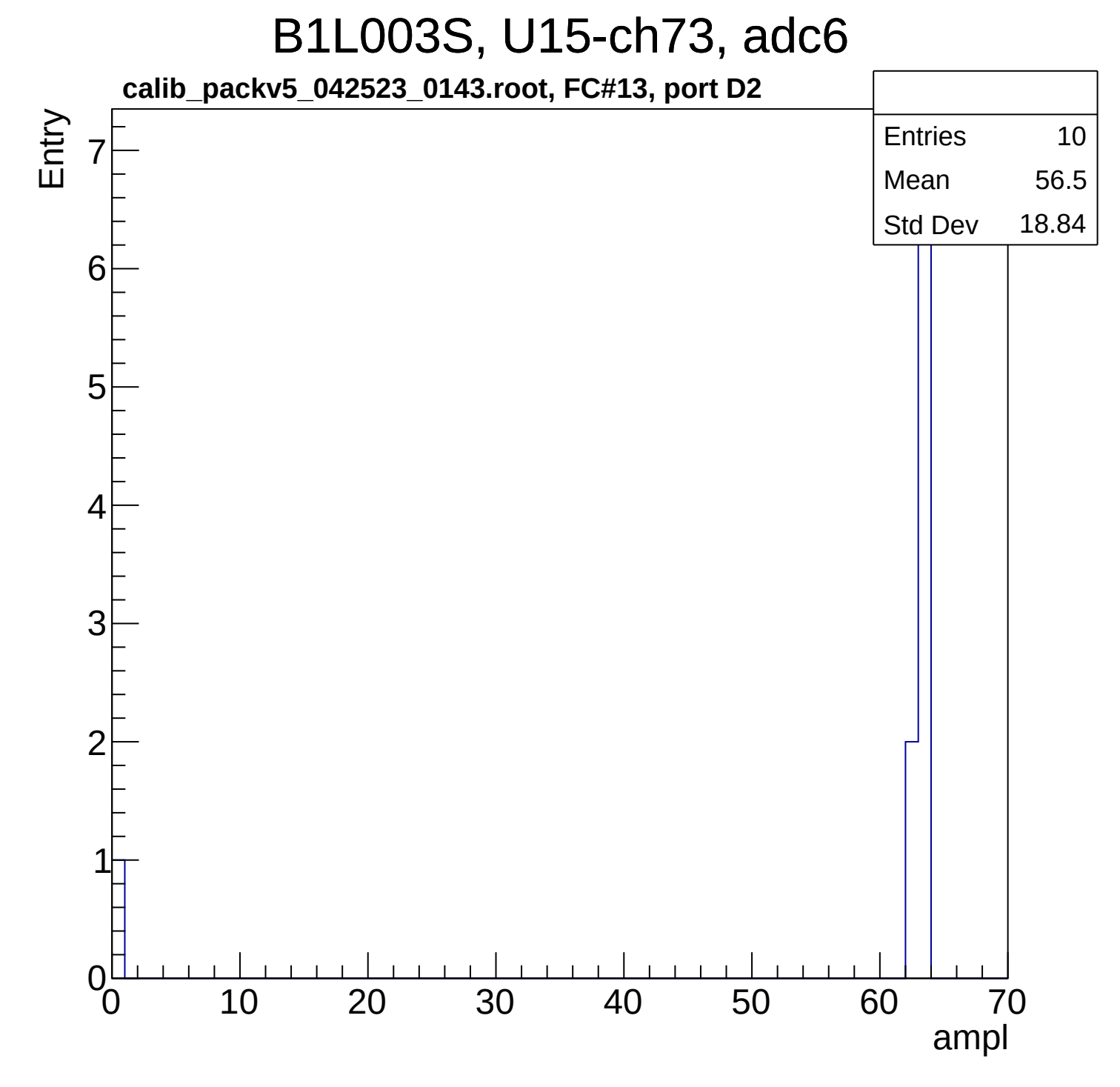
B1L003S, U15-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 56.5
Std Dev 18.84
Entry
ampl
0
1
2
3
4
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6
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0
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60
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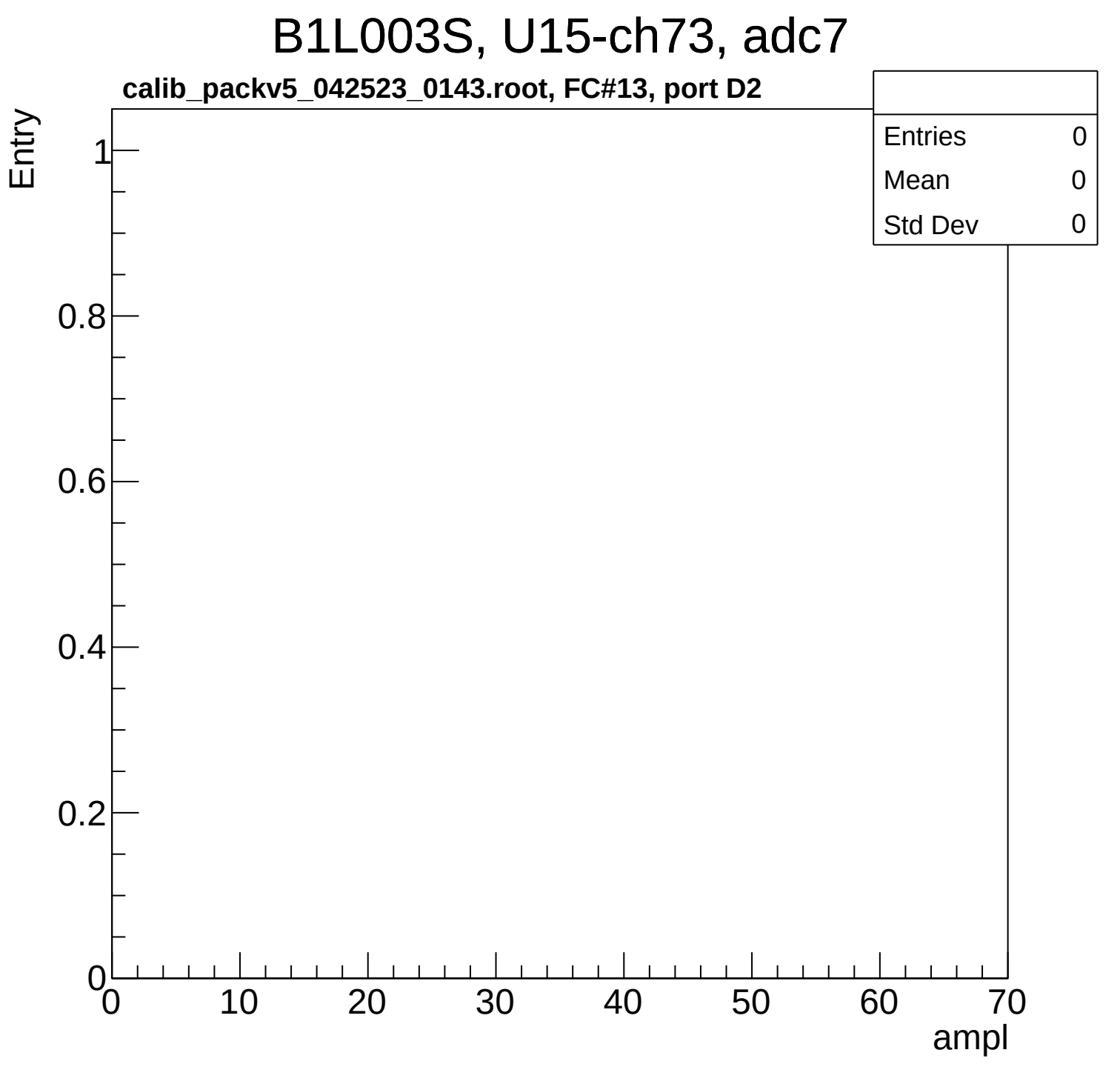
B1L003S, U15-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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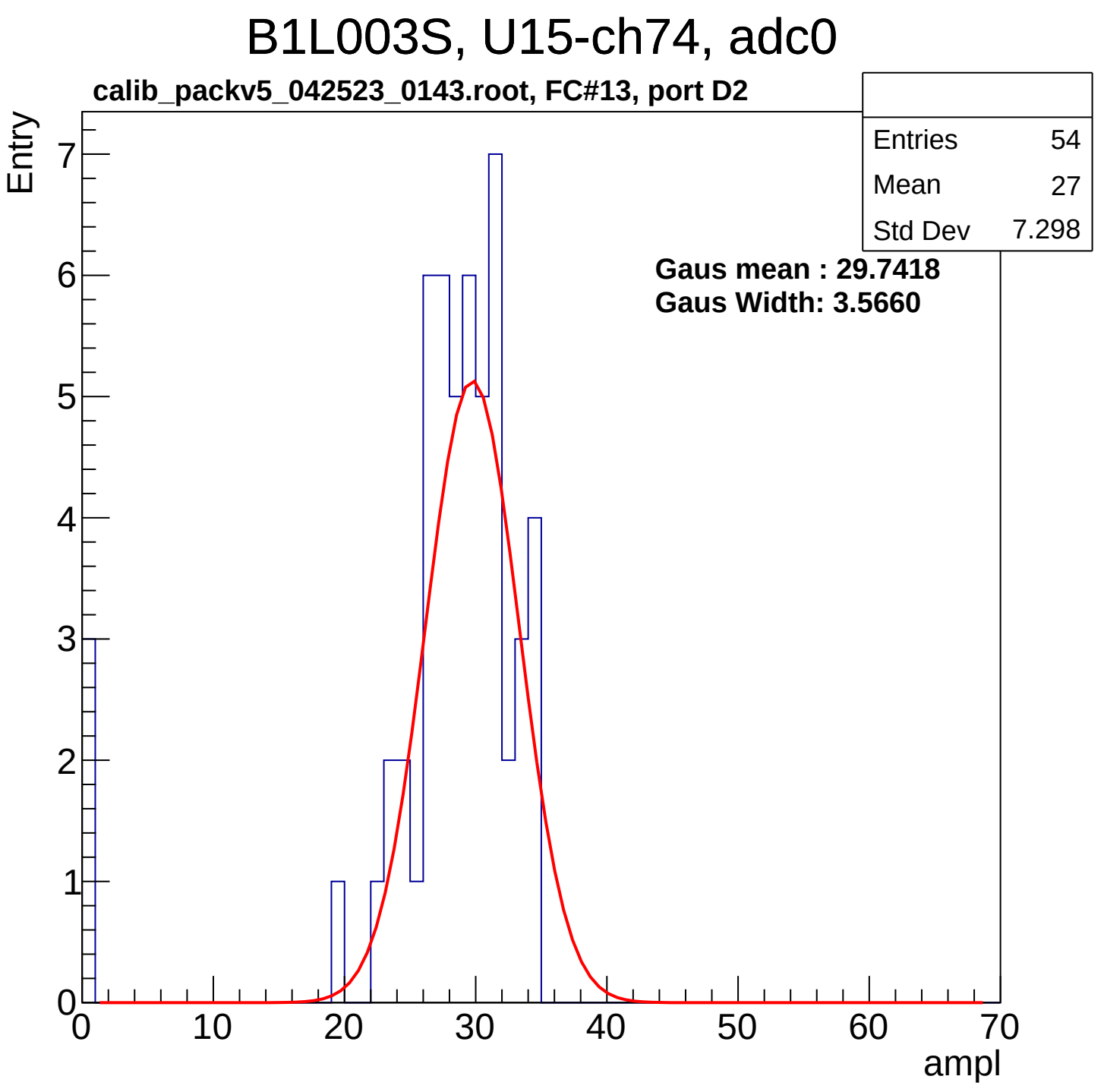
50
60
70
ampl


B1L003S, U15-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 27
Std Dev 7.298
Gaus mean : 29.7418
Gaus Width: 3.5660
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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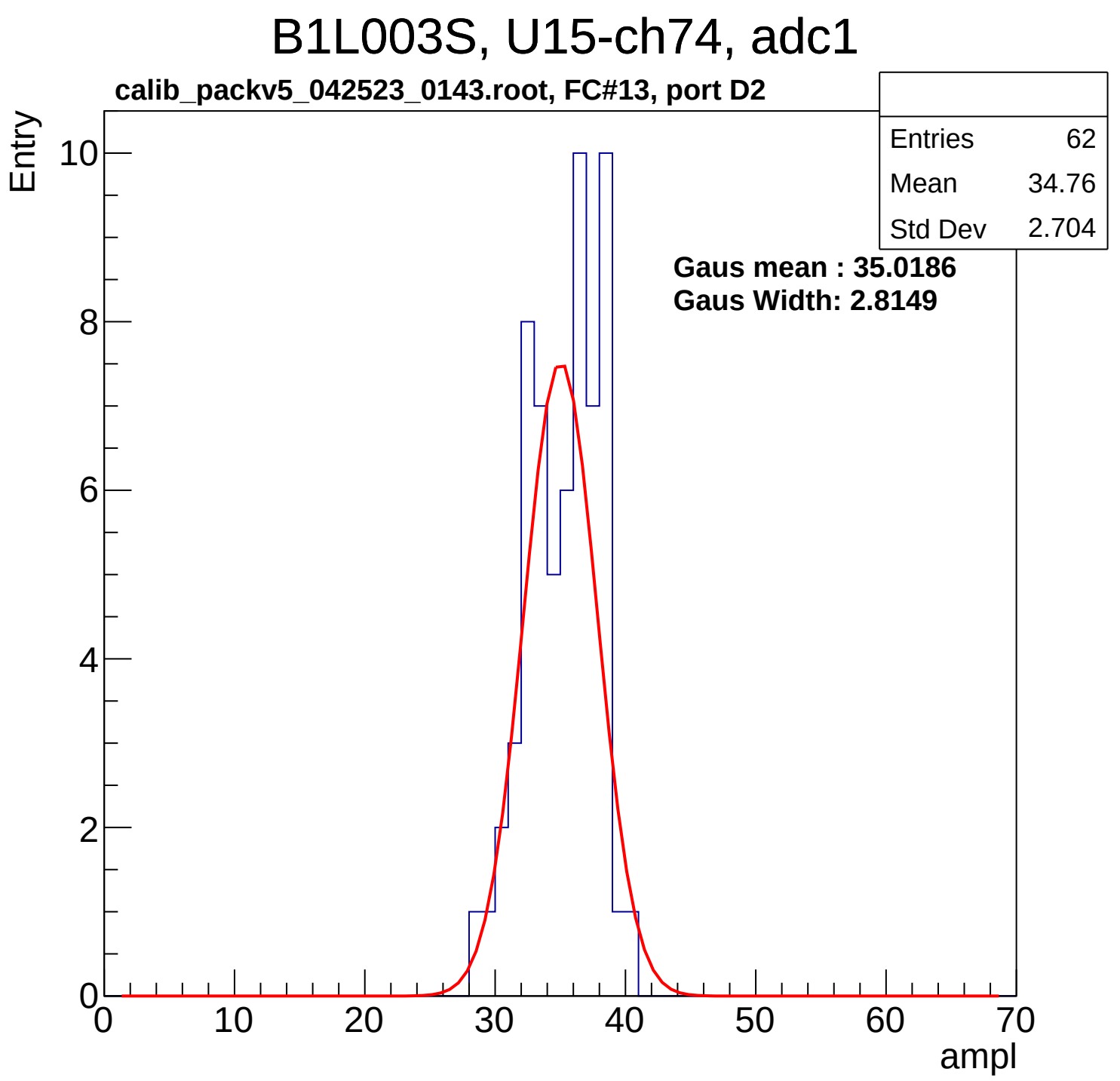
B1L003S, U15-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 34.76
Std Dev 2.704
Gaus mean : 35.0186
Gaus Width: 2.8149
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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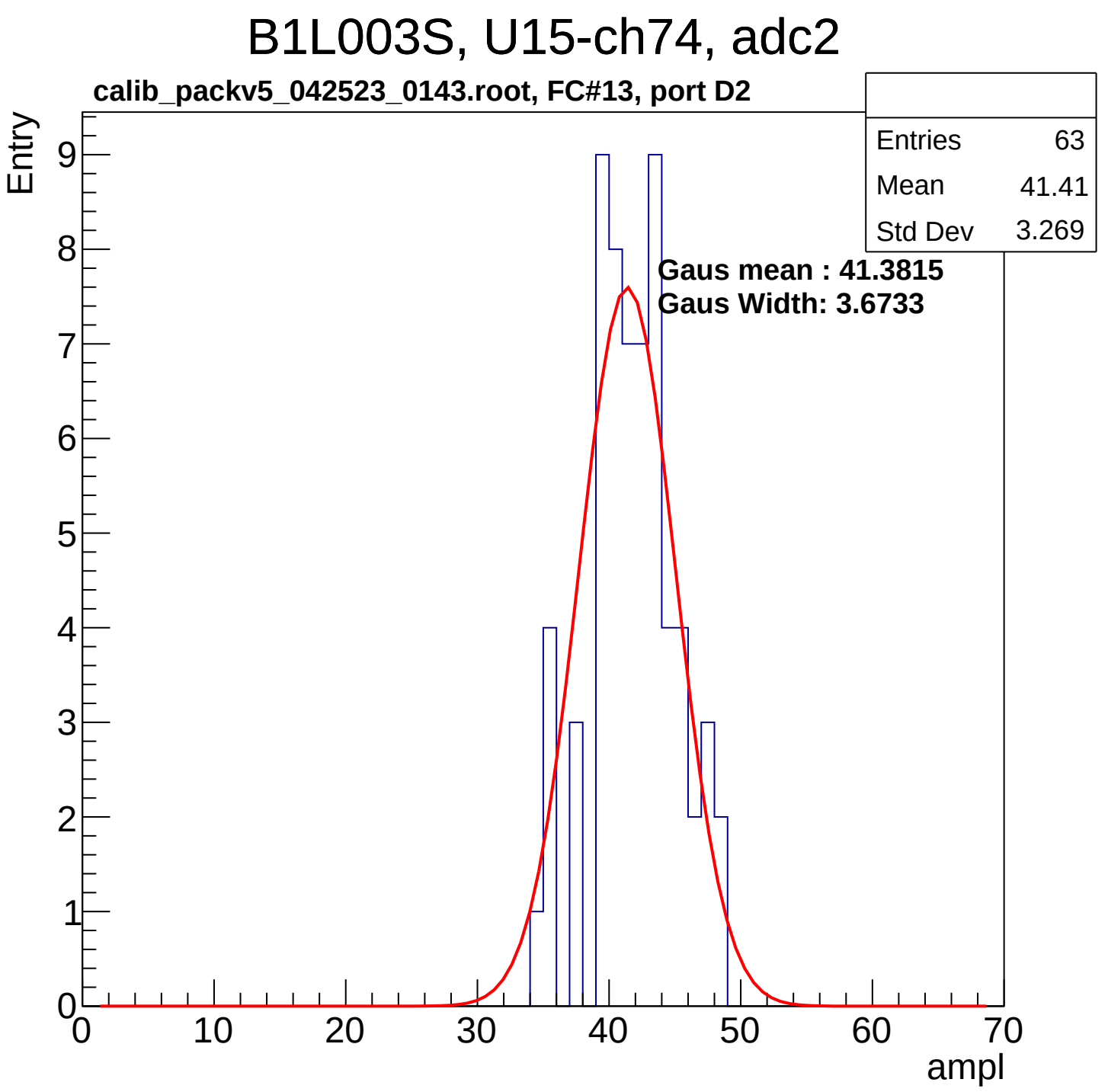
B1L003S, U15-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.41
Std Dev 3.269
Gaus mean : 41.3815
Gaus Width: 3.6733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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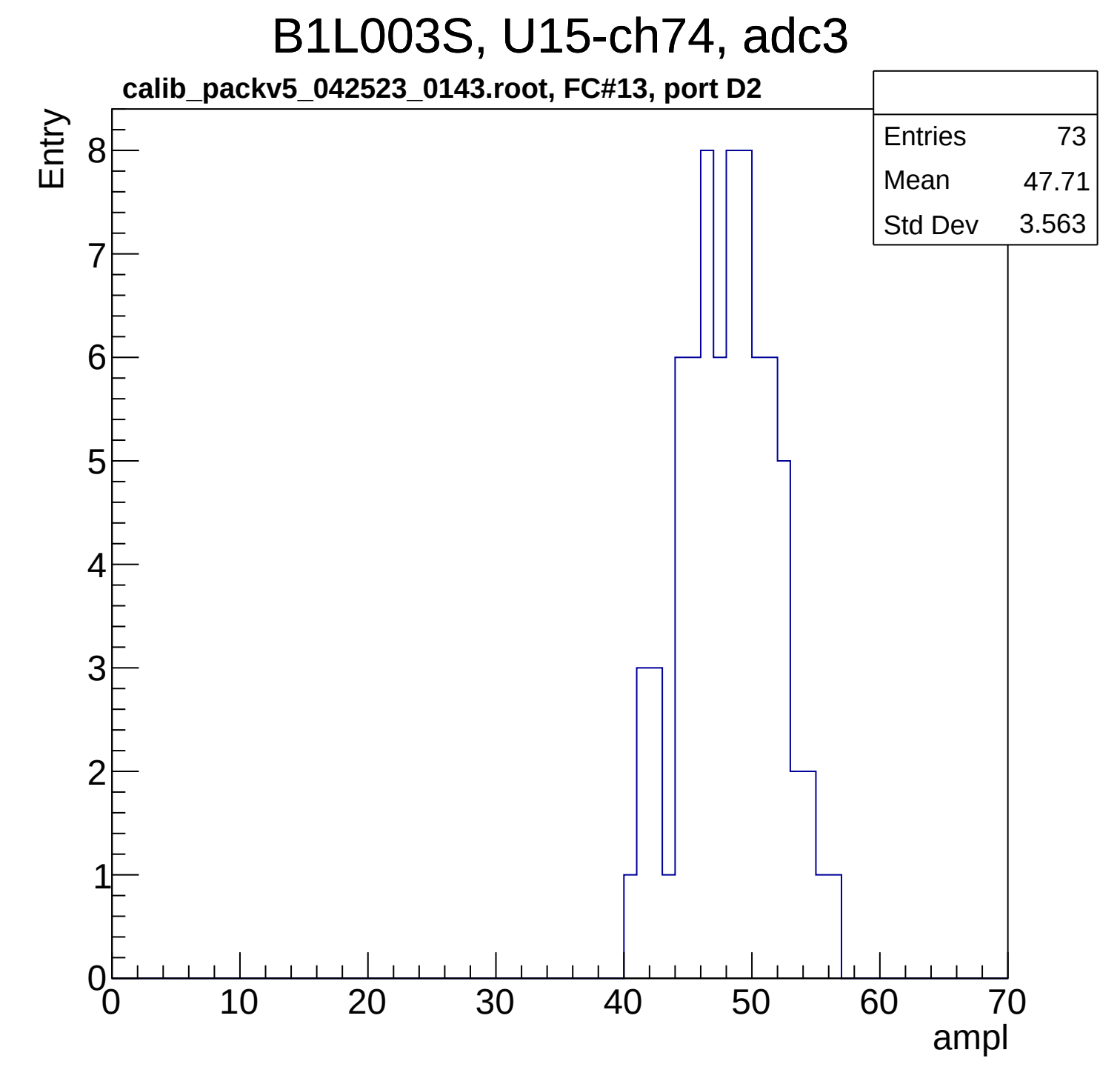
B1L003S, U15-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.71
Std Dev 3.563
Entry
ampl
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1
2
3
4
5
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7
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0
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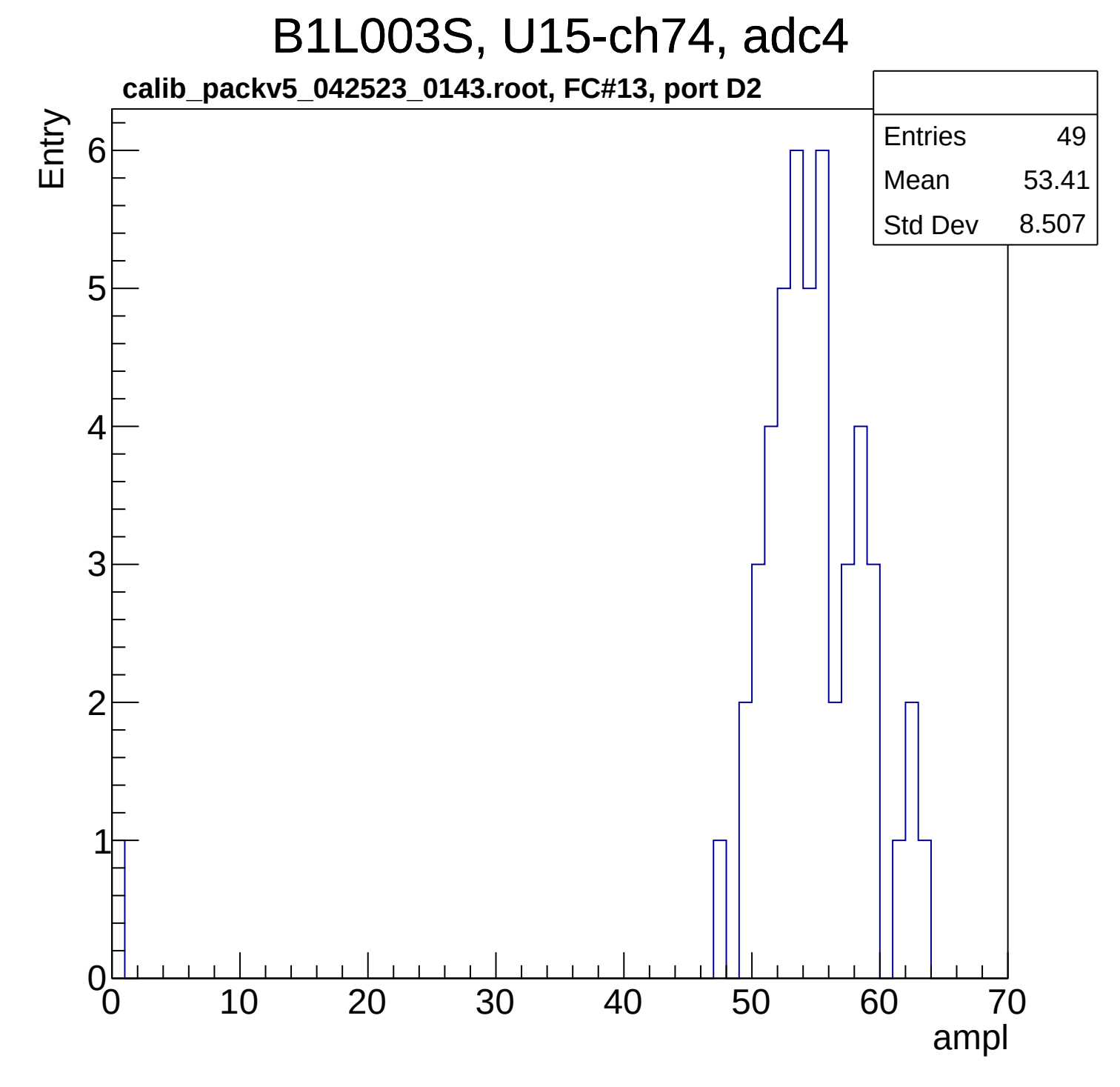
B1L003S, U15-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.41
Std Dev 8.507
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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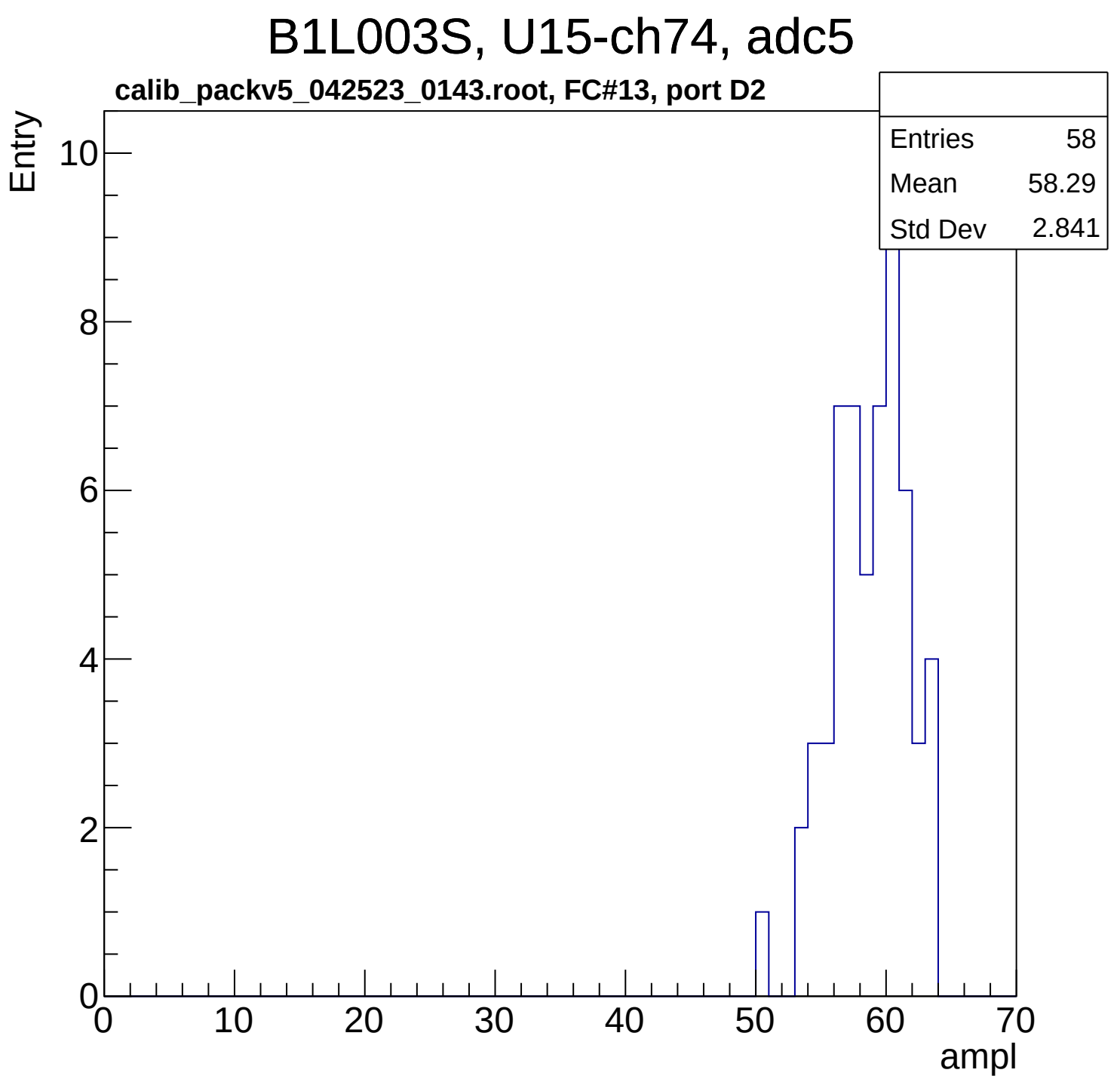
B1L003S, U15-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.29
Std Dev 2.841
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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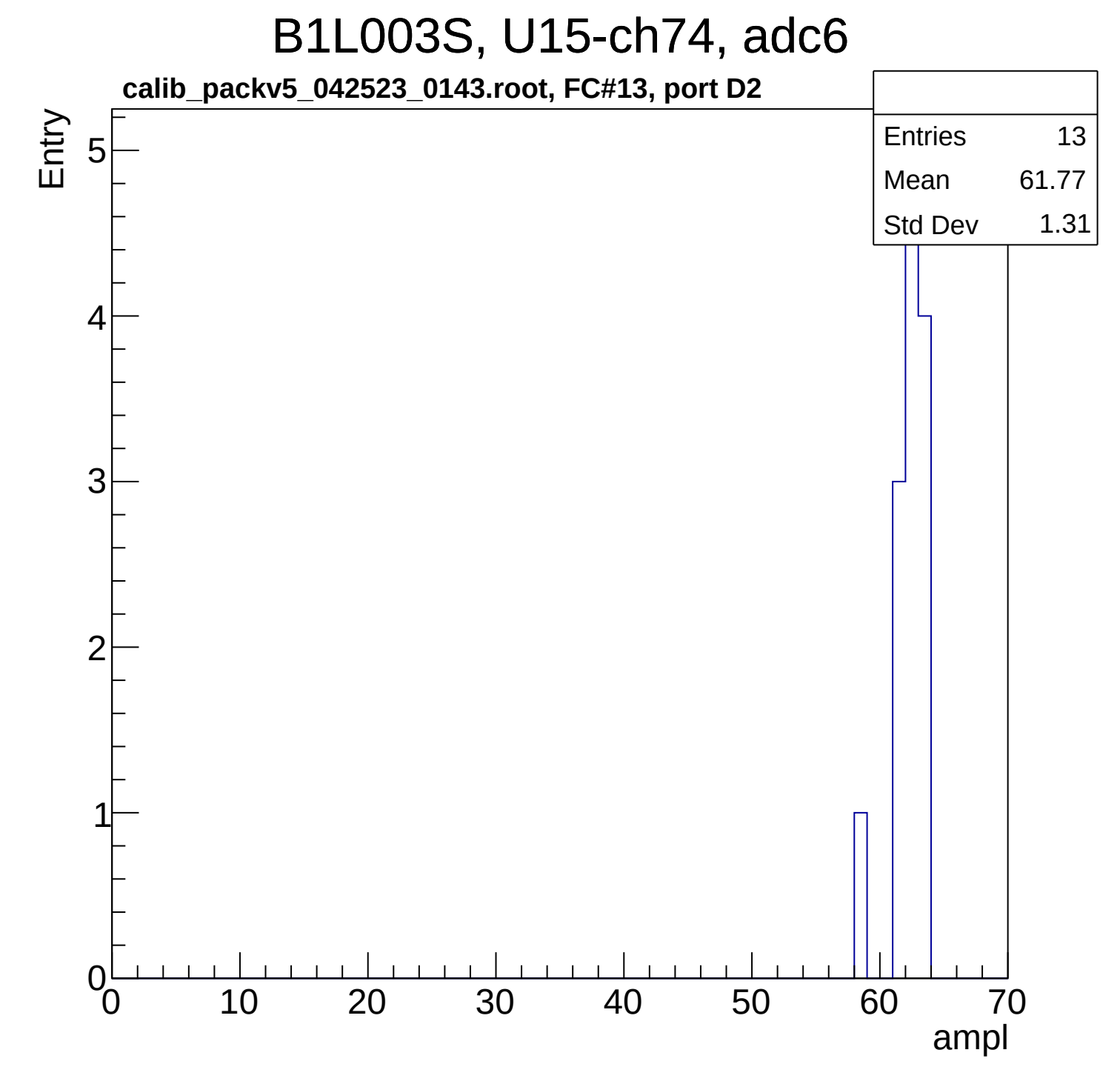
B1L003S, U15-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.77
Std Dev 1.31
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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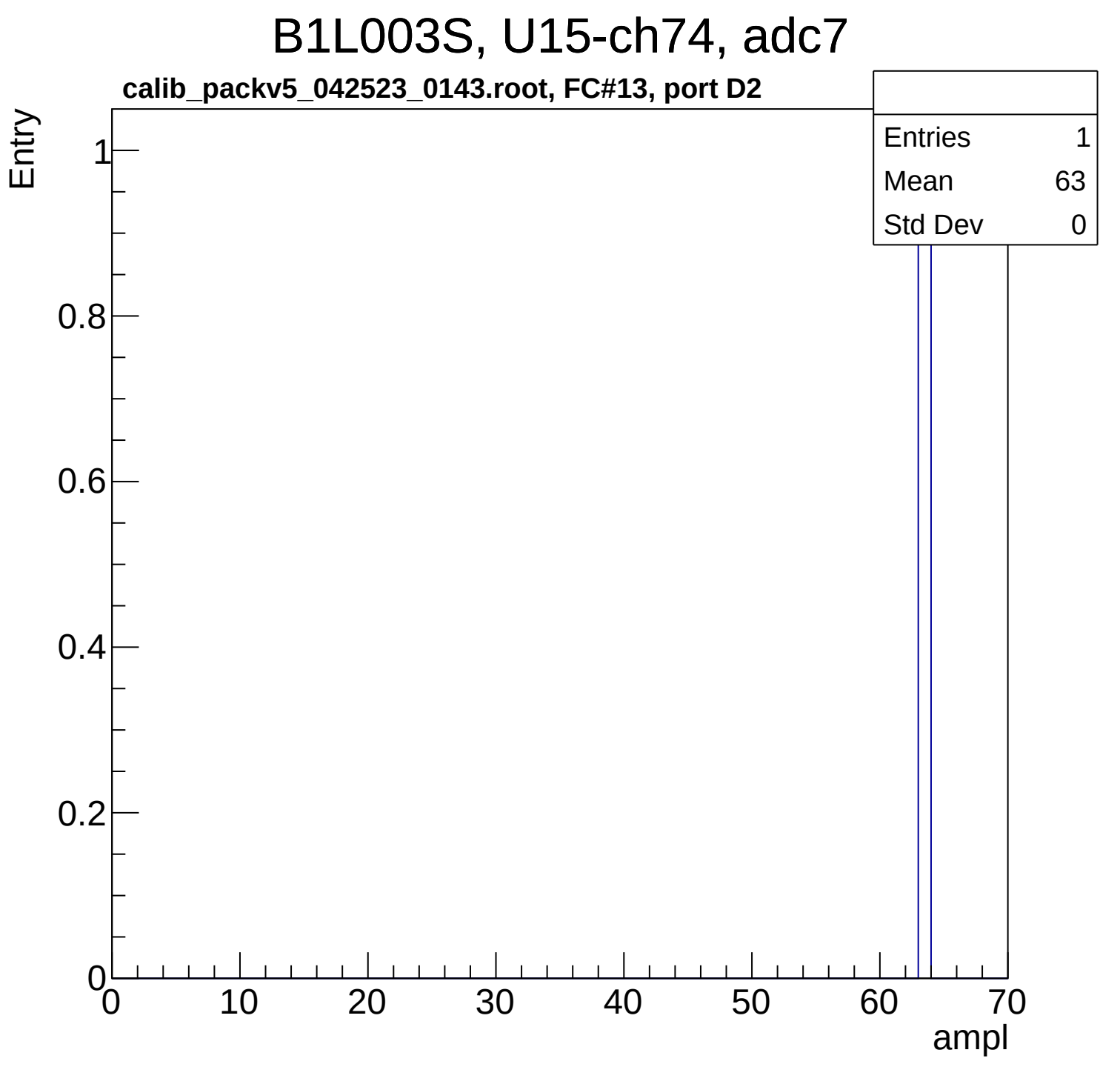
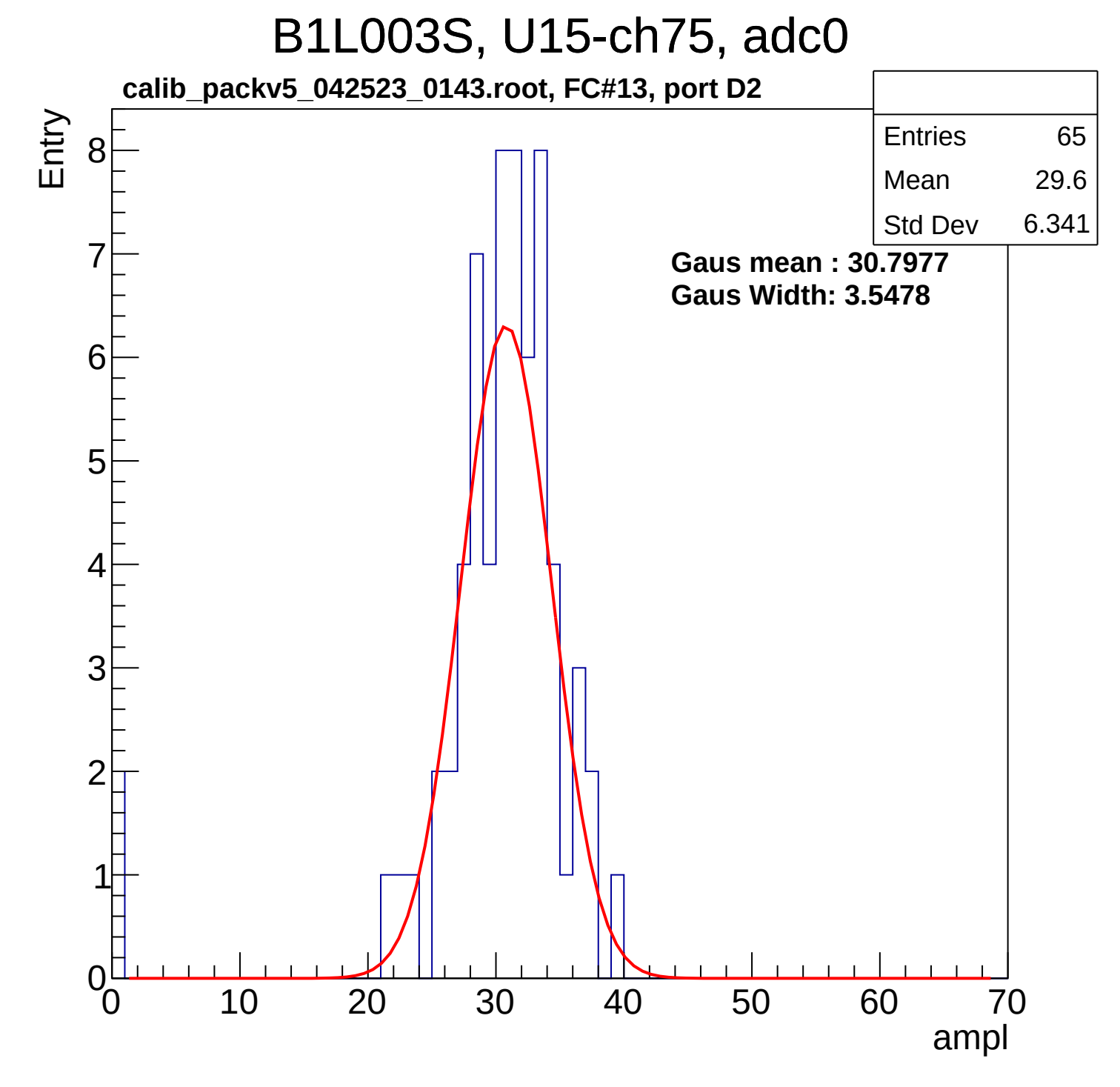
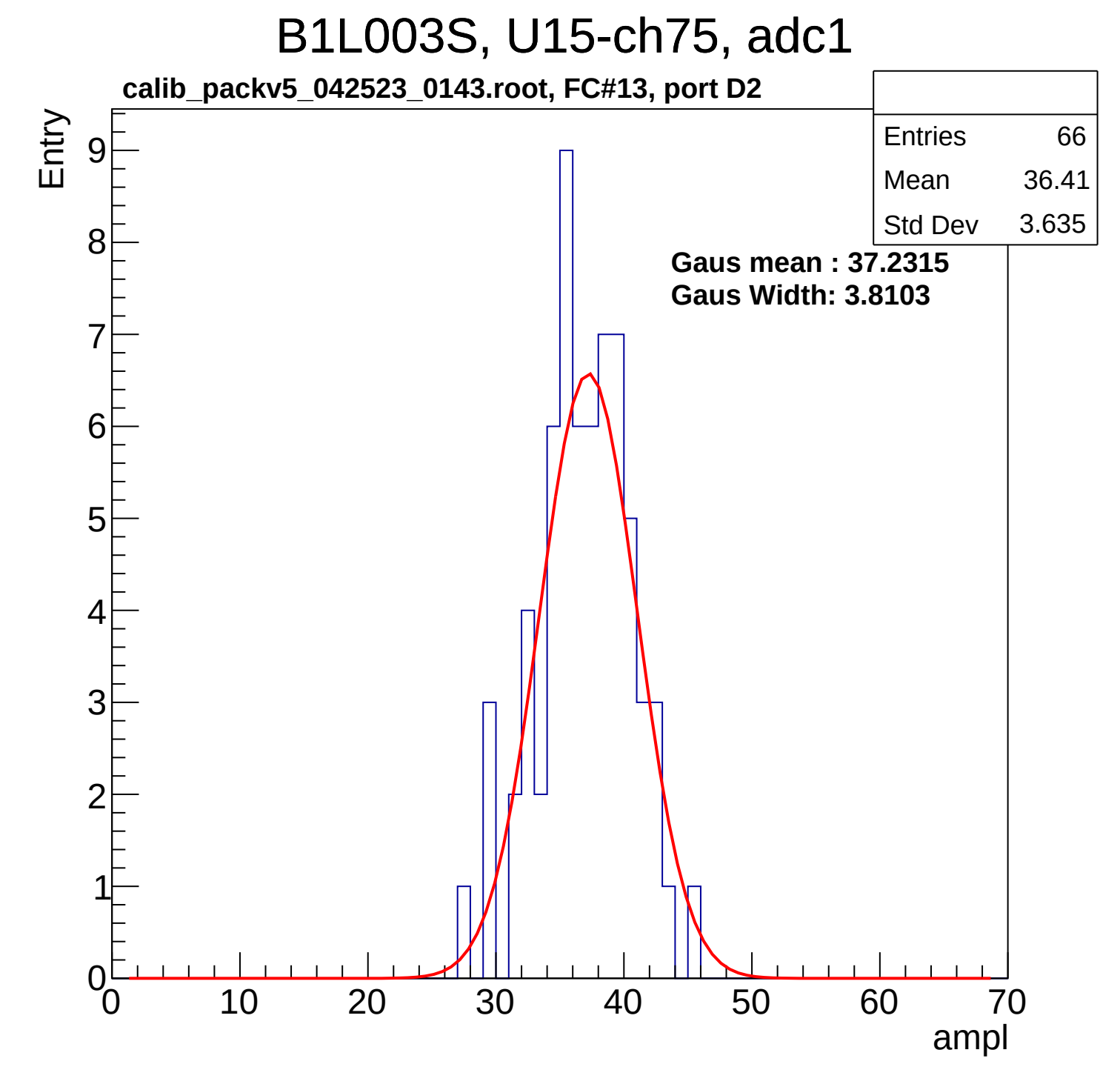

B1L003S, U15-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.6
Std Dev 6.341
Gaus mean : 30.7977
Gaus Width: 3.5478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 36.41
Std Dev 3.635
Gaus mean : 37.2315
Gaus Width: 3.8103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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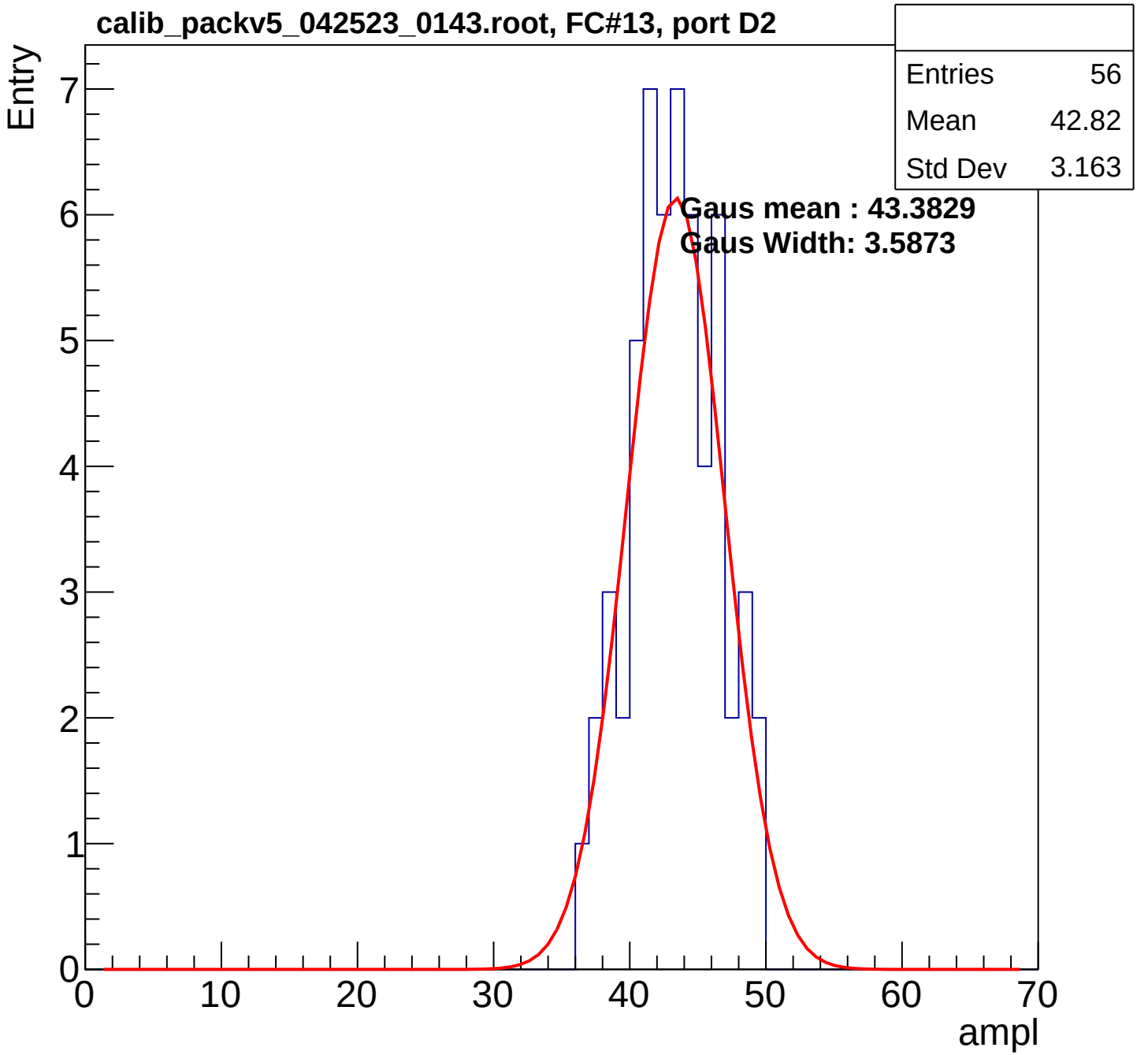
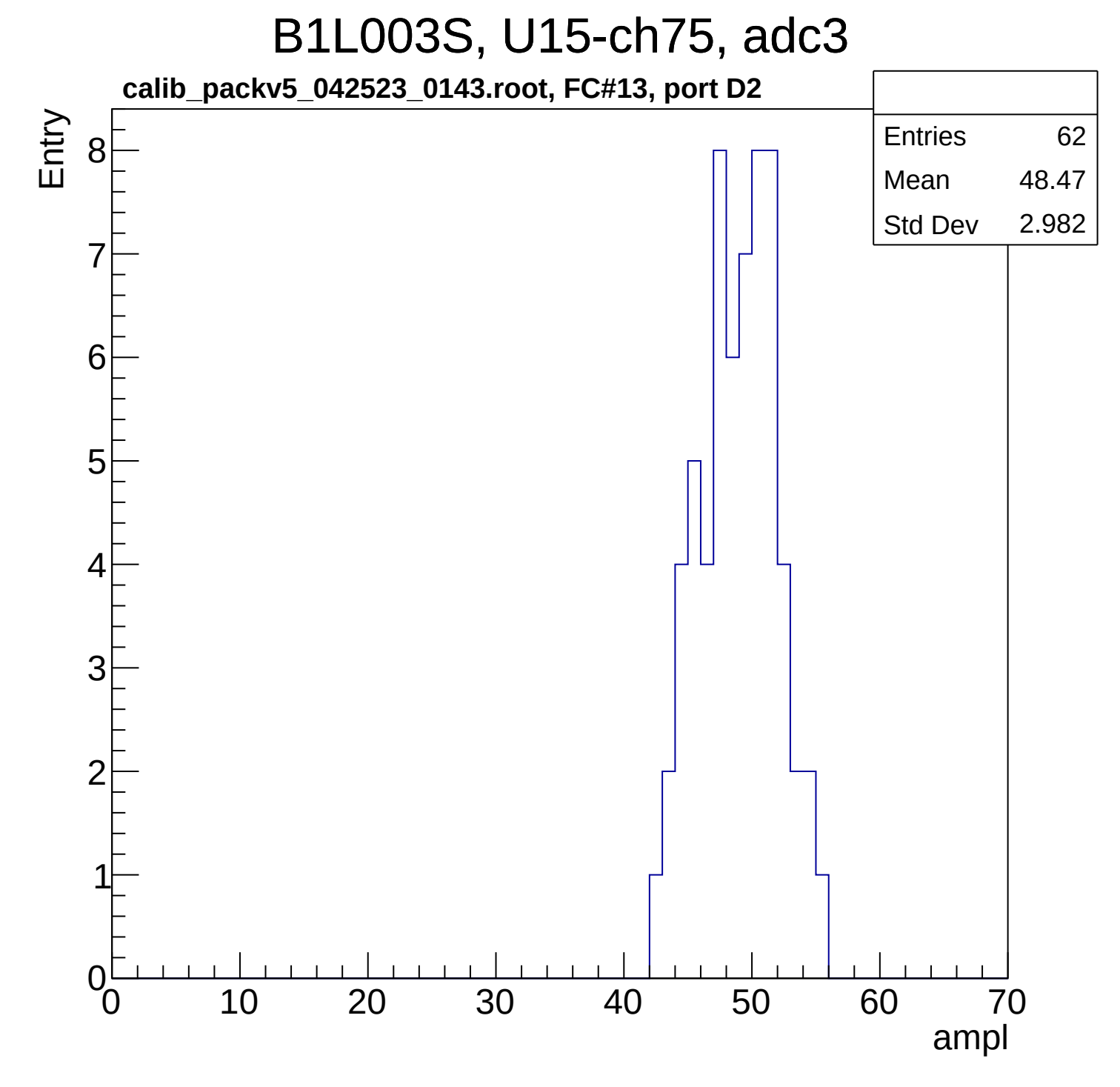

B1L003S, U15-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.82
Std Dev 3.163
Gaus mean : 43.3829
Gaus Width: 3.5873
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.47
Std Dev 2.982
Entry
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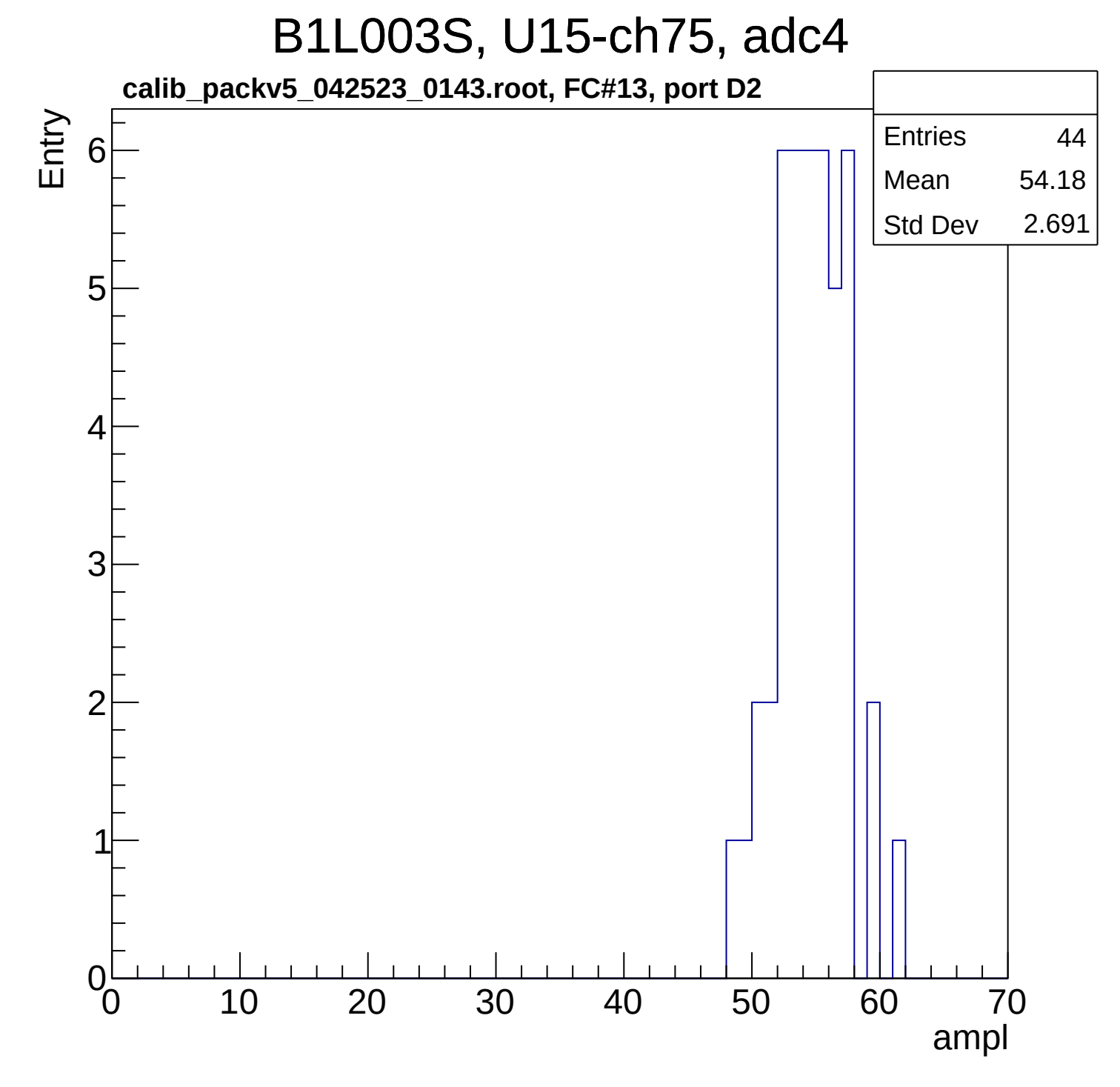
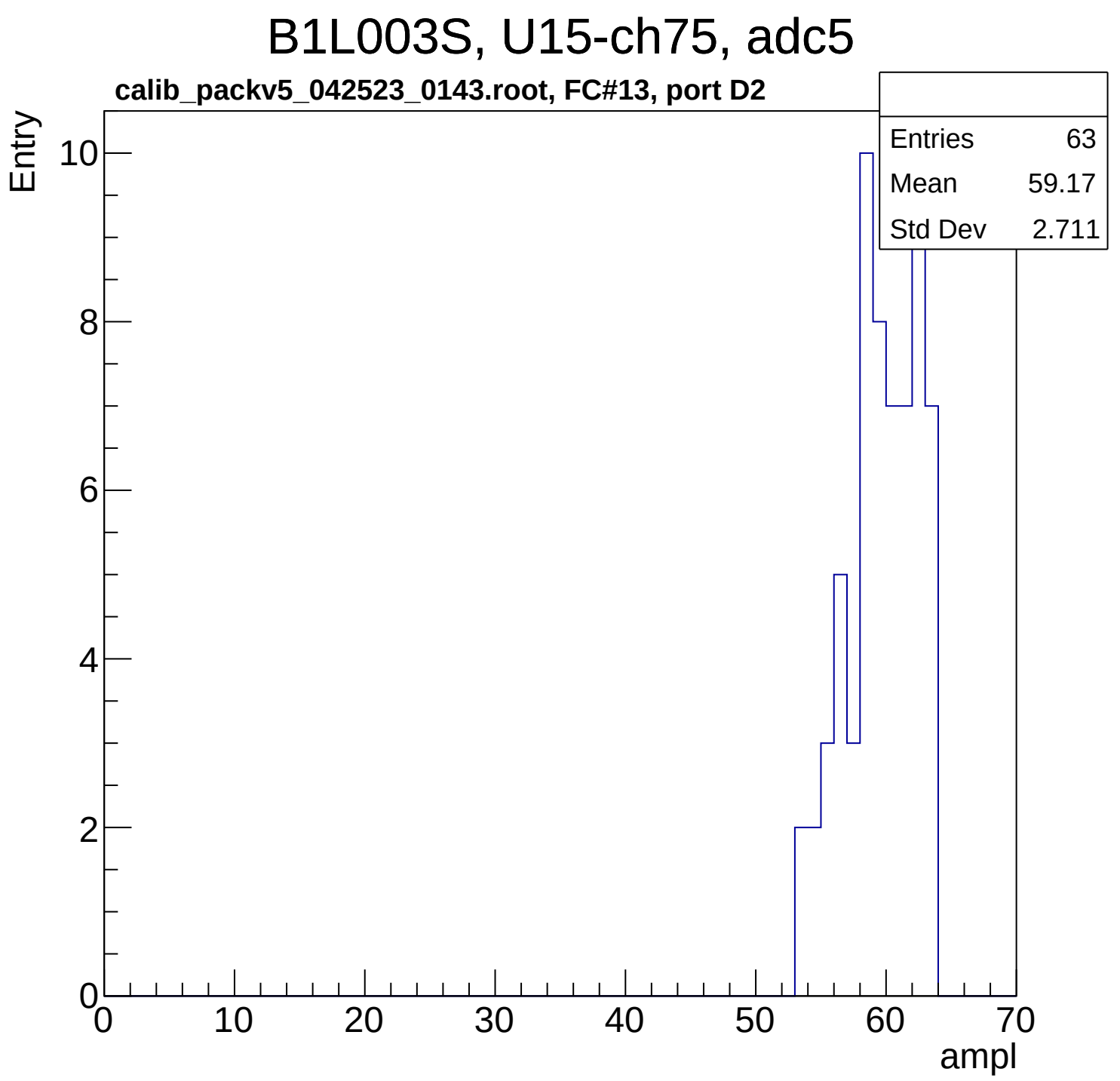
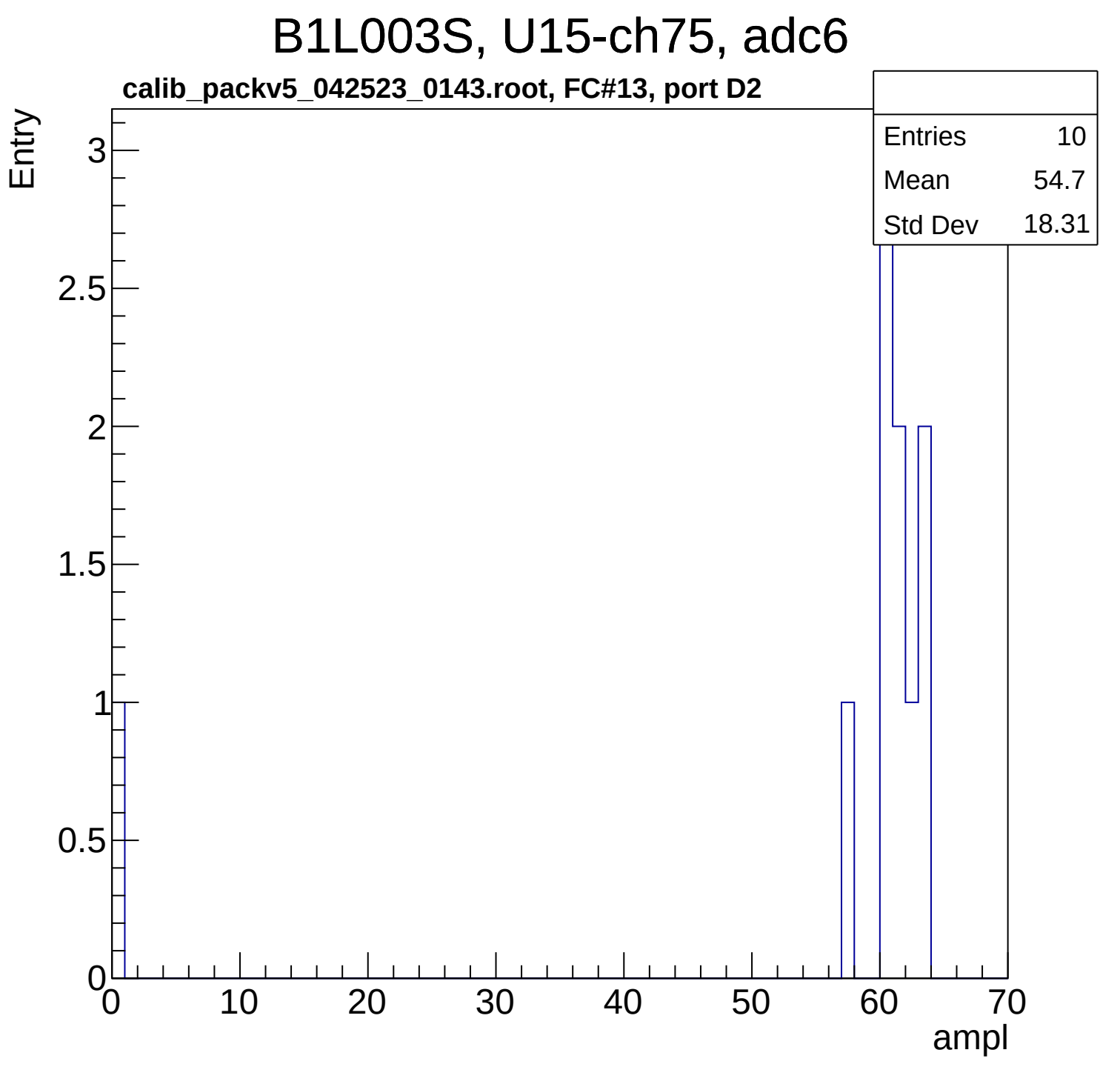
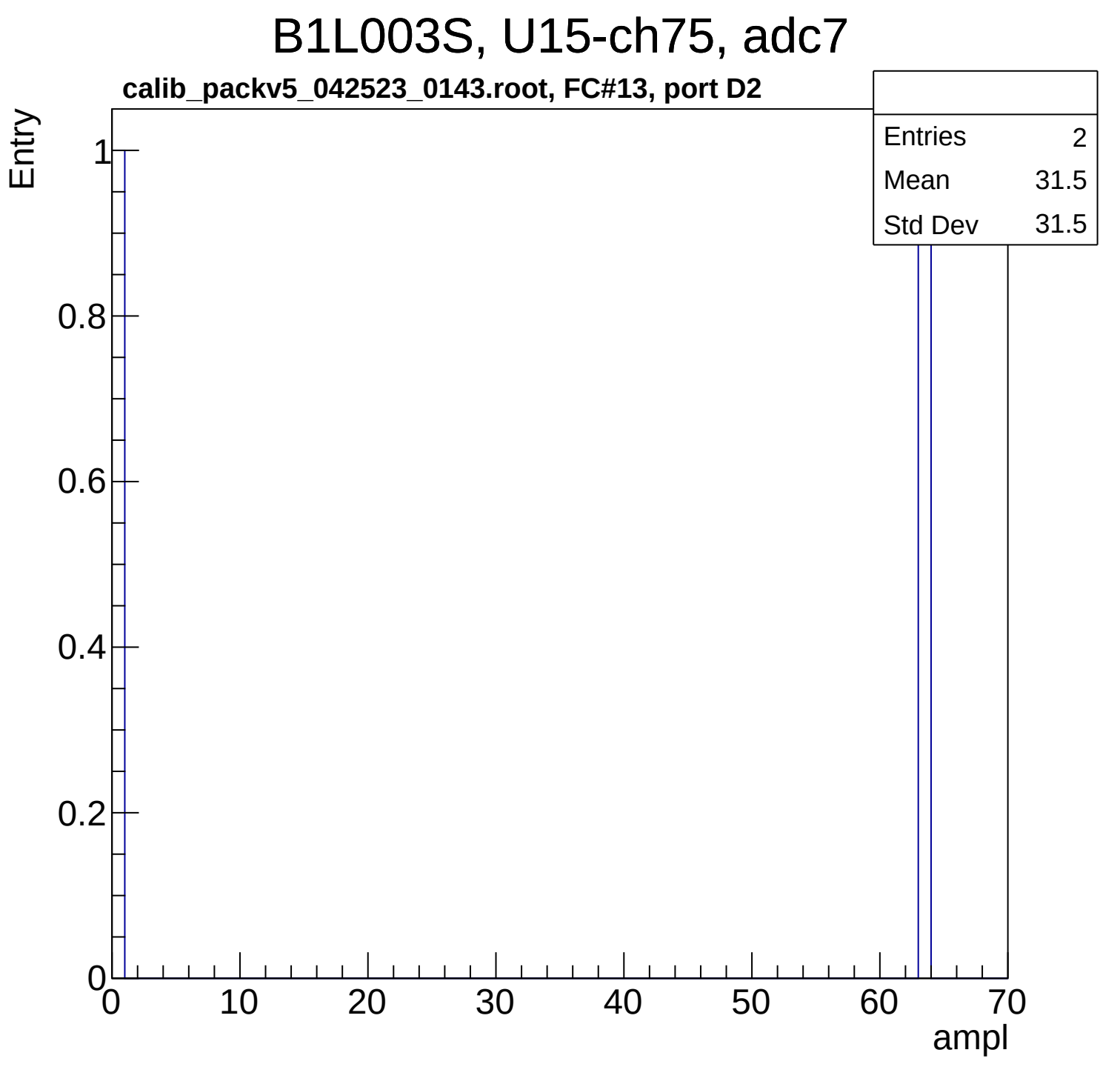
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 54.18
Std Dev 2.691
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U15-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 59.17
Std Dev 2.711
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 54.7
Std Dev 18.31
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
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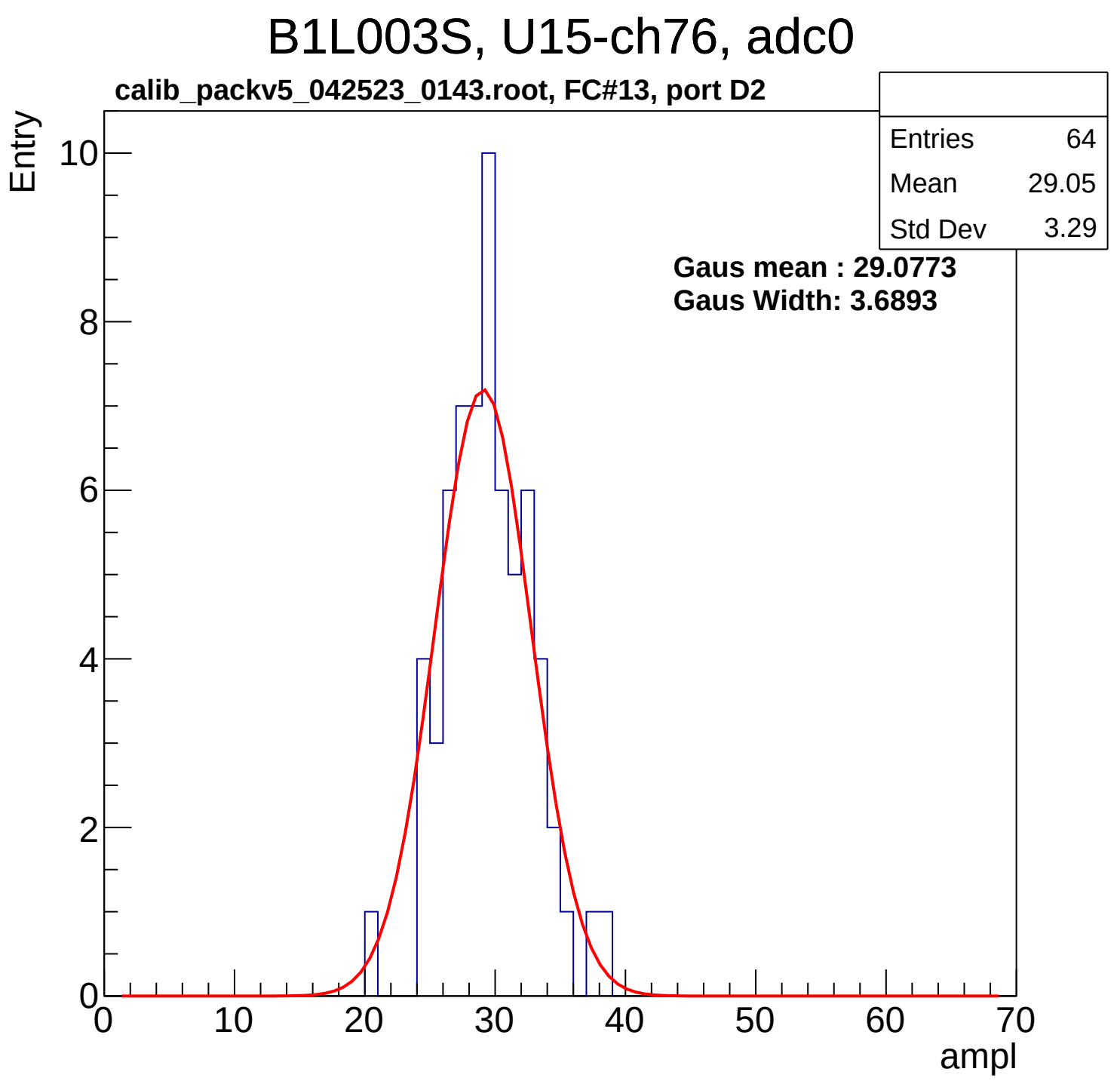
40
50
60
70
ampl


B1L003S, U15-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.05
Std Dev 3.29
Gaus mean : 29.0773
Gaus Width: 3.6893
Entry
ampl
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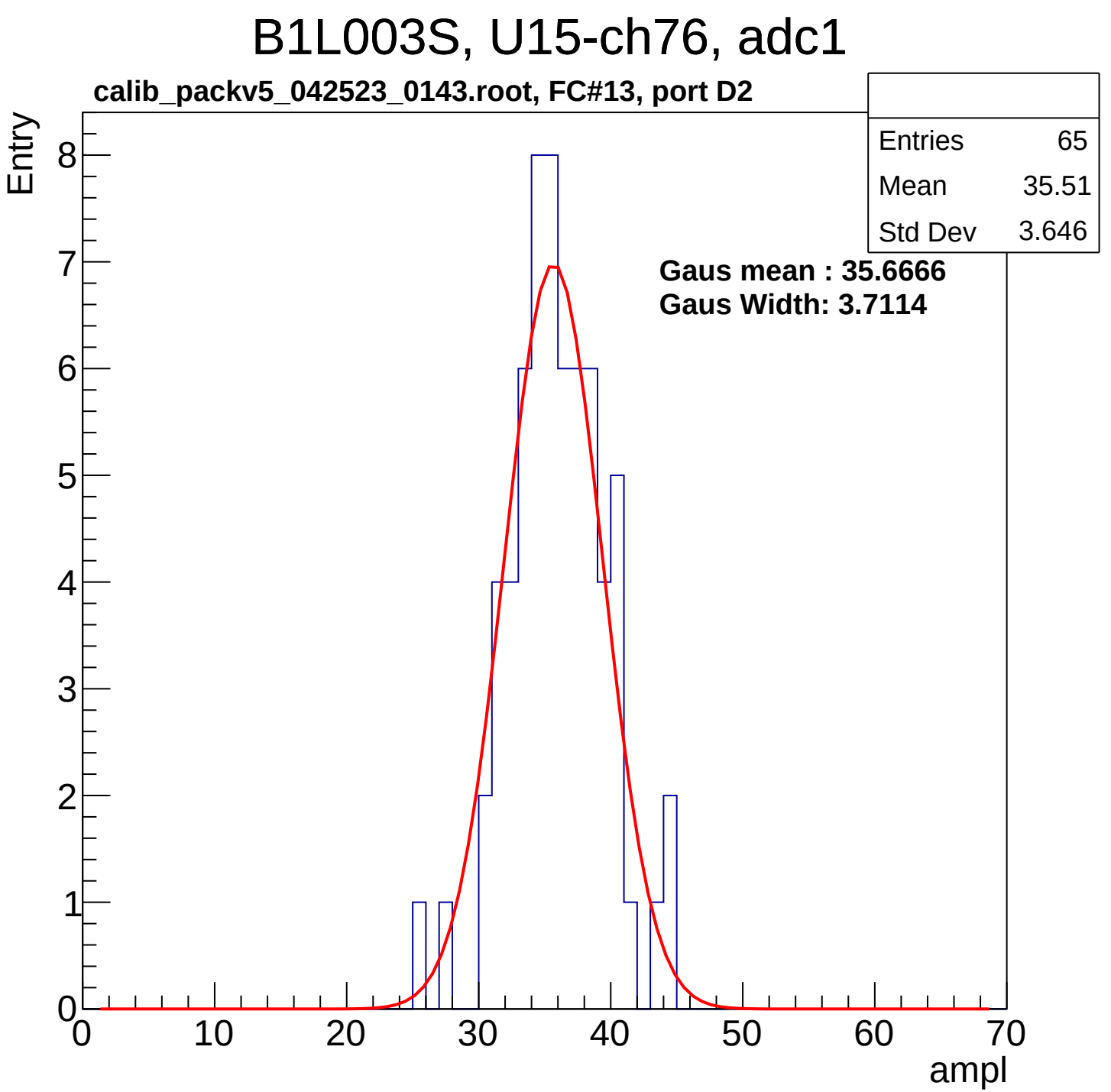
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.51
Std Dev 3.646
Gaus mean : 35.6666
Gaus Width: 3.7114
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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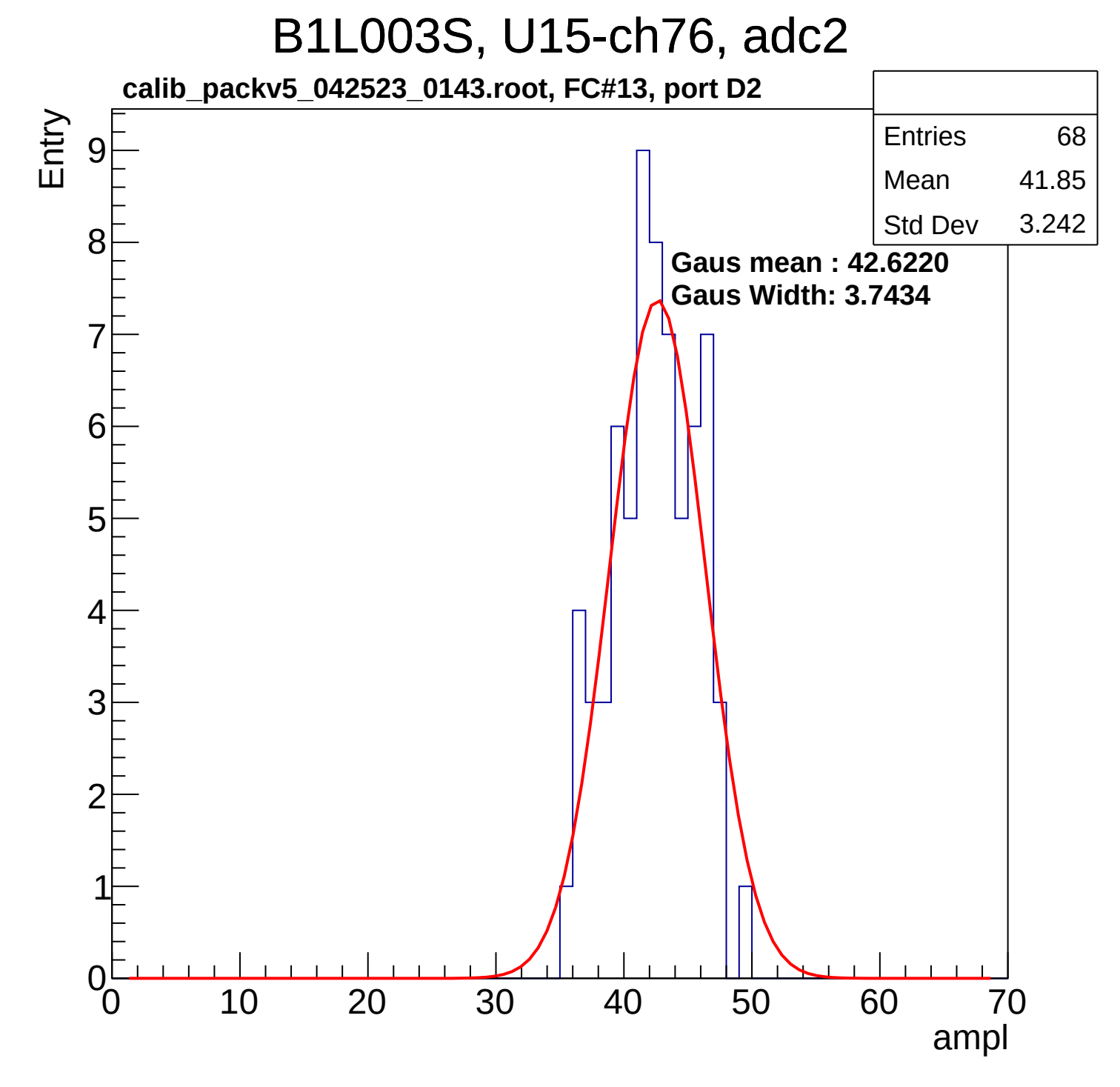
40
50
60
70


B1L003S, U15-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 41.85
Std Dev 3.242
Gaus mean : 42.6220
Gaus Width: 3.7434
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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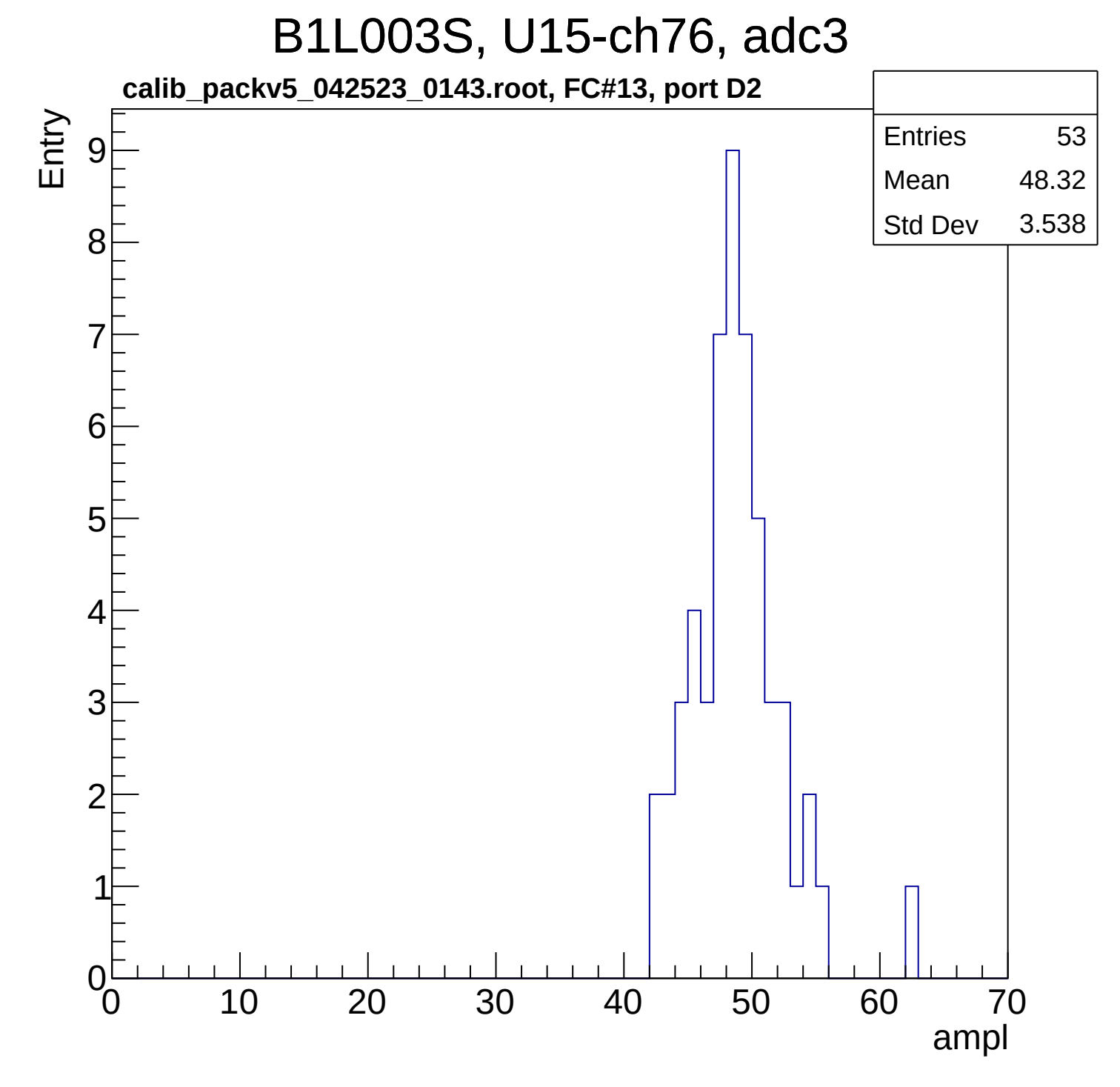
B1L003S, U15-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.32
Std Dev 3.538
Entry
ampl
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3
4
5
6
7
8
9
0
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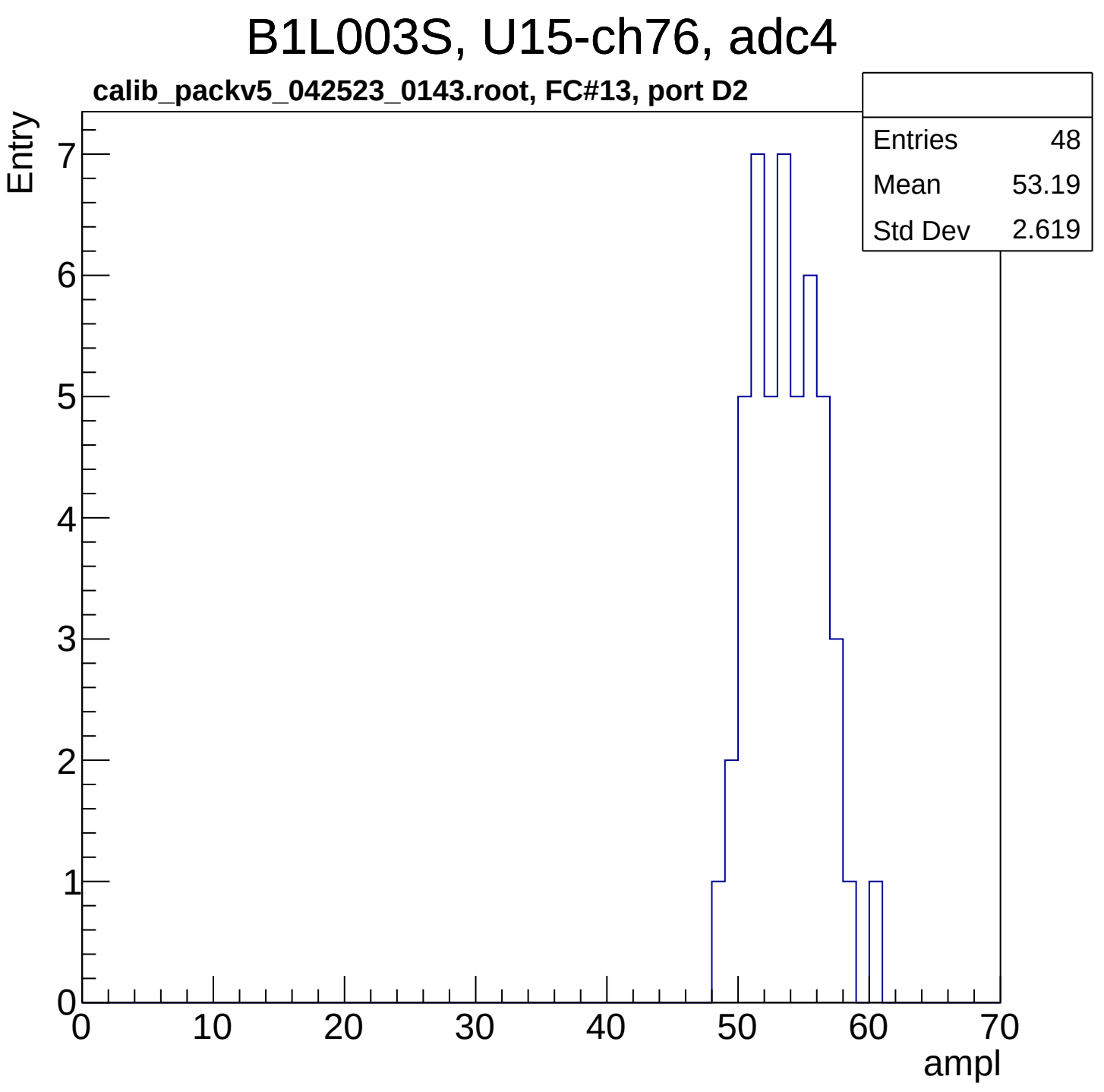
20
30
40
50
60
70


B1L003S, U15-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 53.19
Std Dev 2.619
Entry
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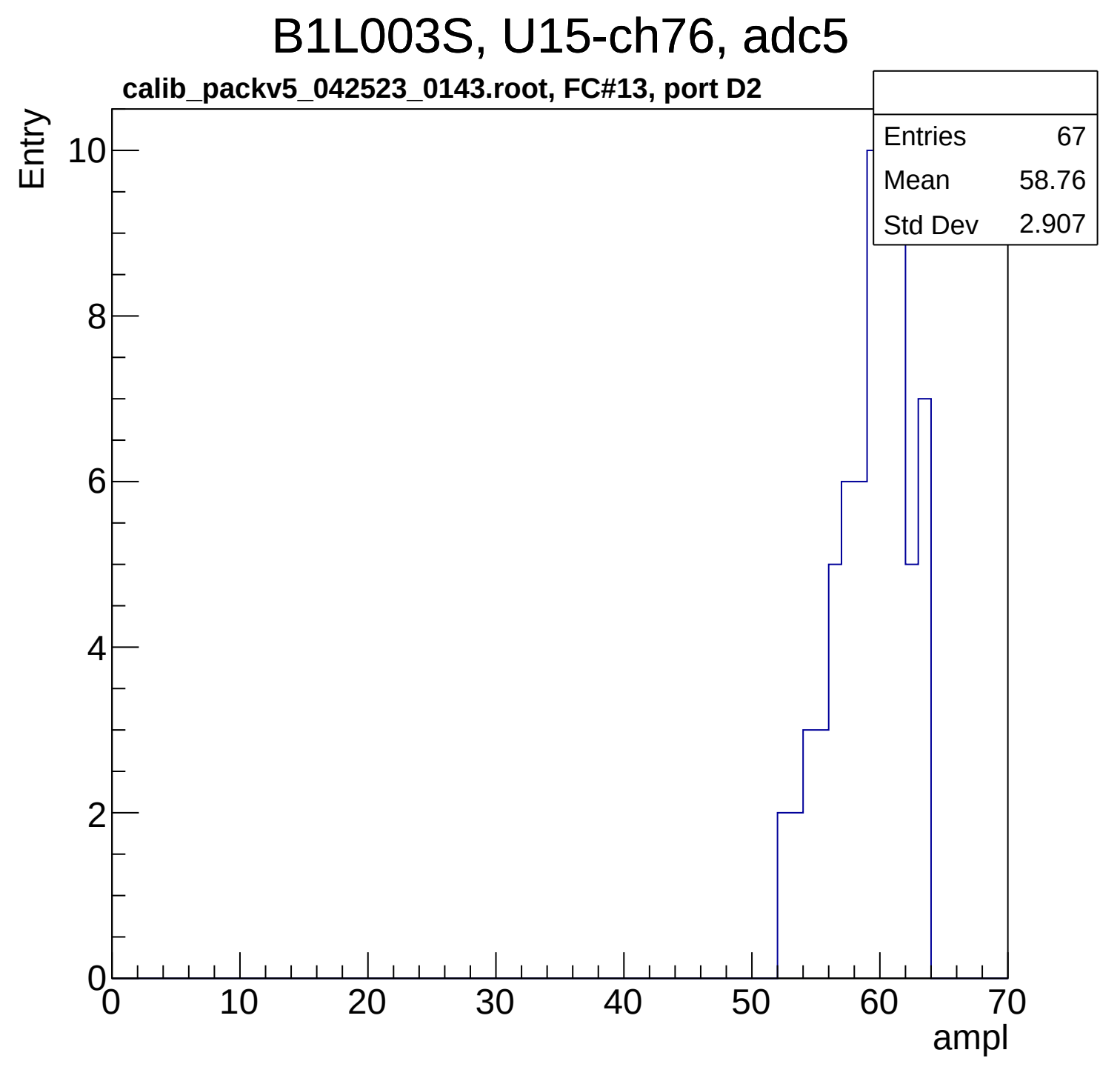
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 58.76
Std Dev 2.907
Entry
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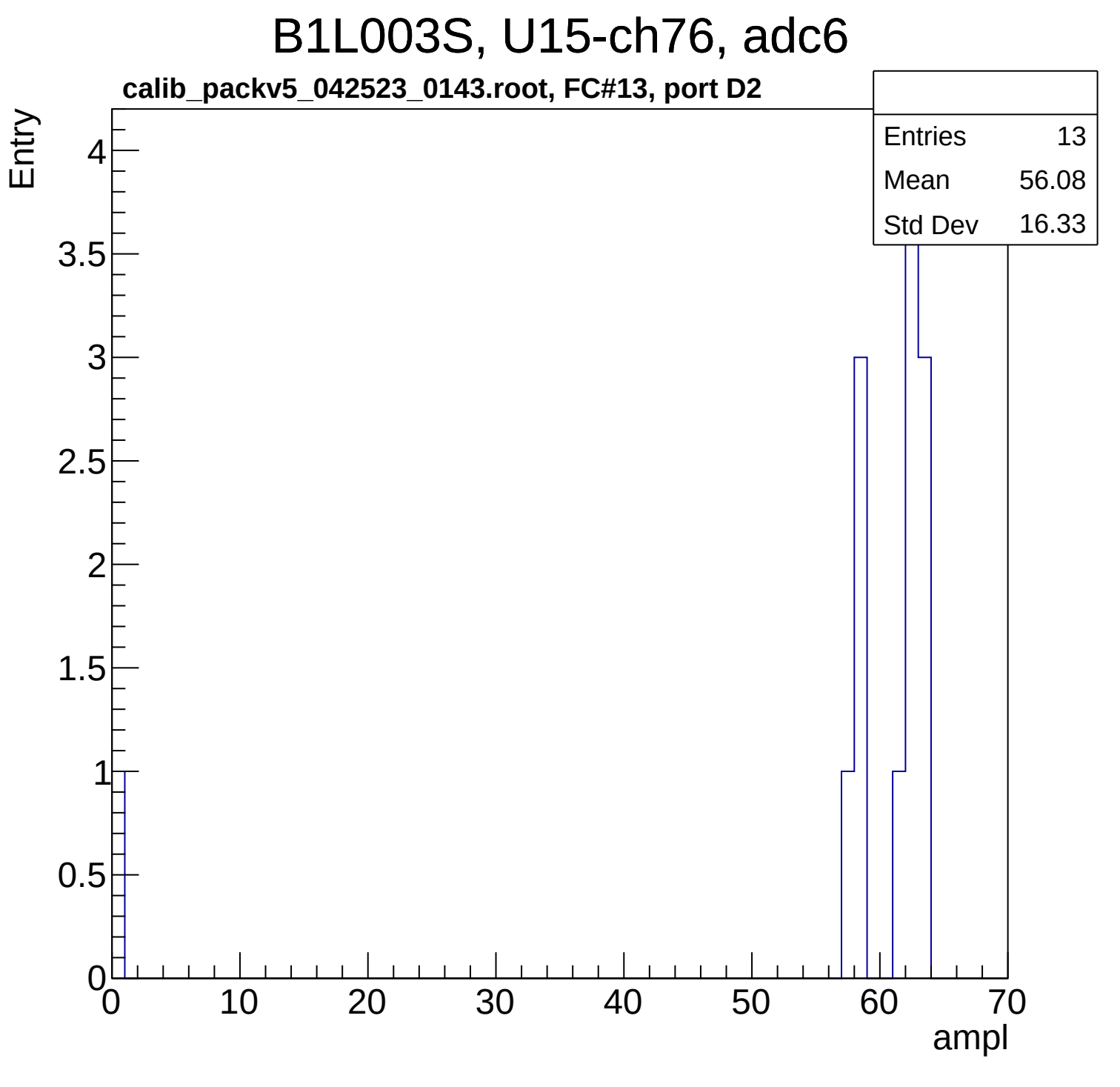
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.08
Std Dev 16.33
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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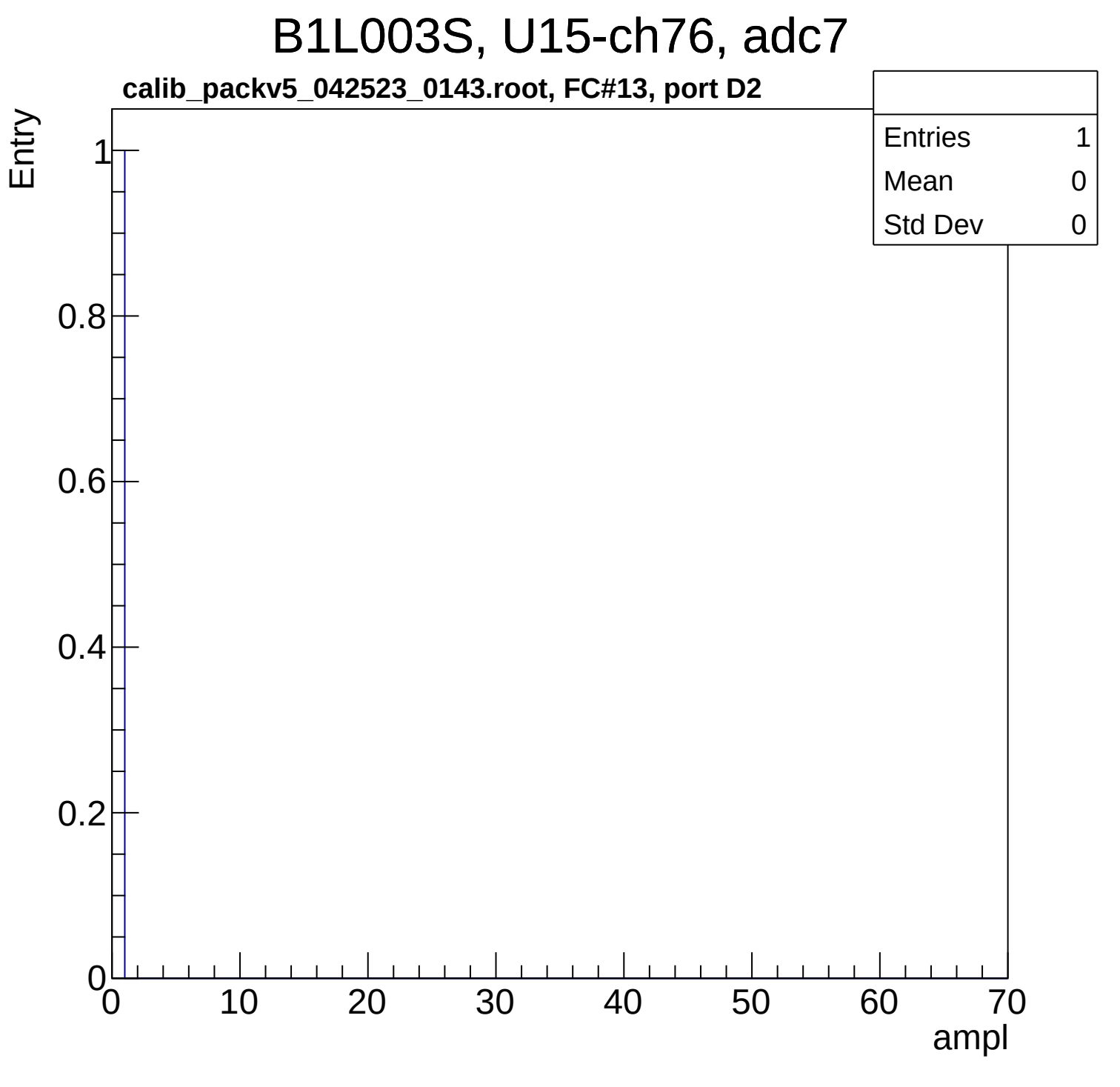
B1L003S, U15-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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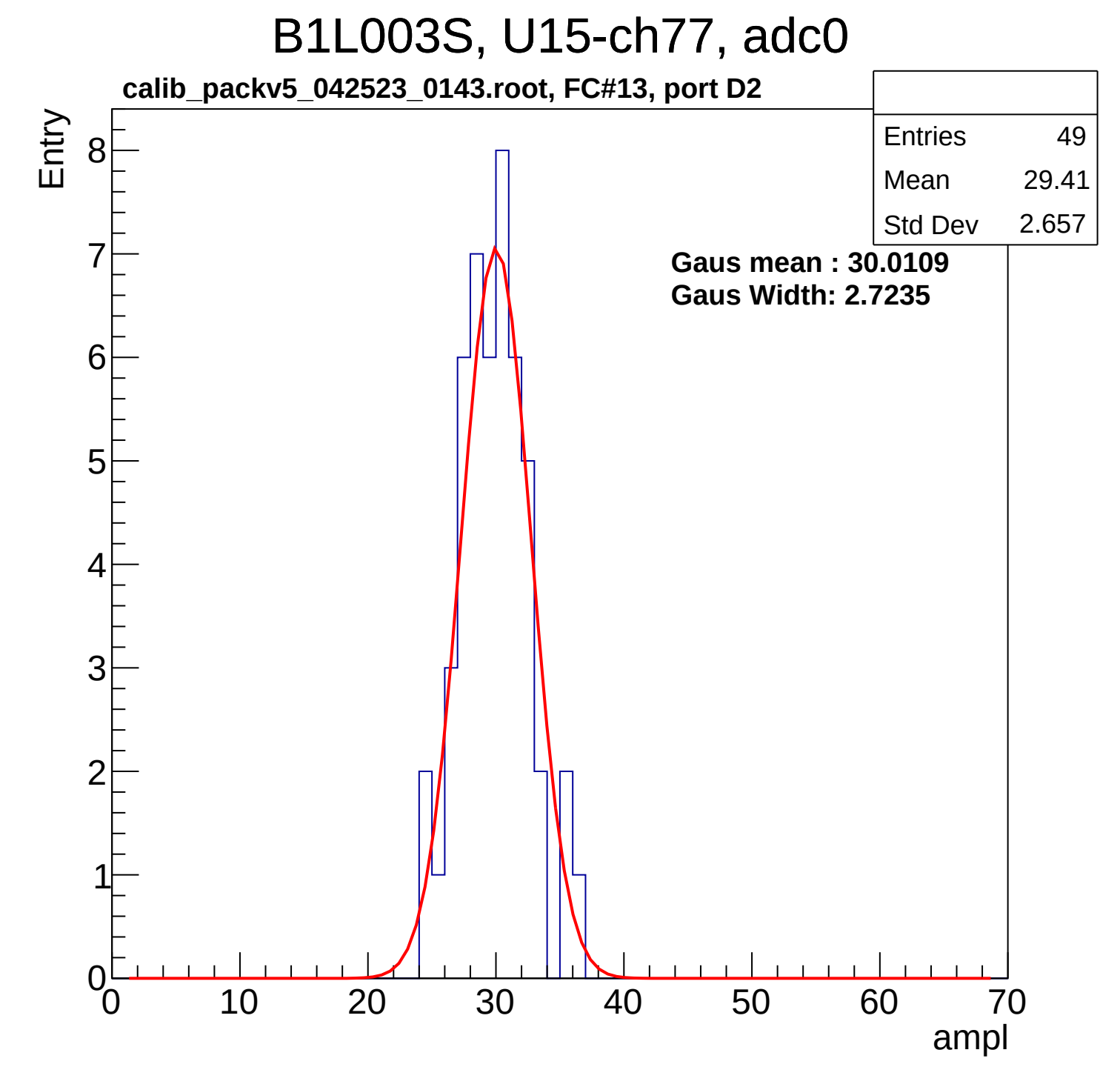
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.41
Std Dev 2.657
Gaus mean : 30.0109
Gaus Width: 2.7235
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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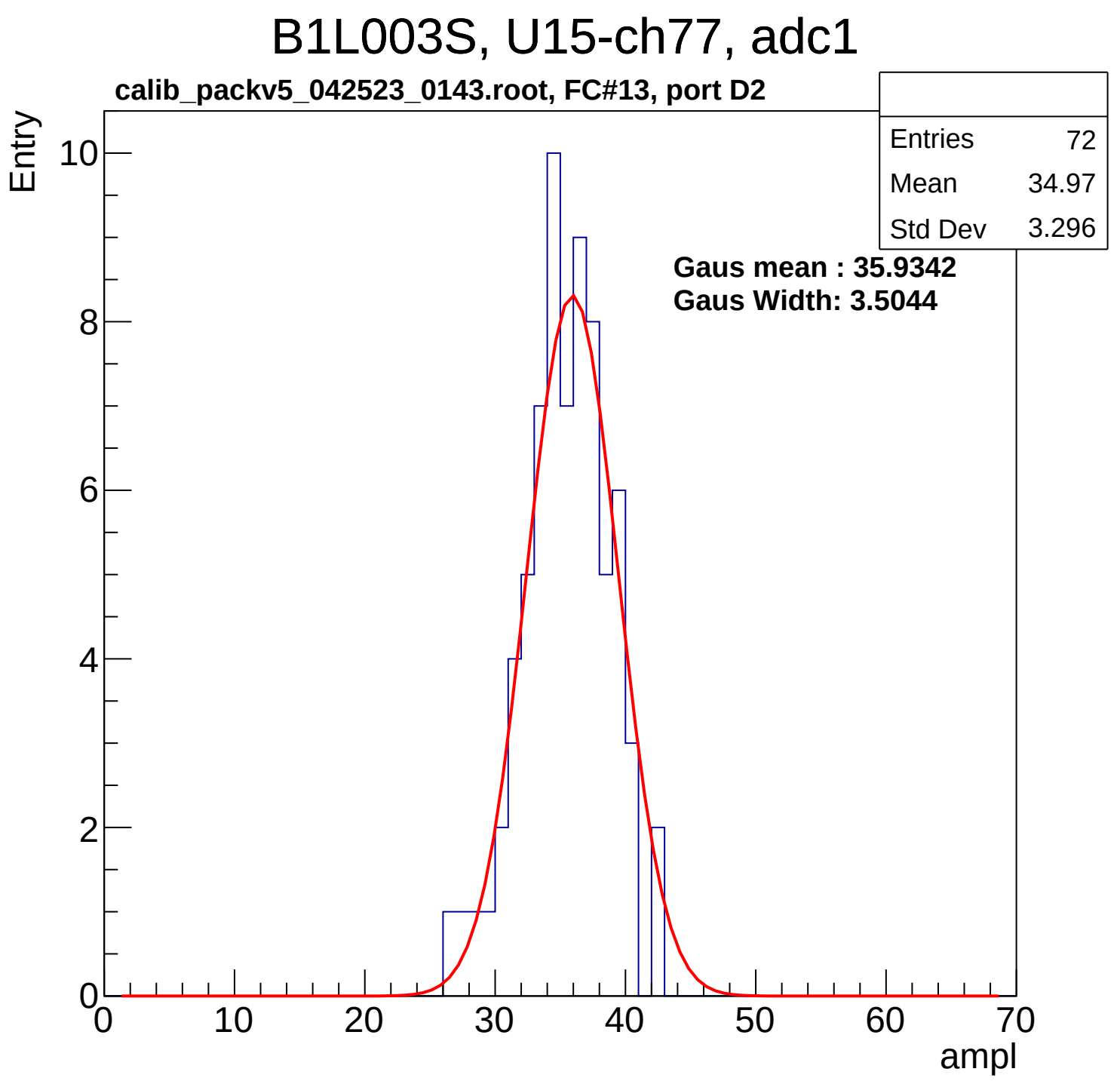
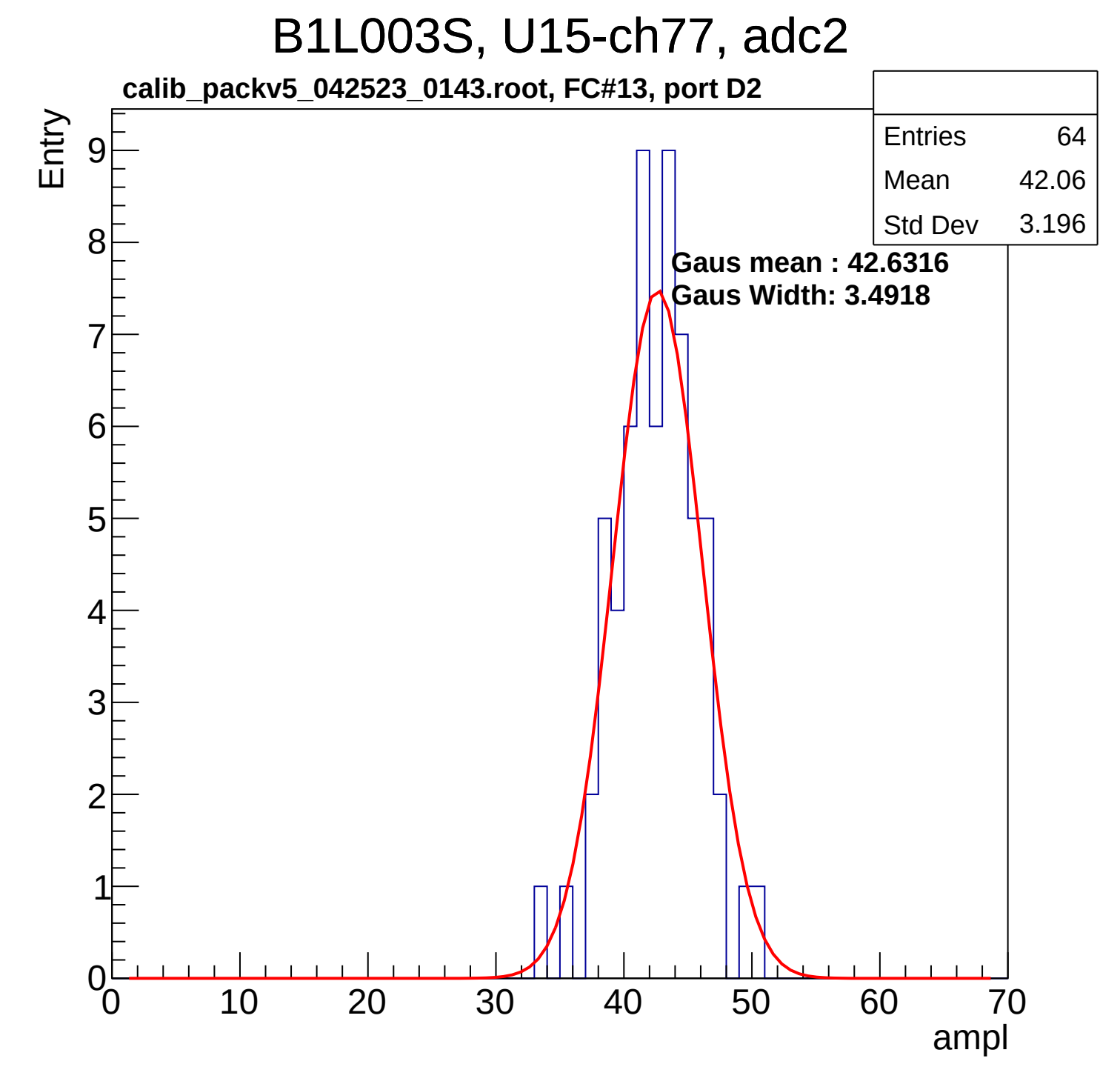
40
50
60
70


B1L003S, U15-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.97
Std Dev 3.296
Gaus mean : 35.9342
Gaus Width: 3.5044
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.06
Std Dev 3.196
Gaus mean : 42.6316
Gaus Width: 3.4918
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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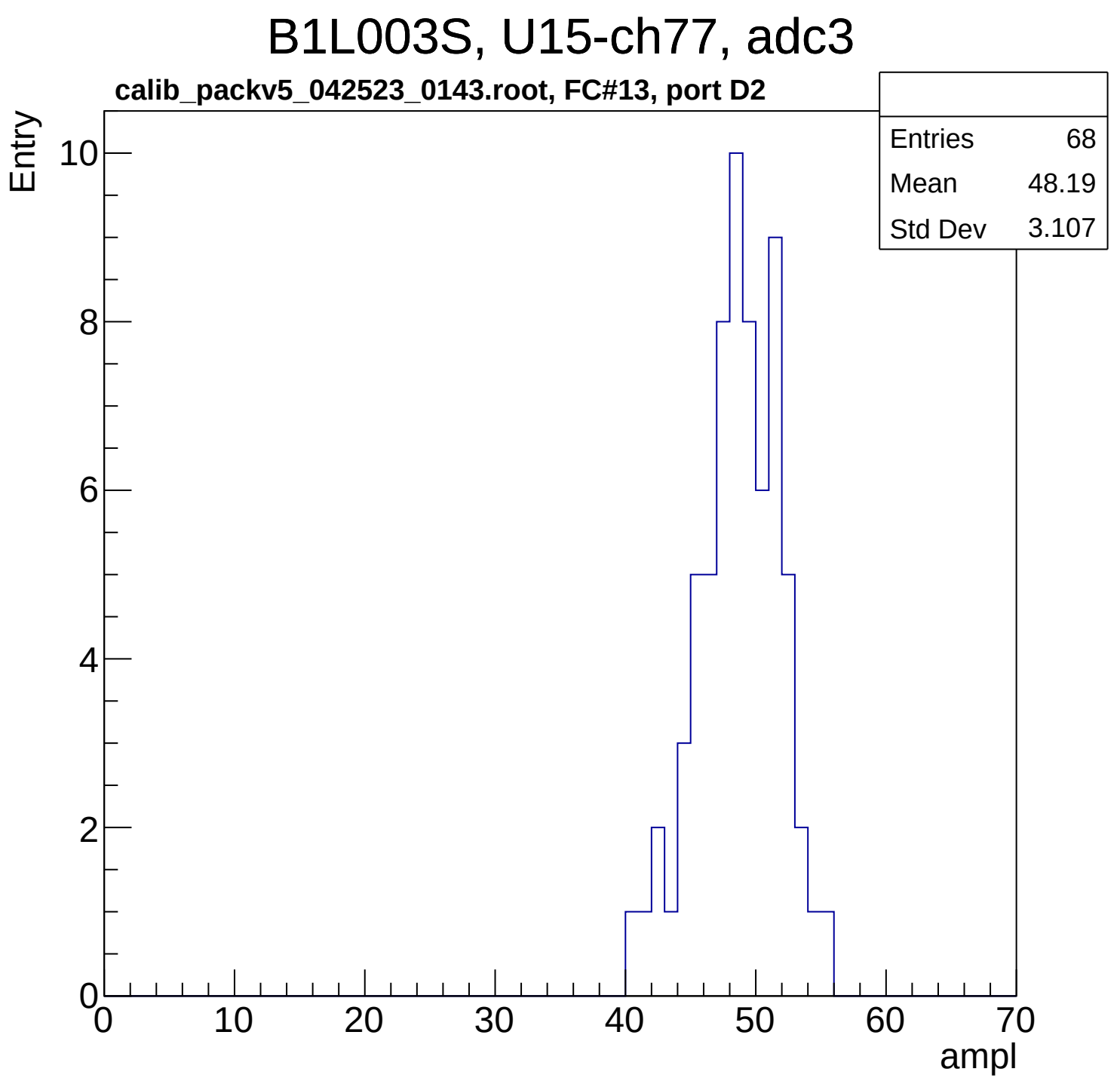
B1L003S, U15-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.19
Std Dev 3.107
Entry
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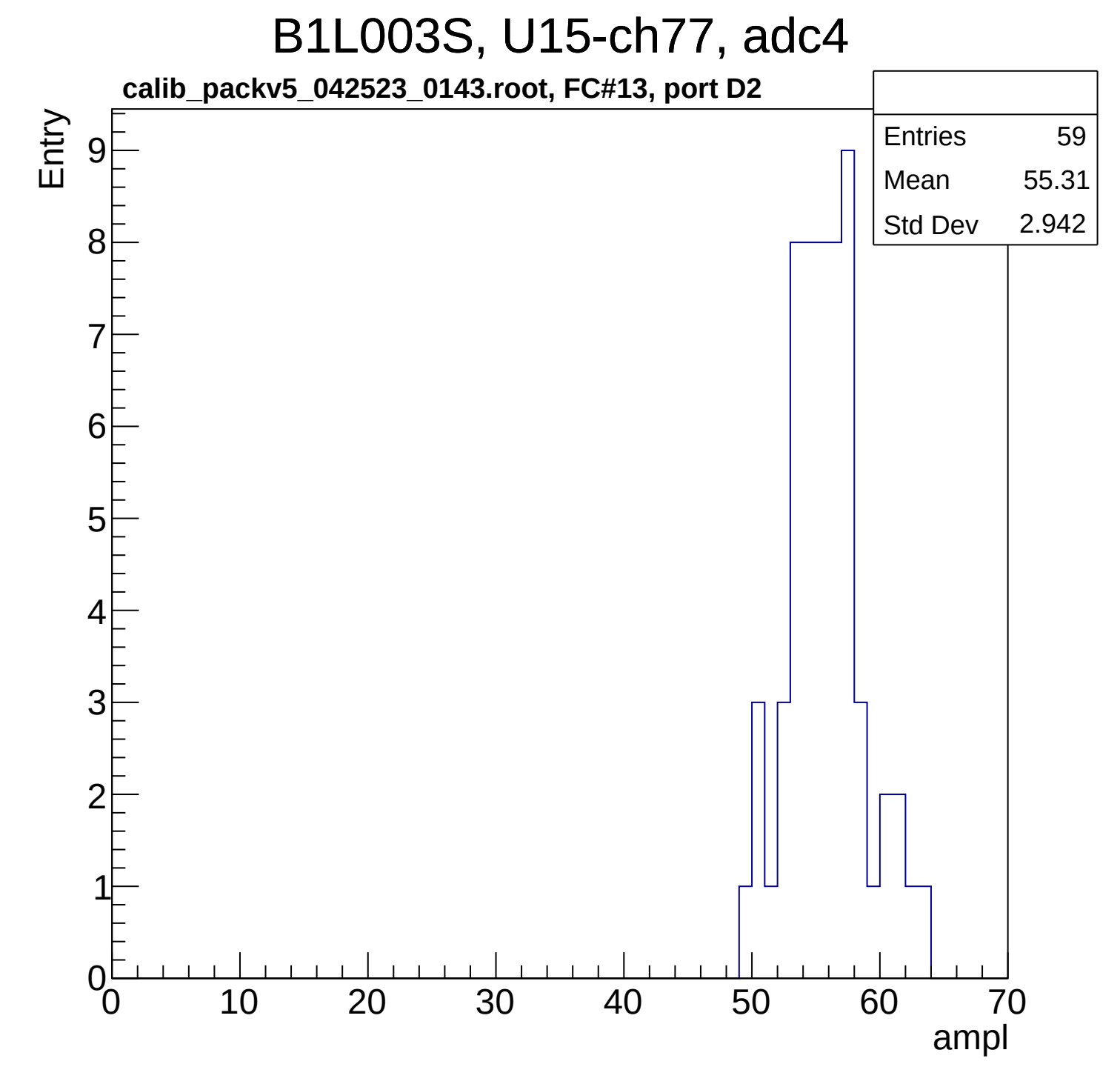
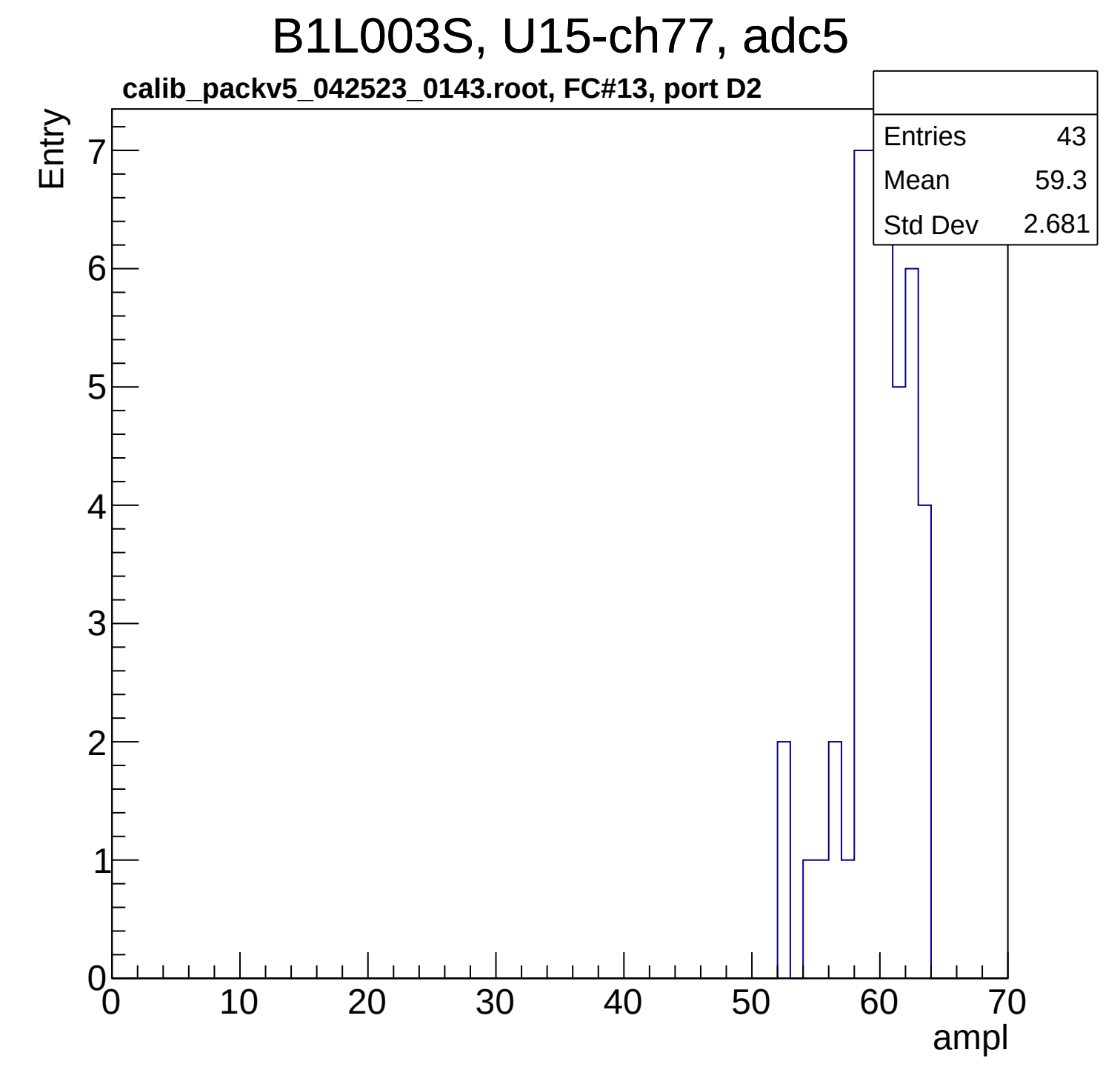
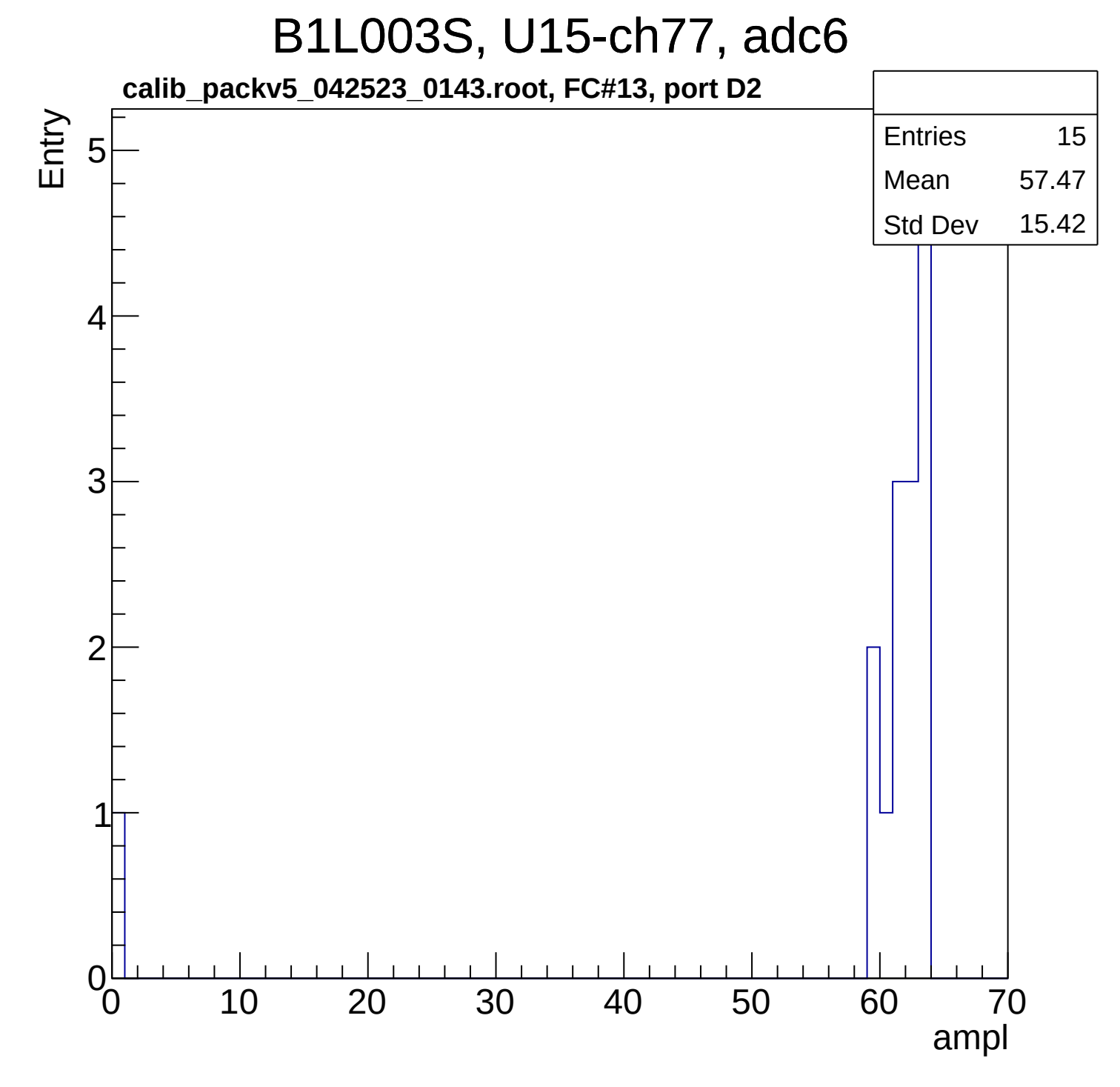
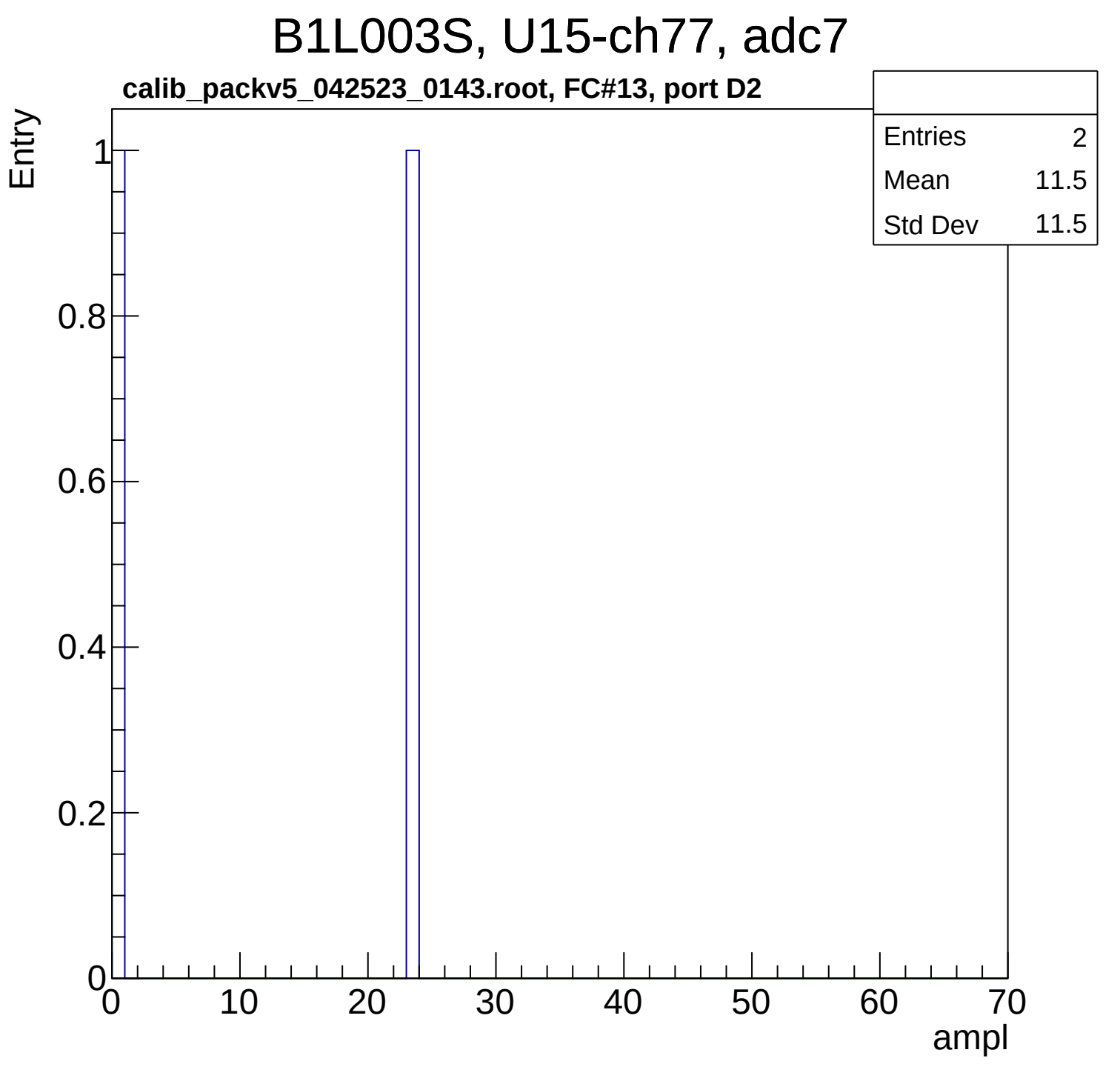
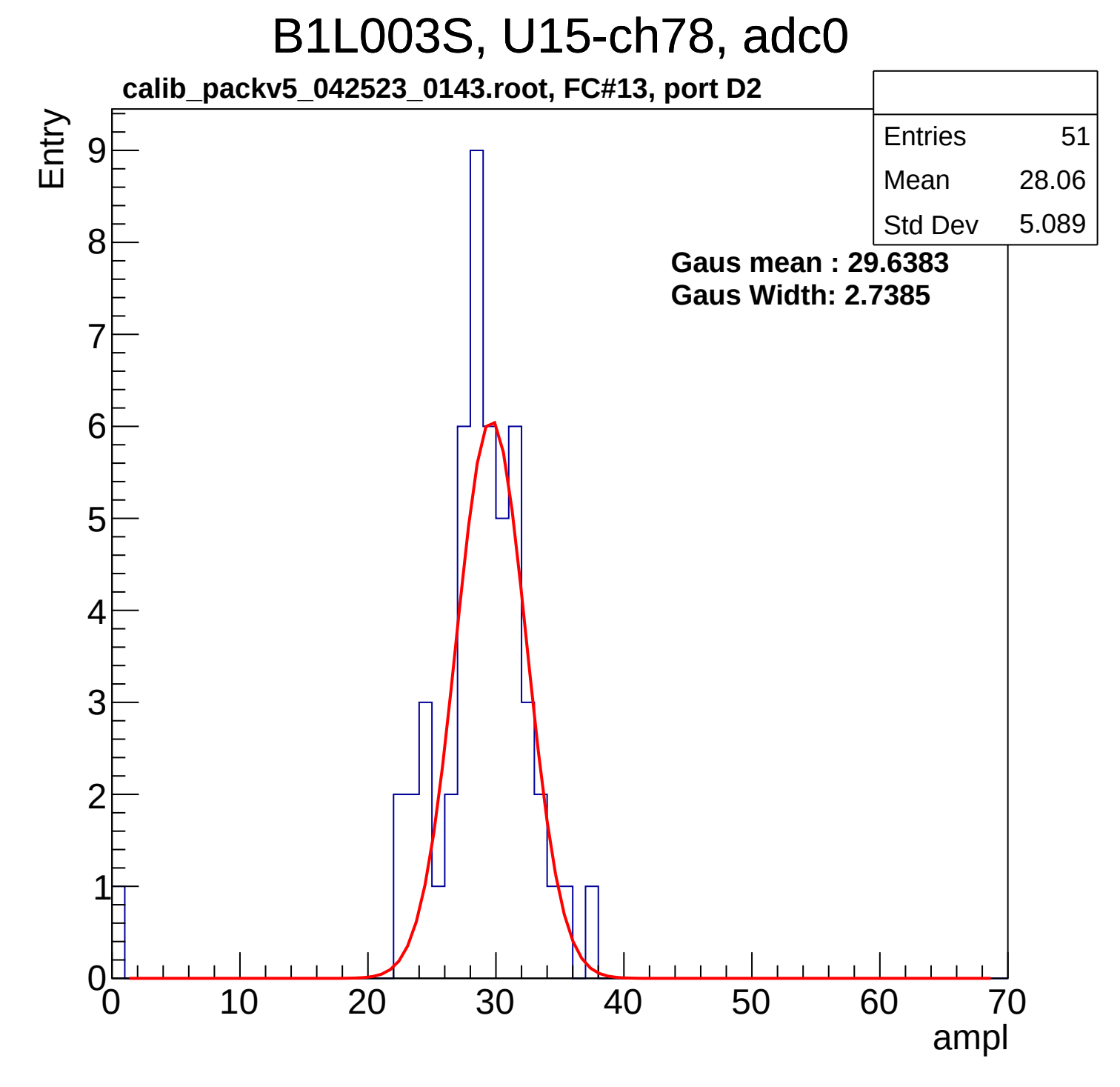
4
6
8
10
0
10
20
30
40
50
60
70
ampl

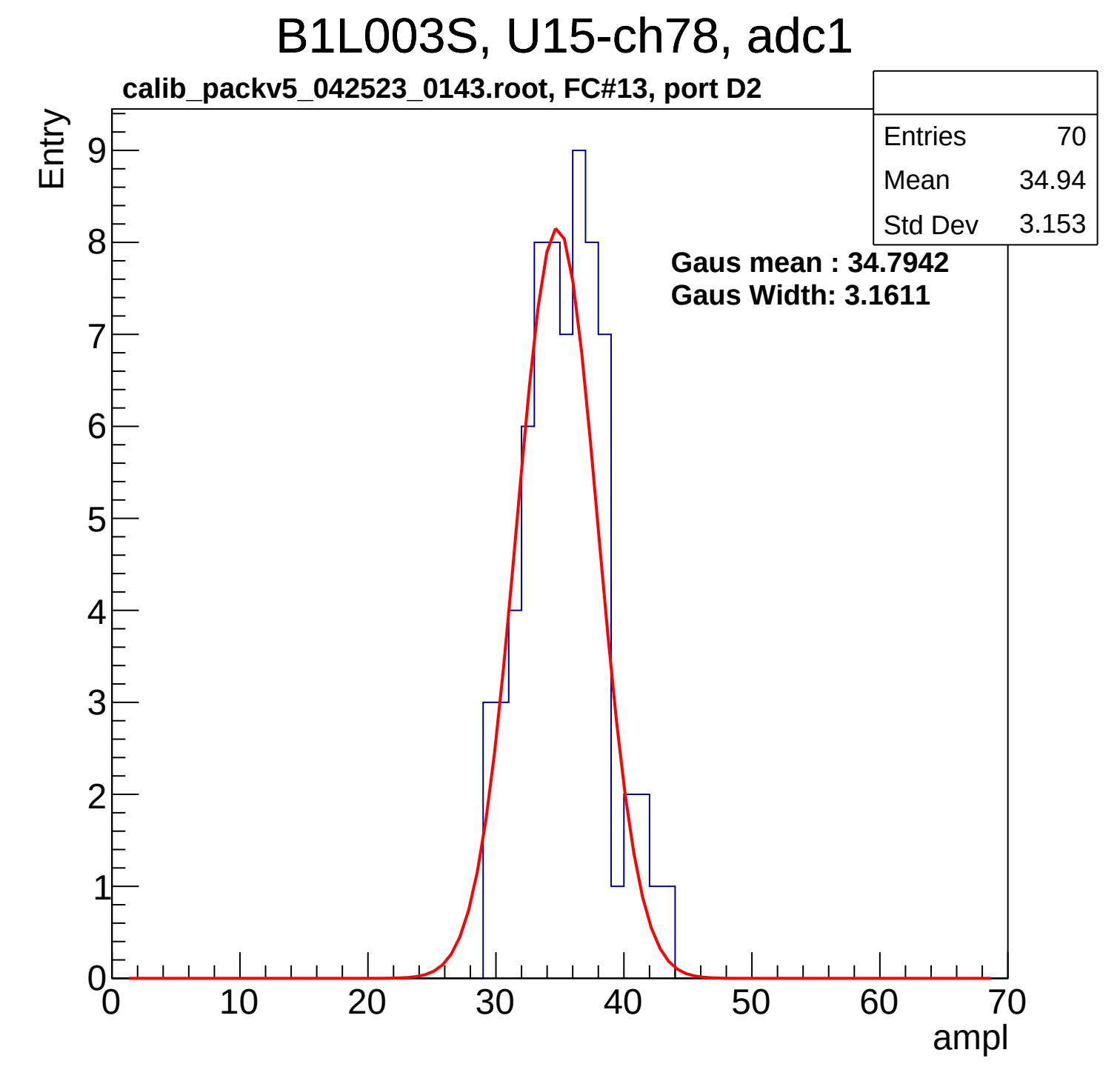

B1L003S, U15-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.31
Std Dev 2.942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.3
Std Dev 2.681
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.47
Std Dev 15.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.06
Std Dev 5.089
Gaus mean : 29.6383
Gaus Width: 2.7385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 34.94
Std Dev 3.153
Gaus mean : 34.7942
Gaus Width: 3.1611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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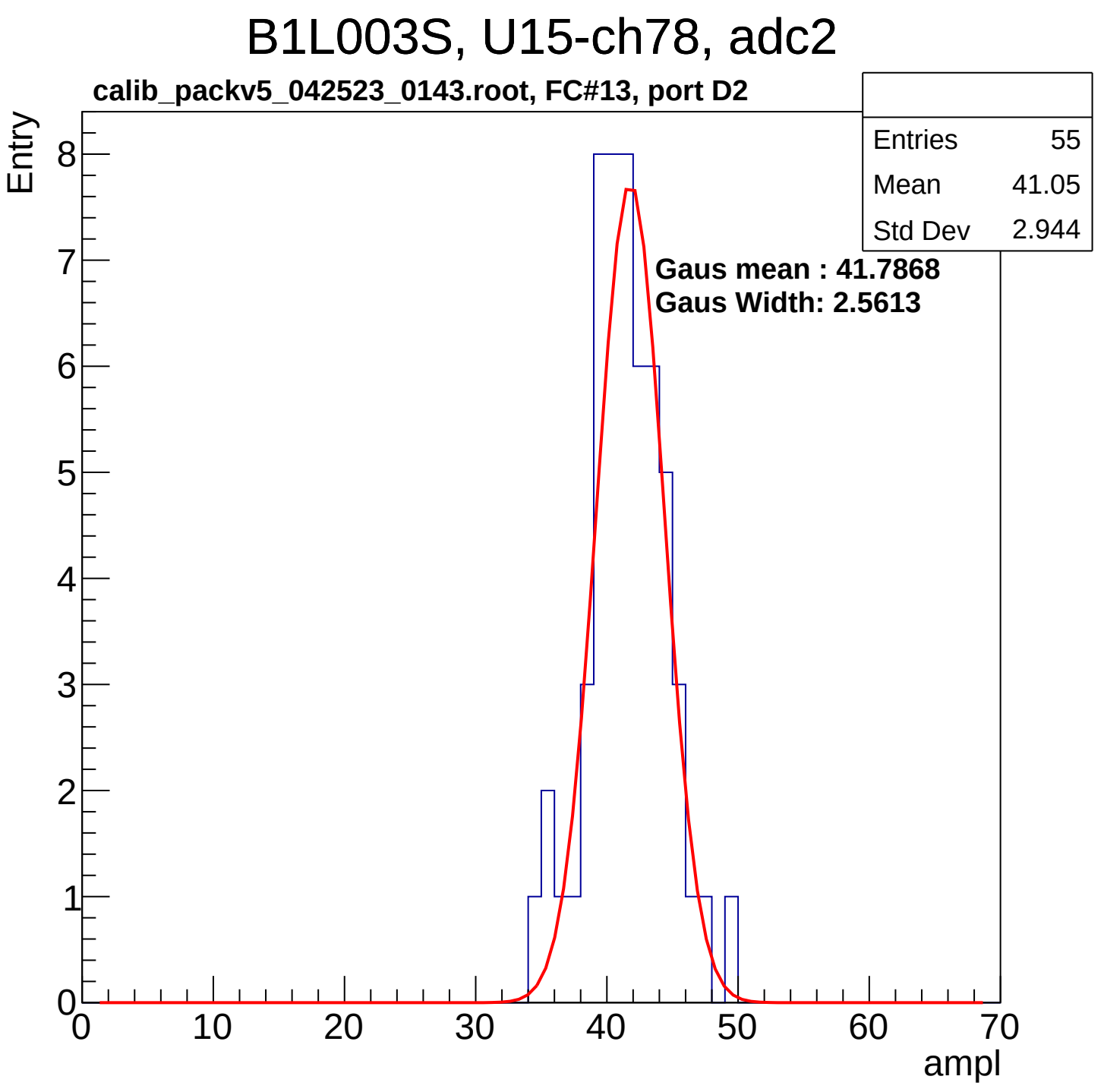
B1L003S, U15-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.05
Std Dev 2.944
Gaus mean : 41.7868
Gaus Width: 2.5613
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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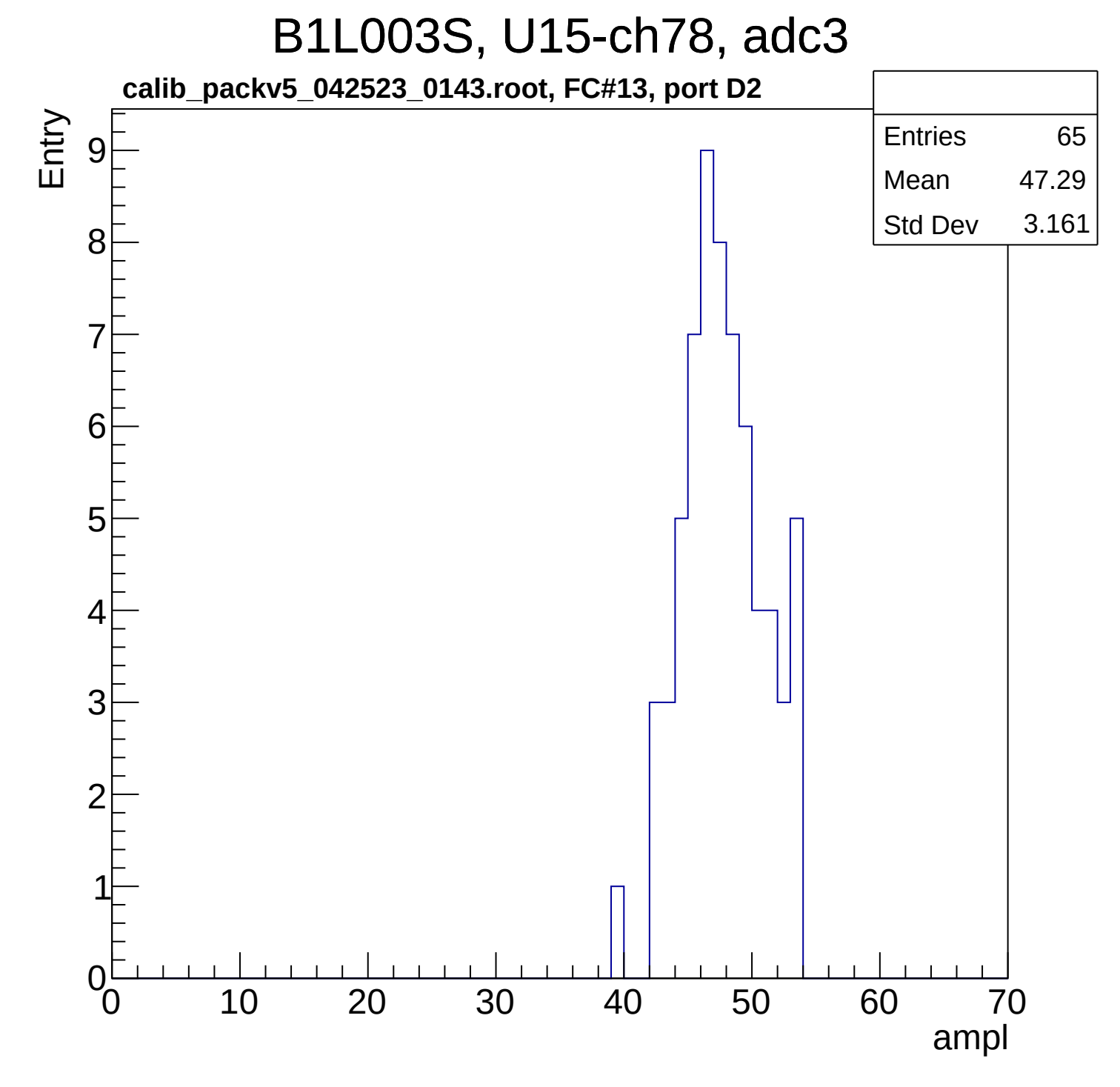
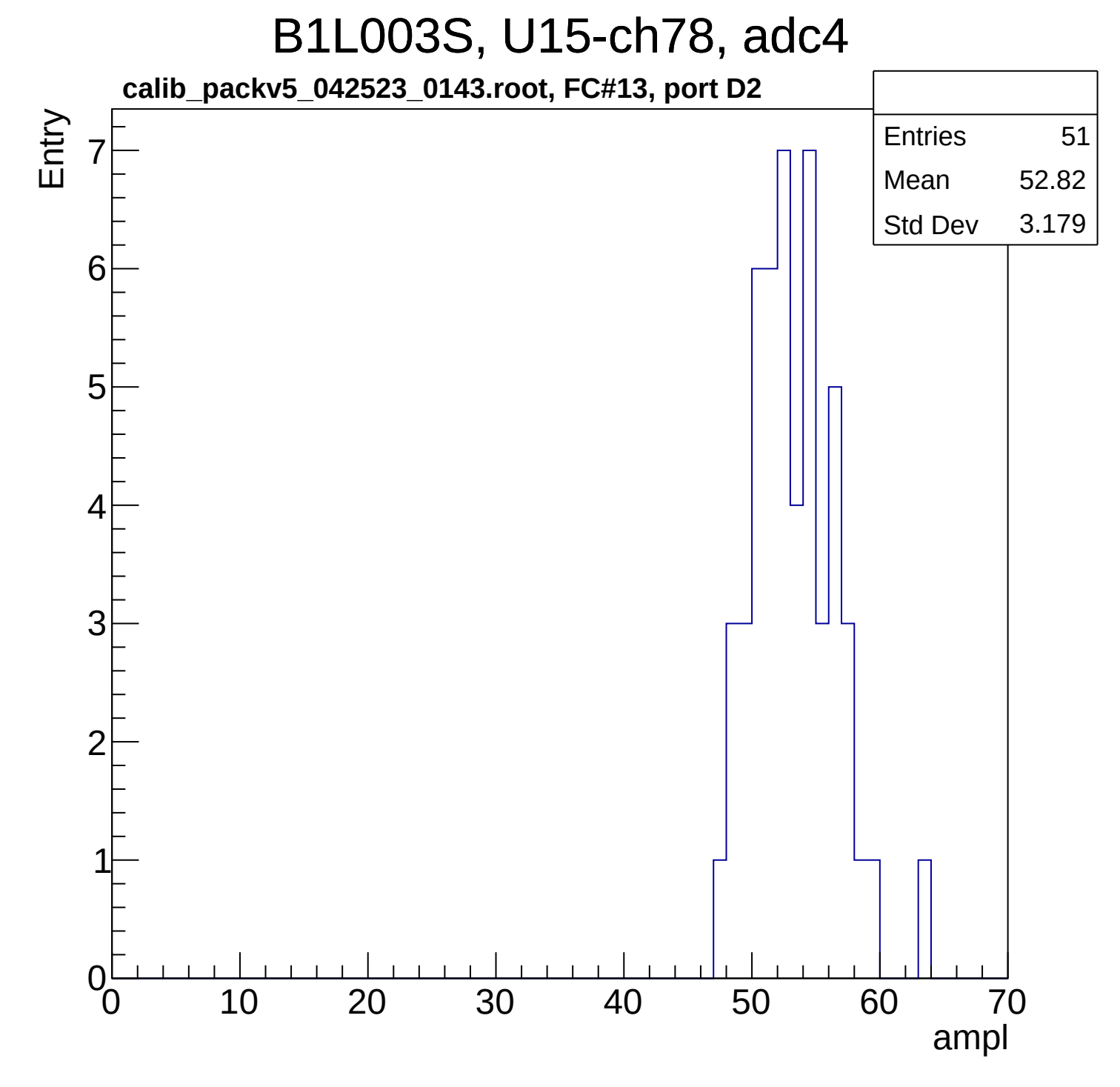
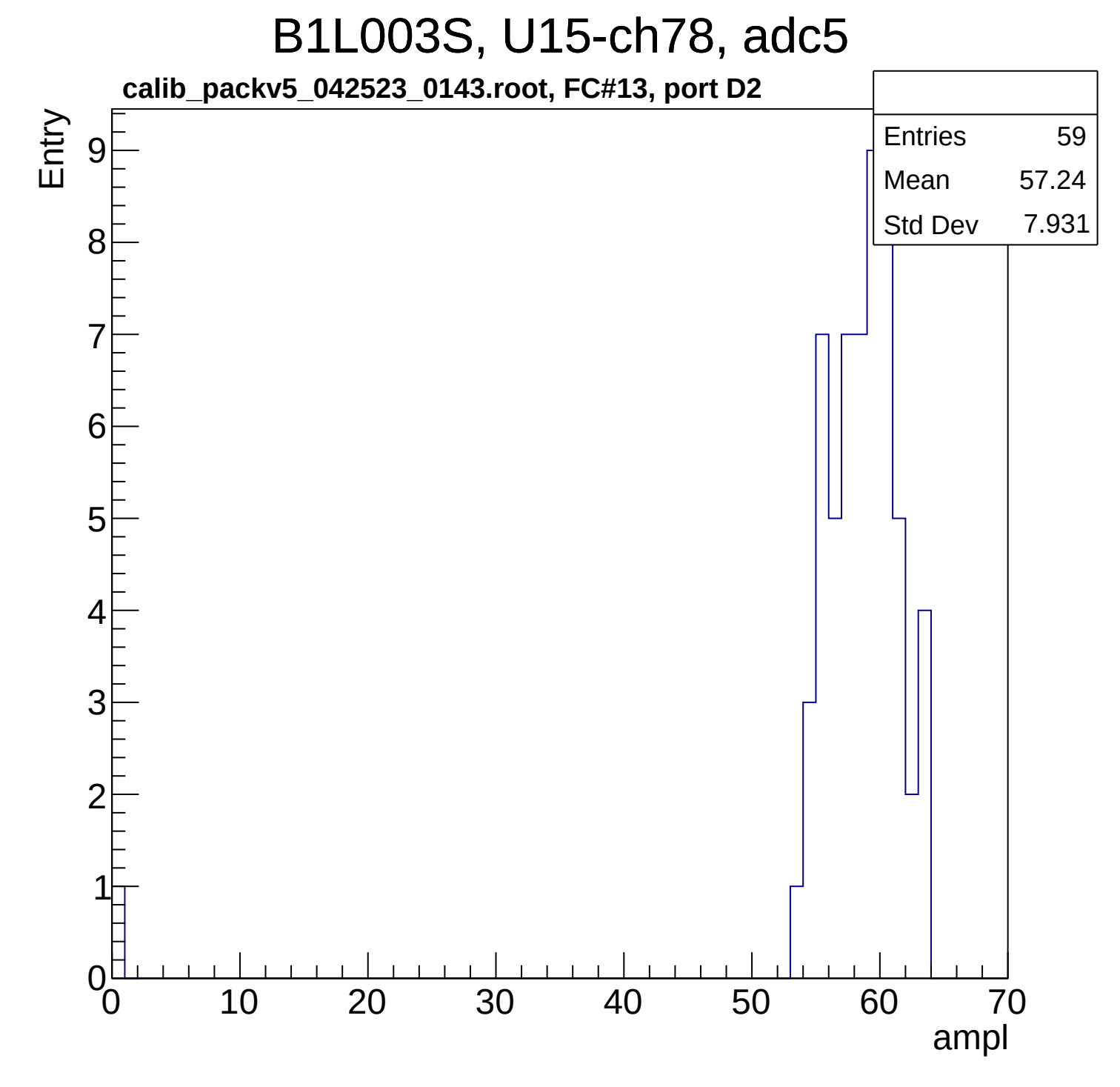
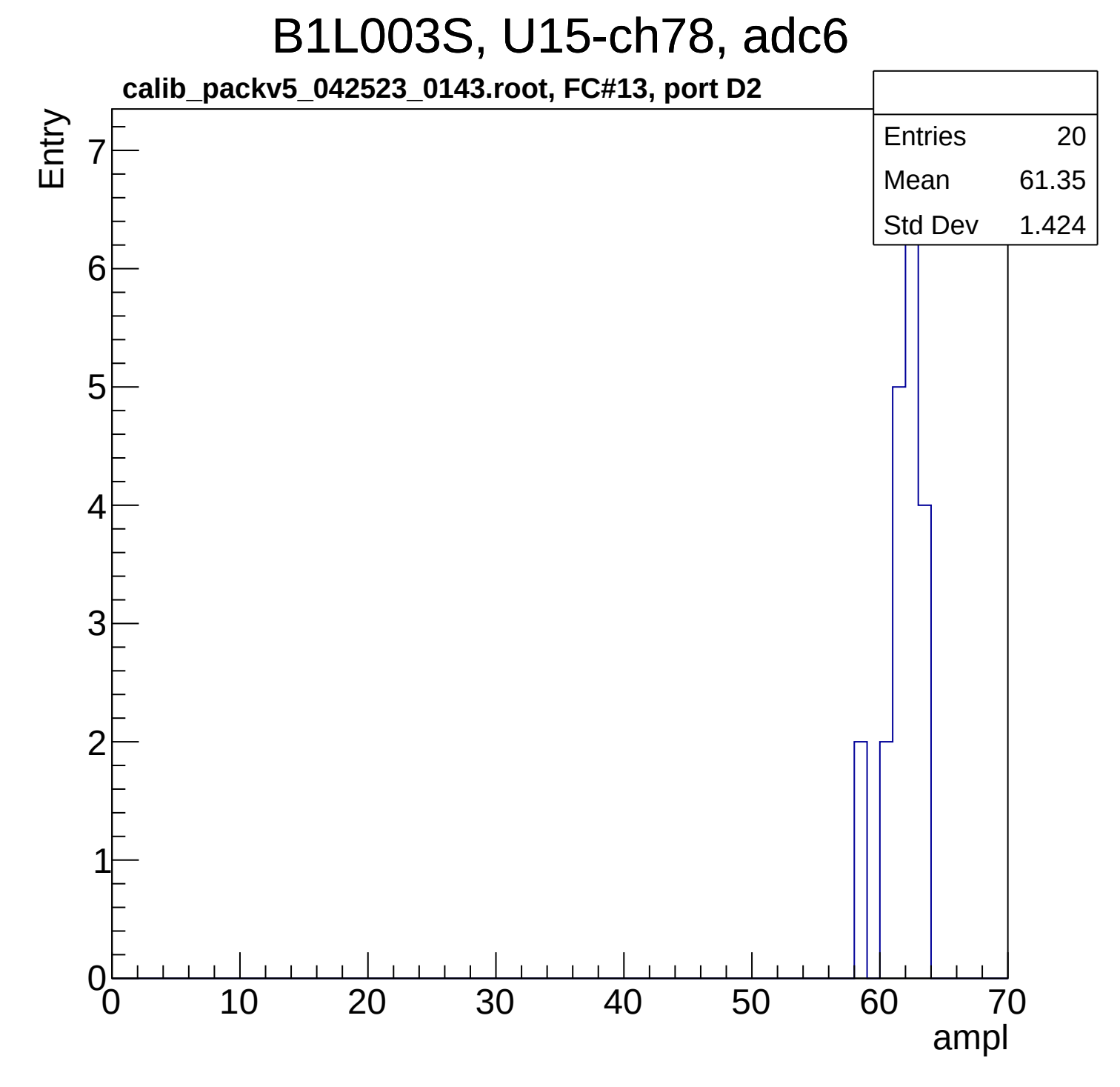
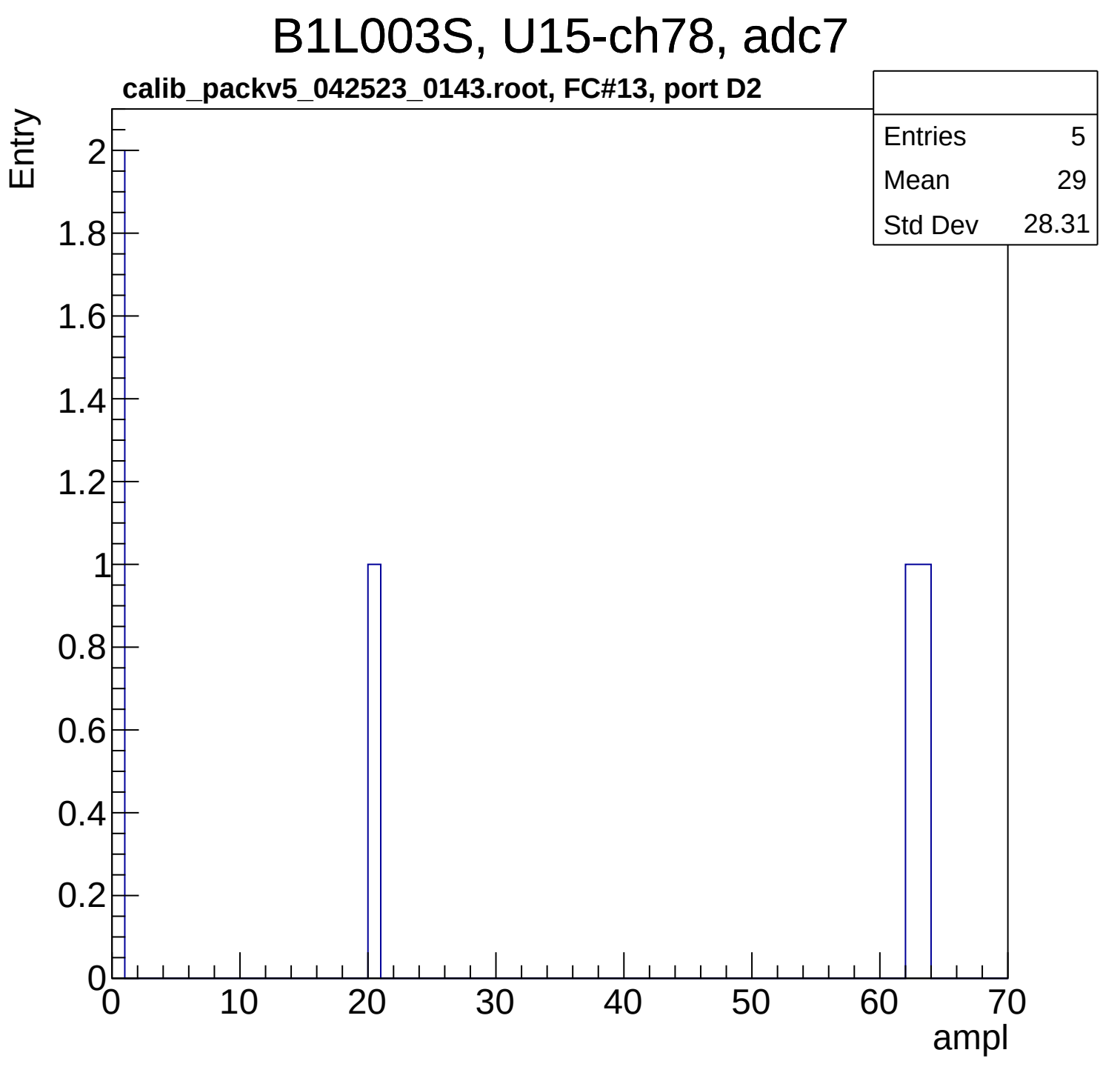
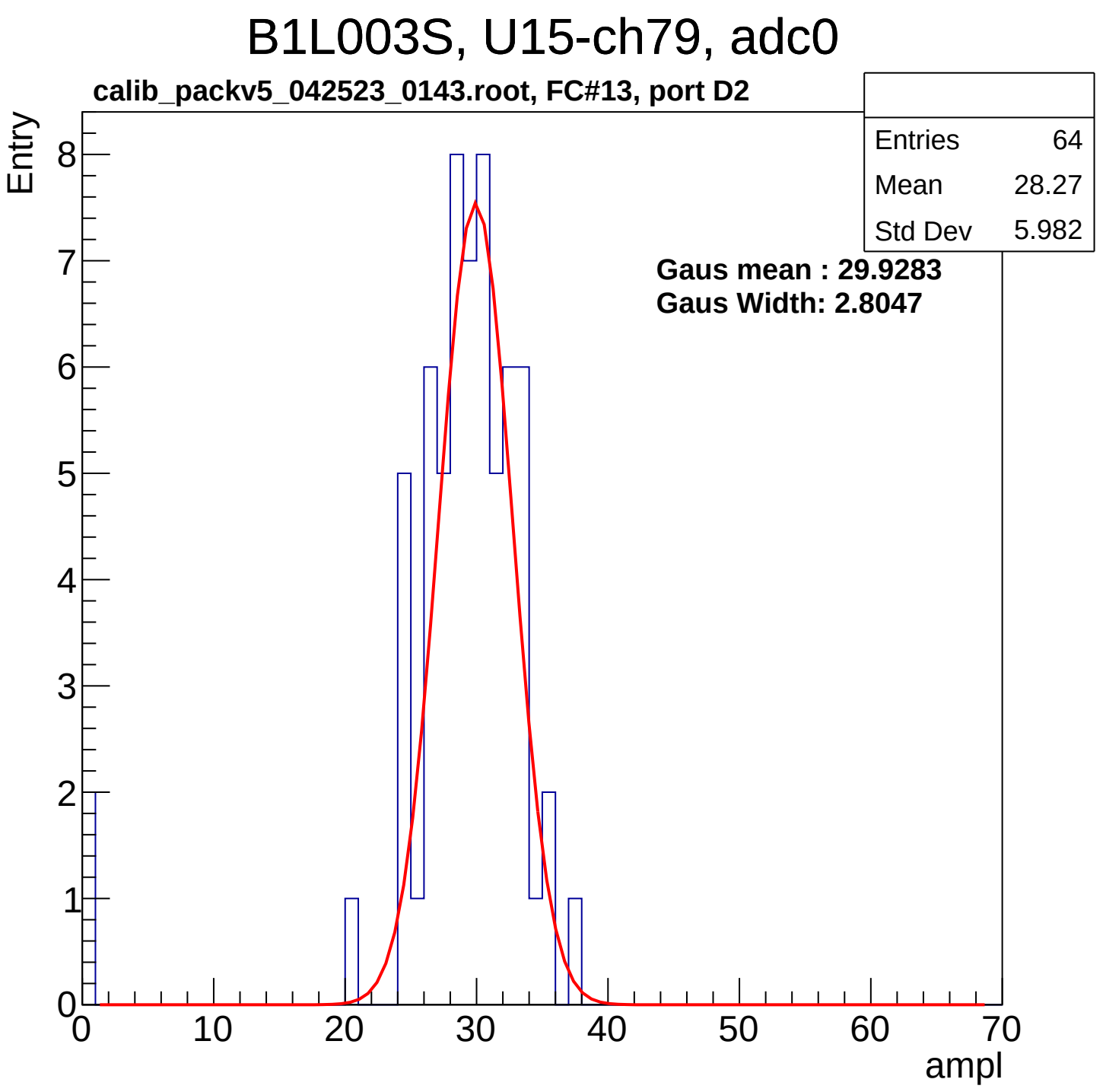

B1L003S, U15-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.29
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

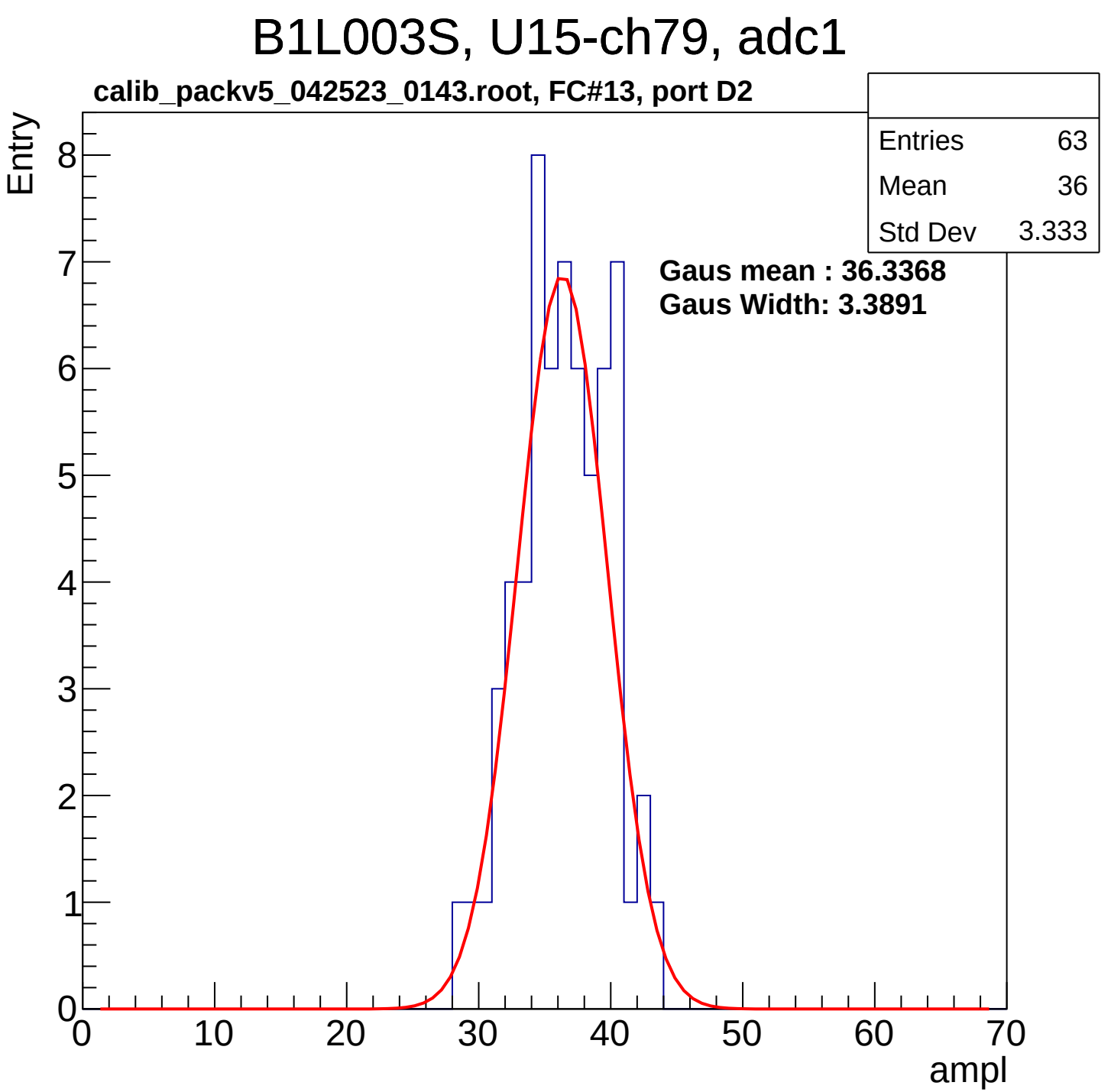

B1L003S, U15-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 52.82
Std Dev 3.179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

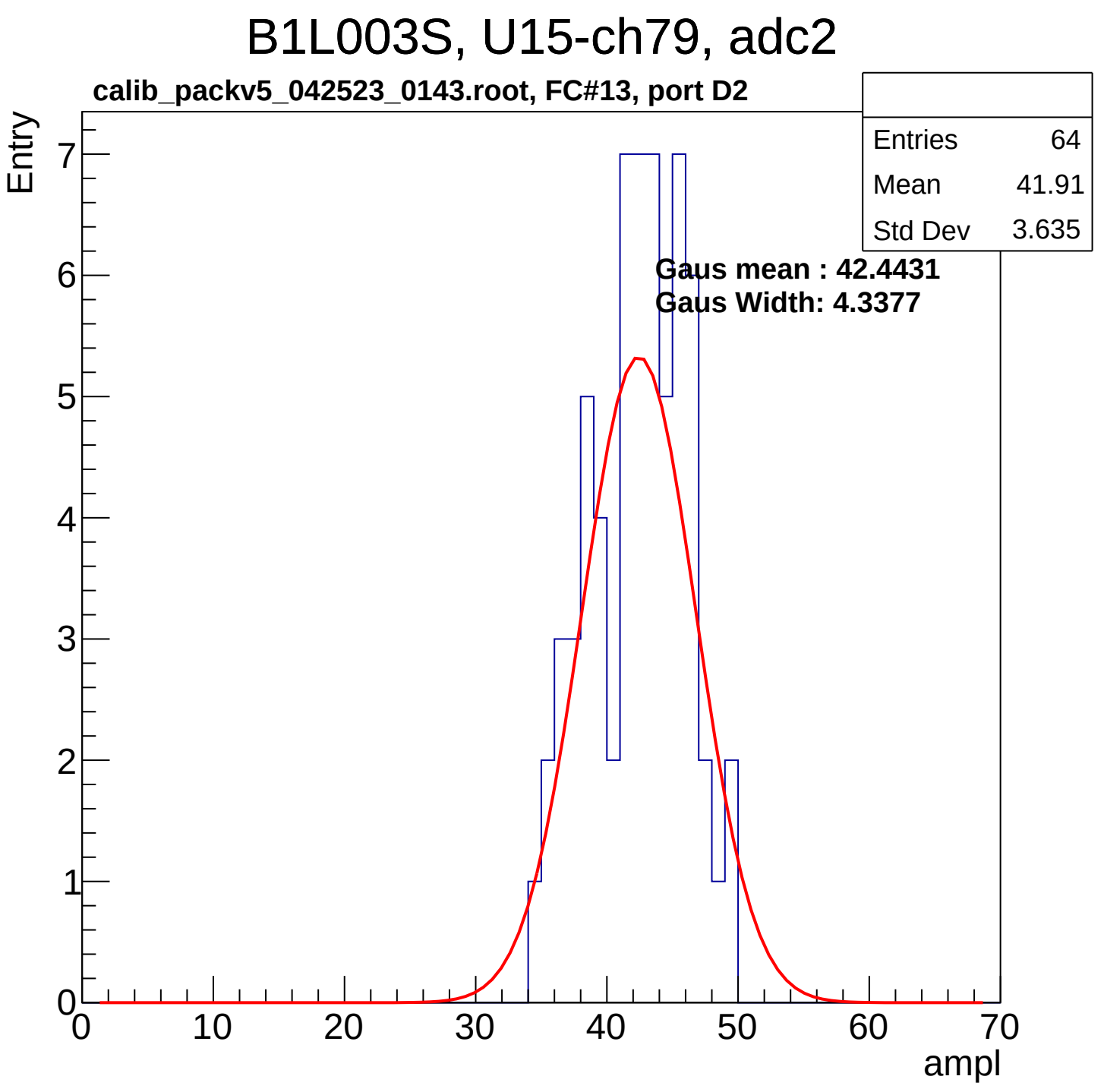

B1L003S, U15-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.24
Std Dev 7.931
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

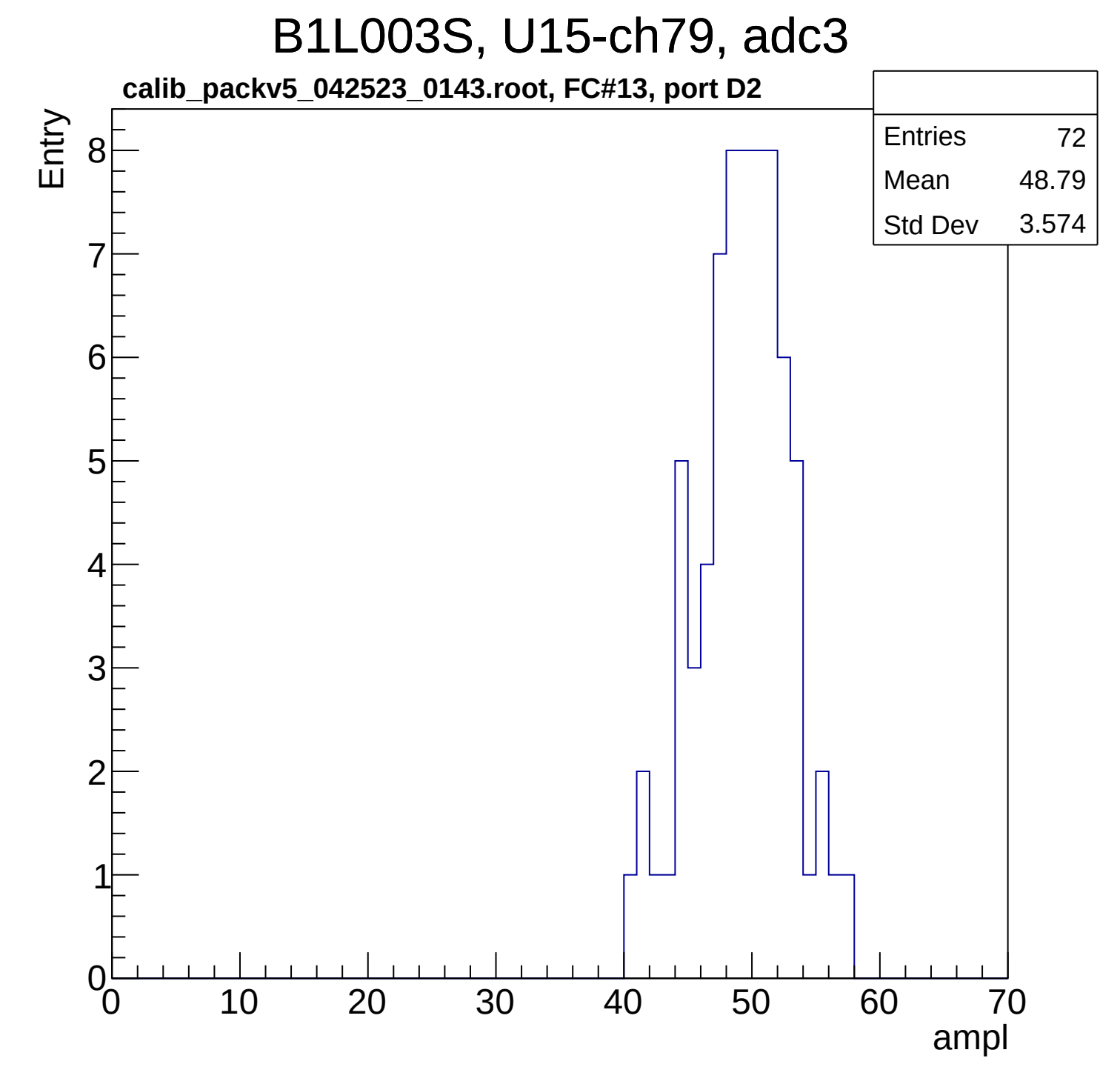

B1L003S, U15-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 61.35
Std Dev 1.424
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

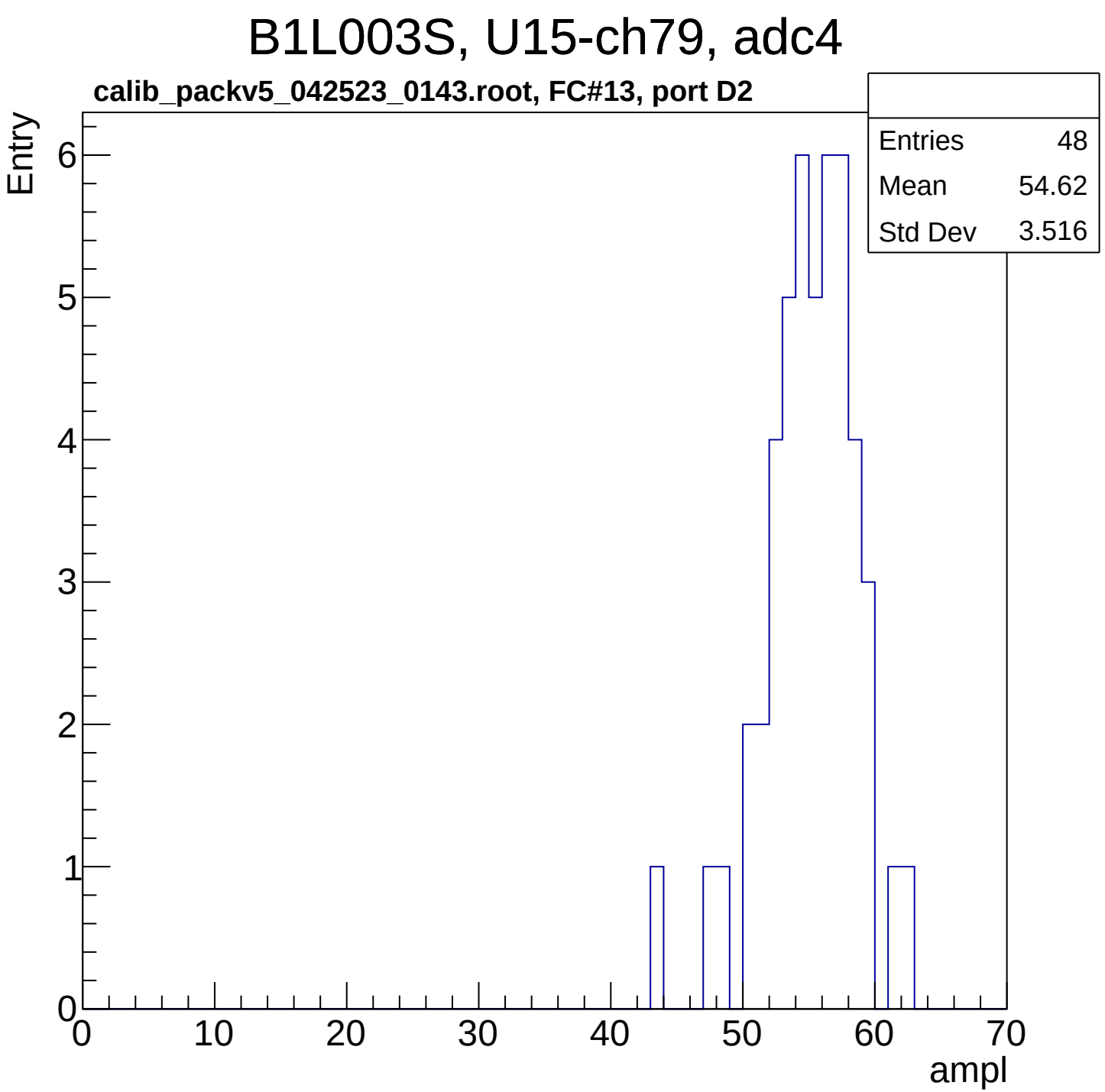

B1L003S, U15-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 29
Std Dev 28.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

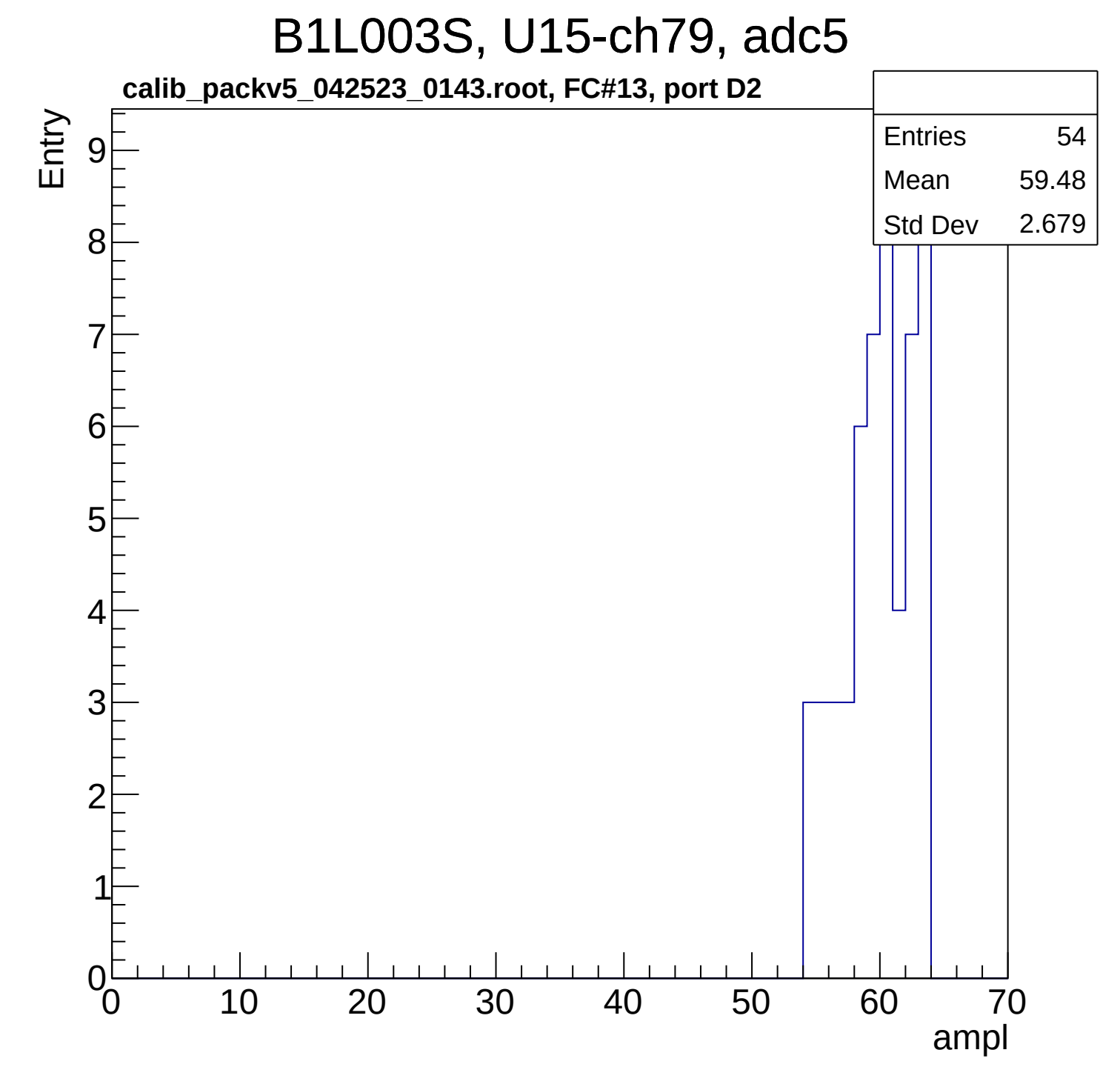

B1L003S, U15-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.27
Std Dev 5.982
Gaus mean : 29.9283
Gaus Width: 2.8047
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

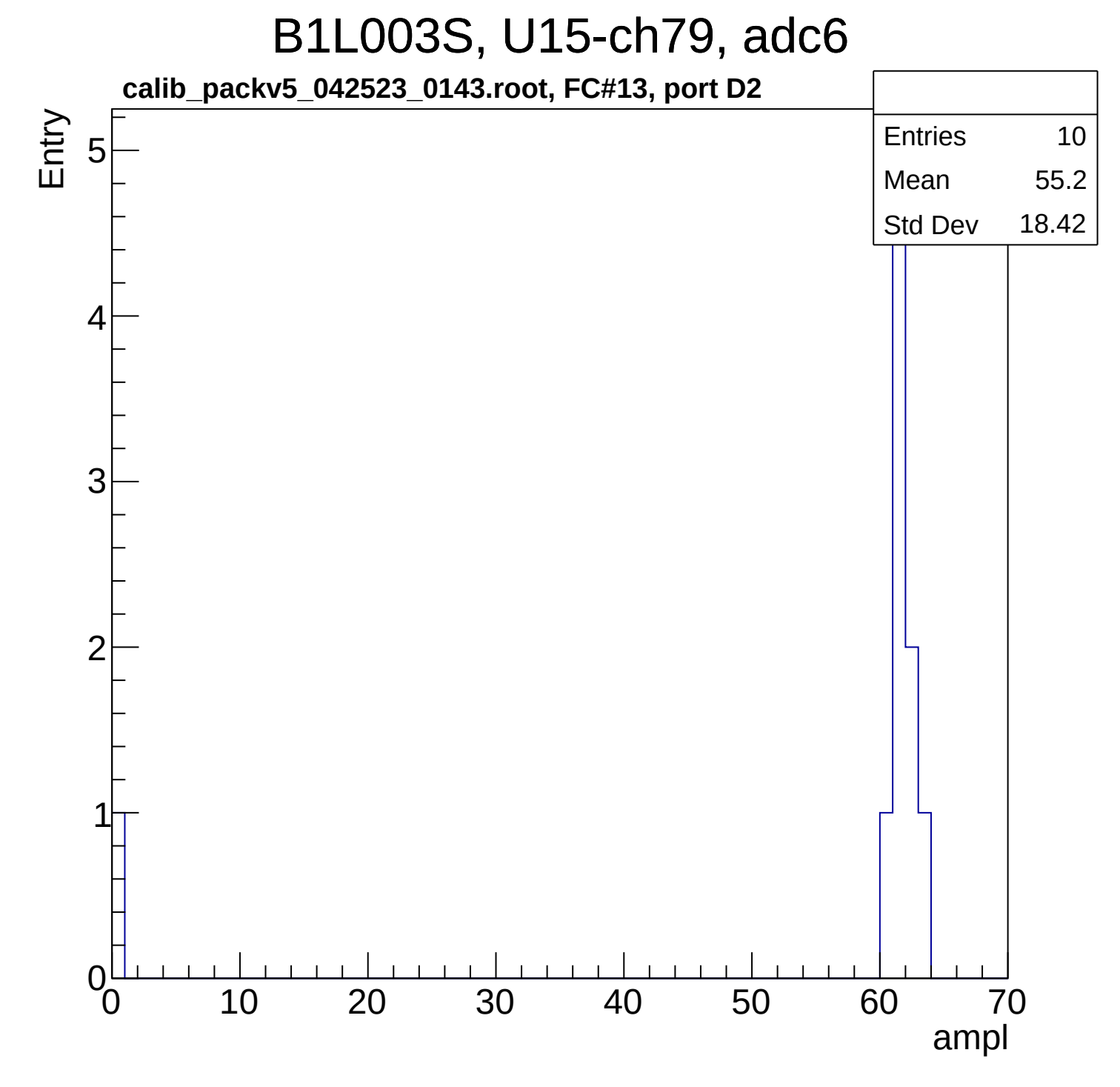

B1L003S, U15-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36
Std Dev 3.333
Gaus mean : 36.3368
Gaus Width: 3.3891
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

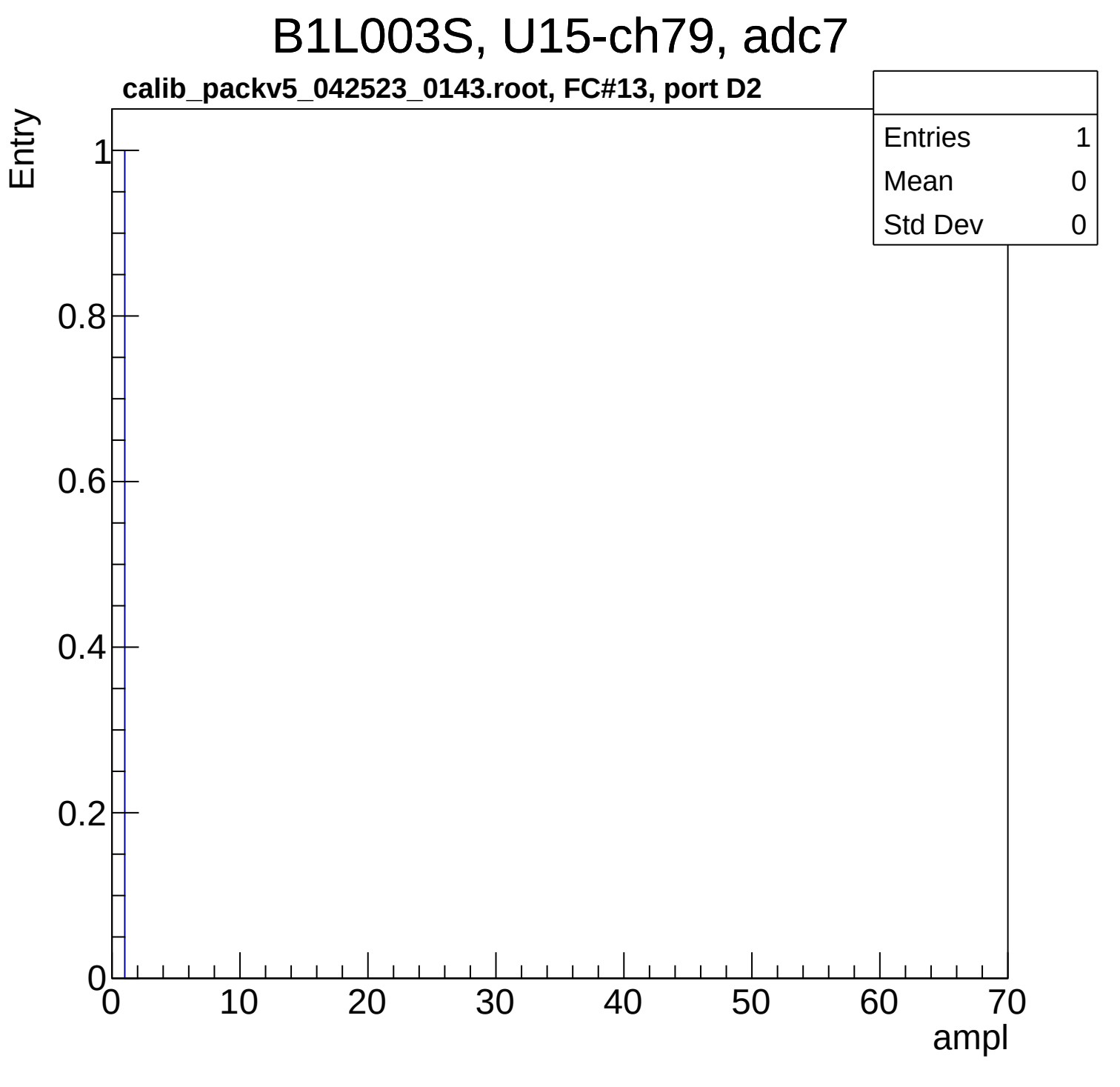

B1L003S, U15-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.91
Std Dev 3.635
Gaus mean : 42.4431
Gaus Width: 4.3377
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

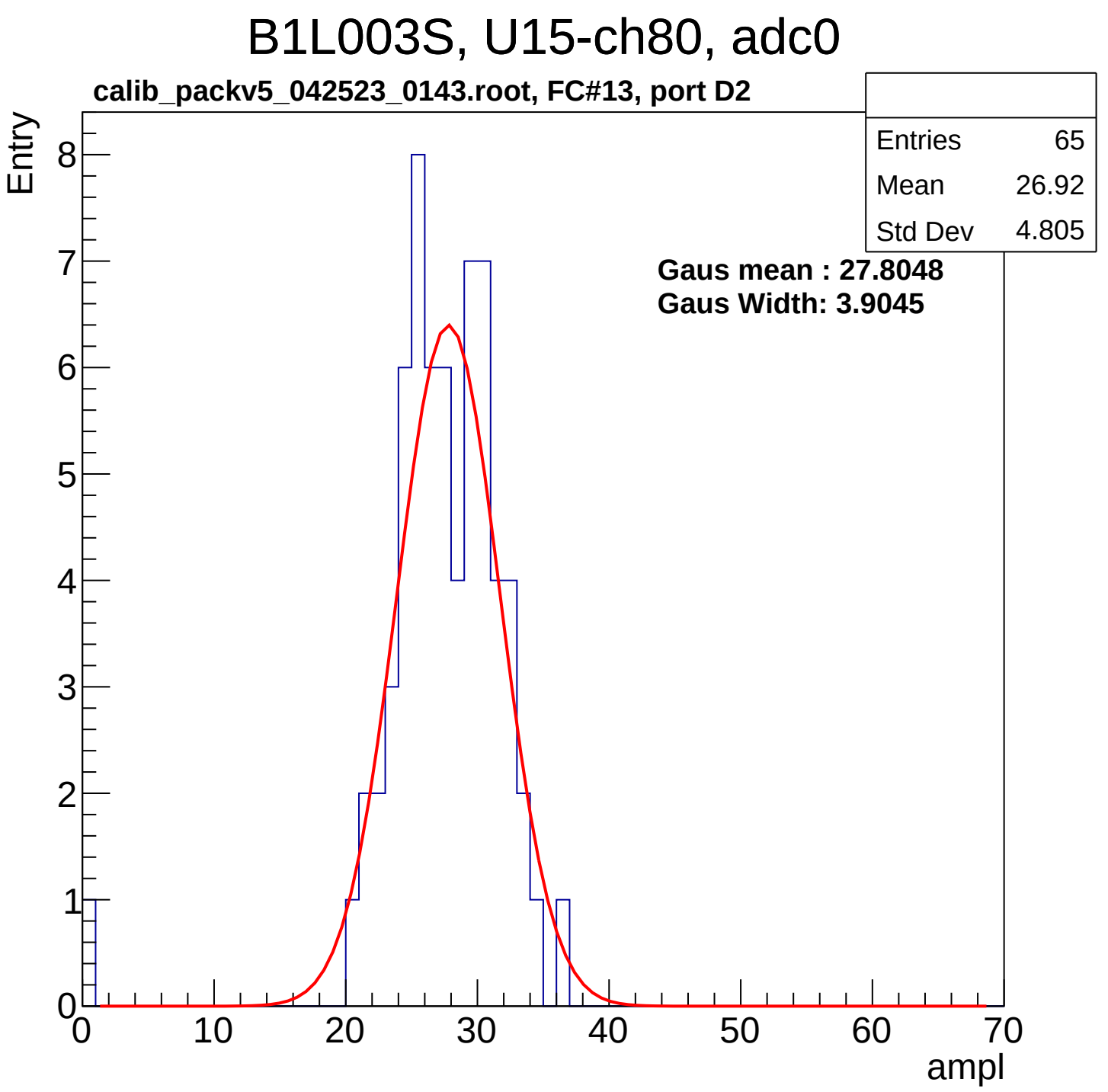

B1L003S, U15-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.79
Std Dev 3.574
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

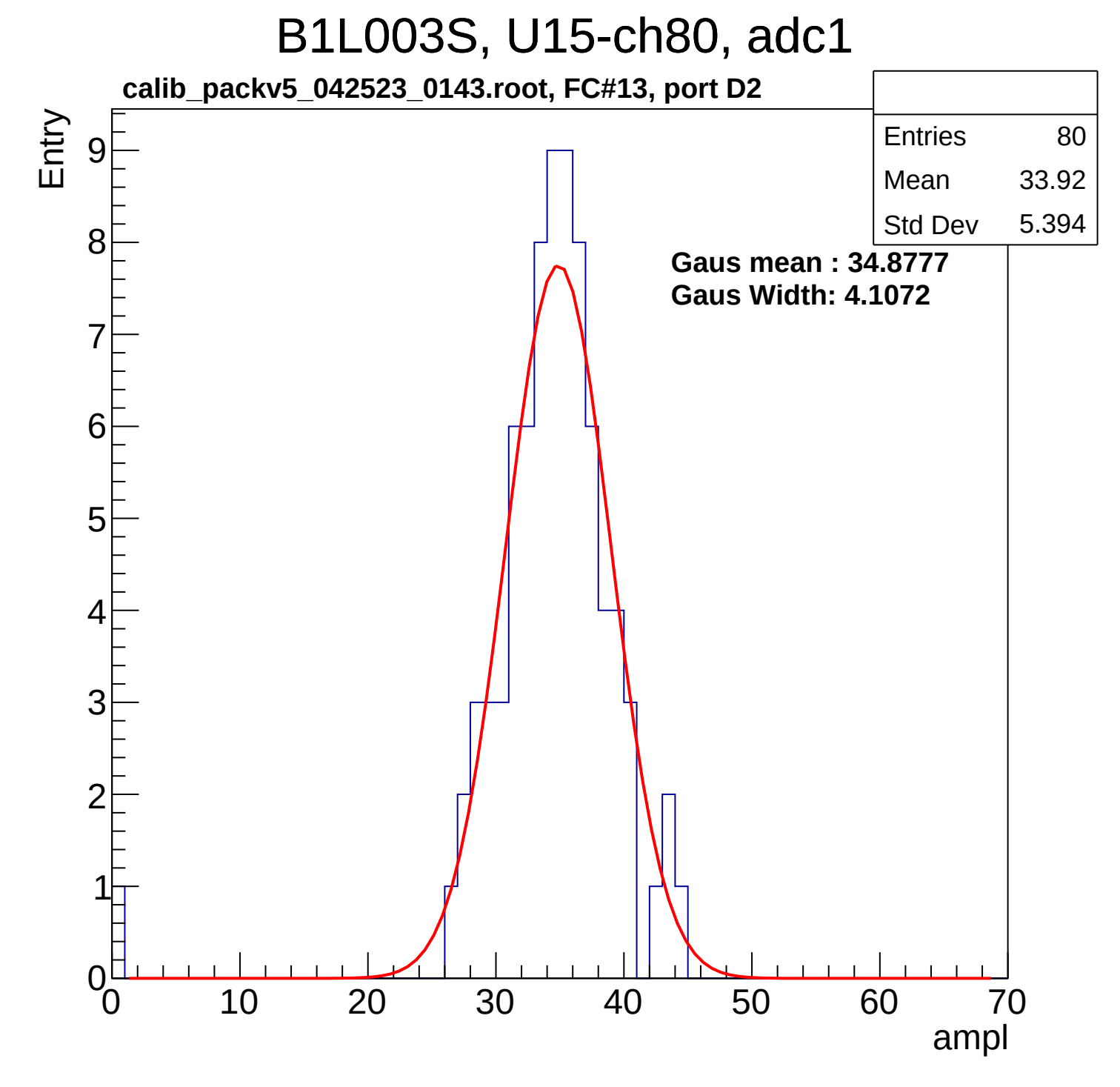

B1L003S, U15-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.62
Std Dev 3.516
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.48
Std Dev 2.679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.2
Std Dev 18.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 26.92
Std Dev 4.805
Gaus mean : 27.8048
Gaus Width: 3.9045
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 33.92
Std Dev 5.394
Gaus mean : 34.8777
Gaus Width: 4.1072
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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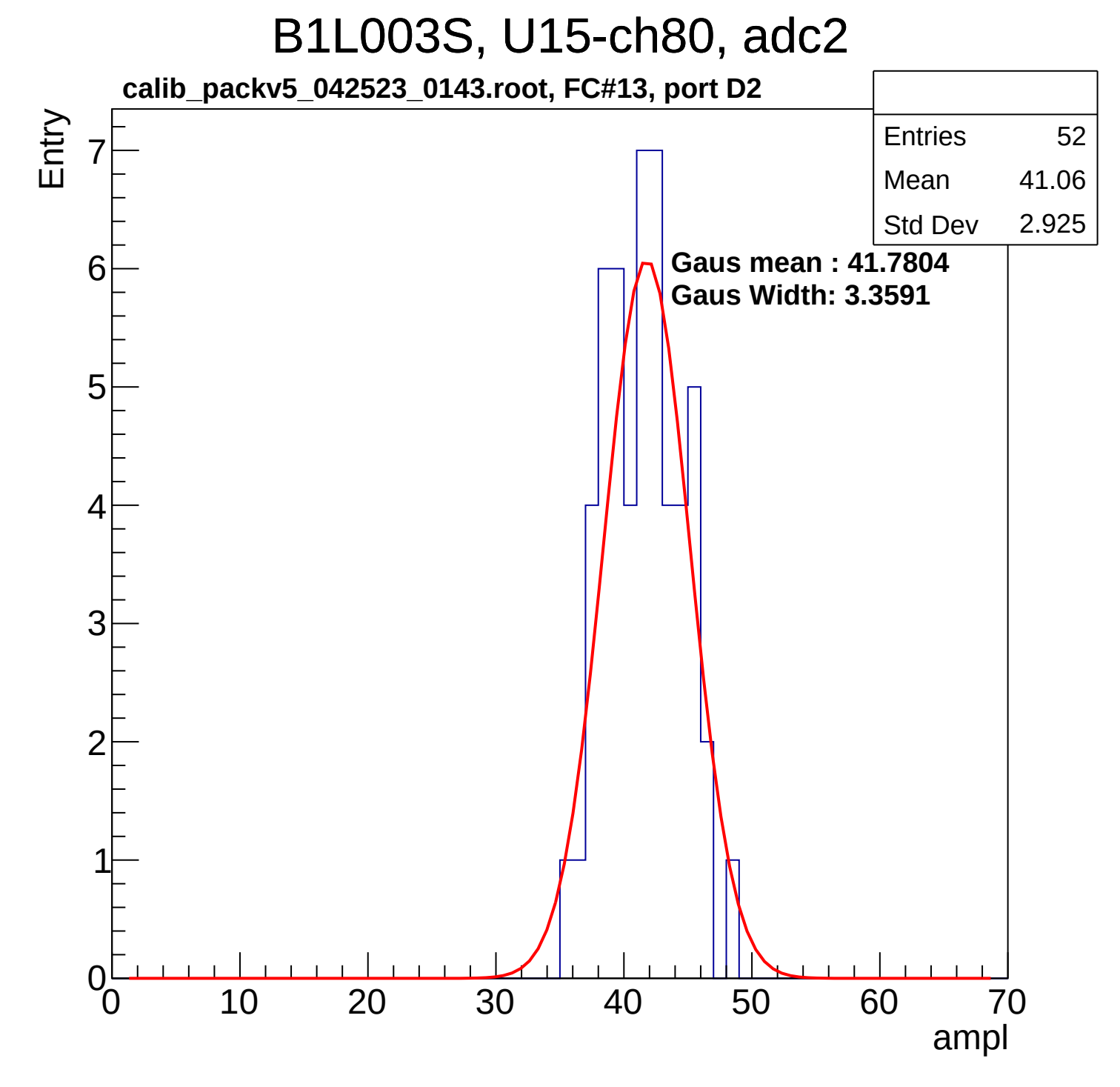
60
70


B1L003S, U15-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 41.06
Std Dev 2.925
Gaus mean : 41.7804
Gaus Width: 3.3591
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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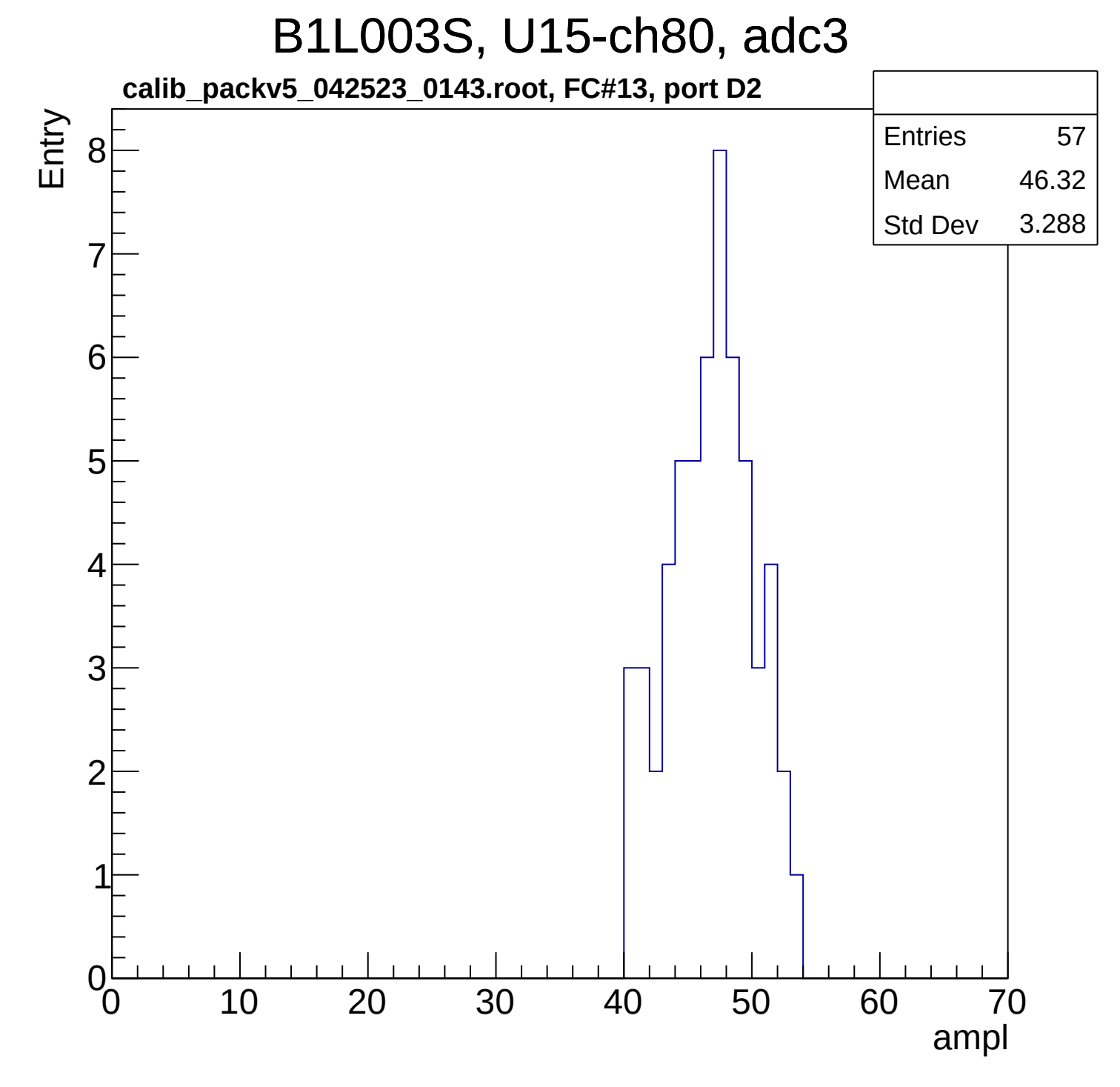
B1L003S, U15-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 46.32
Std Dev 3.288
Entry
ampl
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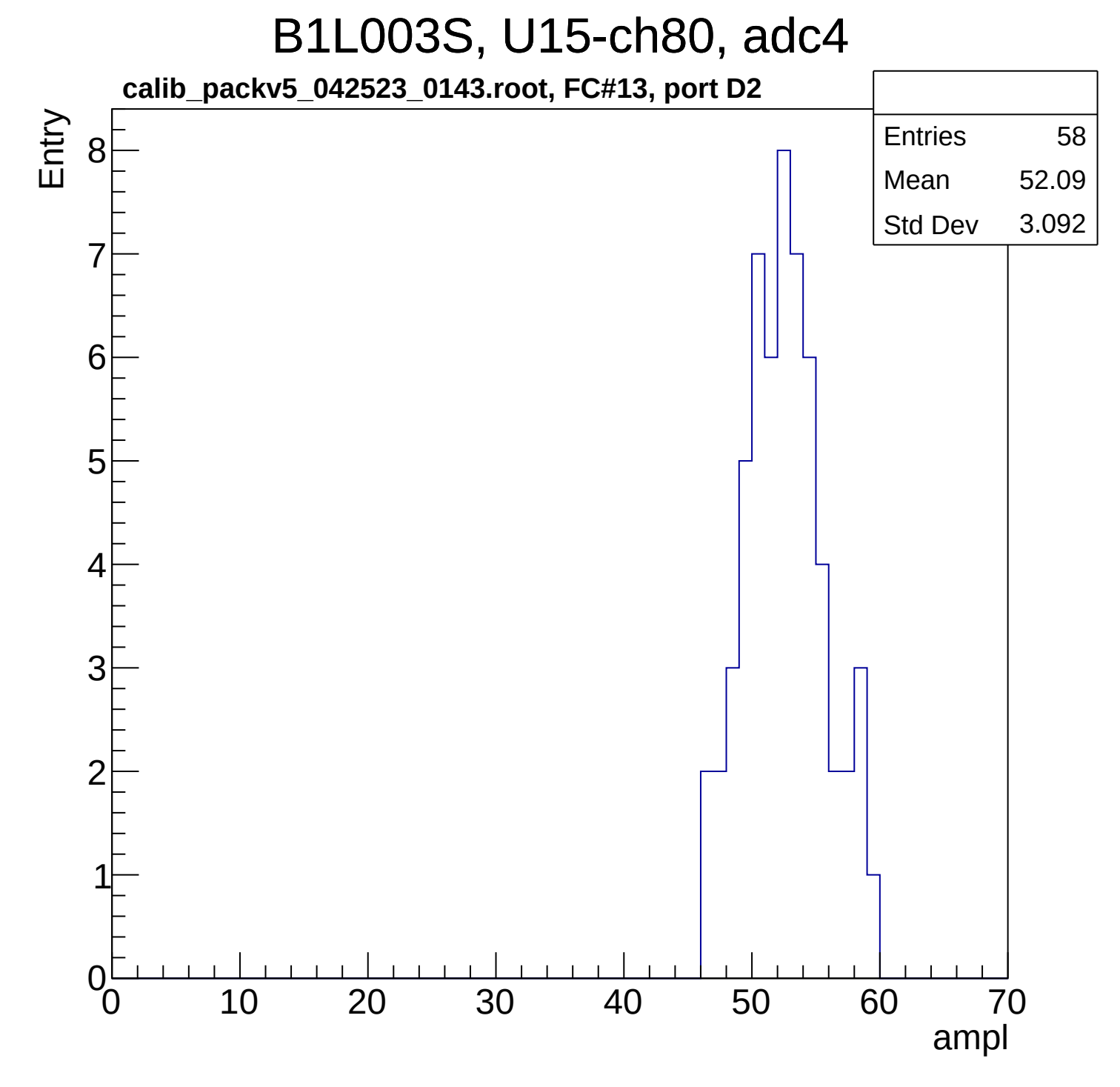
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 52.09
Std Dev 3.092
Entry
ampl
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3
4
5
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7
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0
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20
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40
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60
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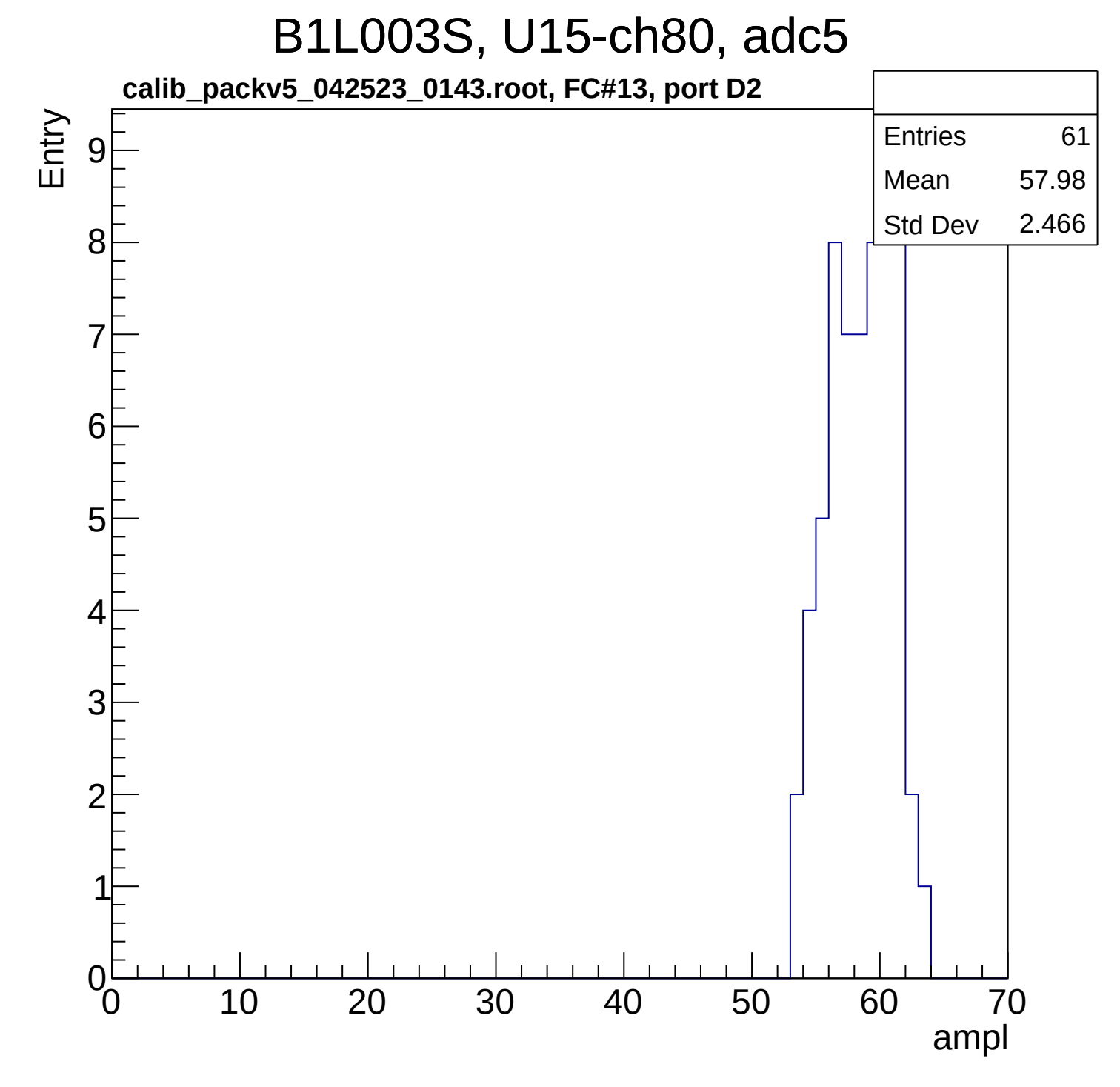
B1L003S, U15-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 57.98
Std Dev 2.466
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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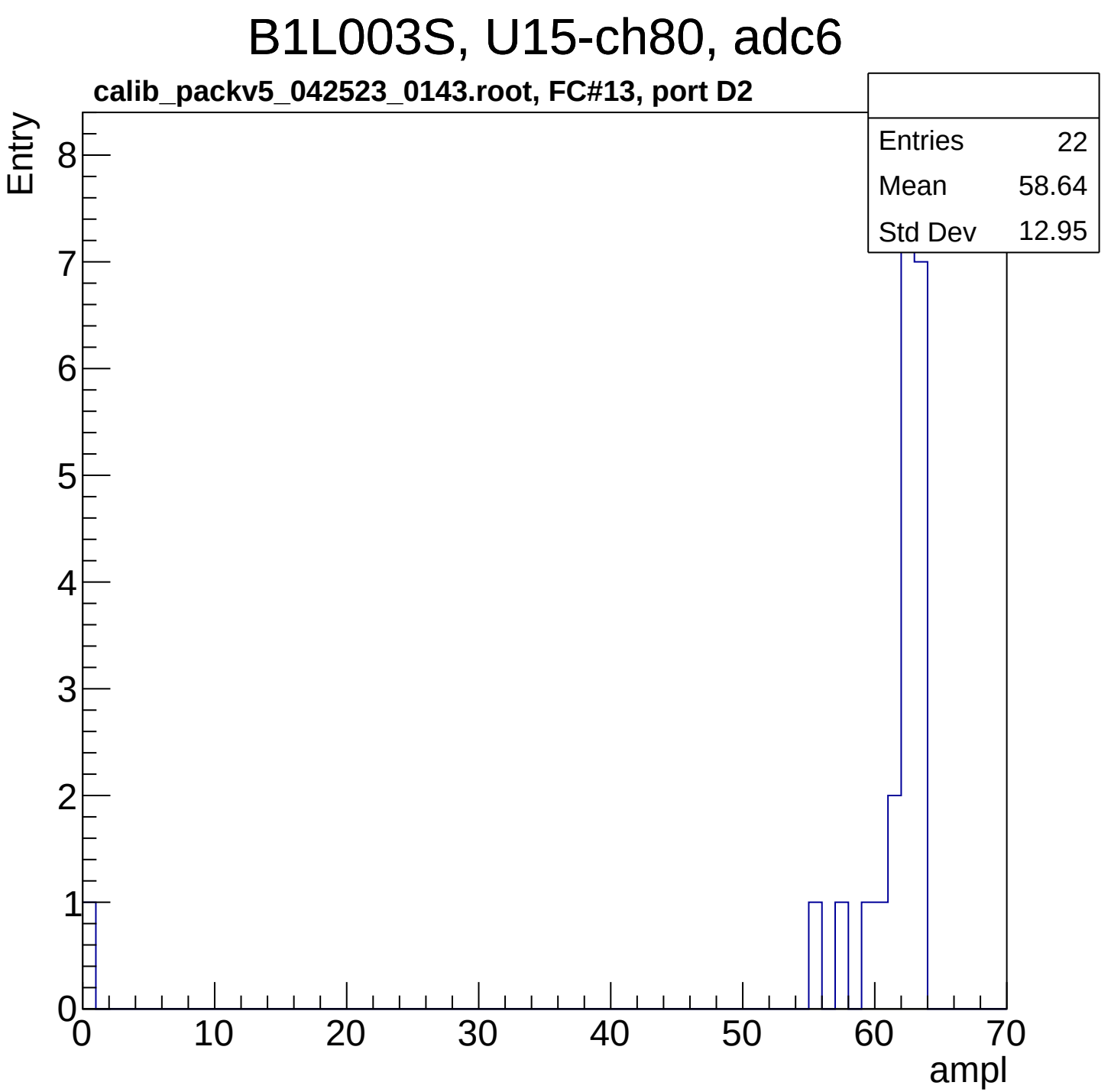
B1L003S, U15-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.64
Std Dev 12.95
Entry
ampl
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1
2
3
4
5
6
7
8
0
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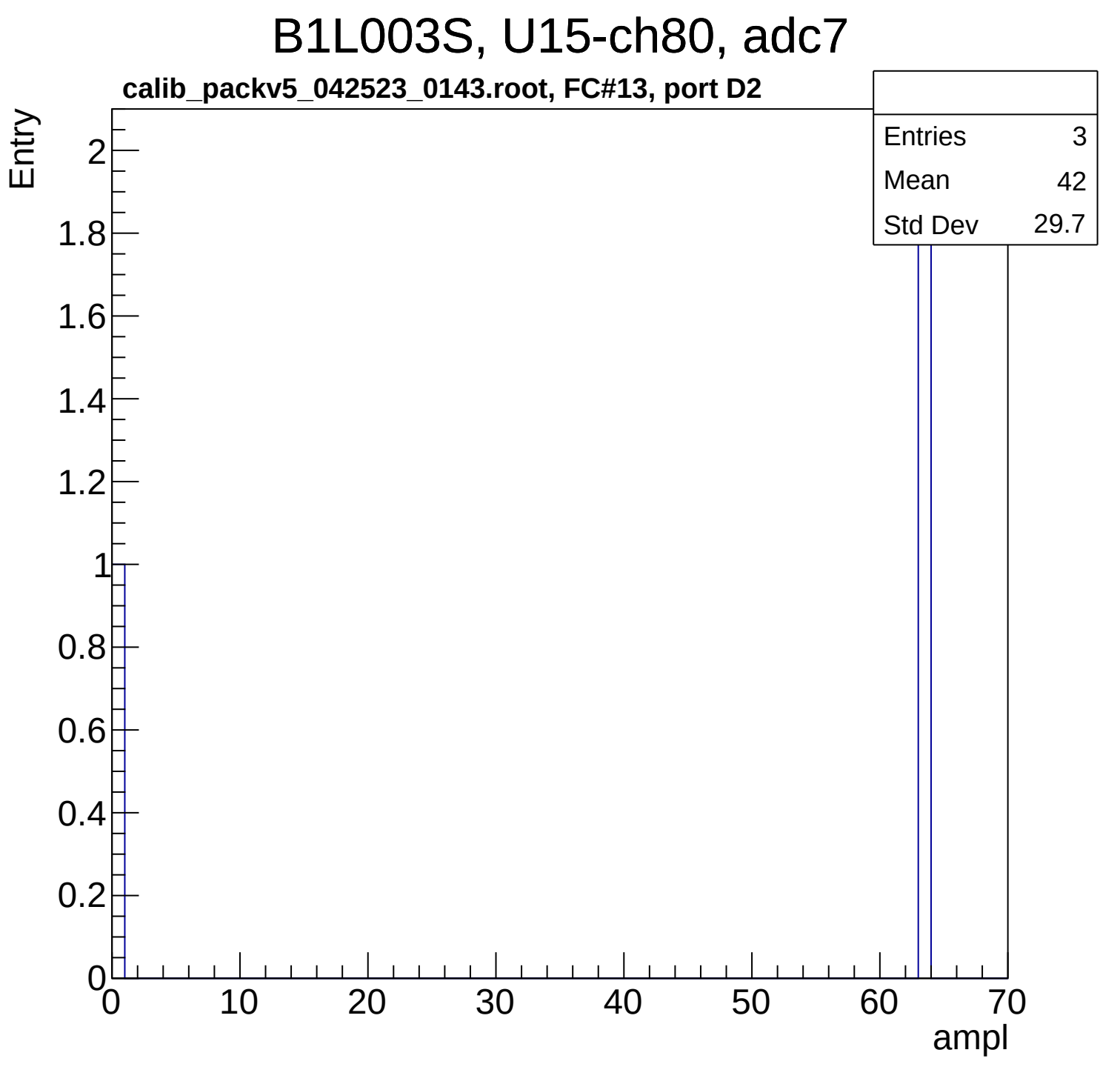
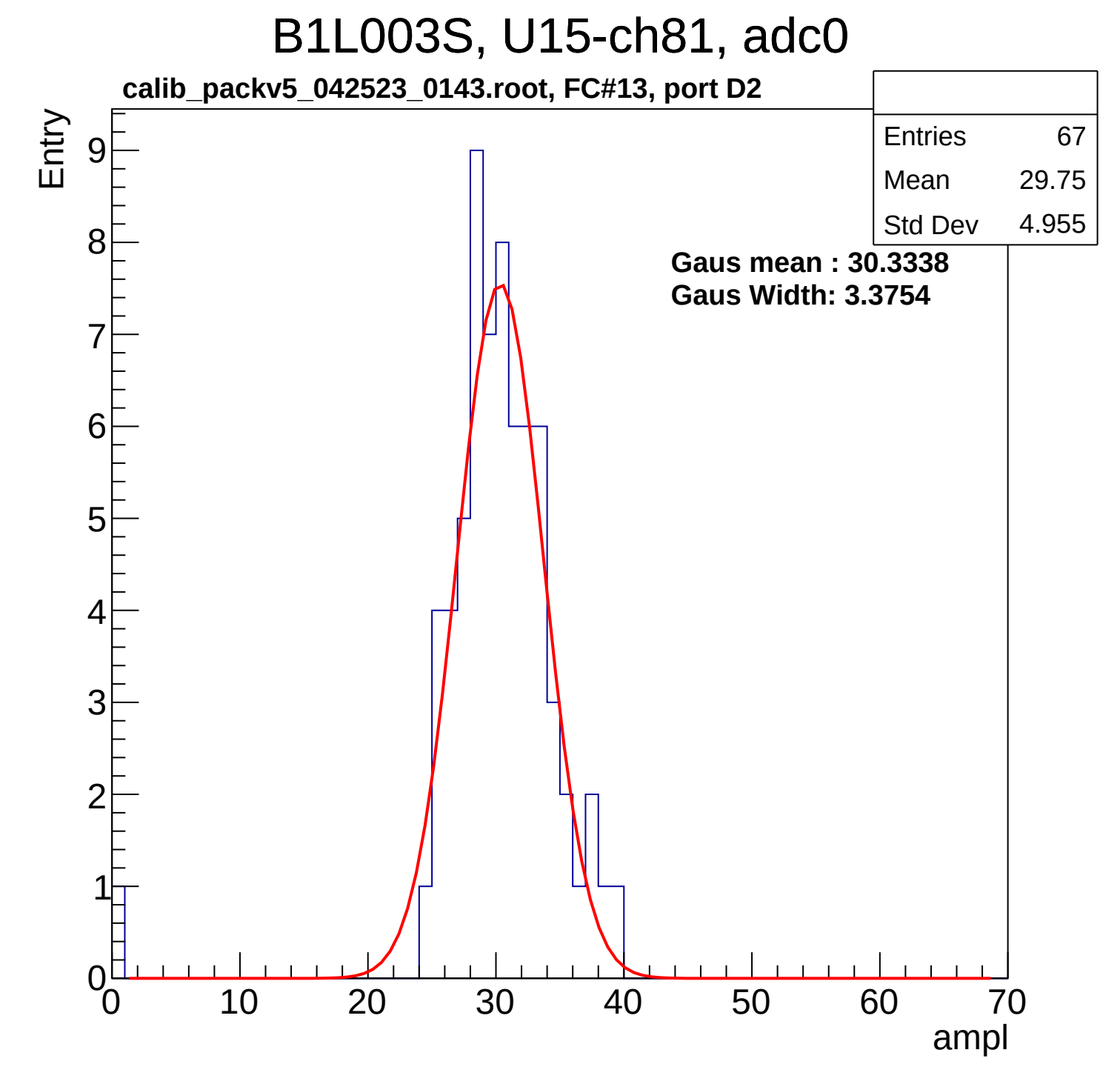
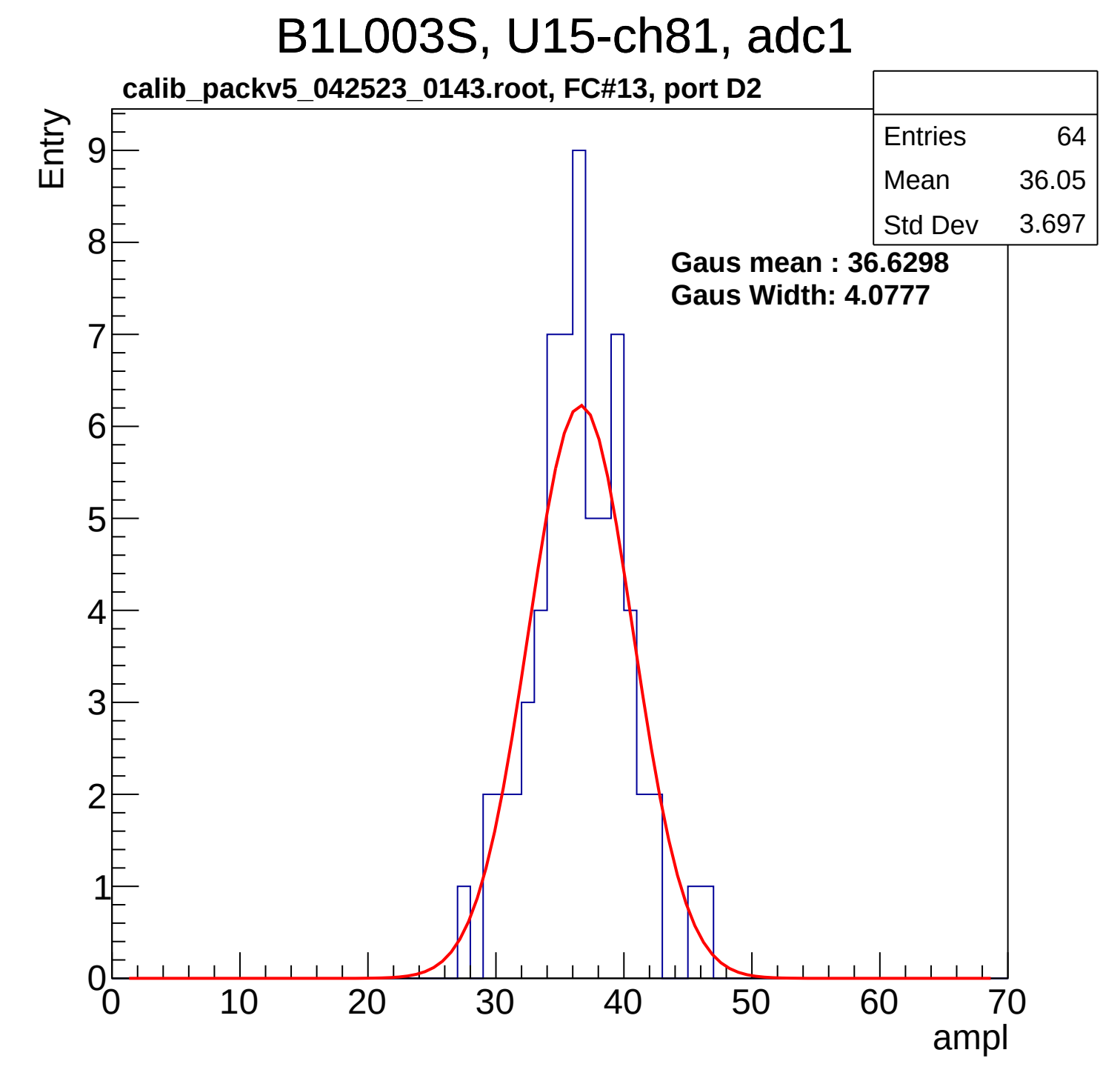
20
30
40
50
60
70


B1L003S, U15-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.75
Std Dev 4.955
Gaus mean : 30.3338
Gaus Width: 3.3754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.05
Std Dev 3.697
Gaus mean : 36.6298
Gaus Width: 4.0777
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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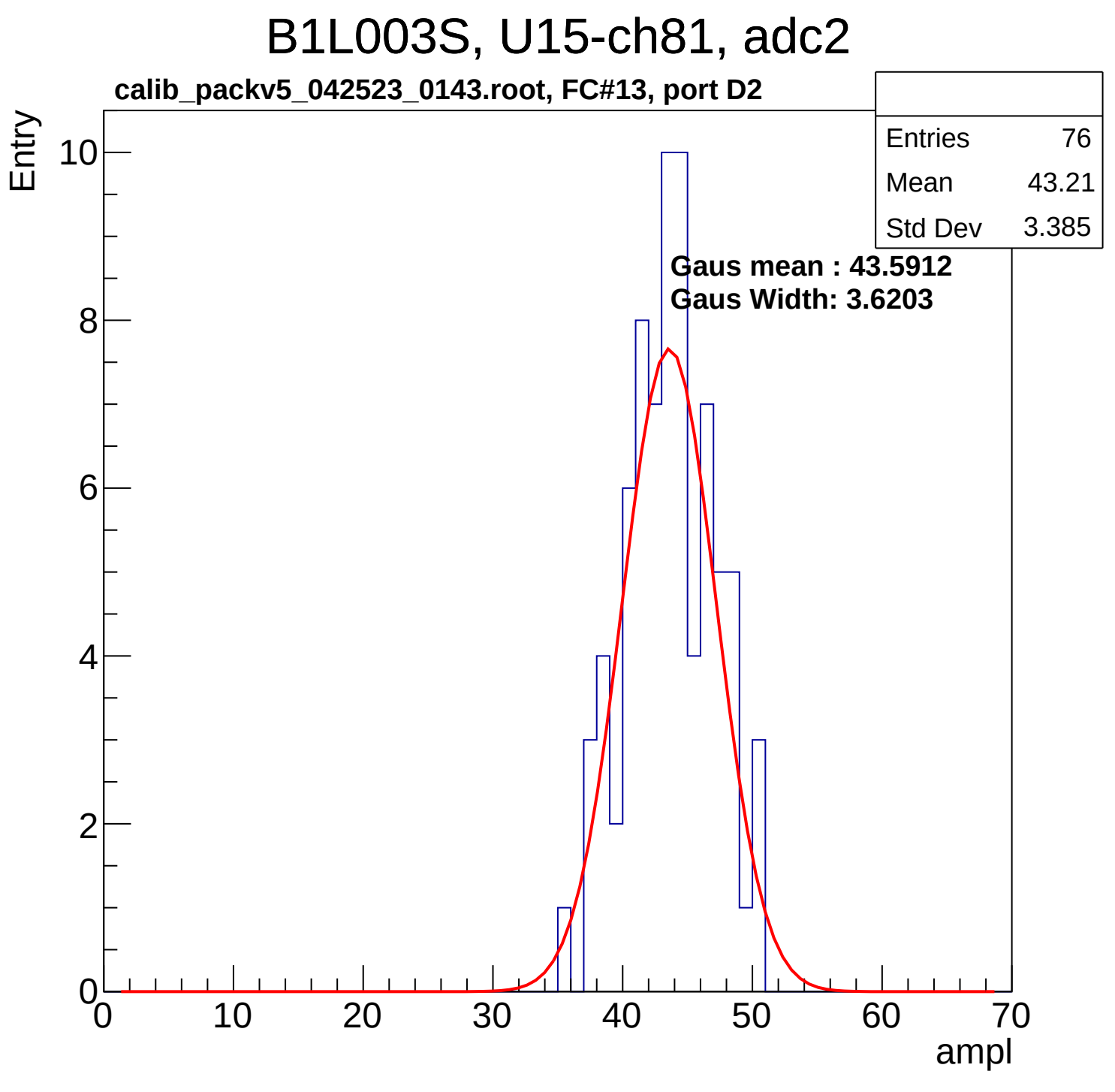
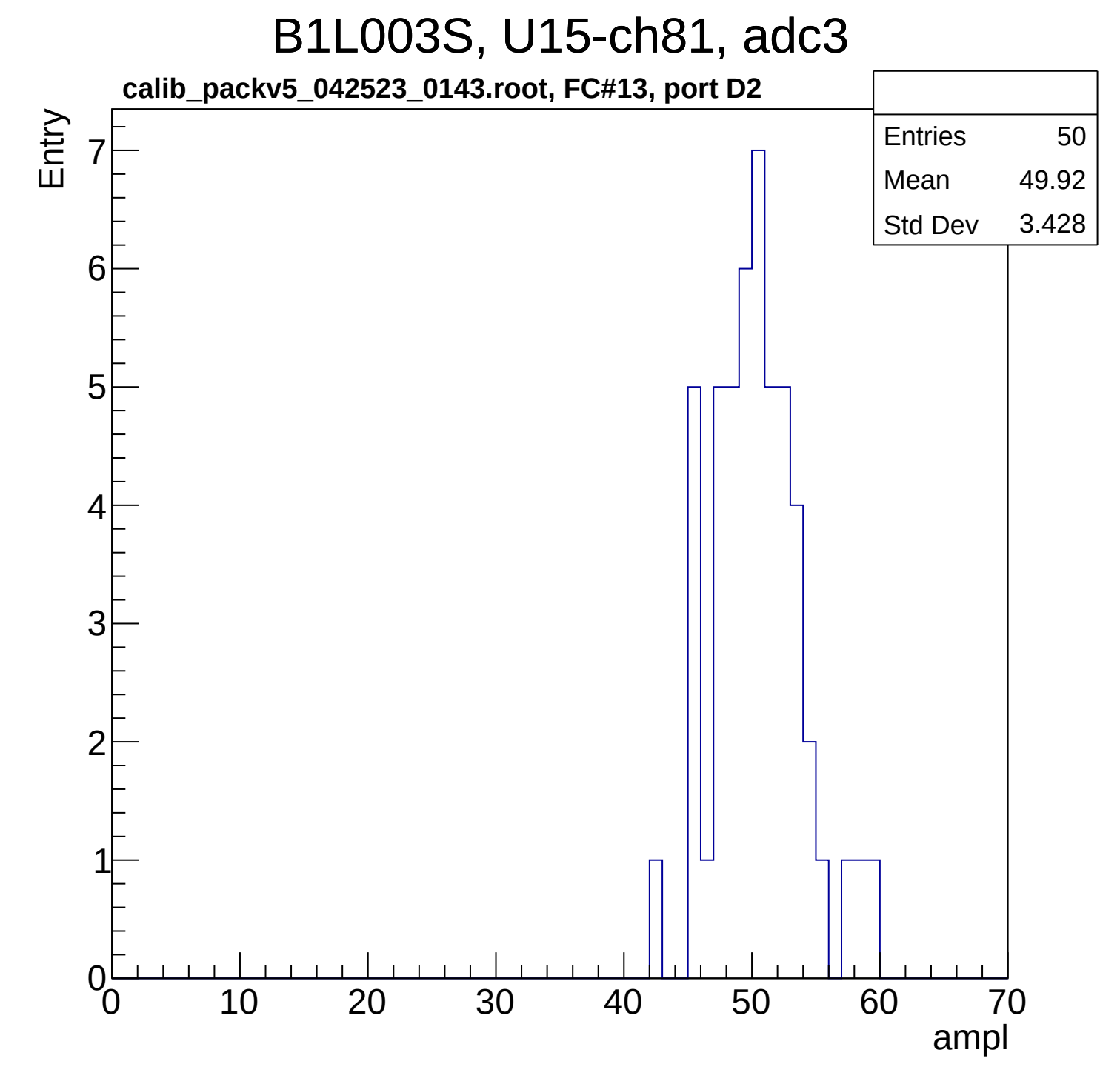

B1L003S, U15-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 43.21
Std Dev 3.385
Gaus mean : 43.5912
Gaus Width: 3.6203
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 49.92
Std Dev 3.428
Entry
ampl
0
1
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4
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60
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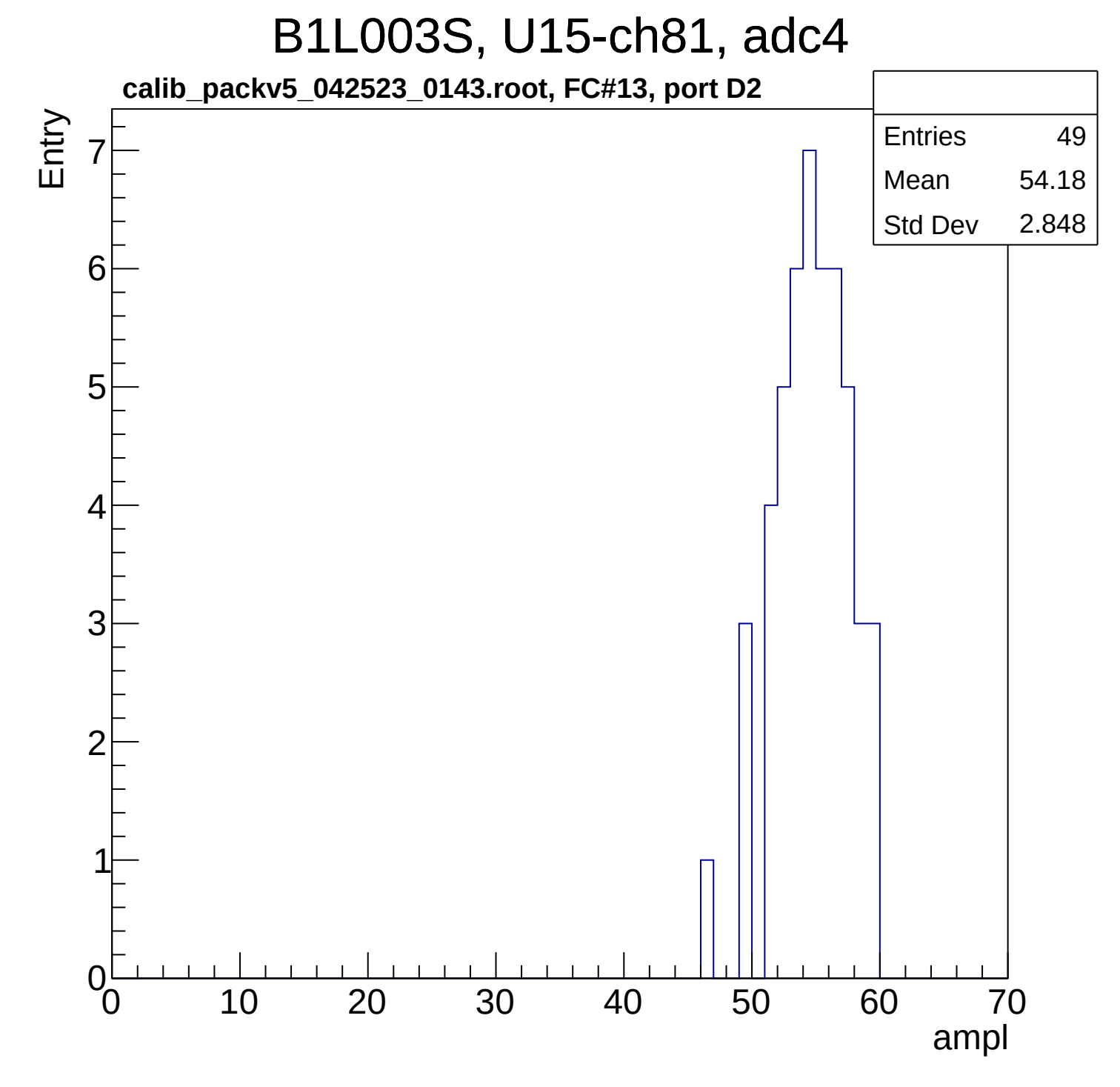
B1L003S, U15-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.18
Std Dev 2.848
Entry
ampl
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1
2
3
4
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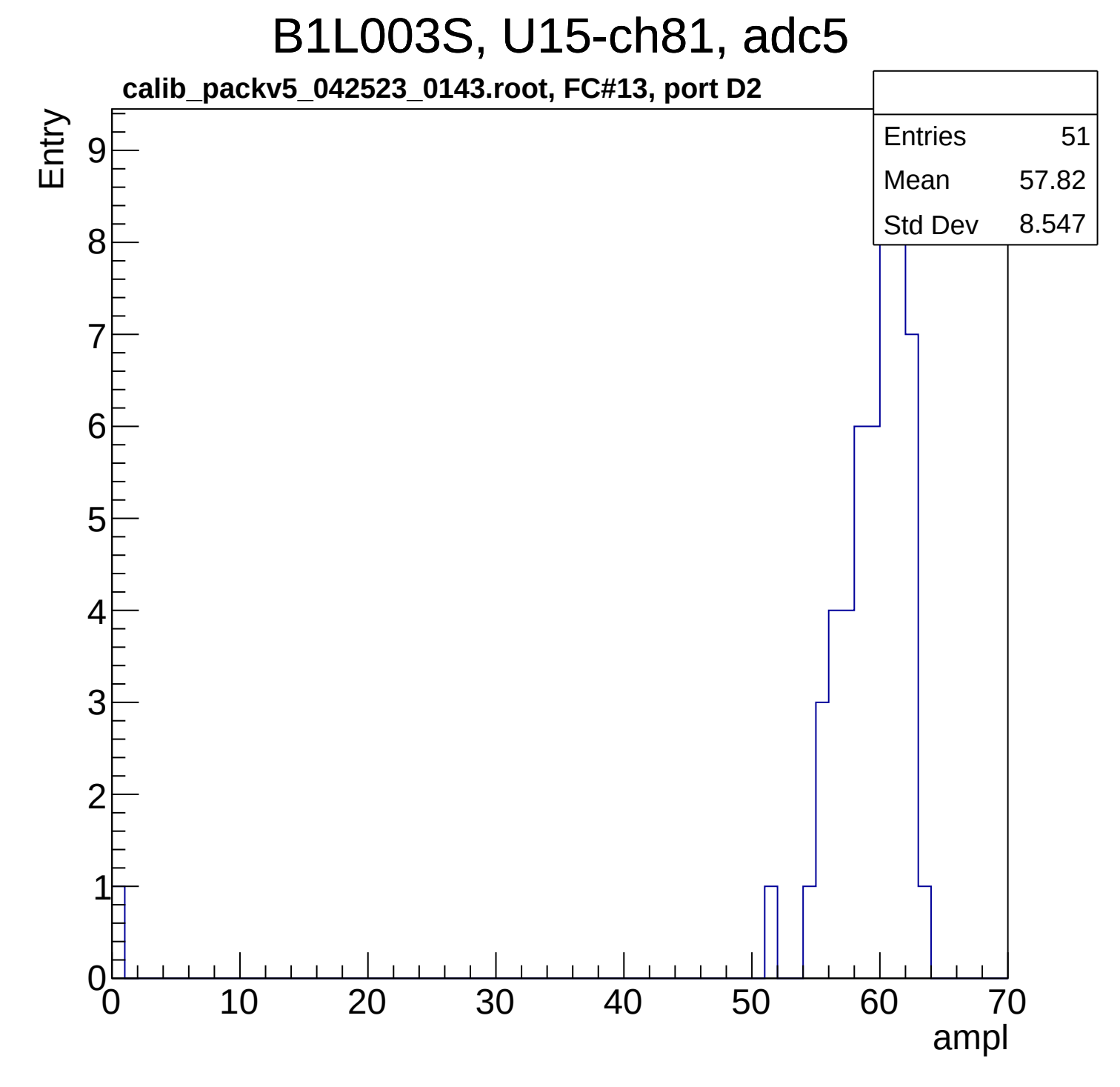
B1L003S, U15-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 57.82
Std Dev 8.547
Entry
ampl
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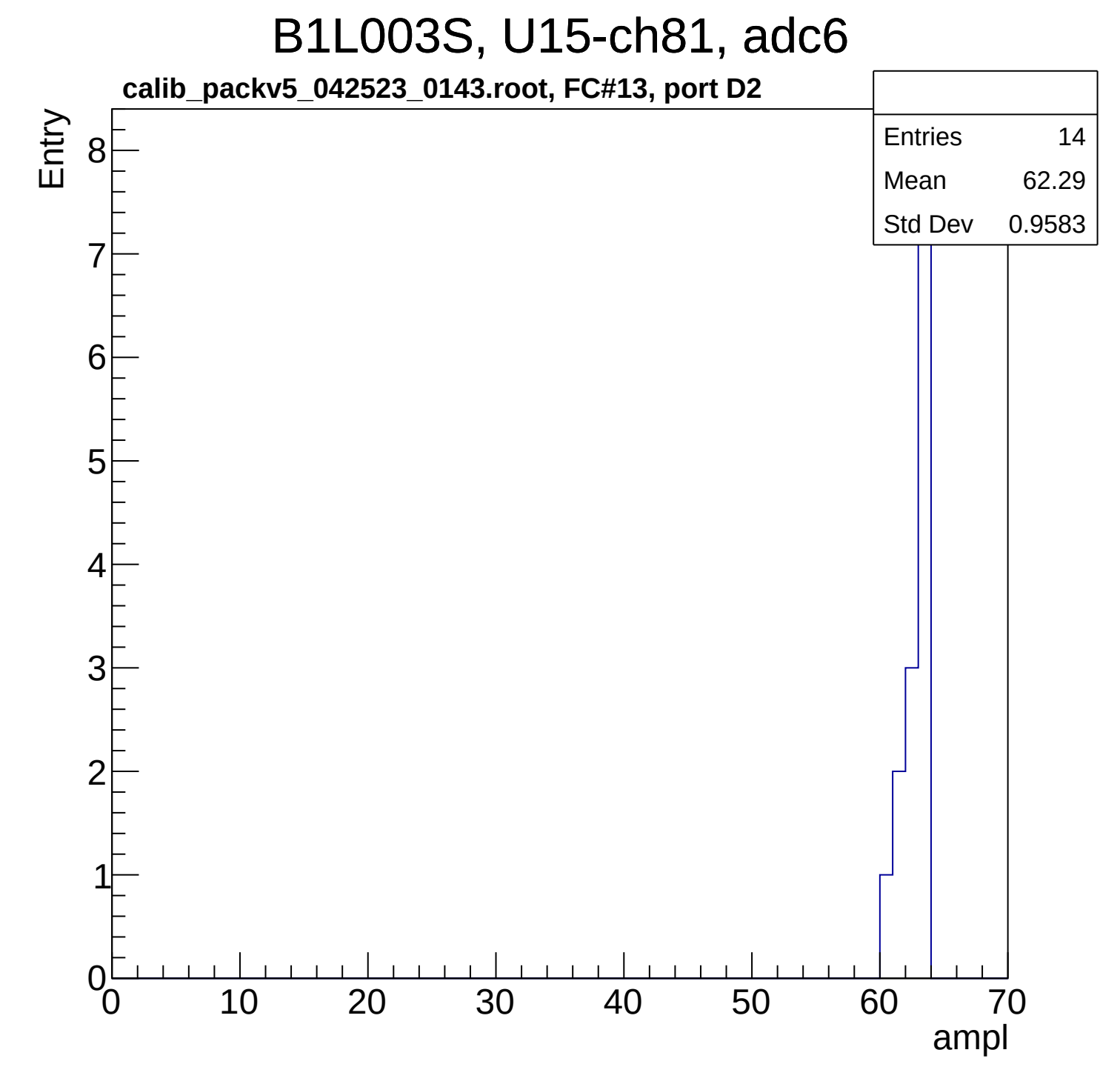
B1L003S, U15-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 62.29
Std Dev 0.9583
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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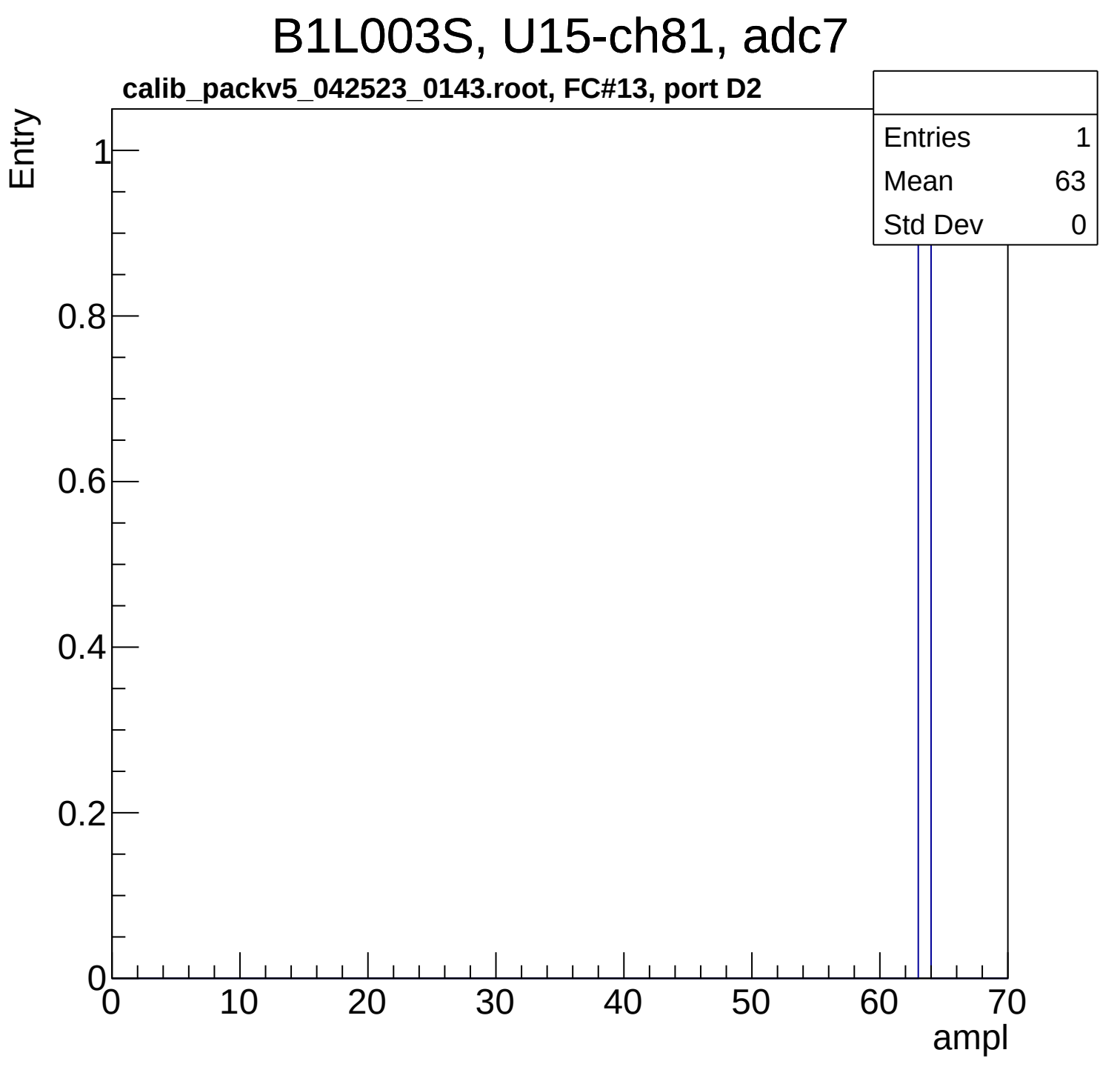
B1L003S, U15-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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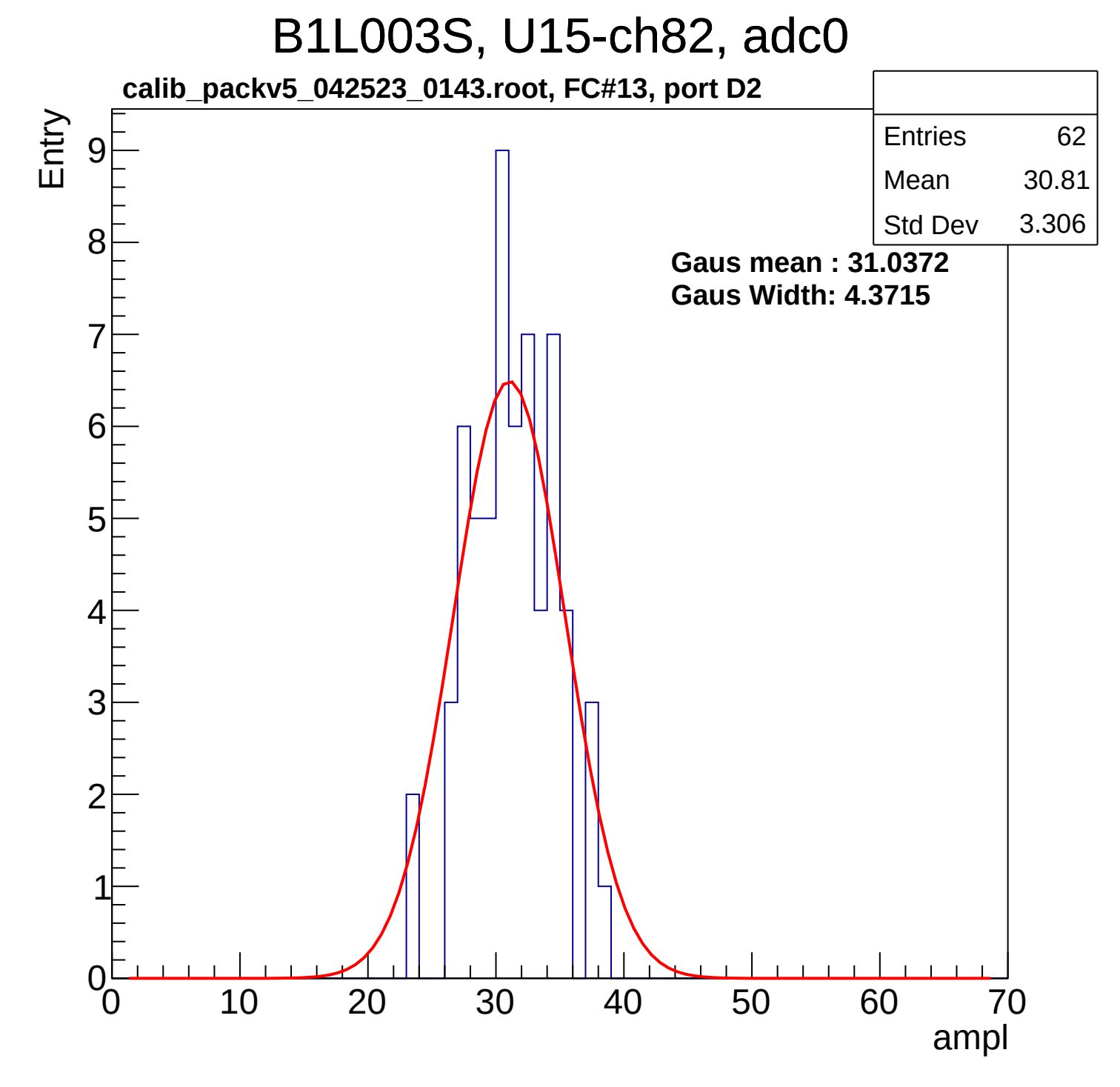
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 30.81
Std Dev 3.306
Gaus mean : 31.0372
Gaus Width: 4.3715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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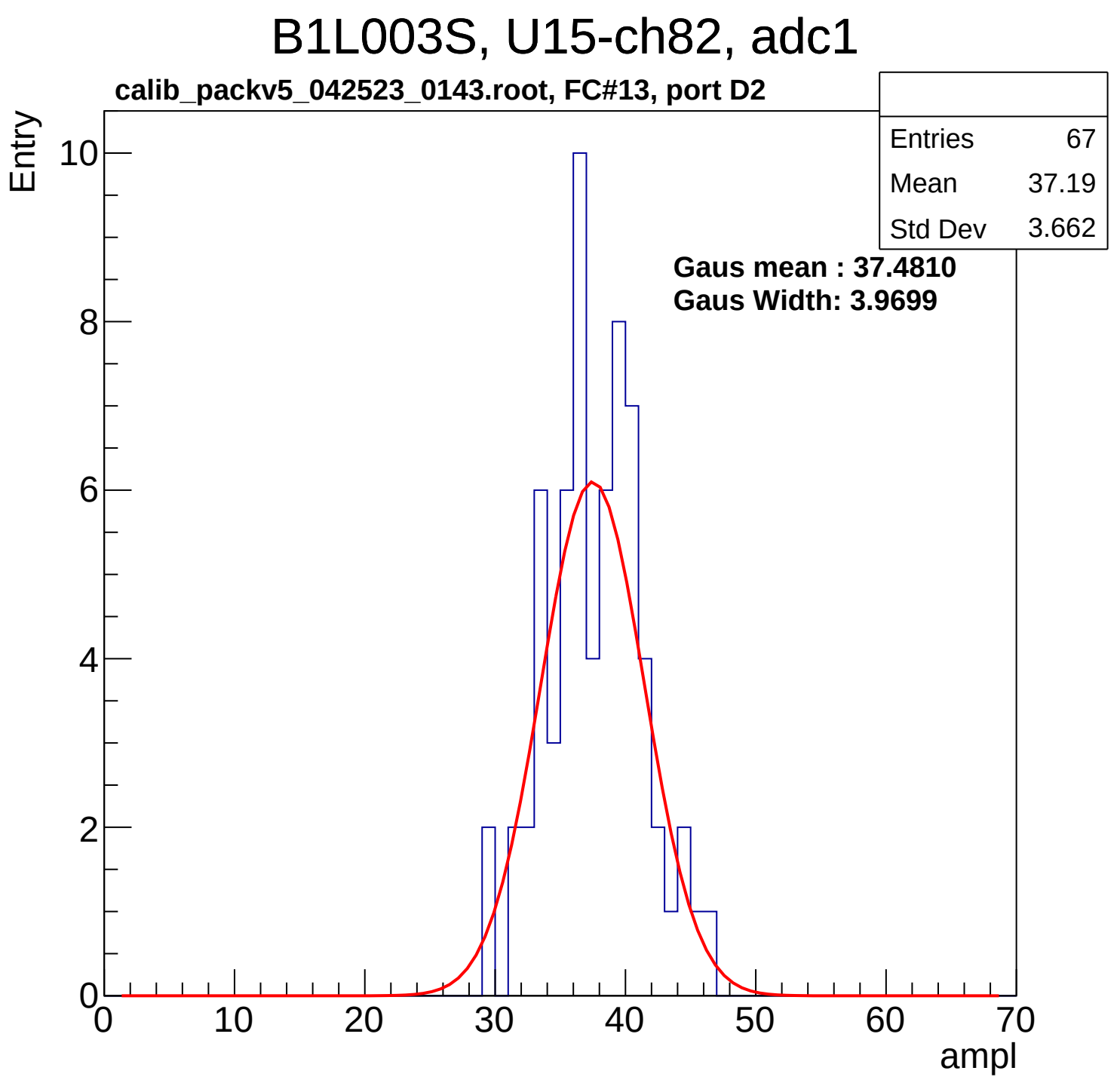
B1L003S, U15-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.19
Std Dev 3.662
Gaus mean : 37.4810
Gaus Width: 3.9699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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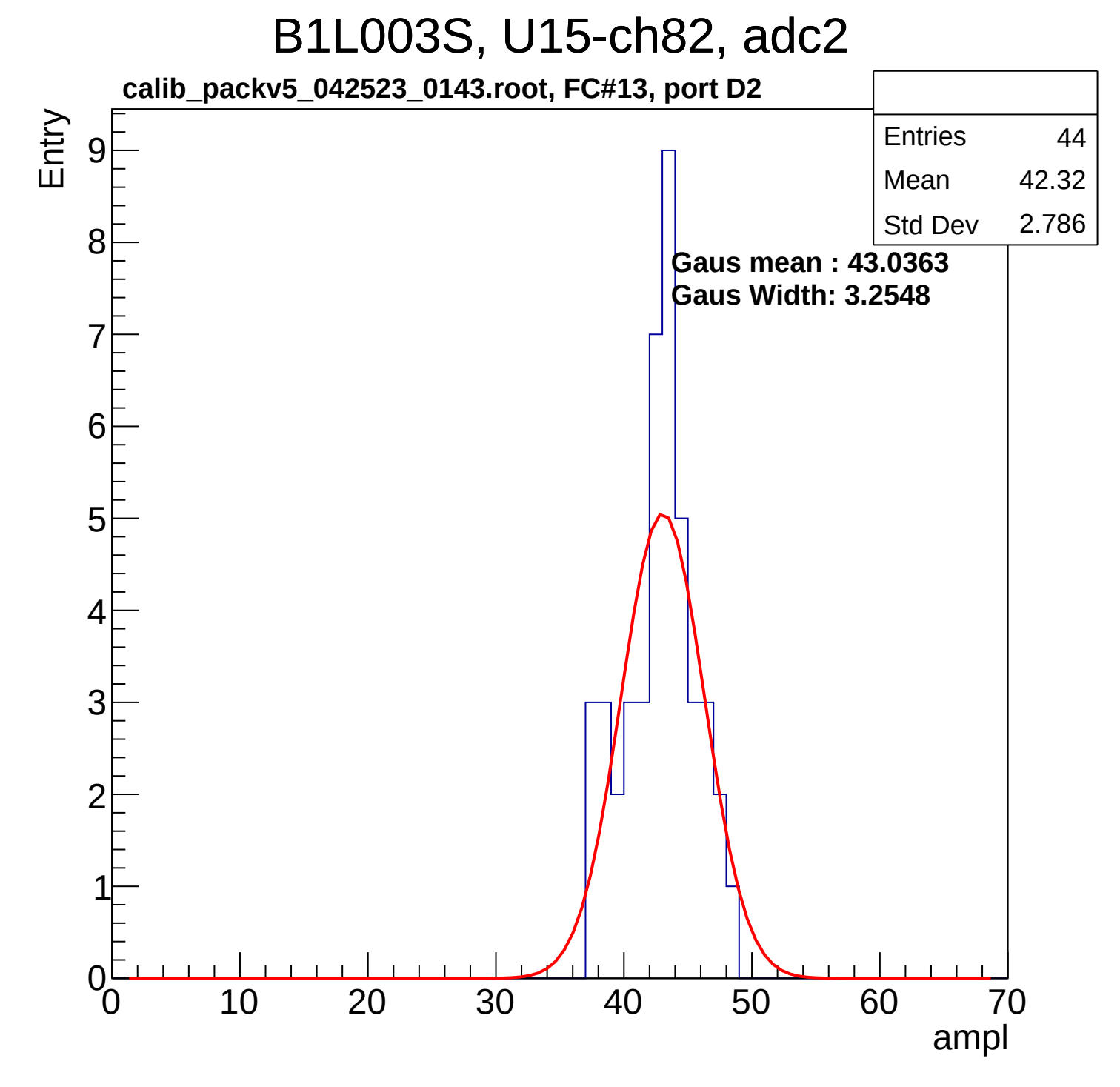
B1L003S, U15-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 42.32
Std Dev 2.786
Gaus mean : 43.0363
Gaus Width: 3.2548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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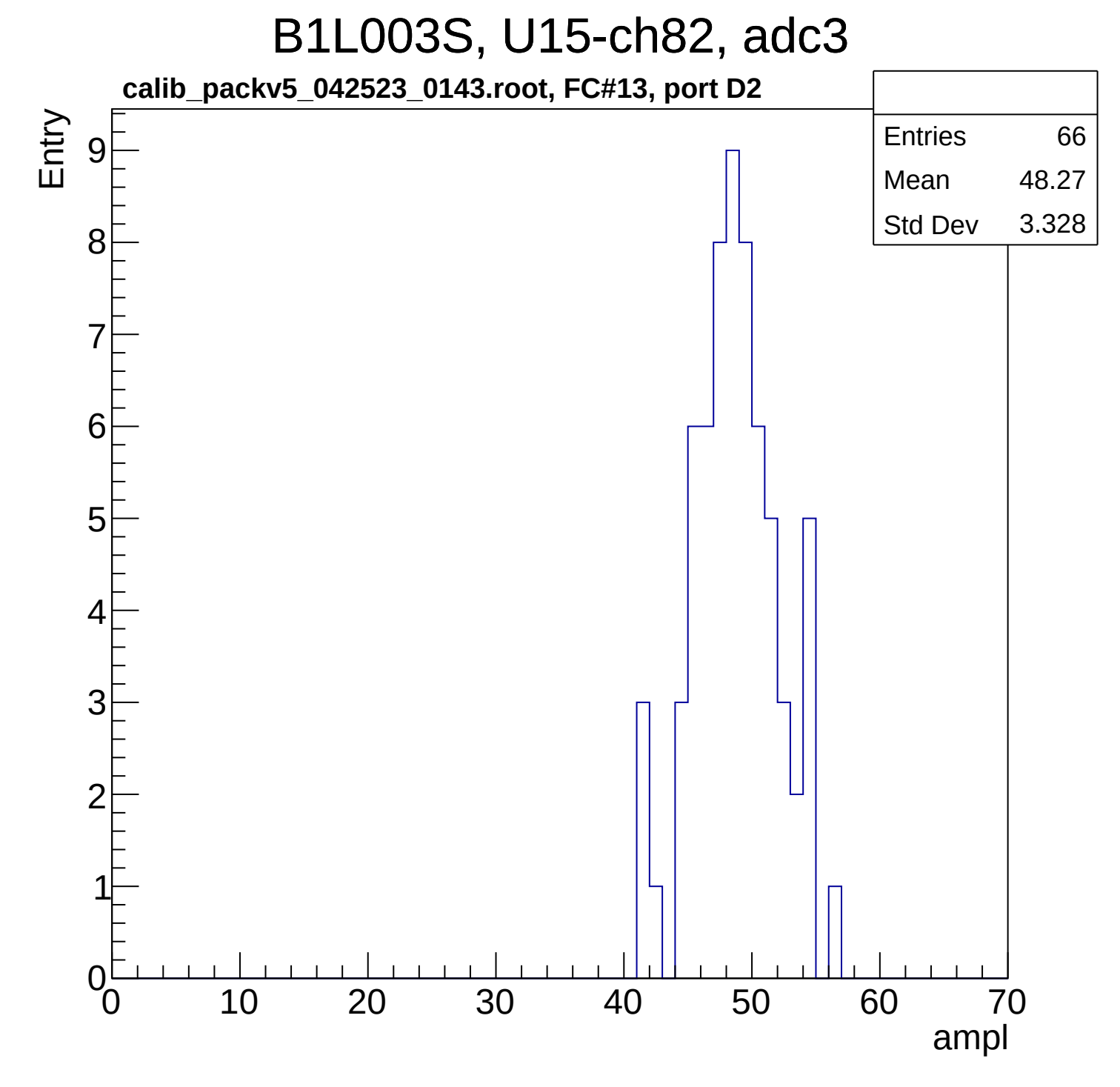
B1L003S, U15-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.27
Std Dev 3.328
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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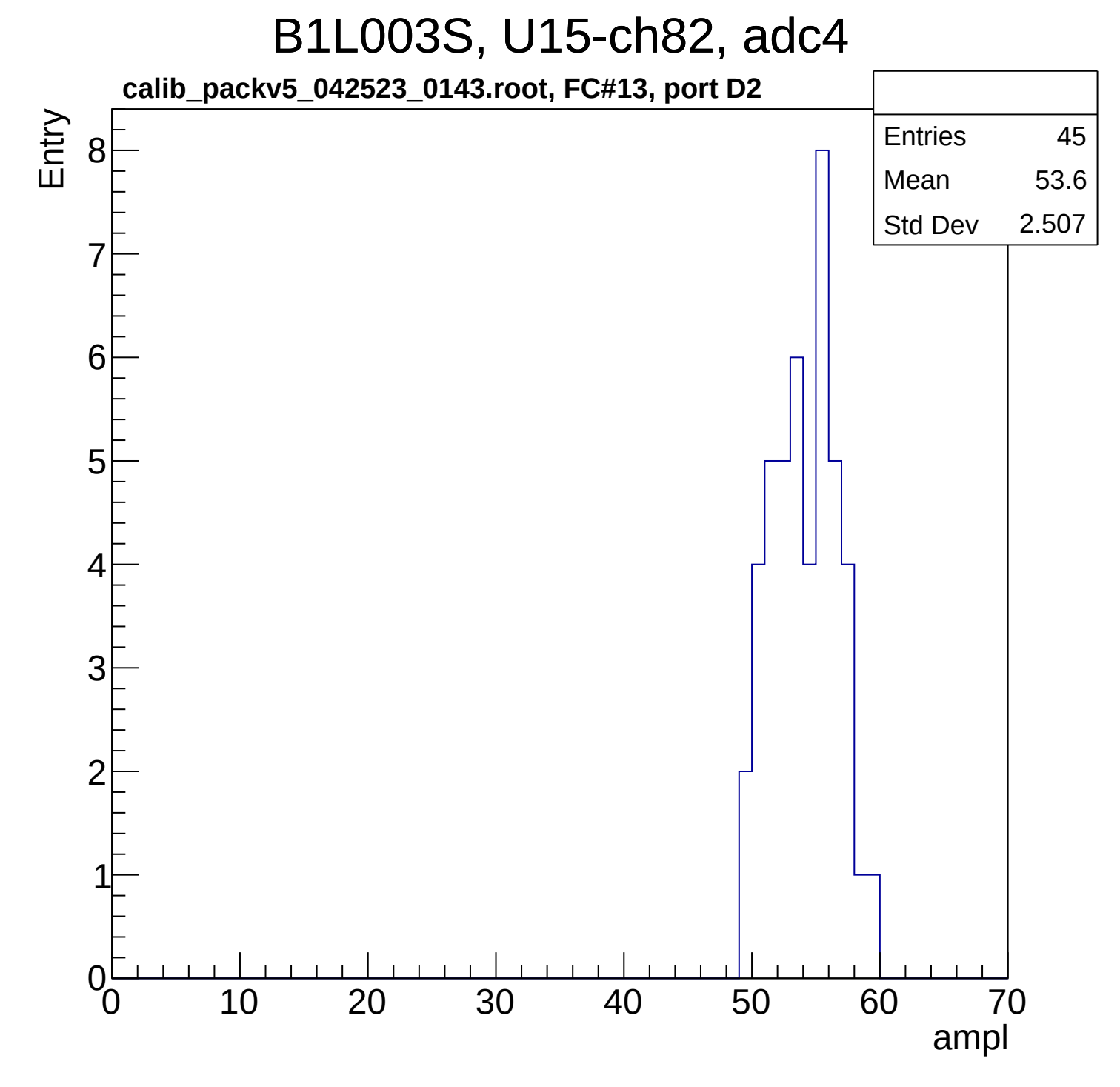
B1L003S, U15-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 53.6
Std Dev 2.507
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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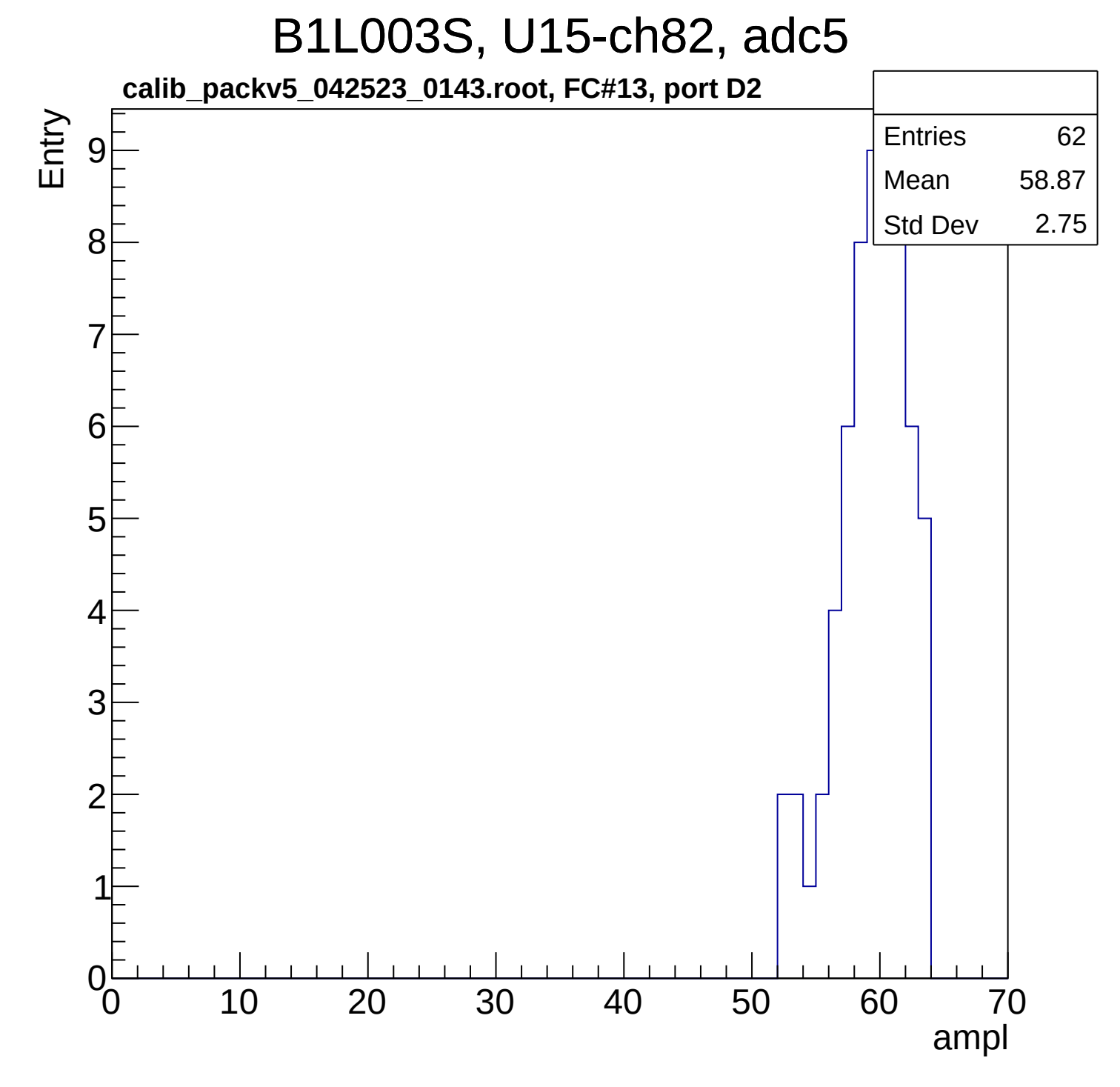
B1L003S, U15-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.87
Std Dev 2.75
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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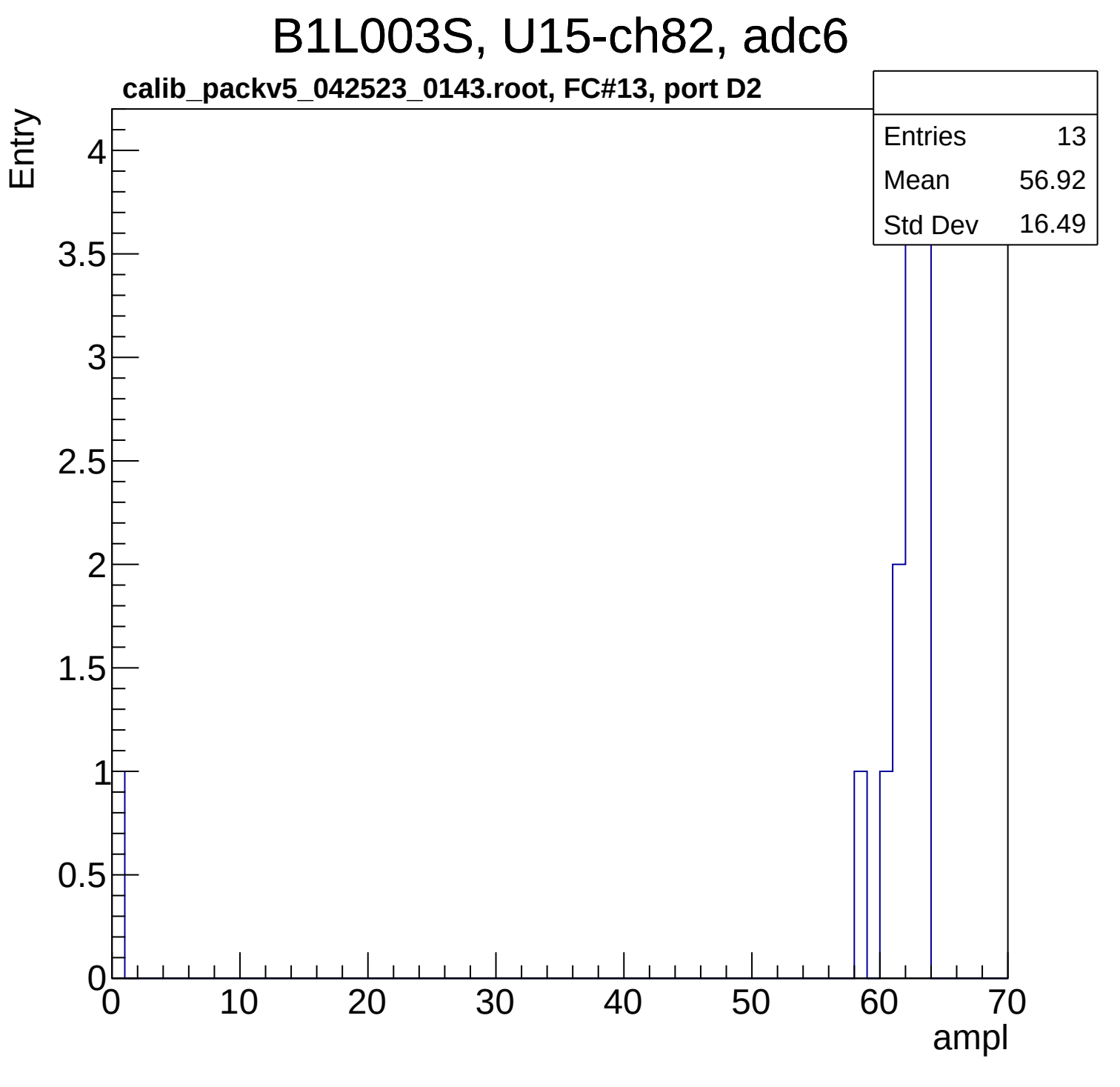
B1L003S, U15-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.92
Std Dev 16.49
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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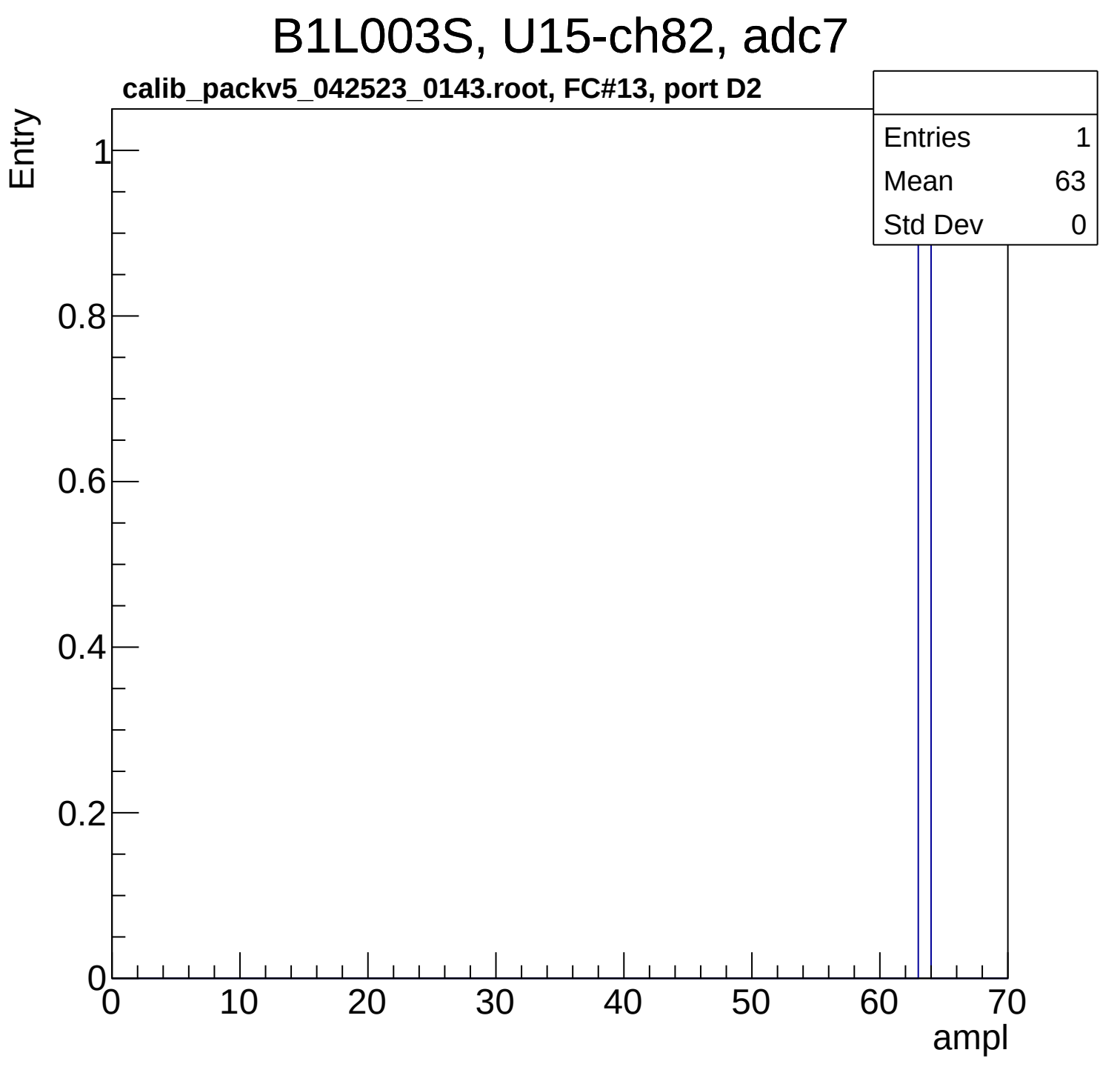
B1L003S, U15-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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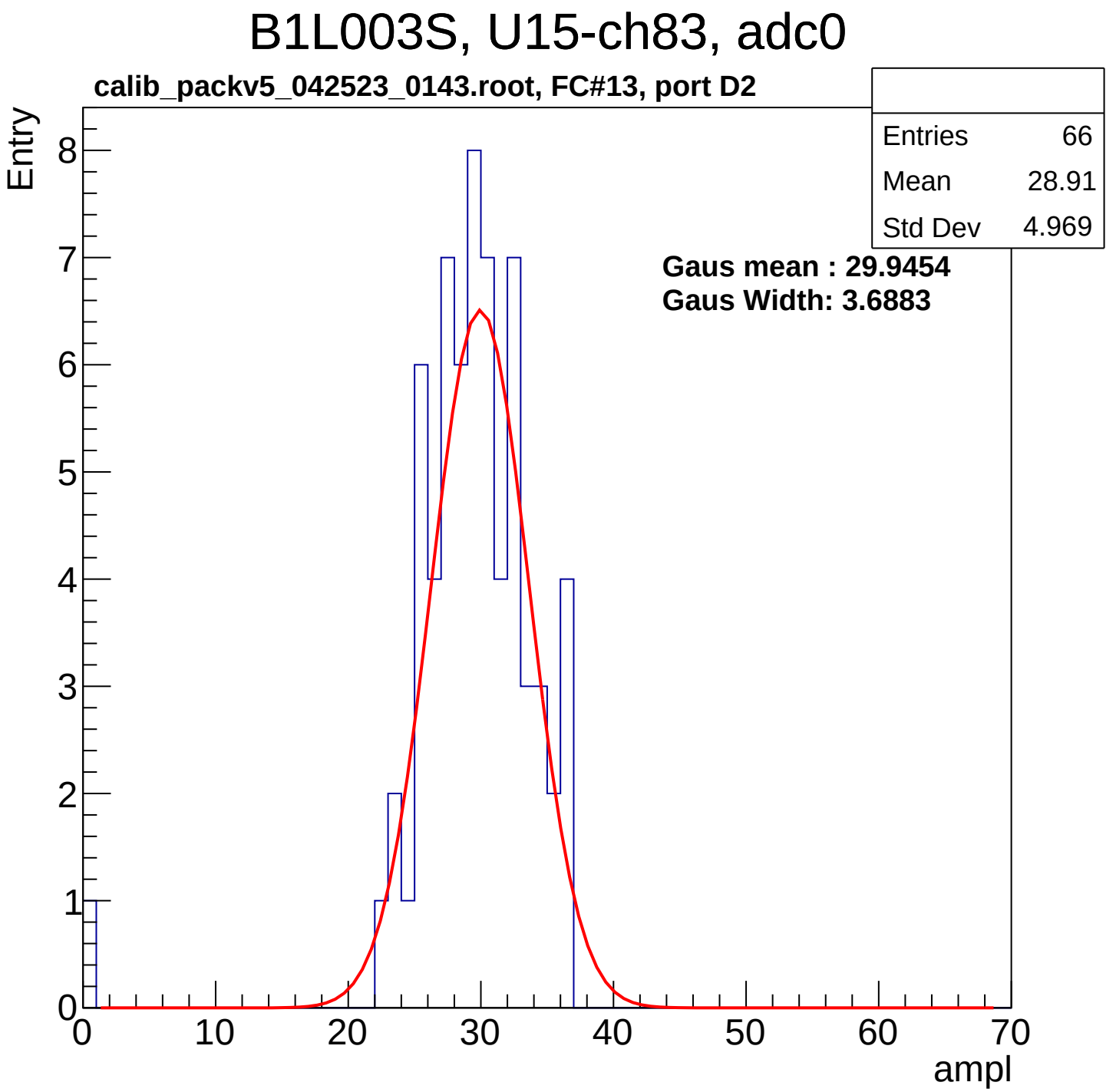
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.91
Std Dev 4.969
Gaus mean : 29.9454
Gaus Width: 3.6883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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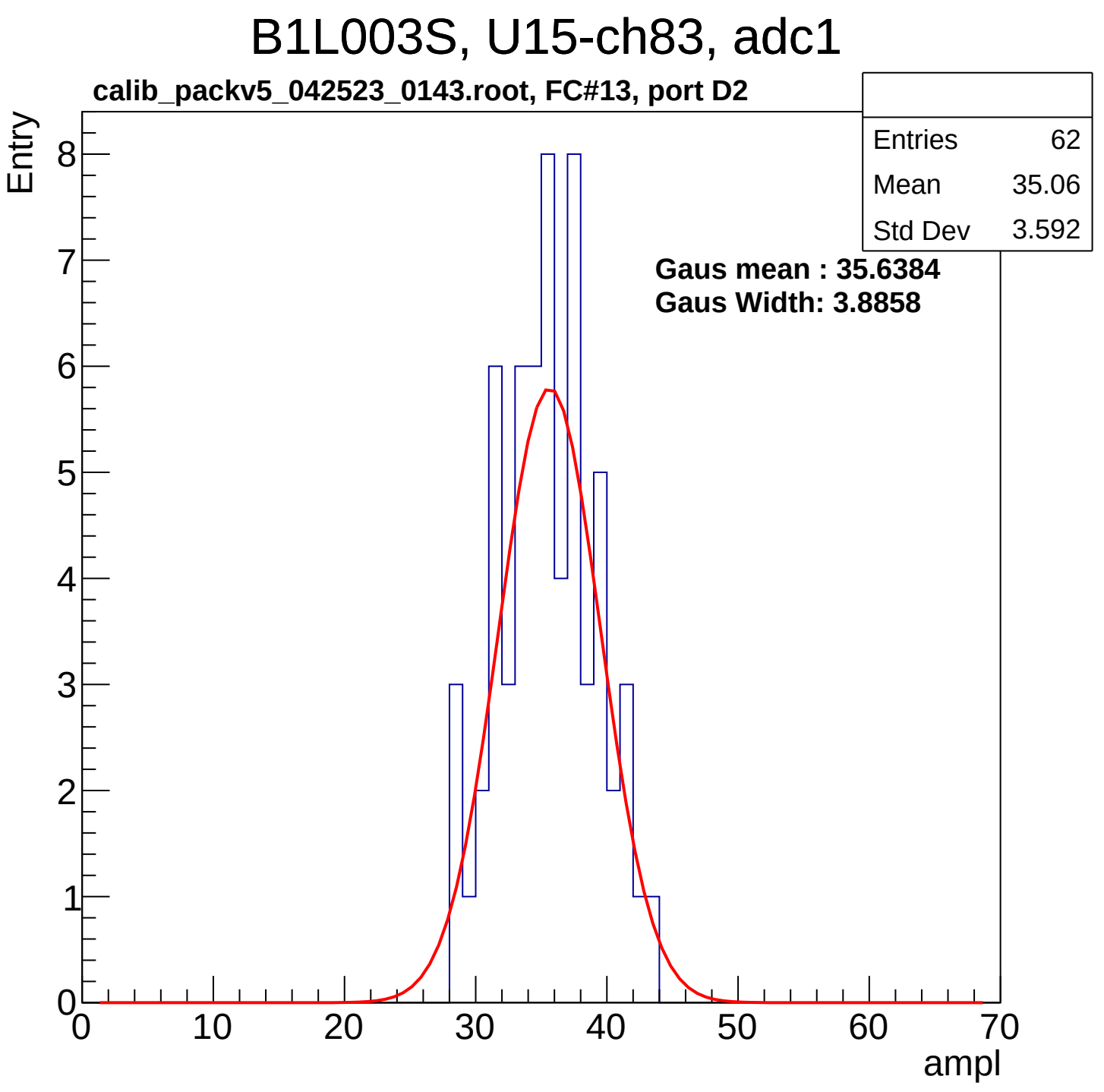
B1L003S, U15-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.06
Std Dev 3.592
Gaus mean : 35.6384
Gaus Width: 3.8858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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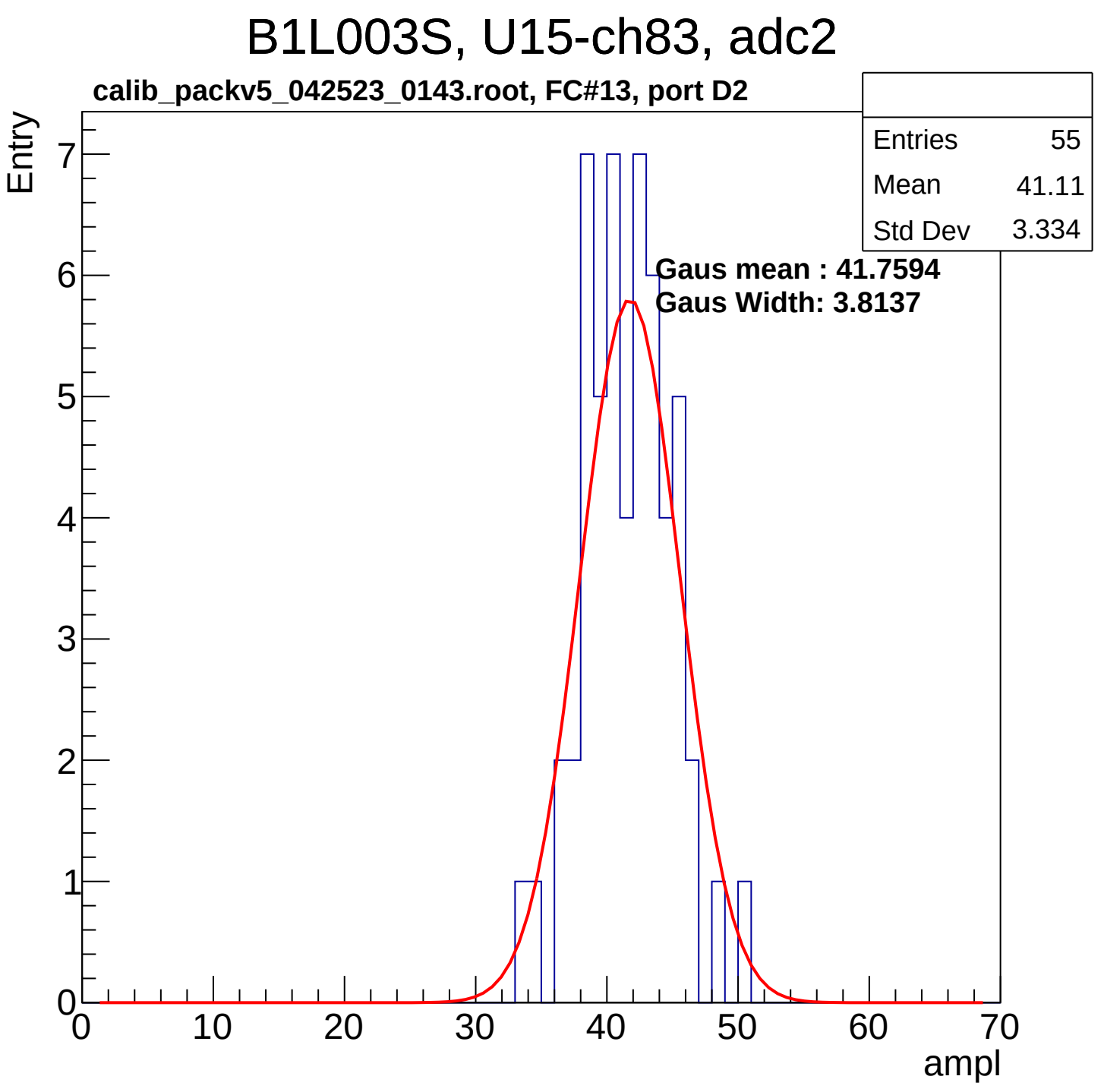
B1L003S, U15-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 41.11
Std Dev 3.334
Gaus mean : 41.7594
Gaus Width: 3.8137
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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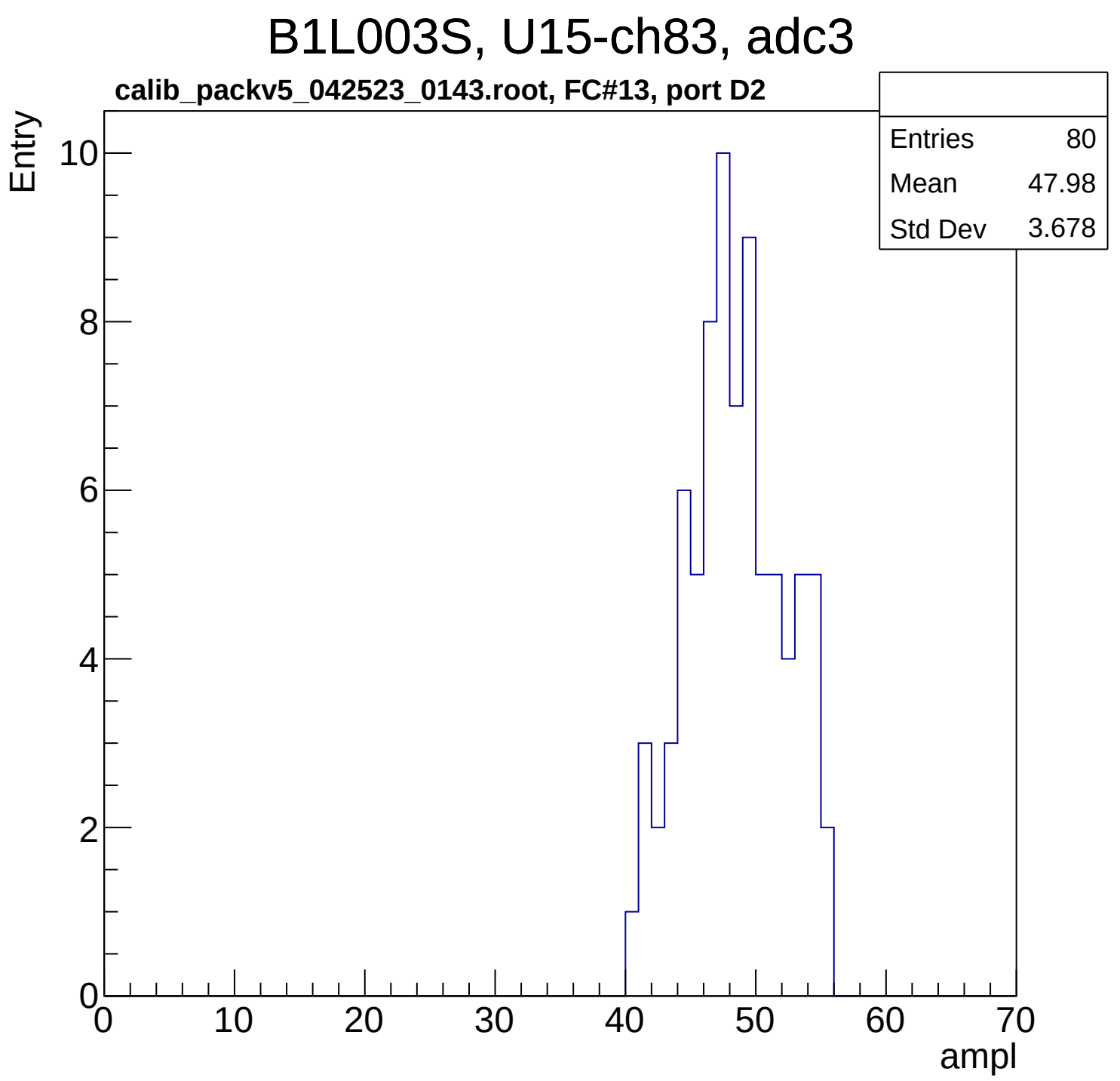
B1L003S, U15-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 47.98
Std Dev 3.678
Entry
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2
4
6
8
10
0
10
20
30
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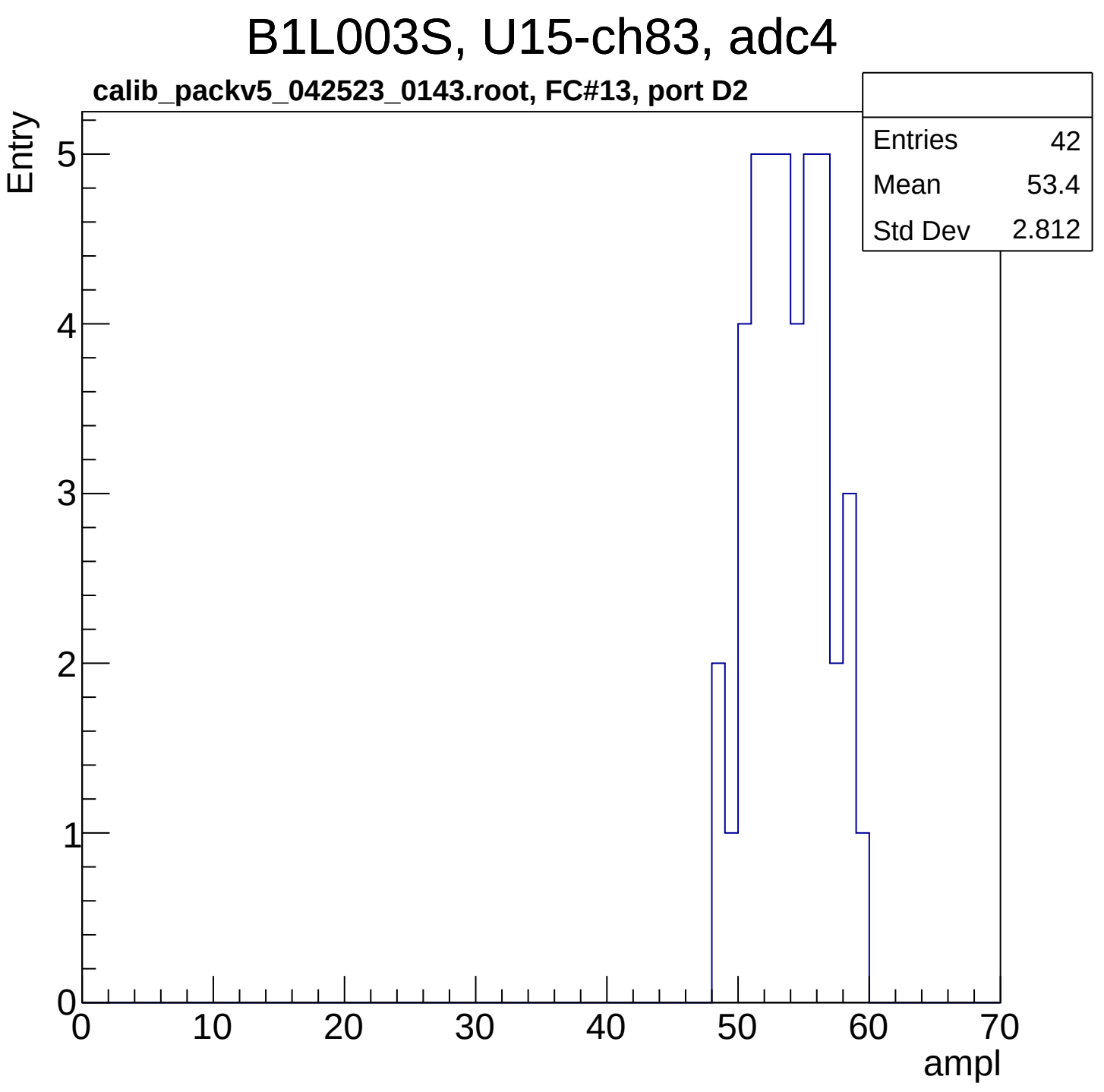
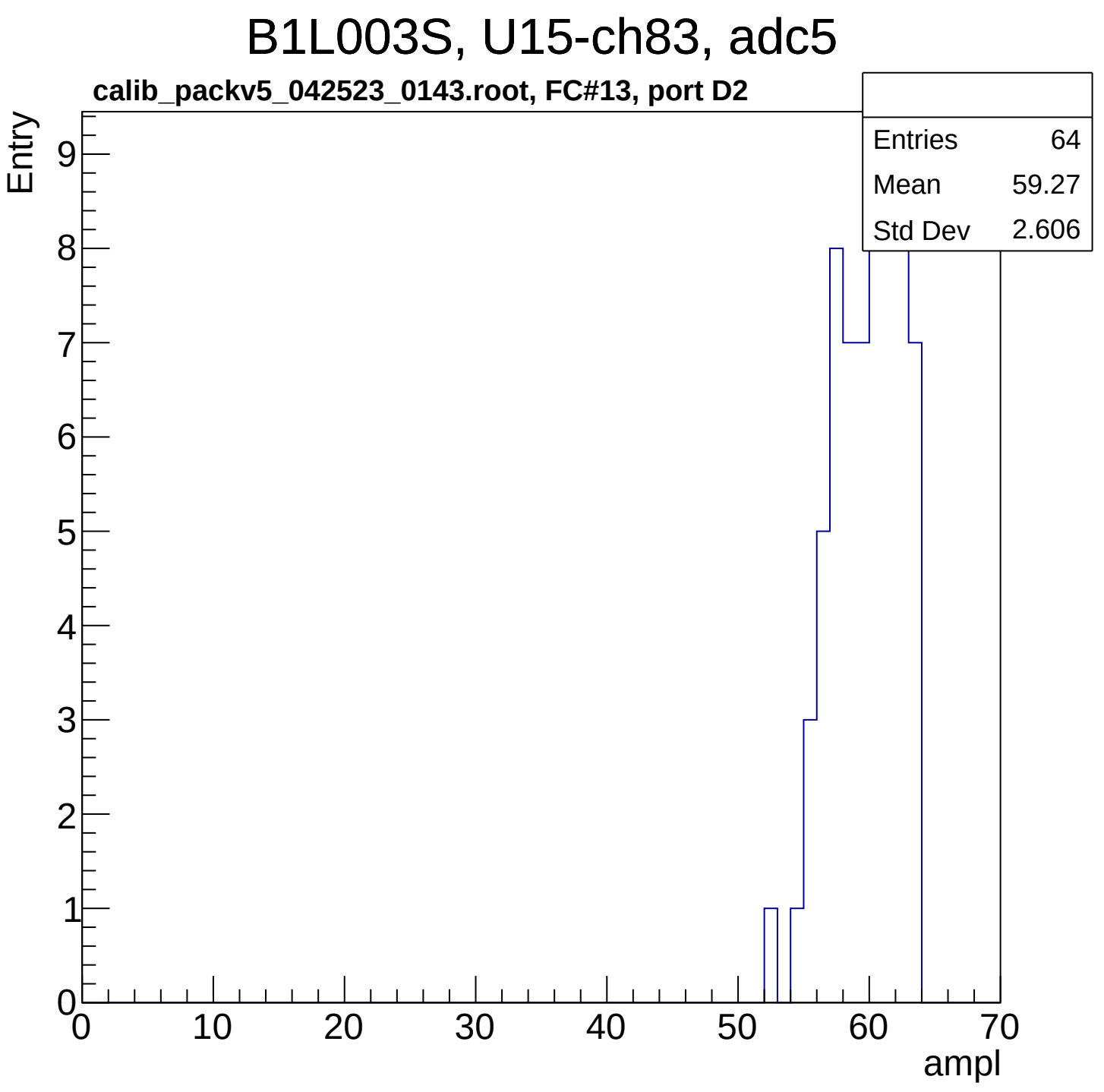
50
60
70
ampl


B1L003S, U15-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 53.4
Std Dev 2.812
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 59.27
Std Dev 2.606
Entry
ampl
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2
3
4
5
6
7
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0
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60
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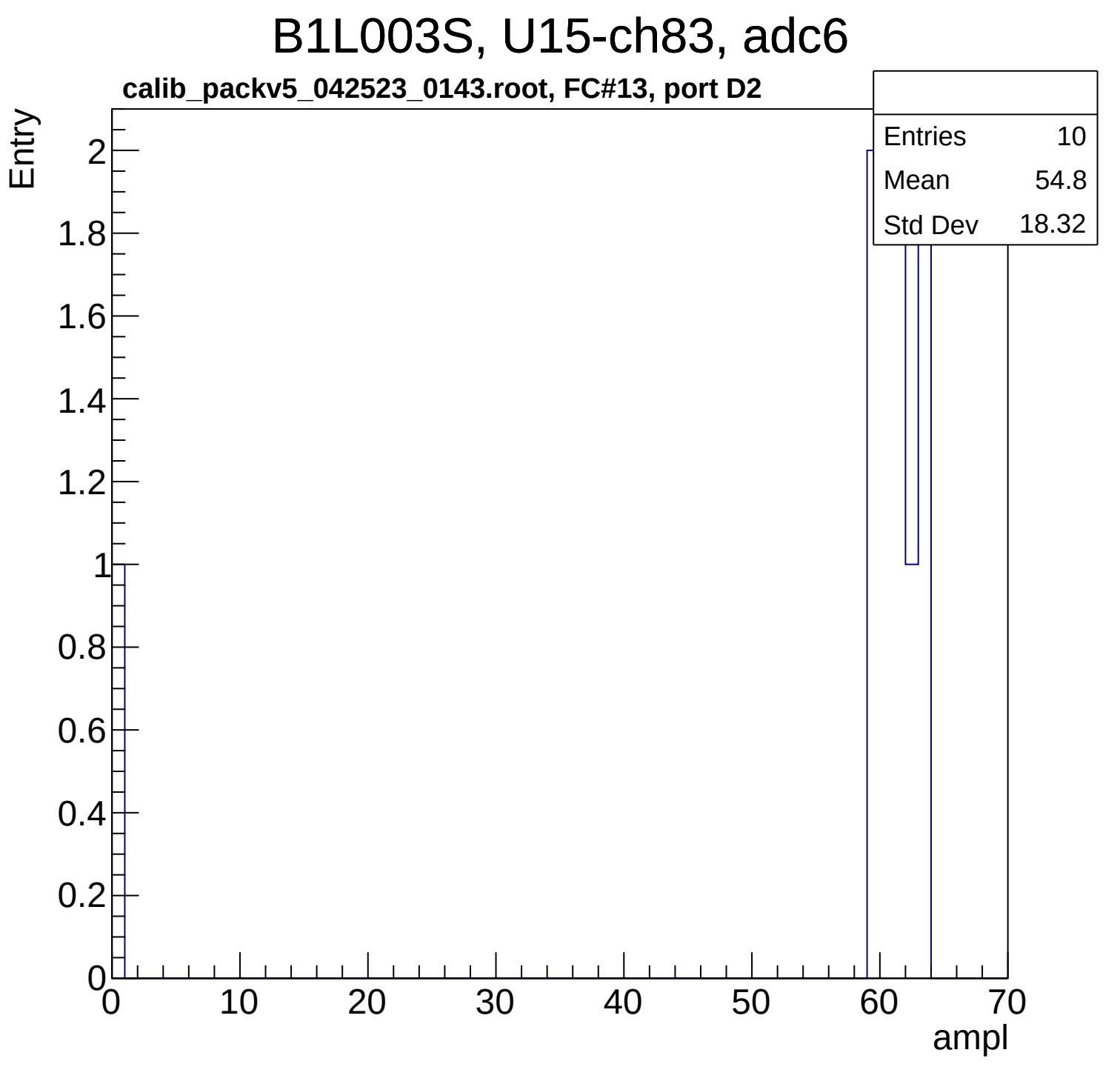
B1L003S, U15-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 54.8
Std Dev 18.32
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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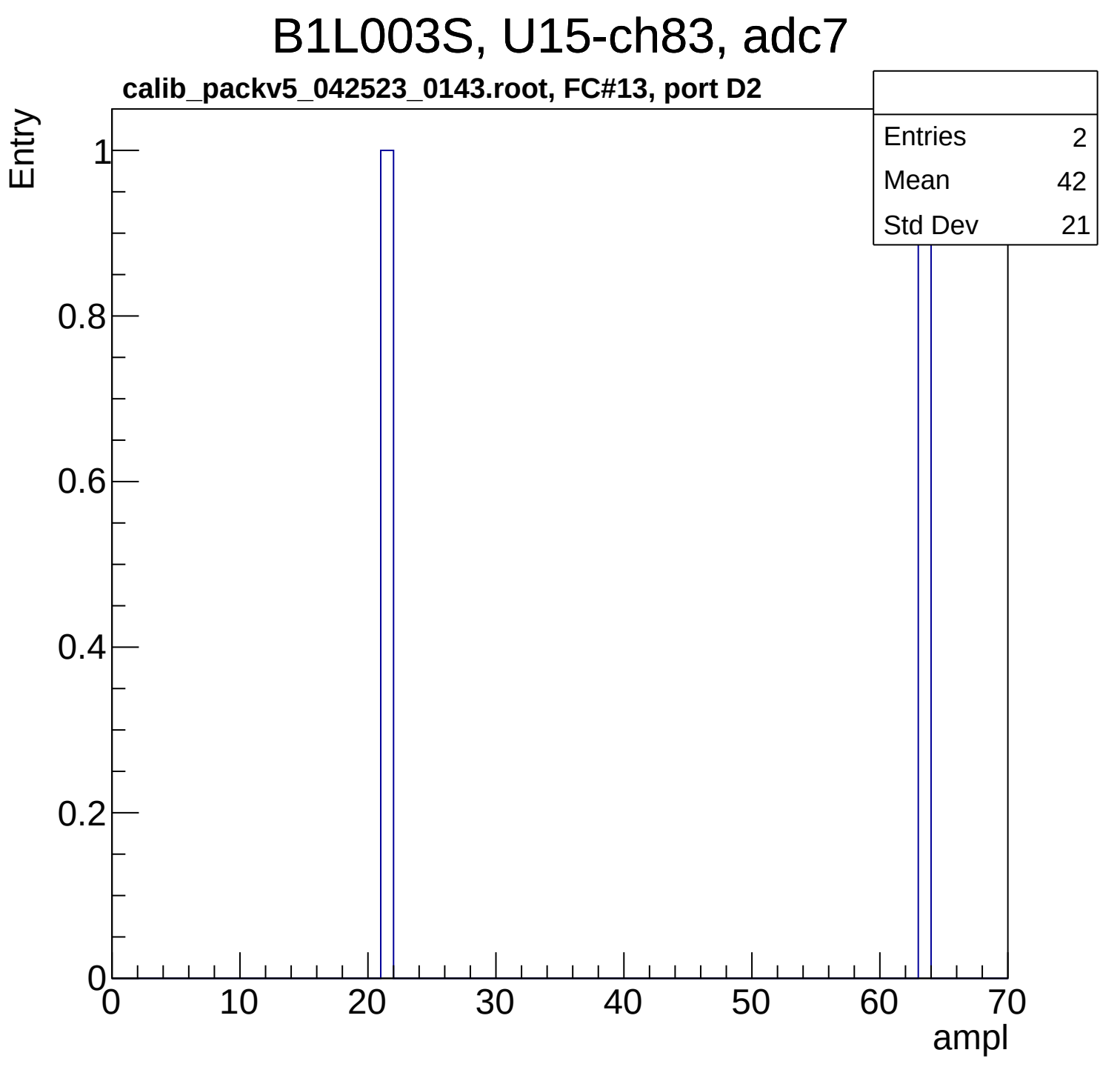
40
50
60
70


B1L003S, U15-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 21
Entry
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0.6
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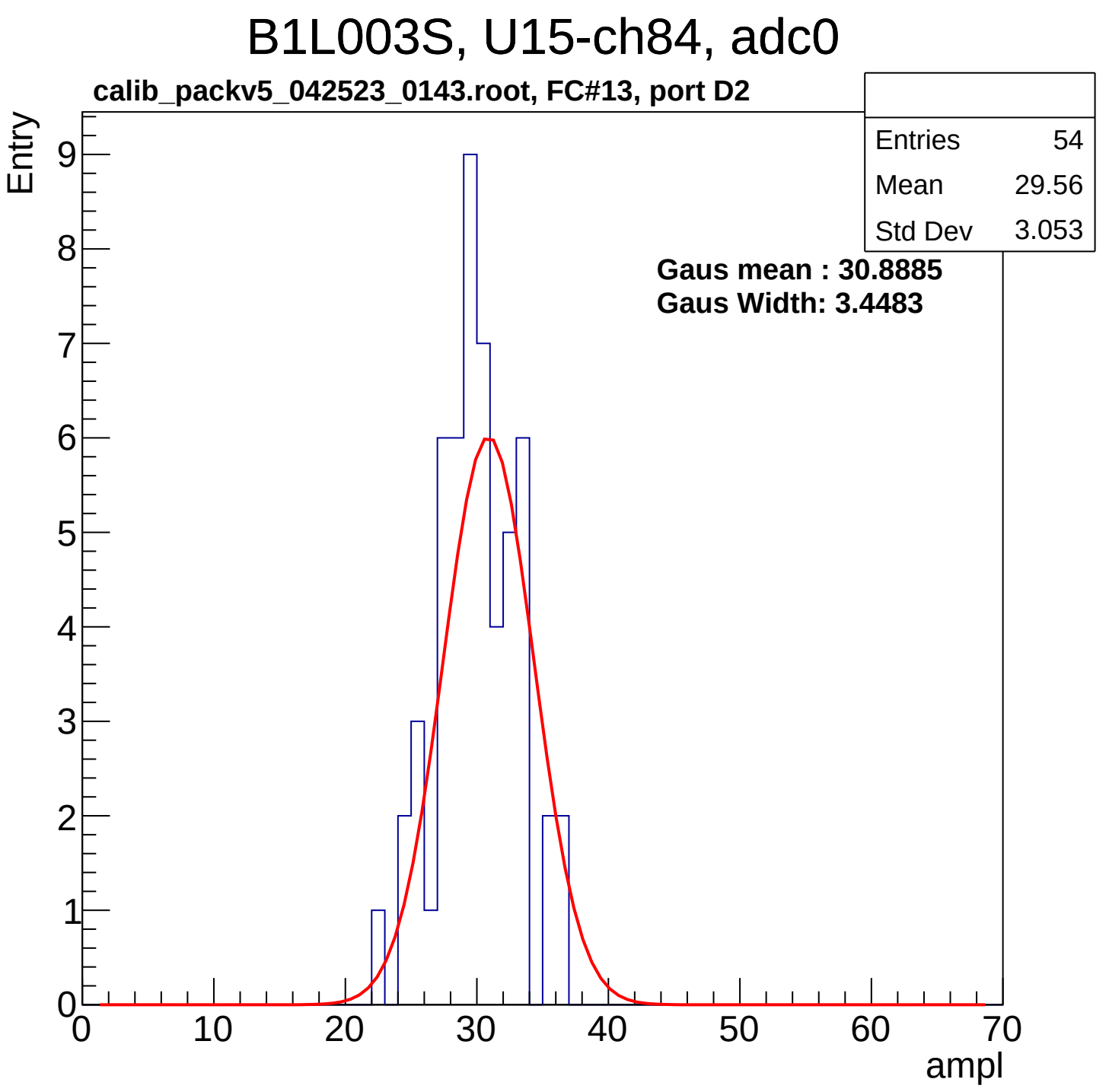
70
ampl


B1L003S, U15-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 29.56
Std Dev 3.053
Gaus mean : 30.8885
Gaus Width: 3.4483
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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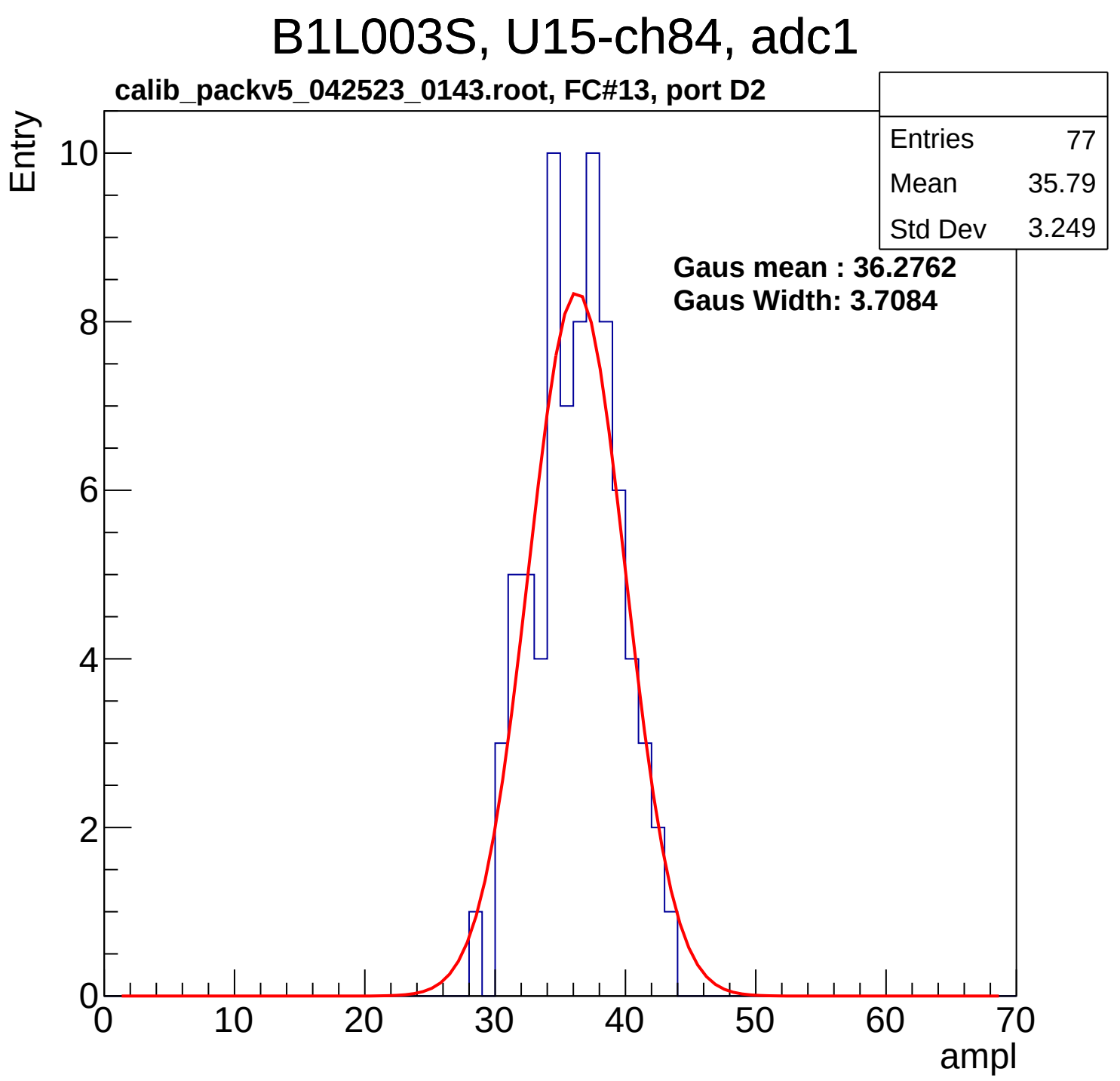
B1L003S, U15-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.79
Std Dev 3.249
Gaus mean : 36.2762
Gaus Width: 3.7084
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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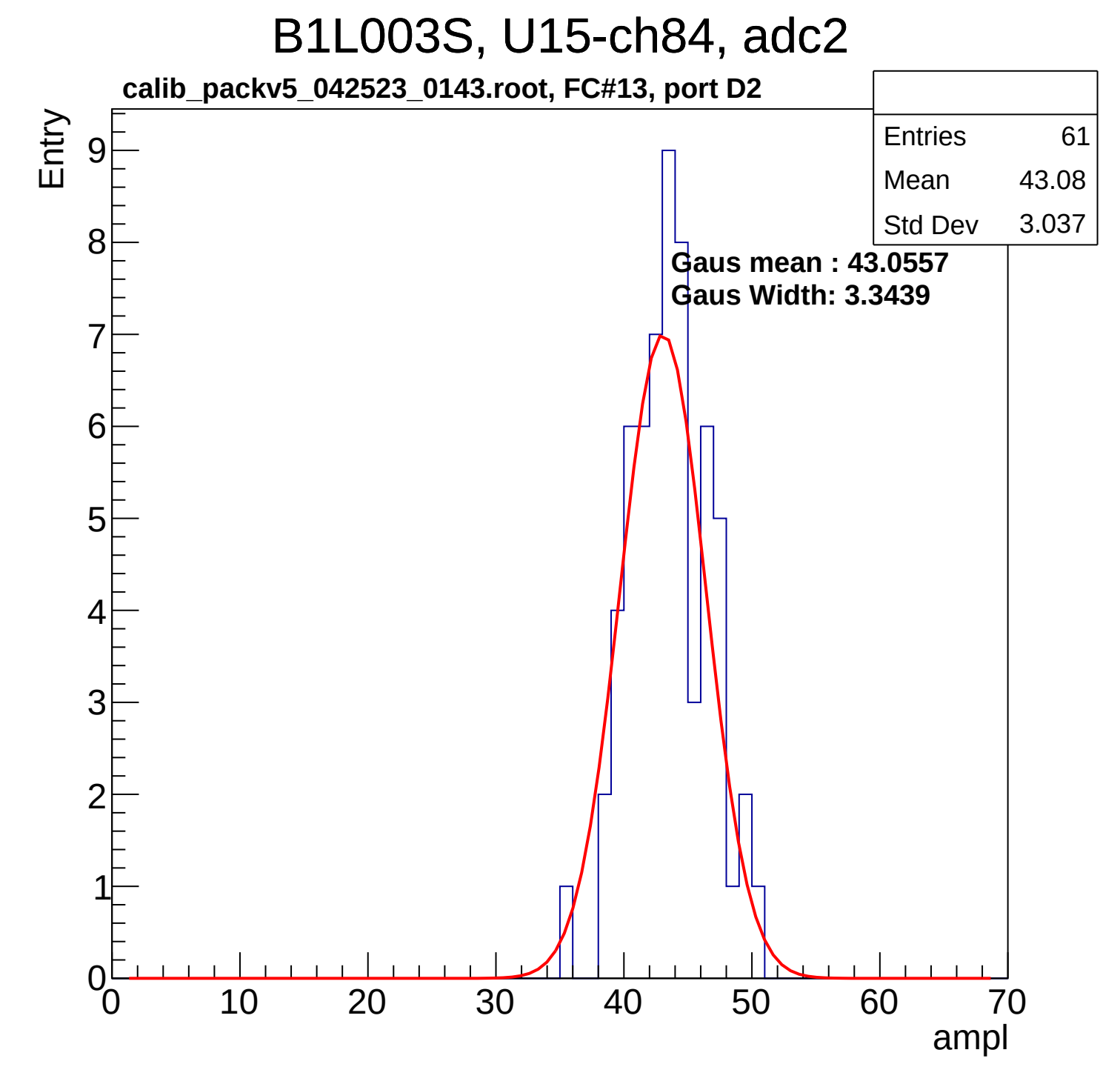
60
70


B1L003S, U15-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43.08
Std Dev 3.037
Gaus mean : 43.0557
Gaus Width: 3.3439
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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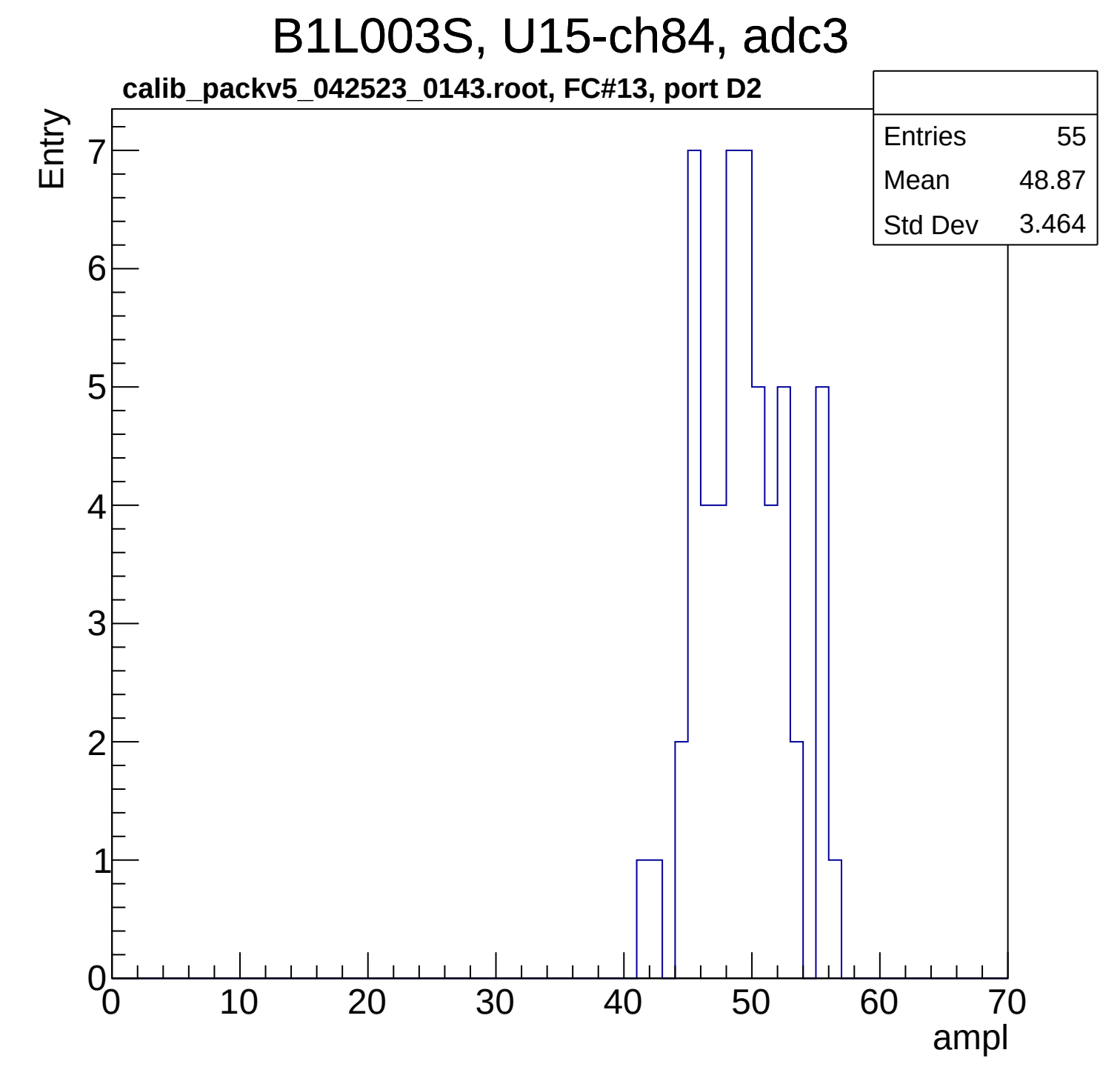
B1L003S, U15-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.87
Std Dev 3.464
Entry
ampl
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0
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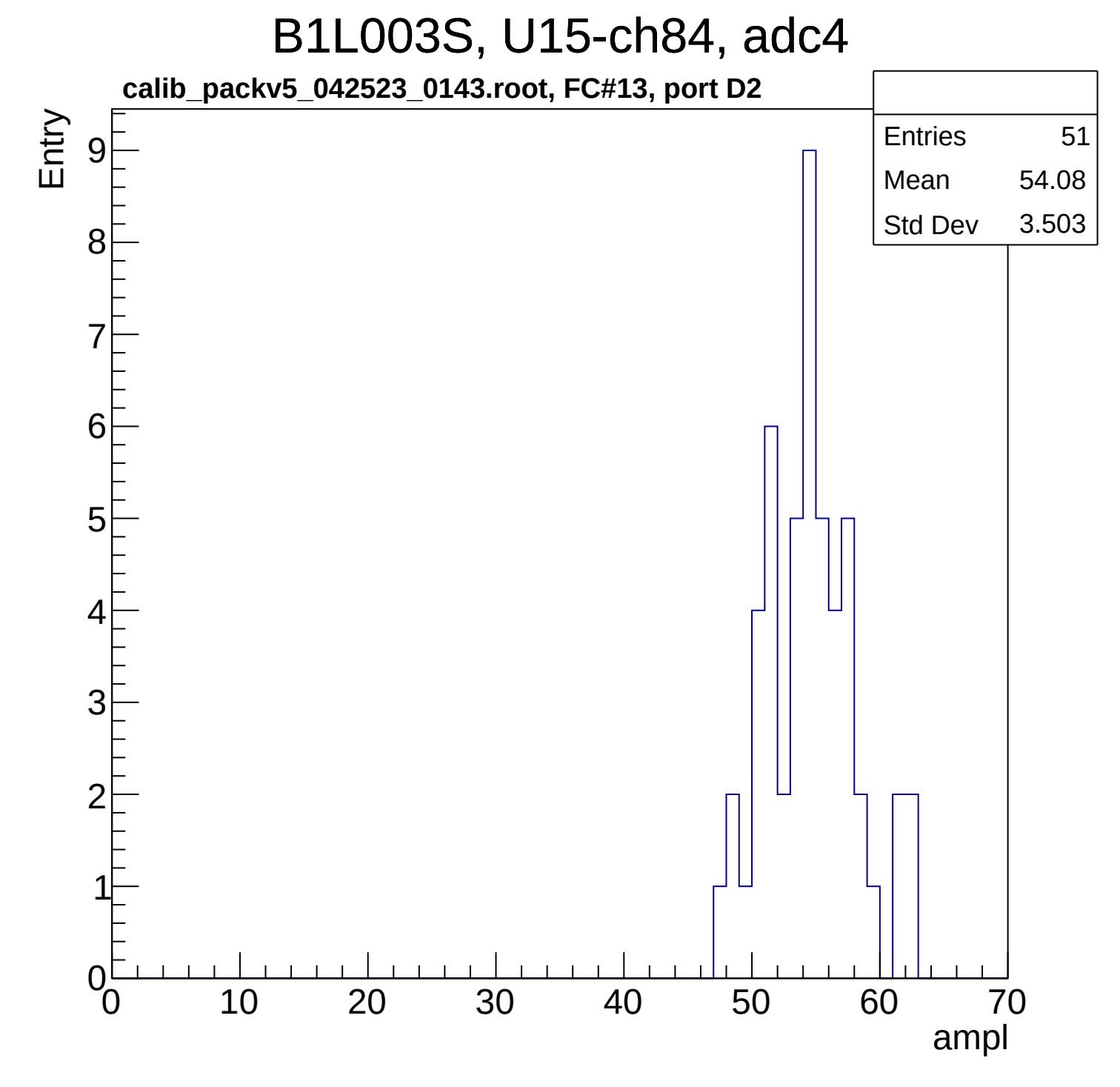
B1L003S, U15-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.08
Std Dev 3.503
Entry
ampl
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3
4
5
6
7
8
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0
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60
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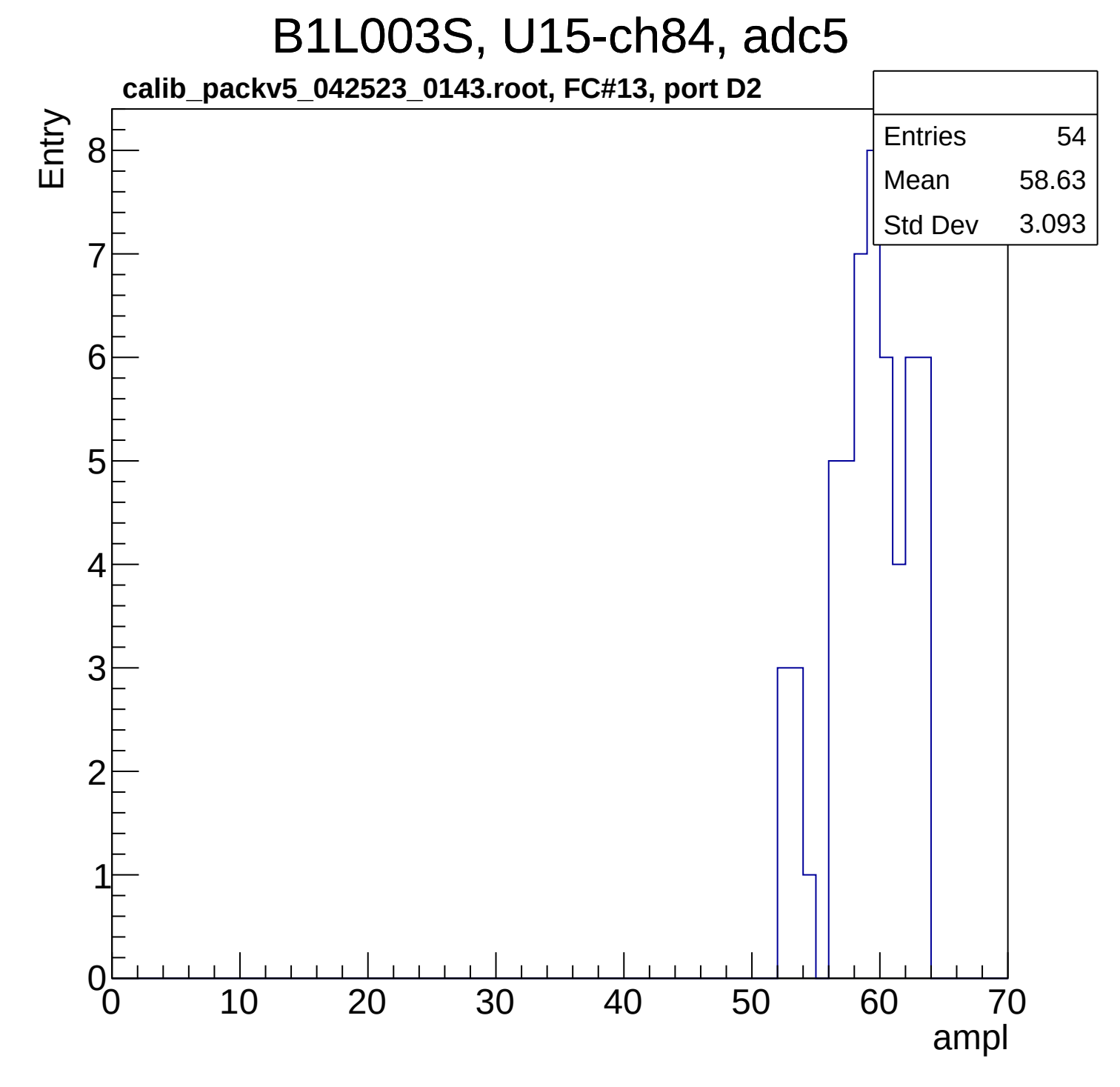
B1L003S, U15-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.63
Std Dev 3.093
Entry
ampl
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2
3
4
5
6
7
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40
50
60
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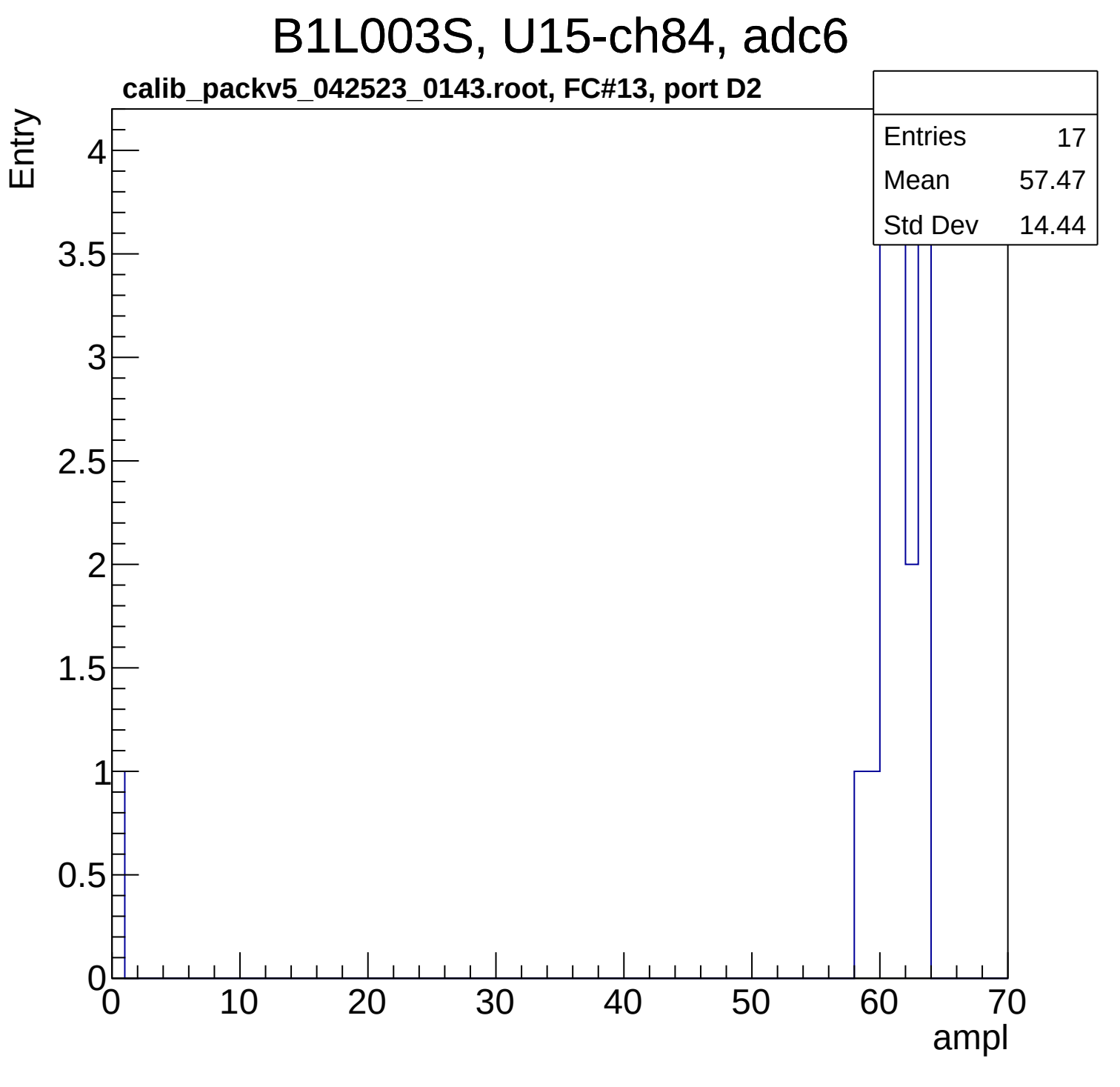
B1L003S, U15-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.47
Std Dev 14.44
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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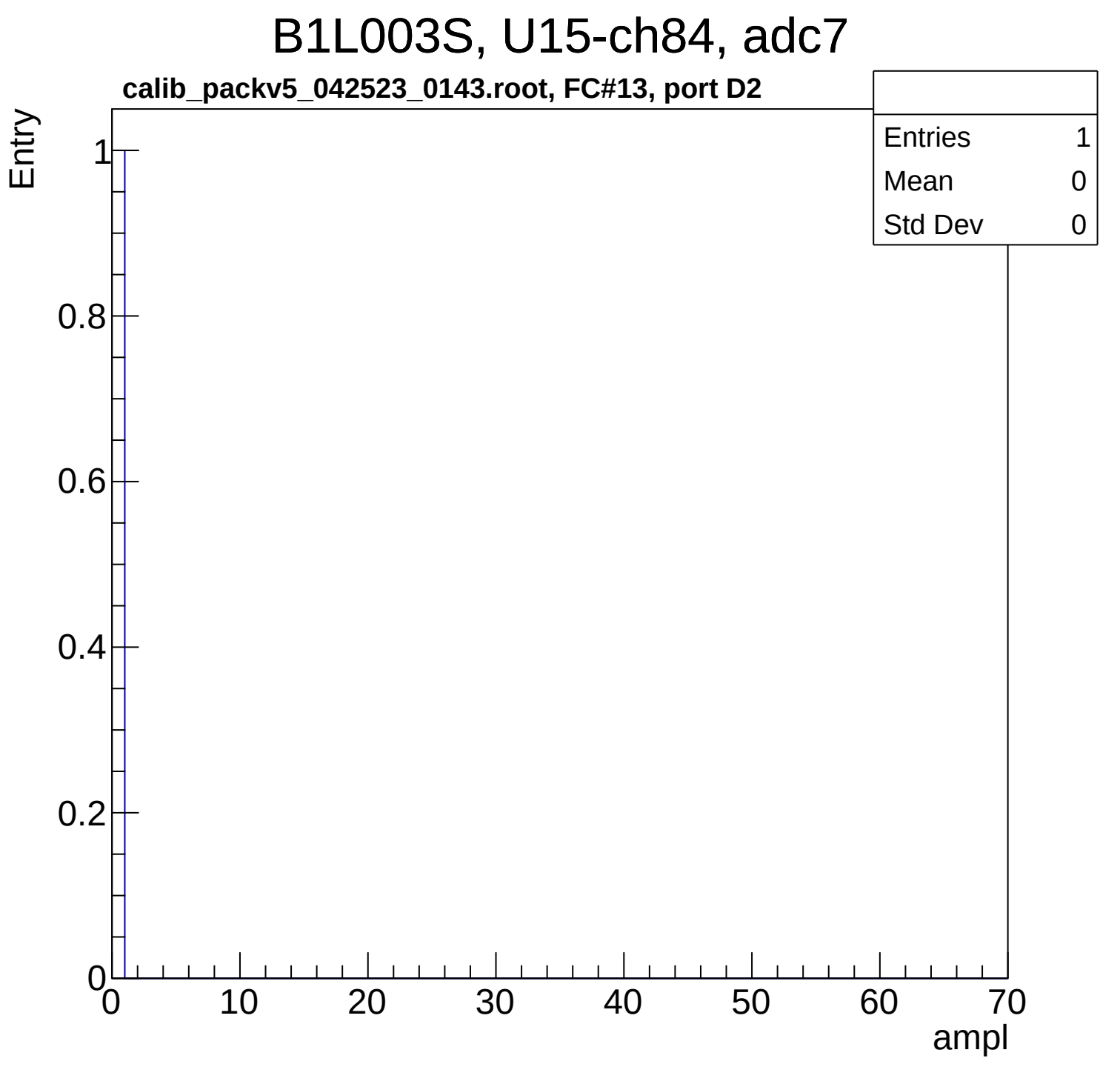
B1L003S, U15-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.4
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10
20
30
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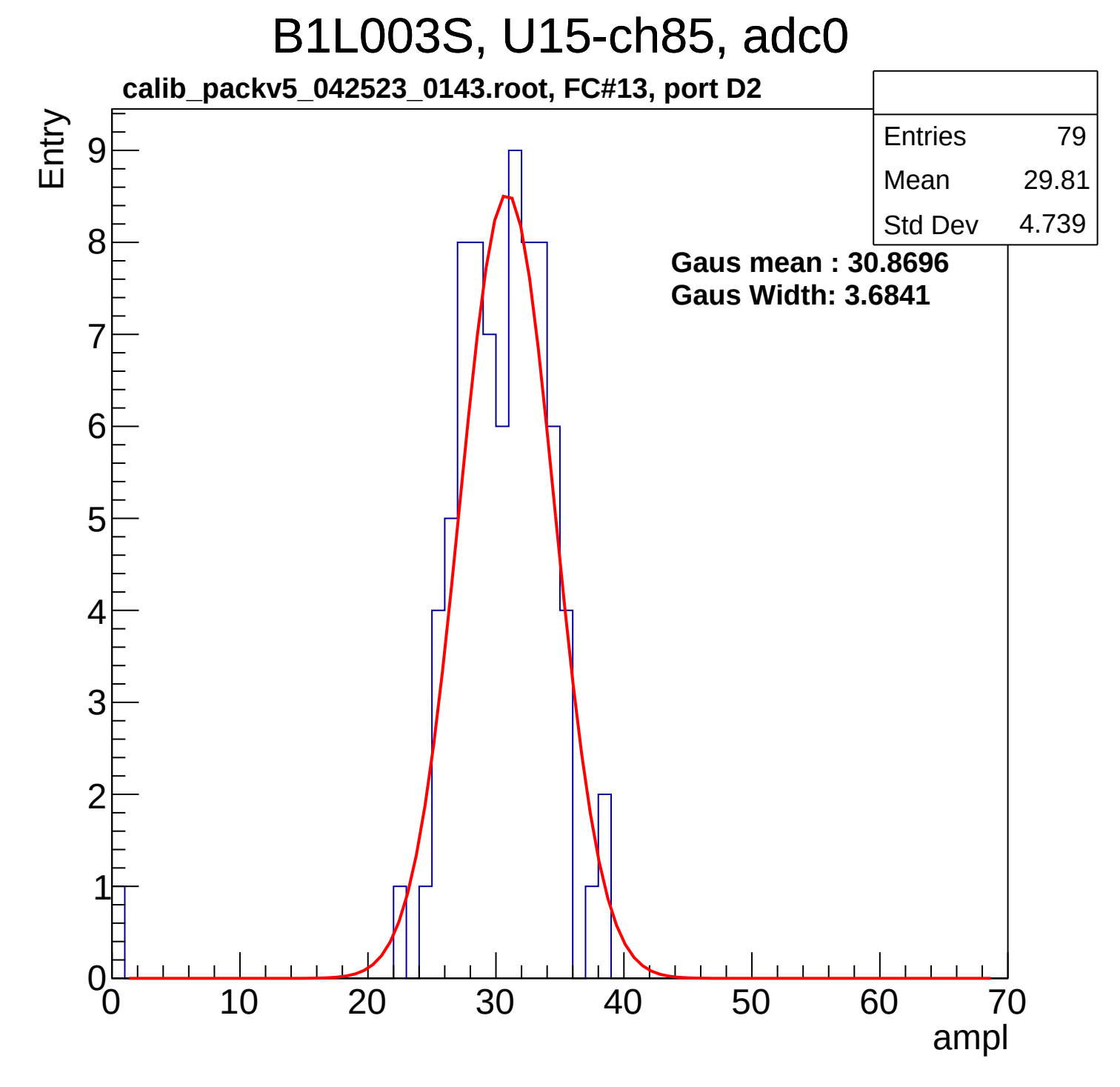
50
60
70
ampl


B1L003S, U15-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 29.81
Std Dev 4.739
Gaus mean : 30.8696
Gaus Width: 3.6841
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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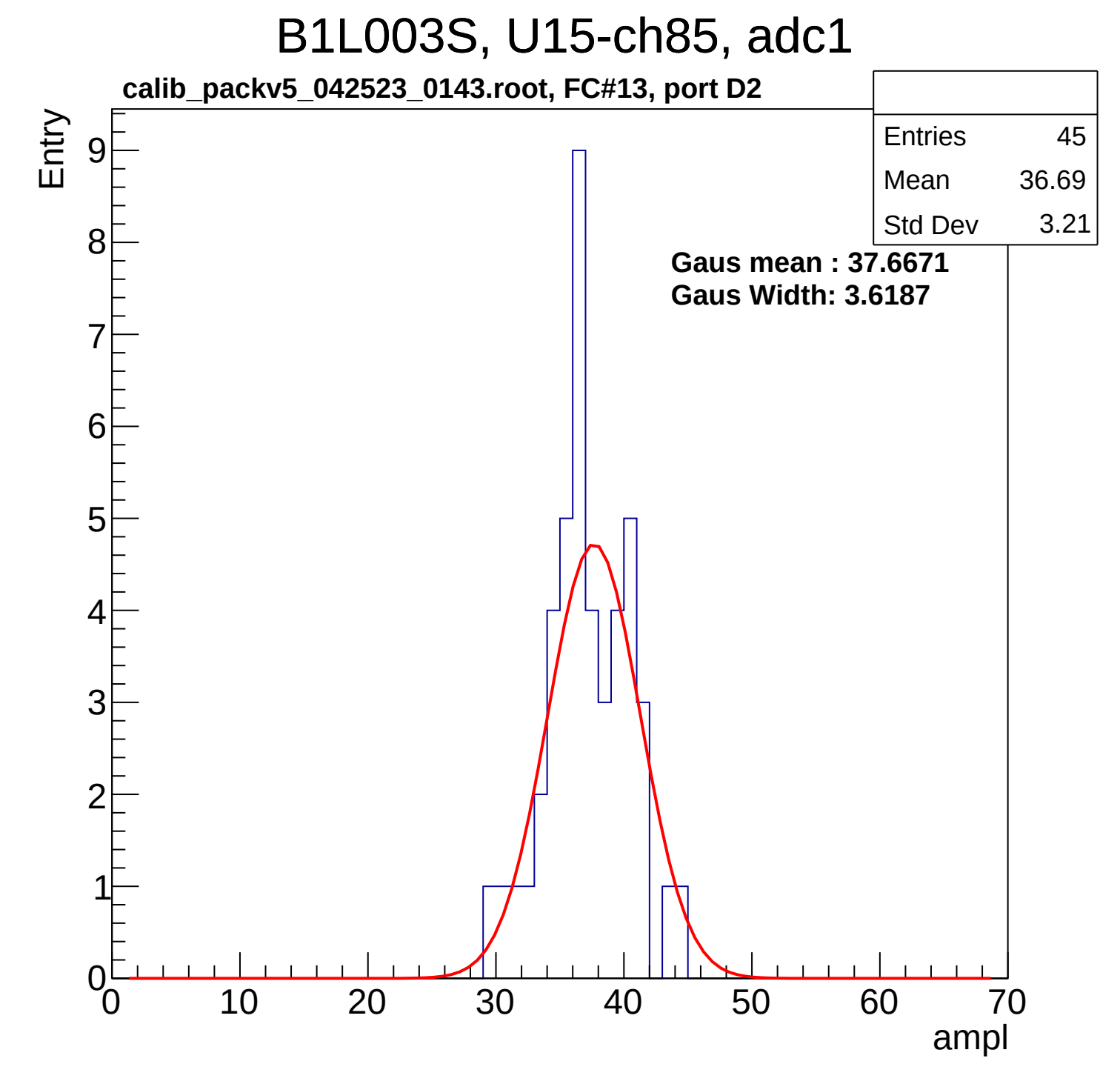
B1L003S, U15-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 36.69
Std Dev 3.21
Gaus mean : 37.6671
Gaus Width: 3.6187
Entry
ampl
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2
3
4
5
6
7
8
9
0
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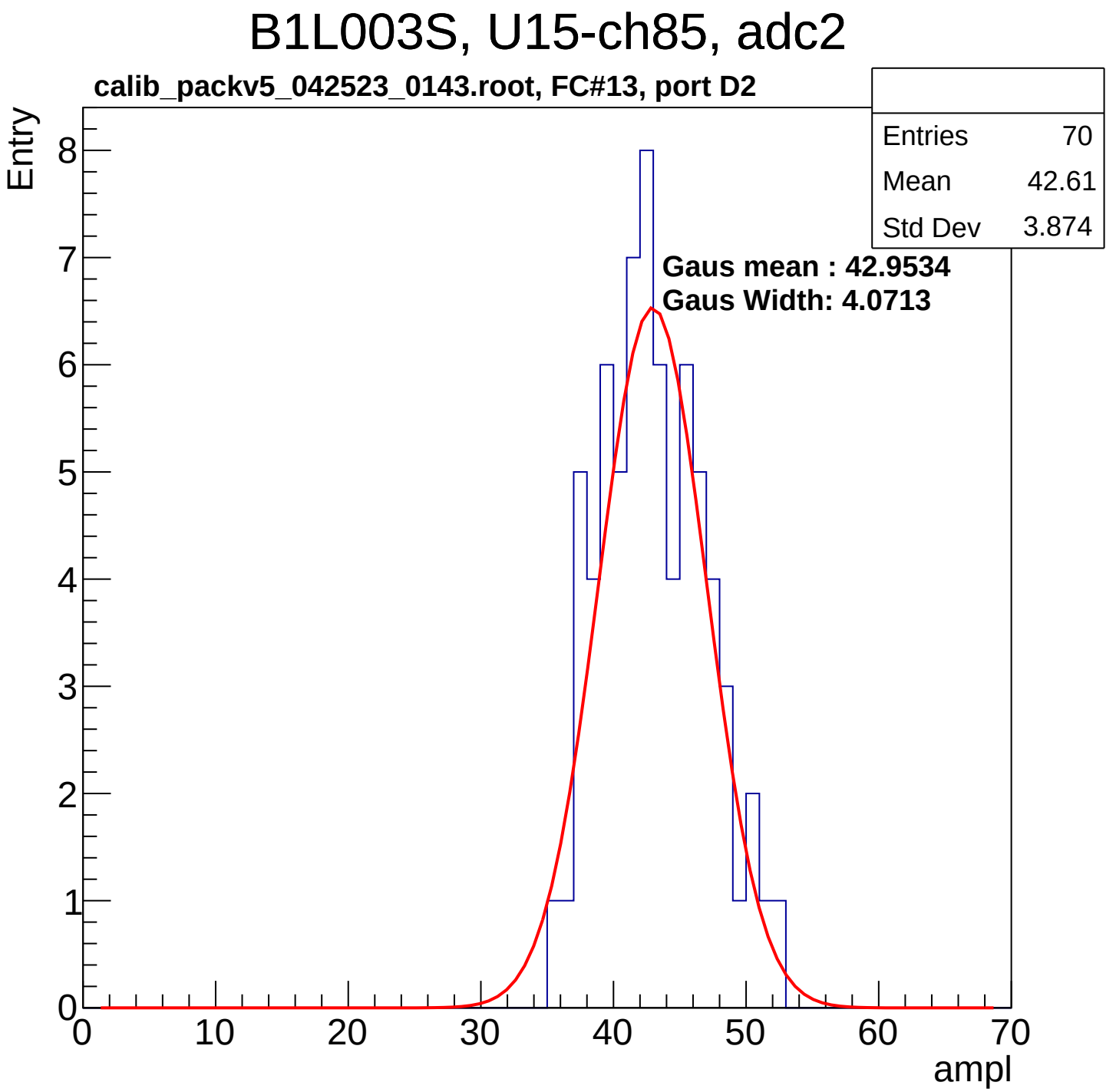
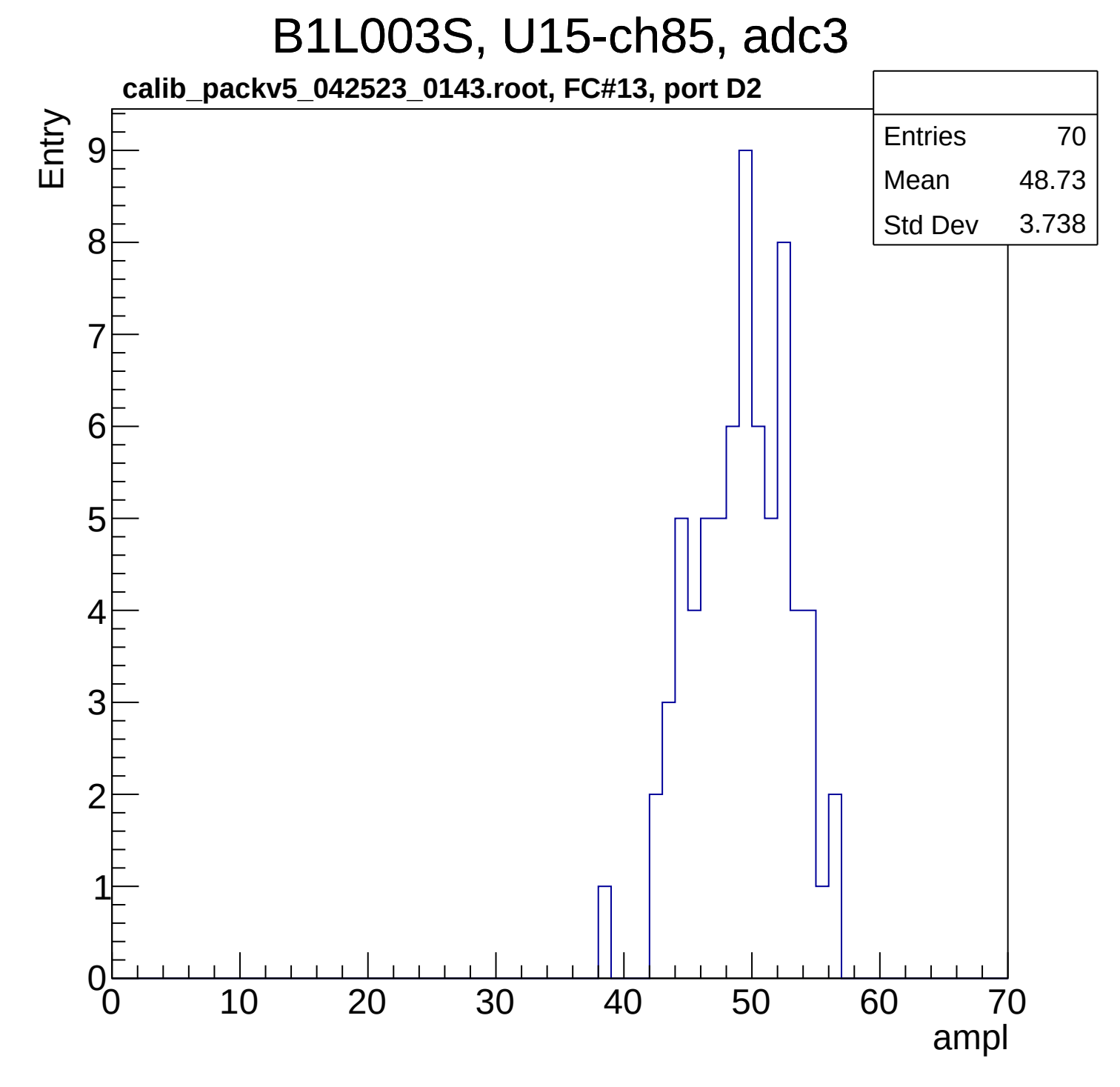

B1L003S, U15-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.61
Std Dev 3.874
Gaus mean : 42.9534
Gaus Width: 4.0713
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.73
Std Dev 3.738
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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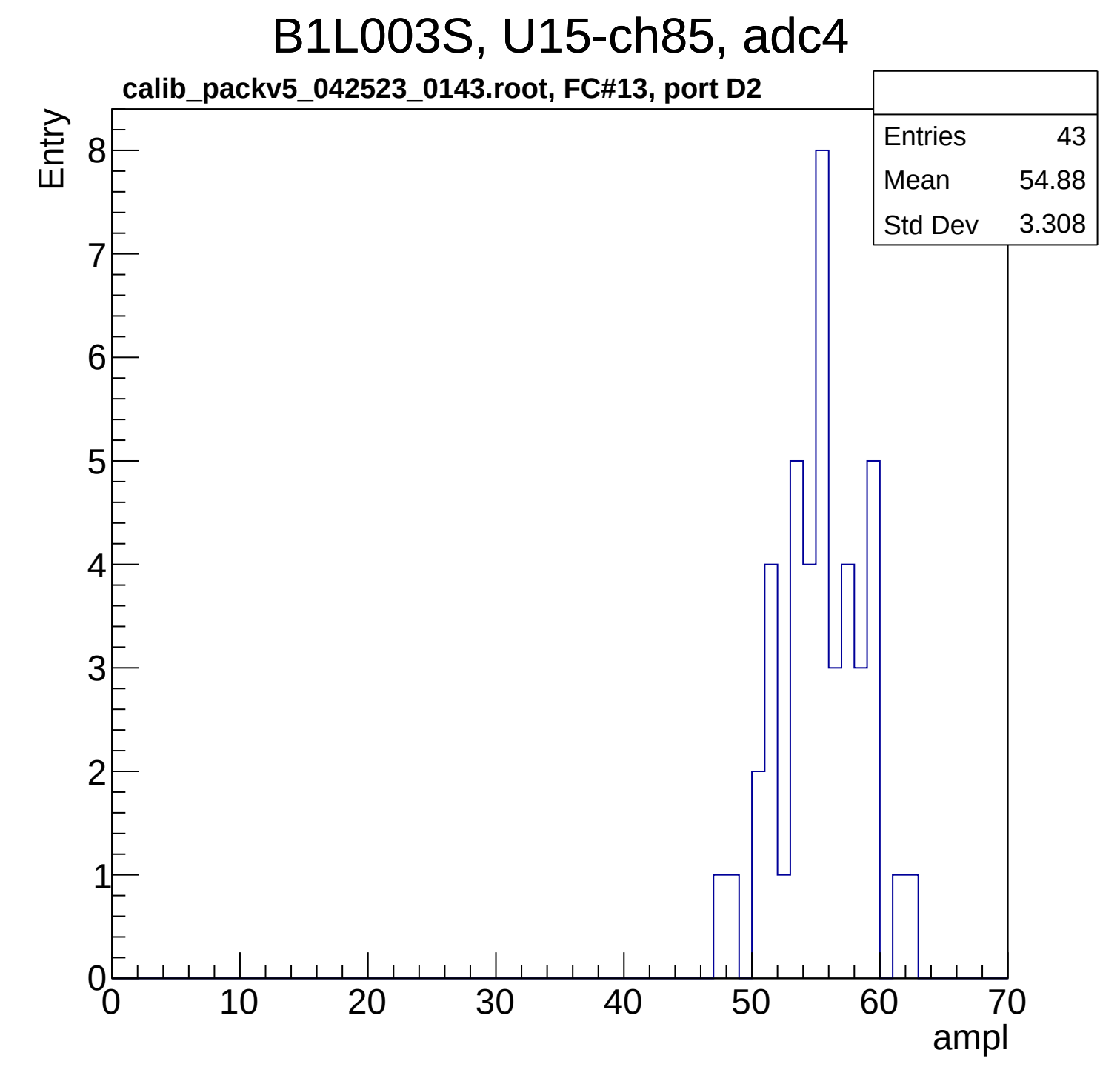
B1L003S, U15-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 54.88
Std Dev 3.308
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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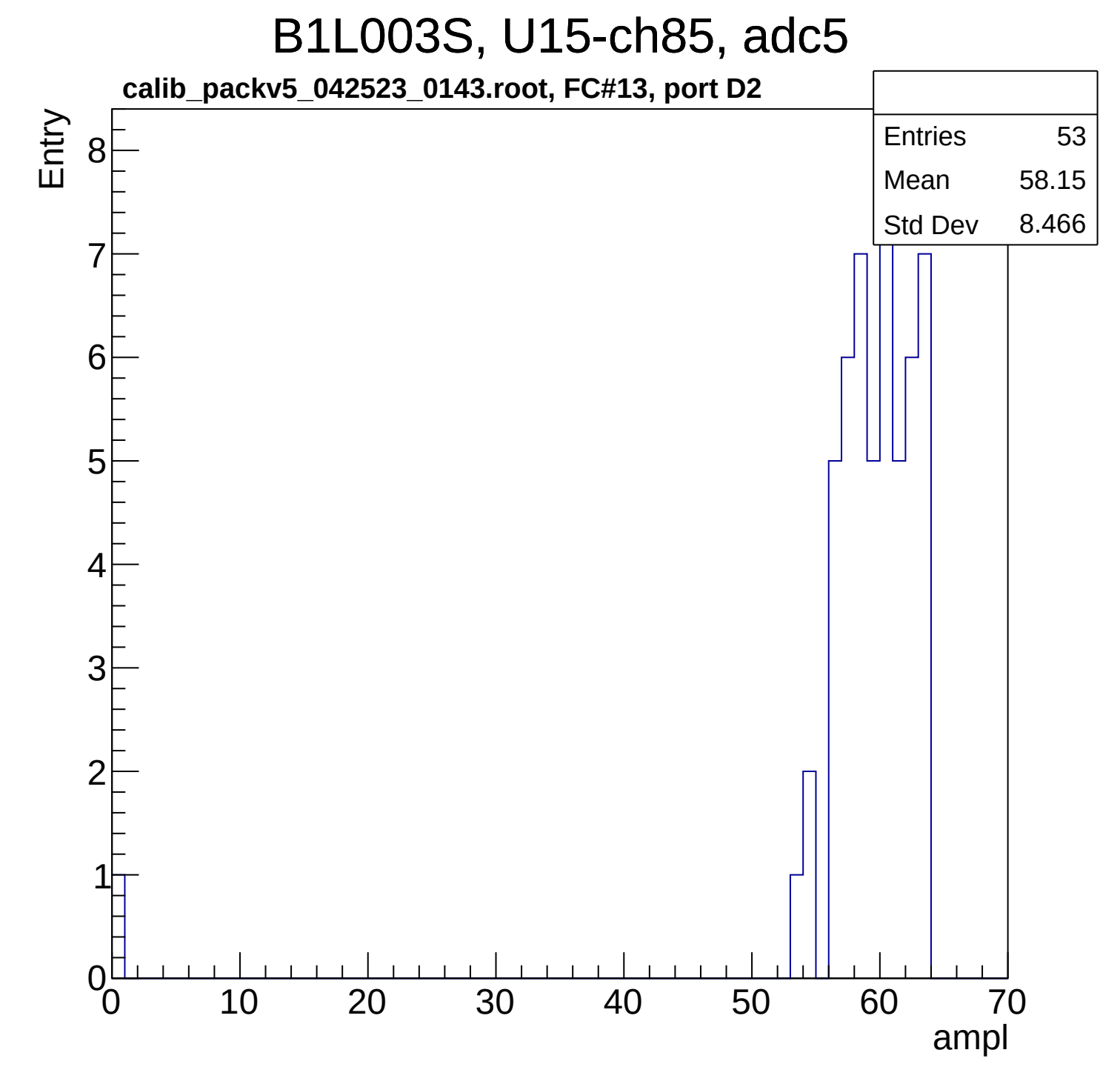
B1L003S, U15-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.15
Std Dev 8.466
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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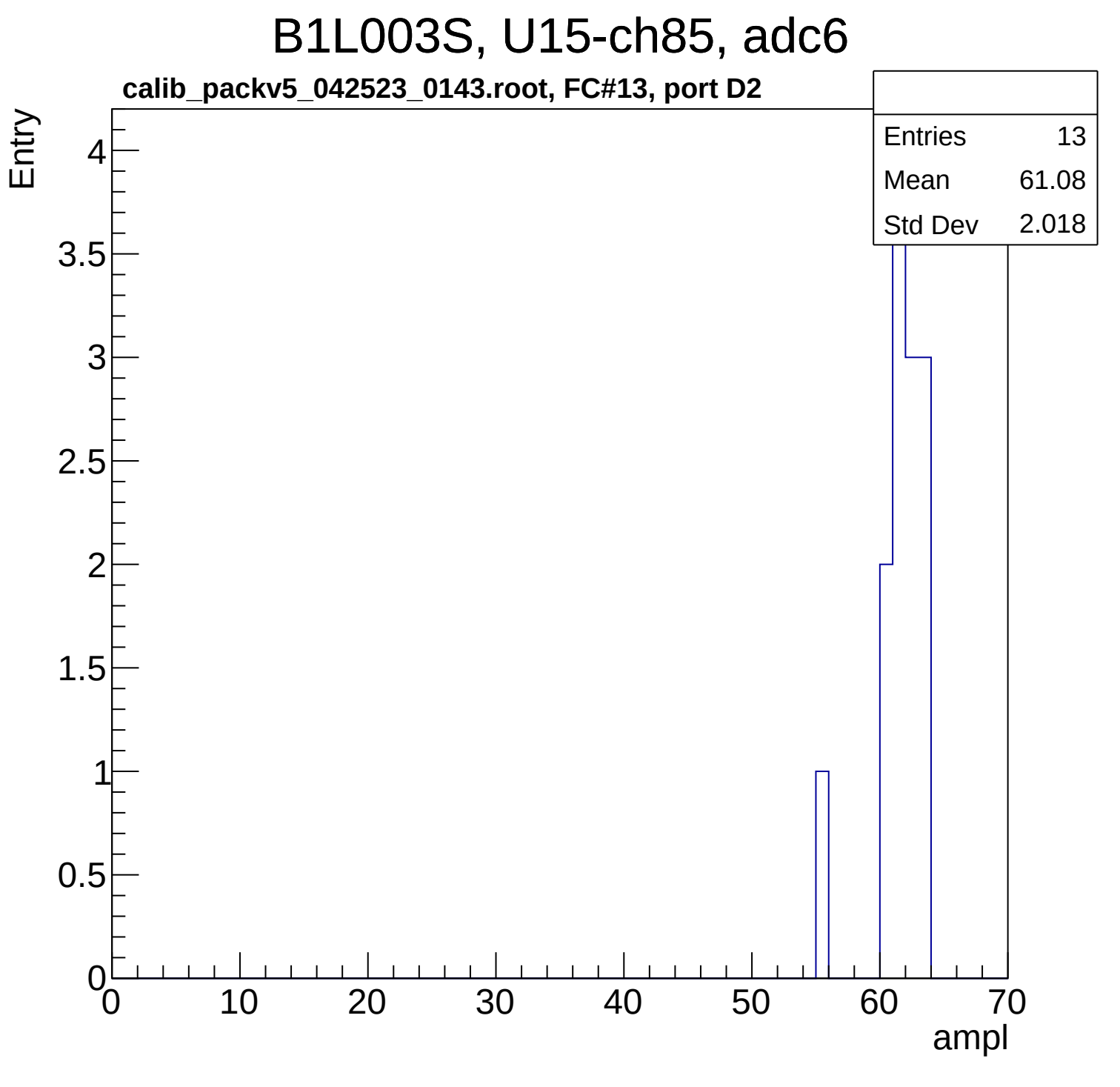
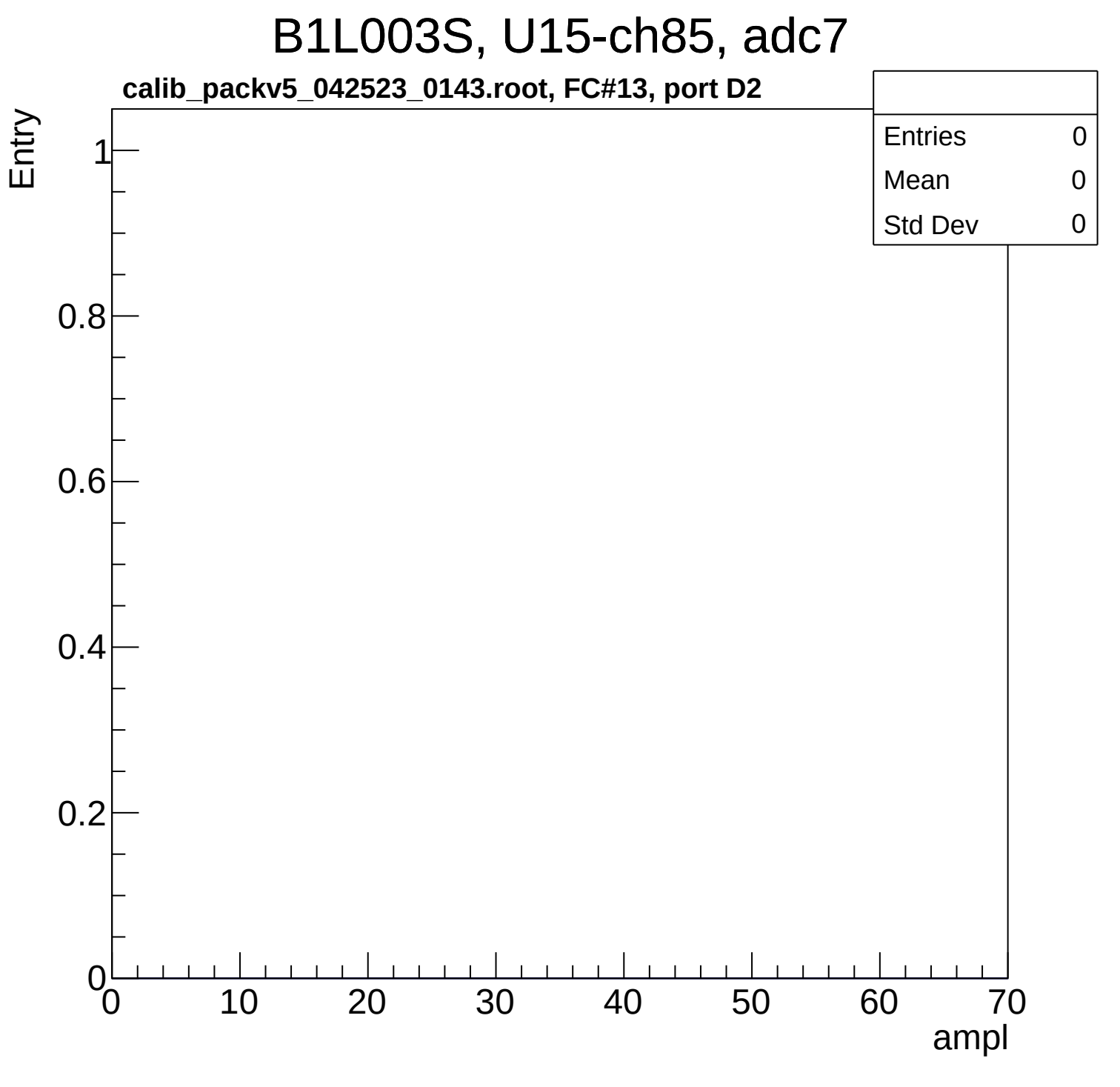
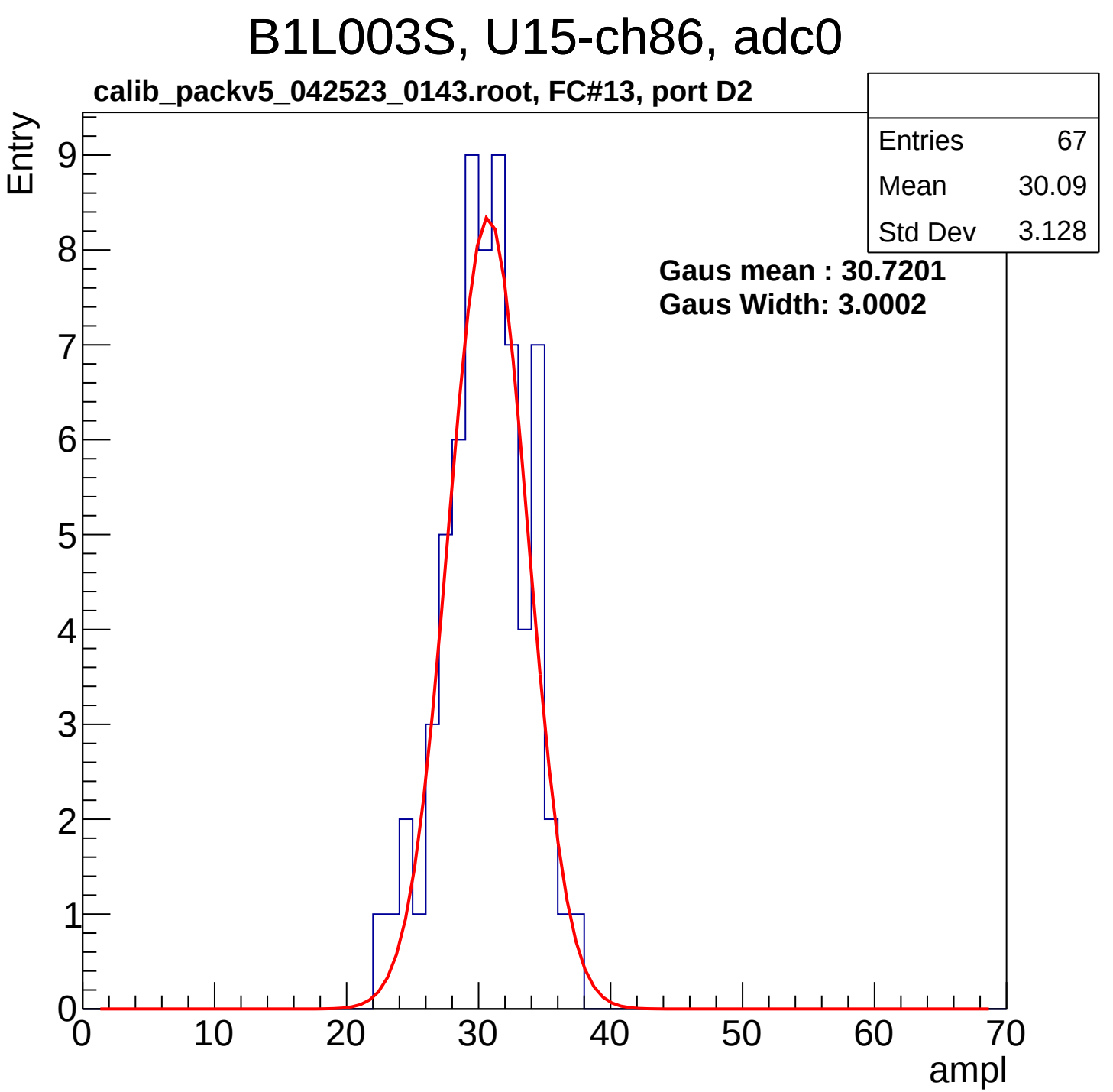
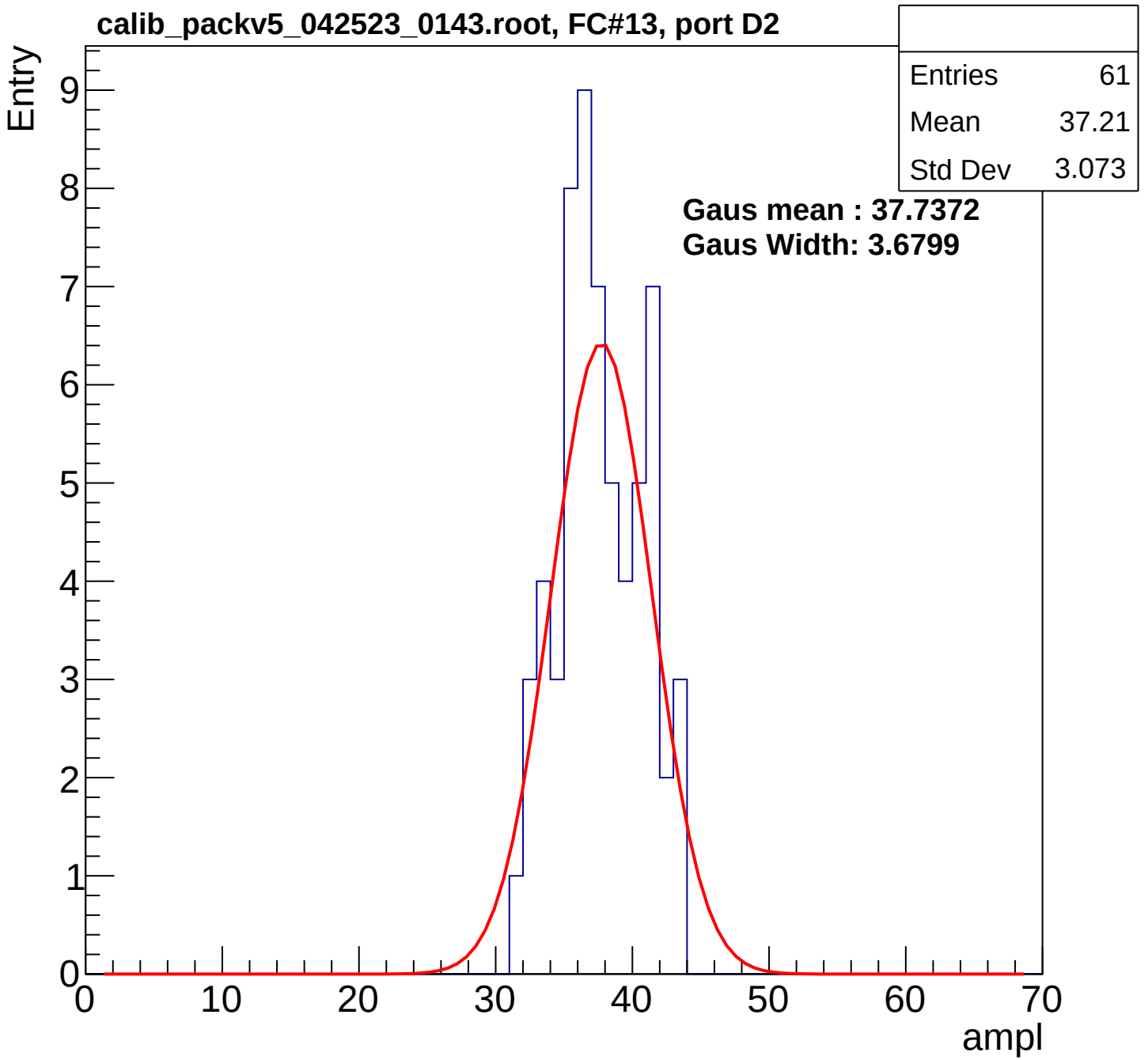

B1L003S, U15-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.08
Std Dev 2.018
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.09
Std Dev 3.128
Gaus mean : 30.7201
Gaus Width: 3.0002
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 37.21
Std Dev 3.073
Gaus mean : 37.7372
Gaus Width: 3.6799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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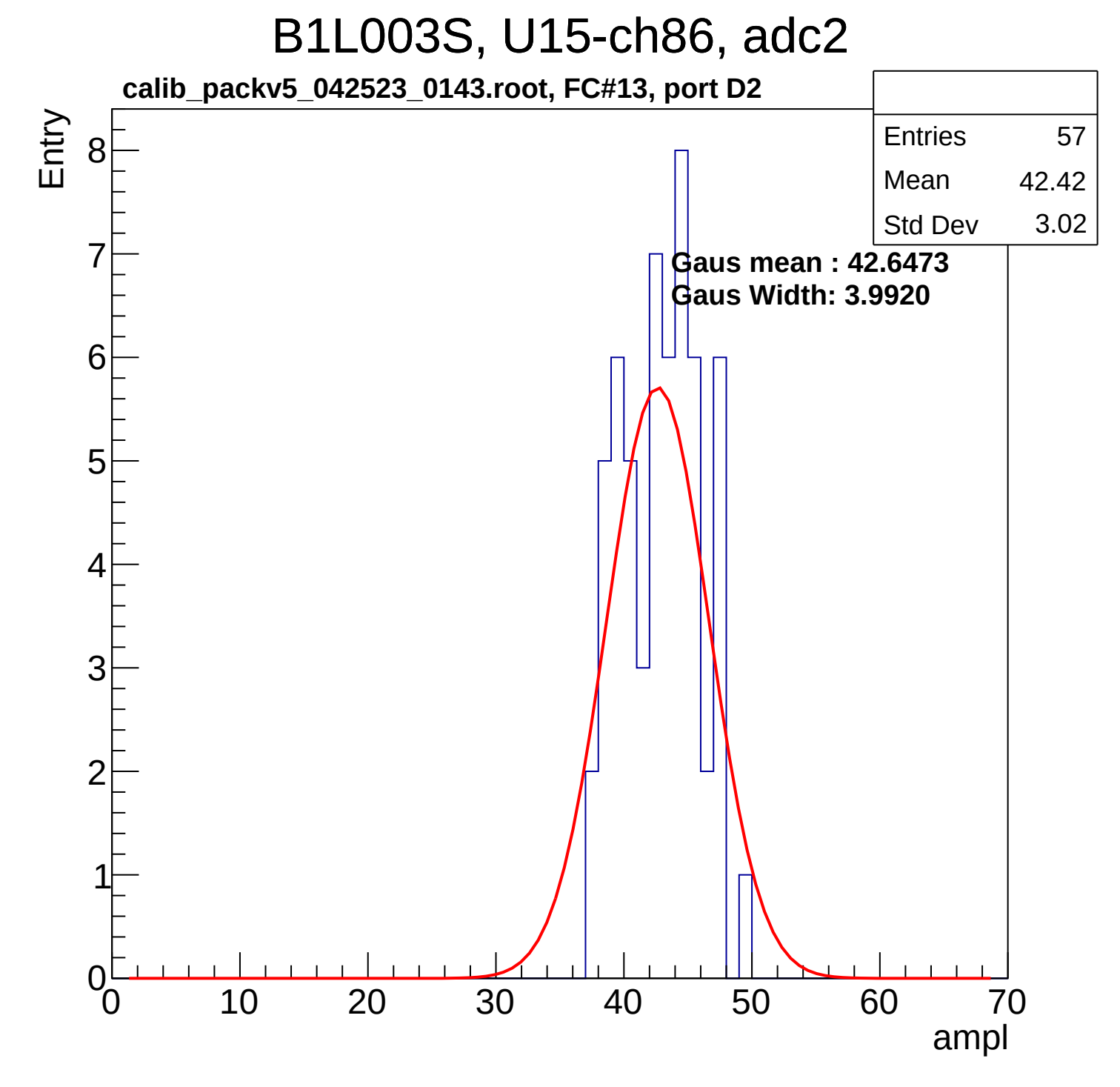
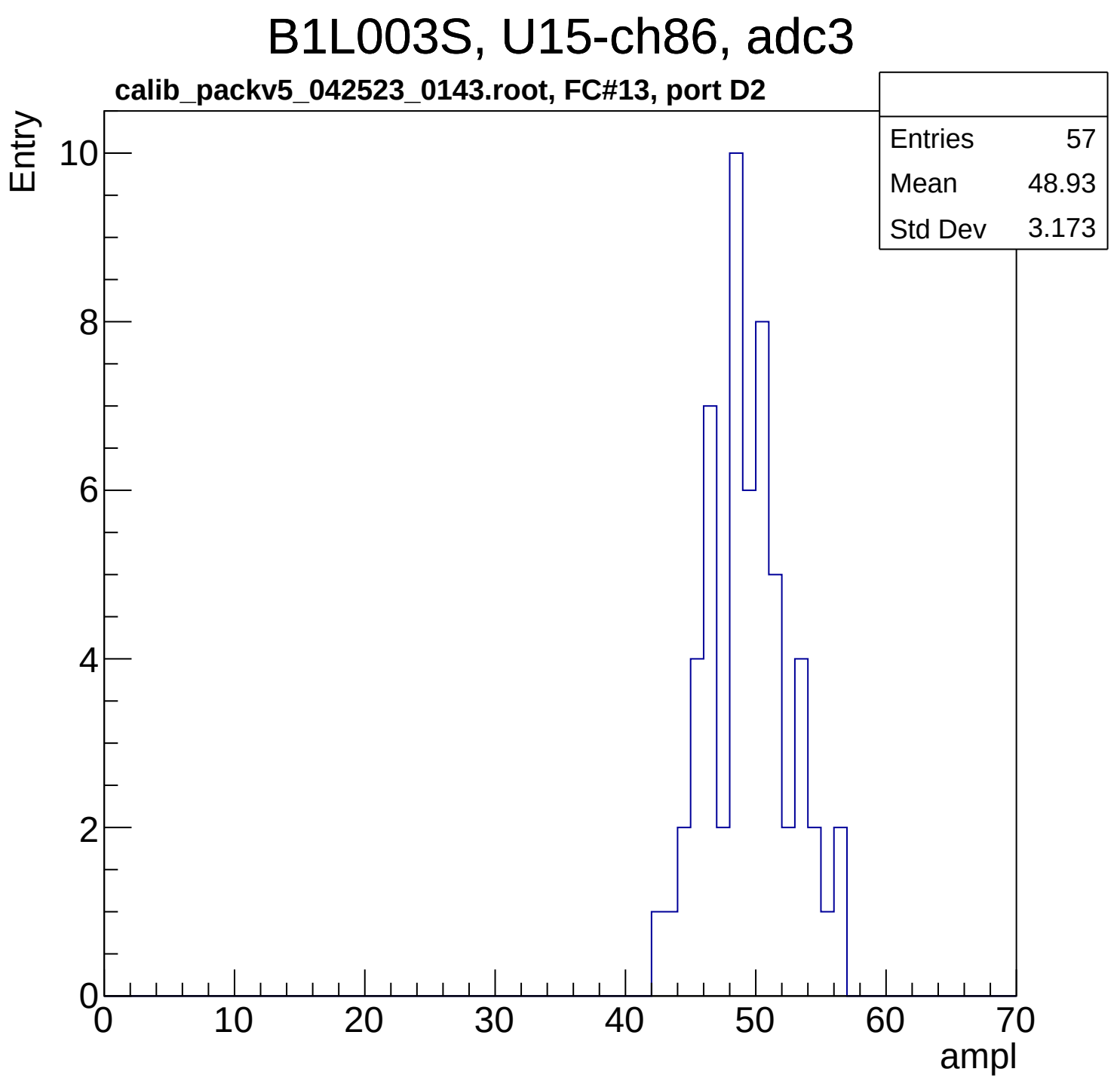
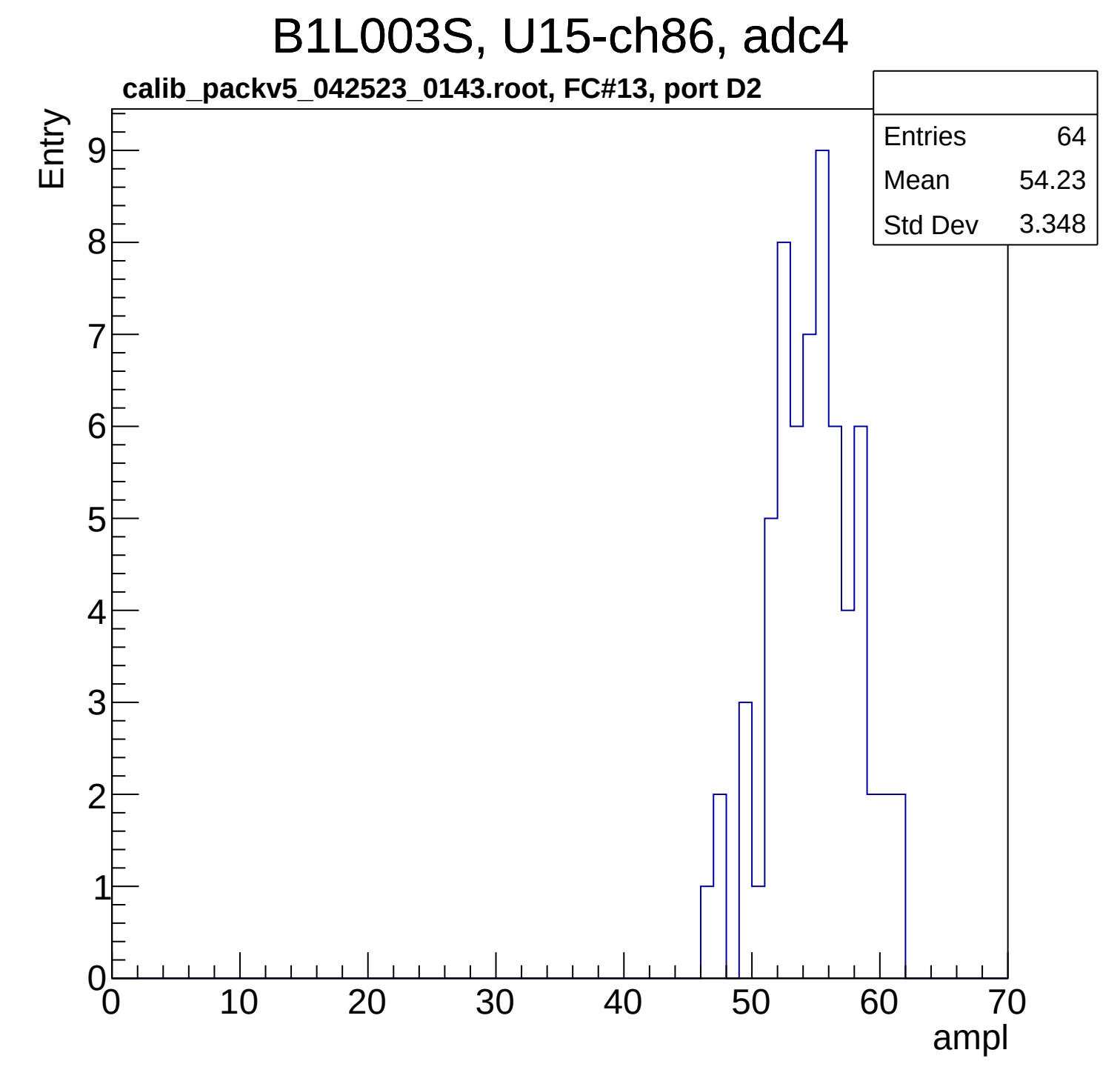
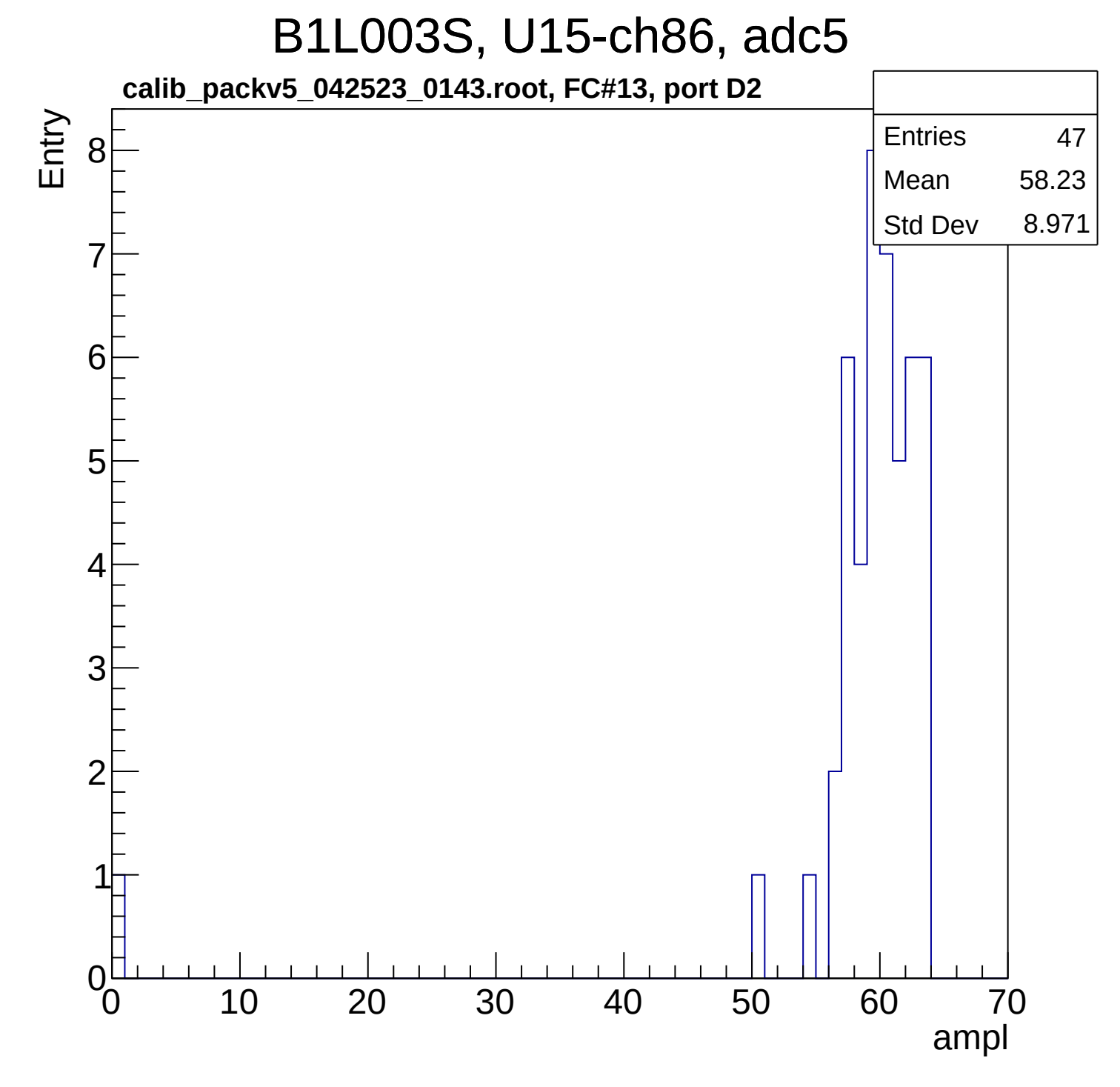

B1L003S, U15-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 42.42
Std Dev 3.02
Gaus mean : 42.6473
Gaus Width: 3.9920
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.93
Std Dev 3.173
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.23
Std Dev 3.348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.23
Std Dev 8.971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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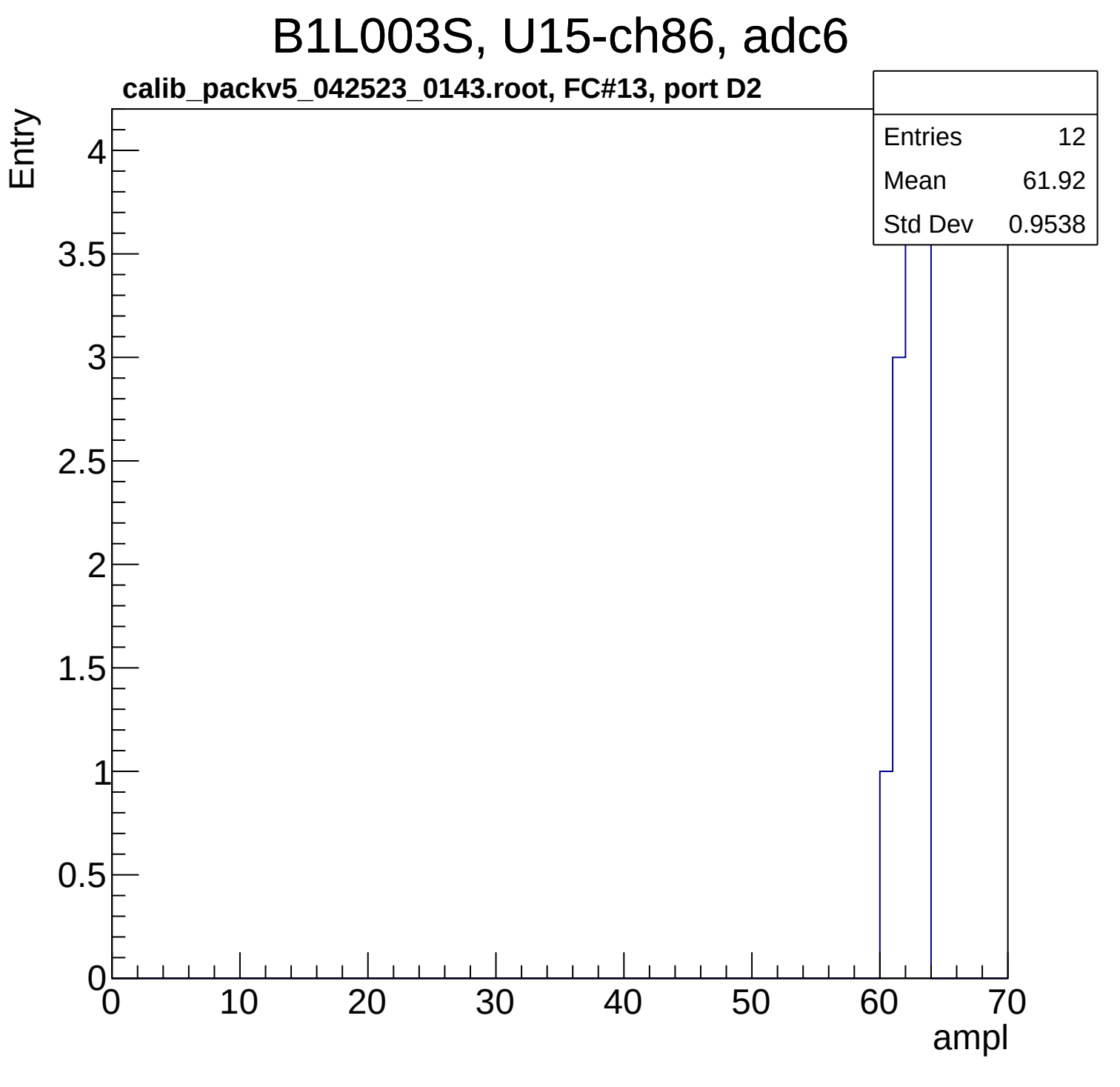
B1L003S, U15-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 61.92
Std Dev 0.9538
Entry
ampl
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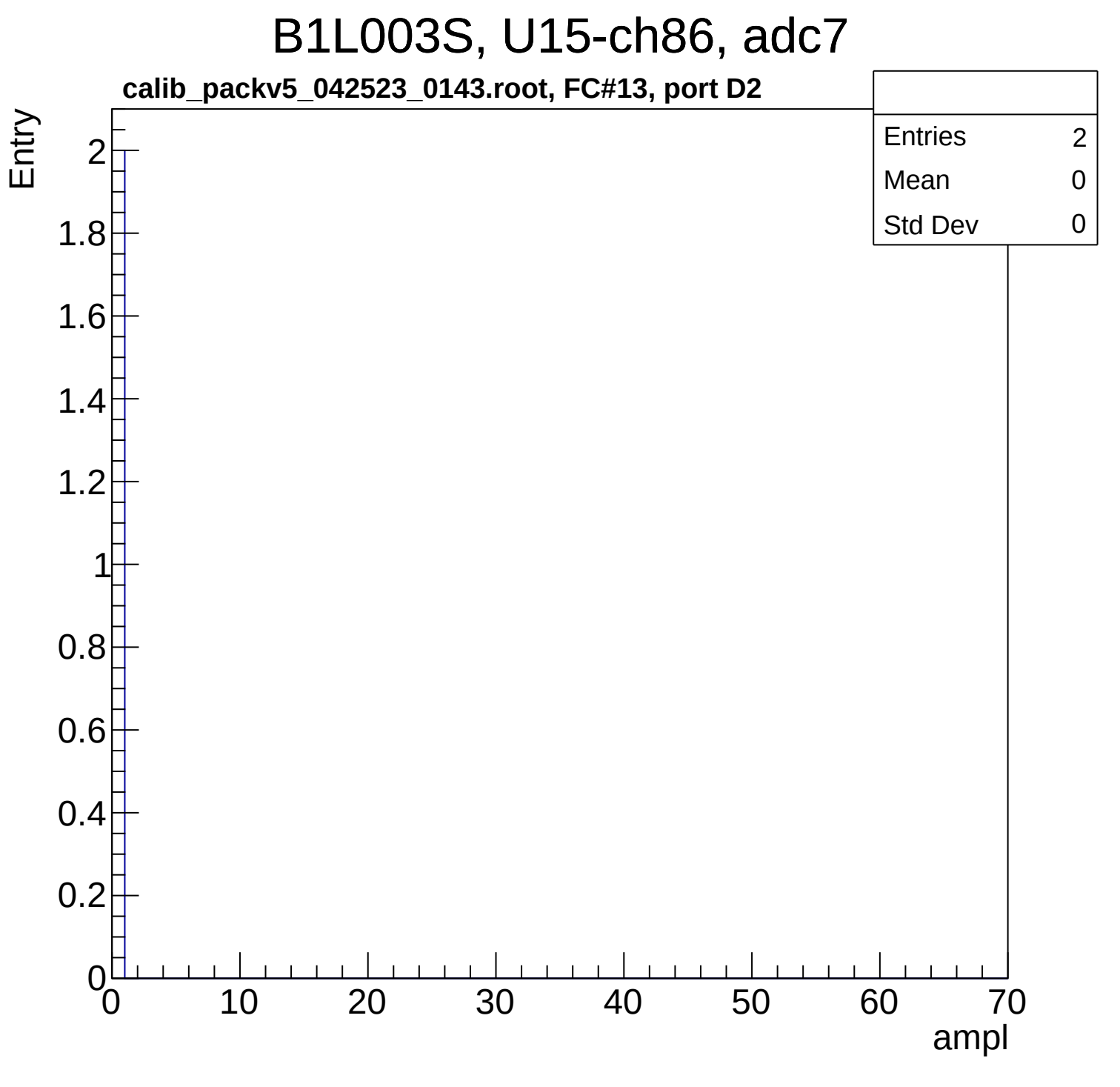
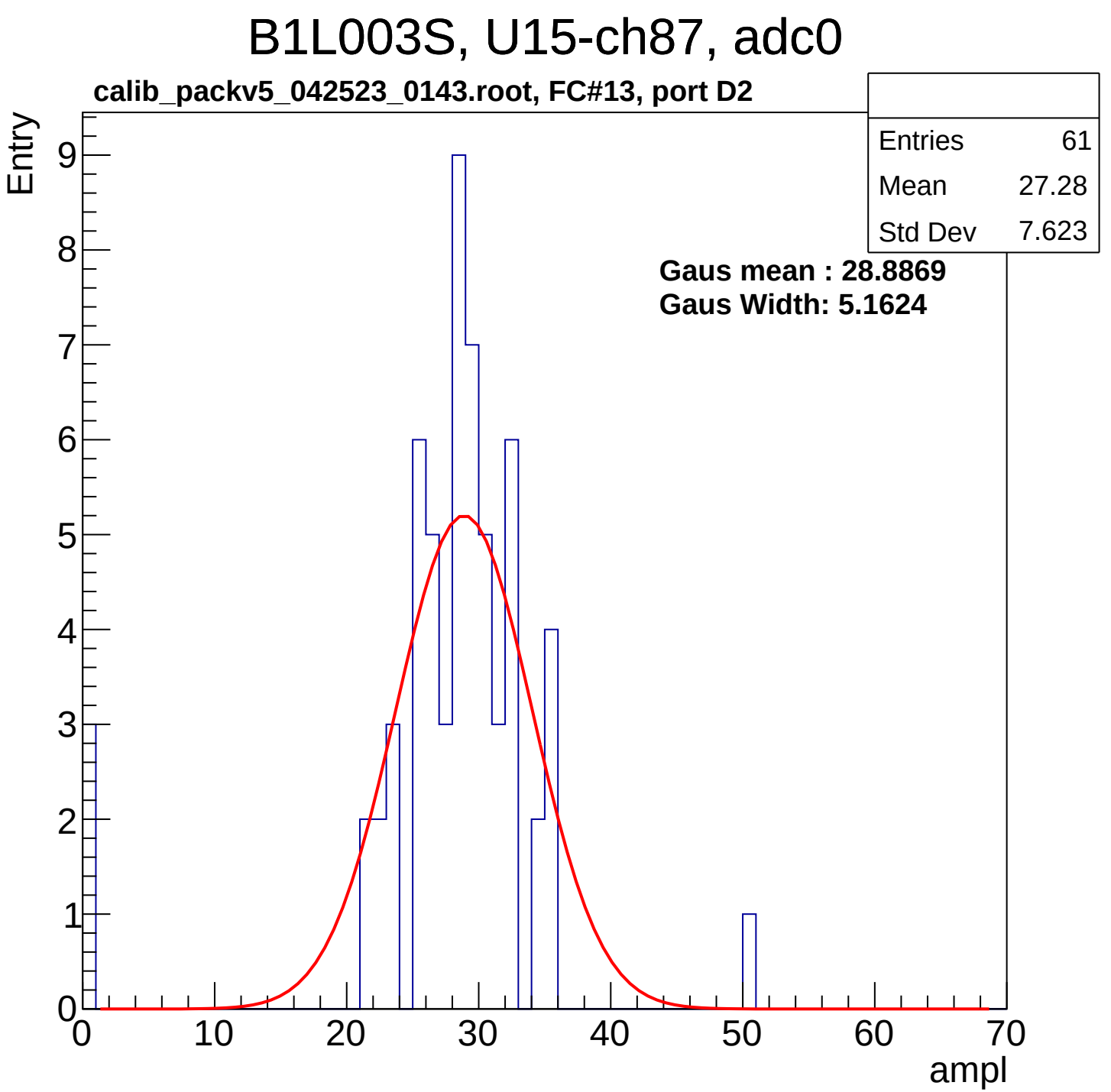
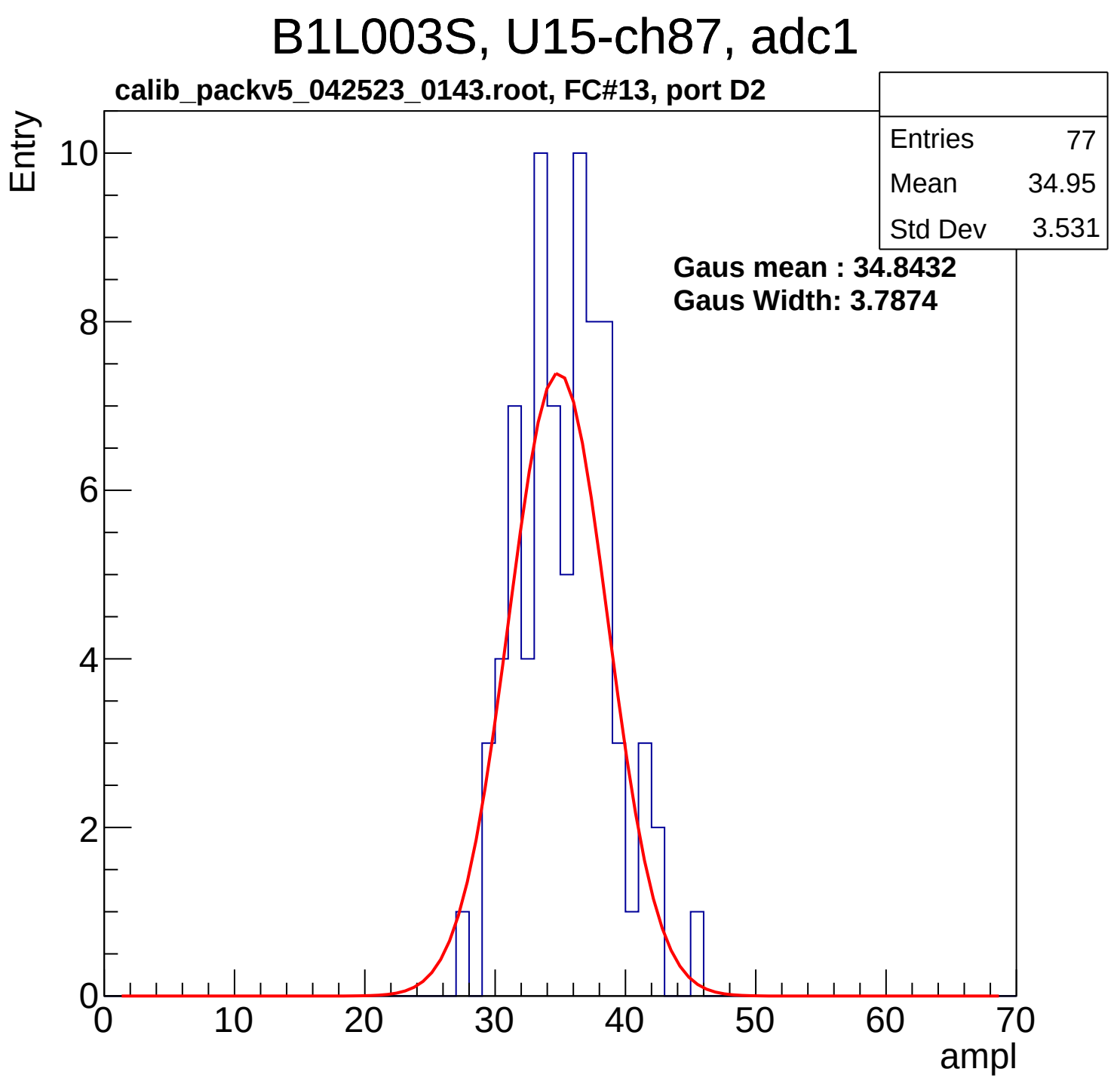
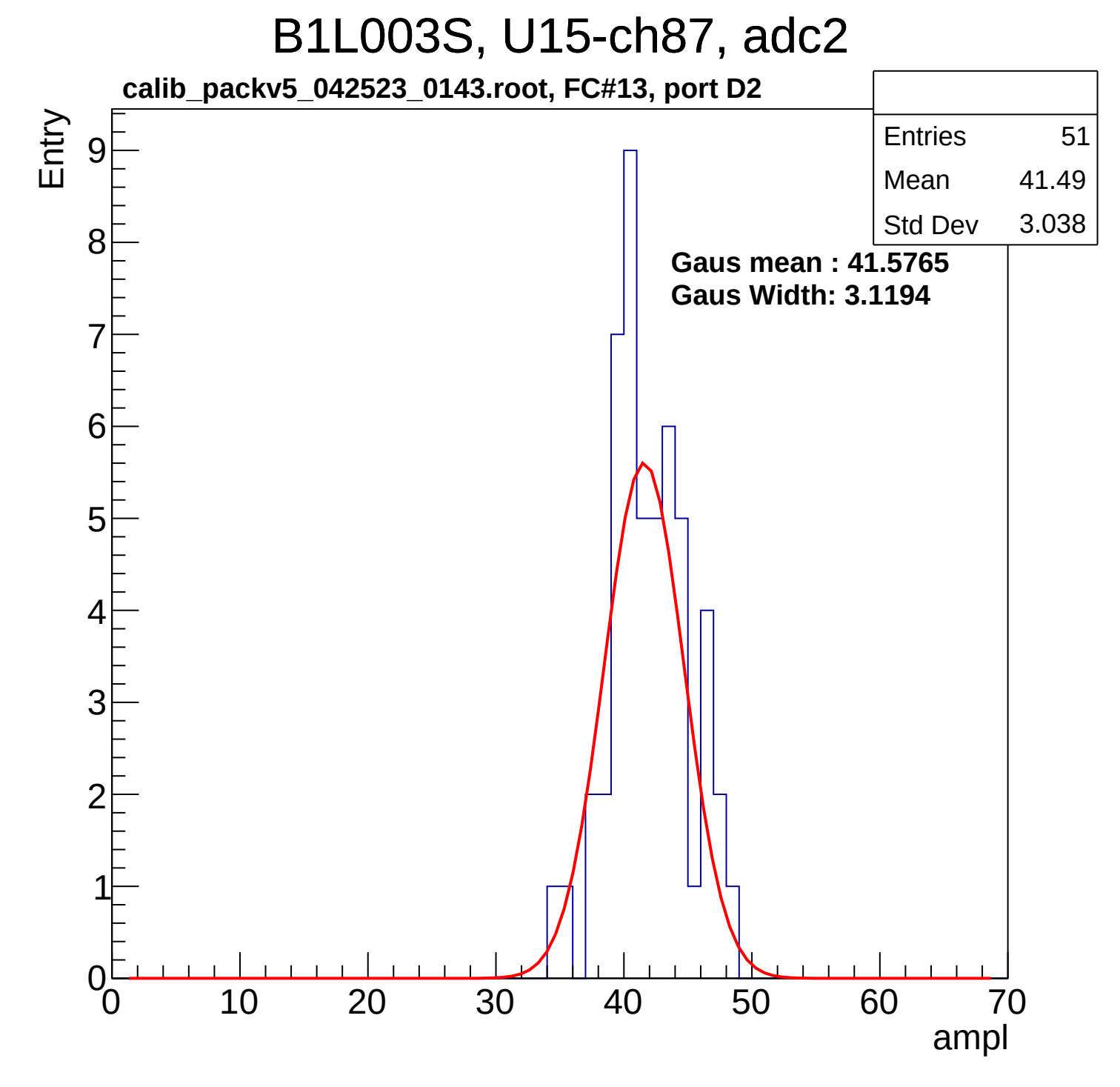
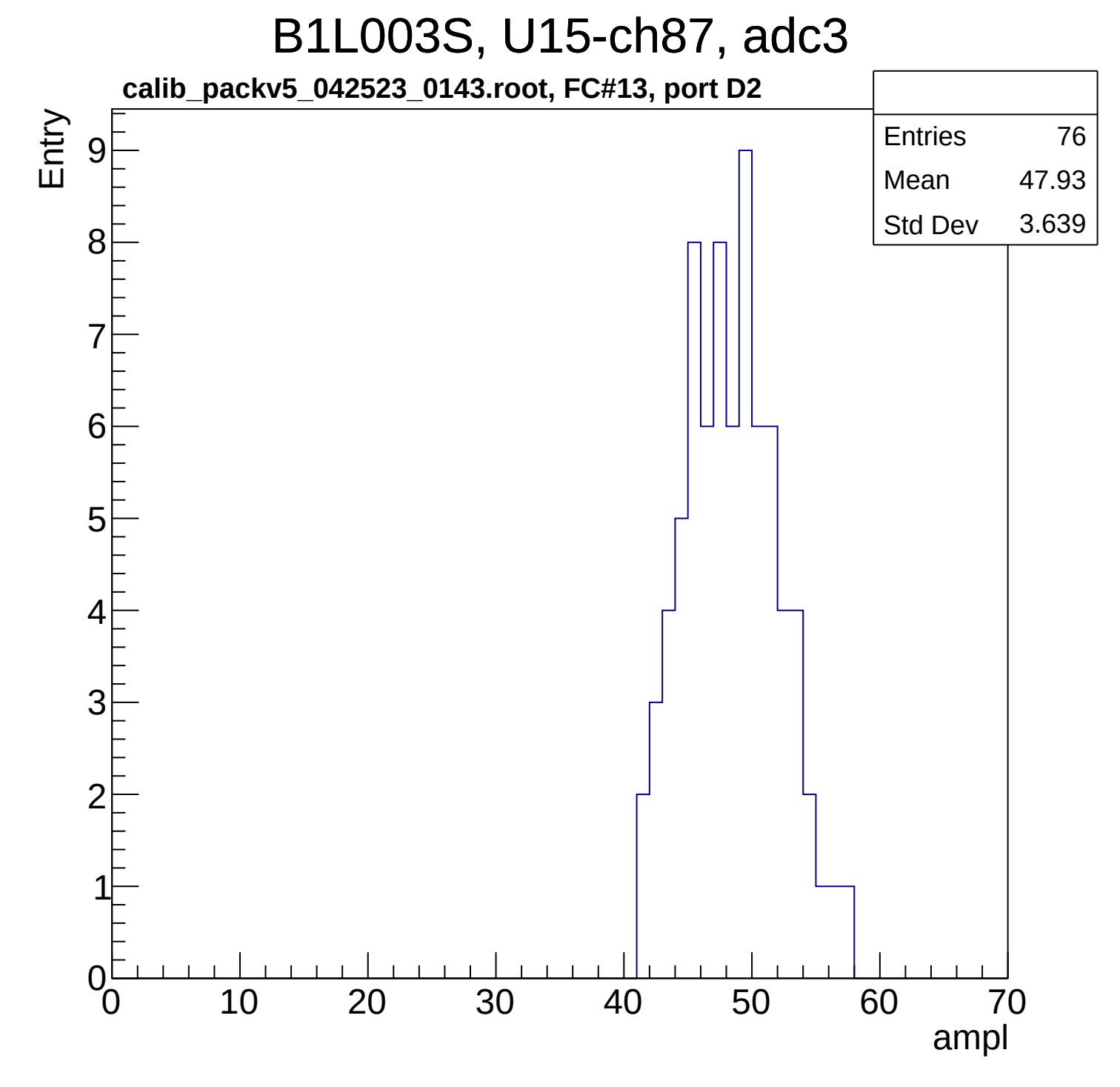
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

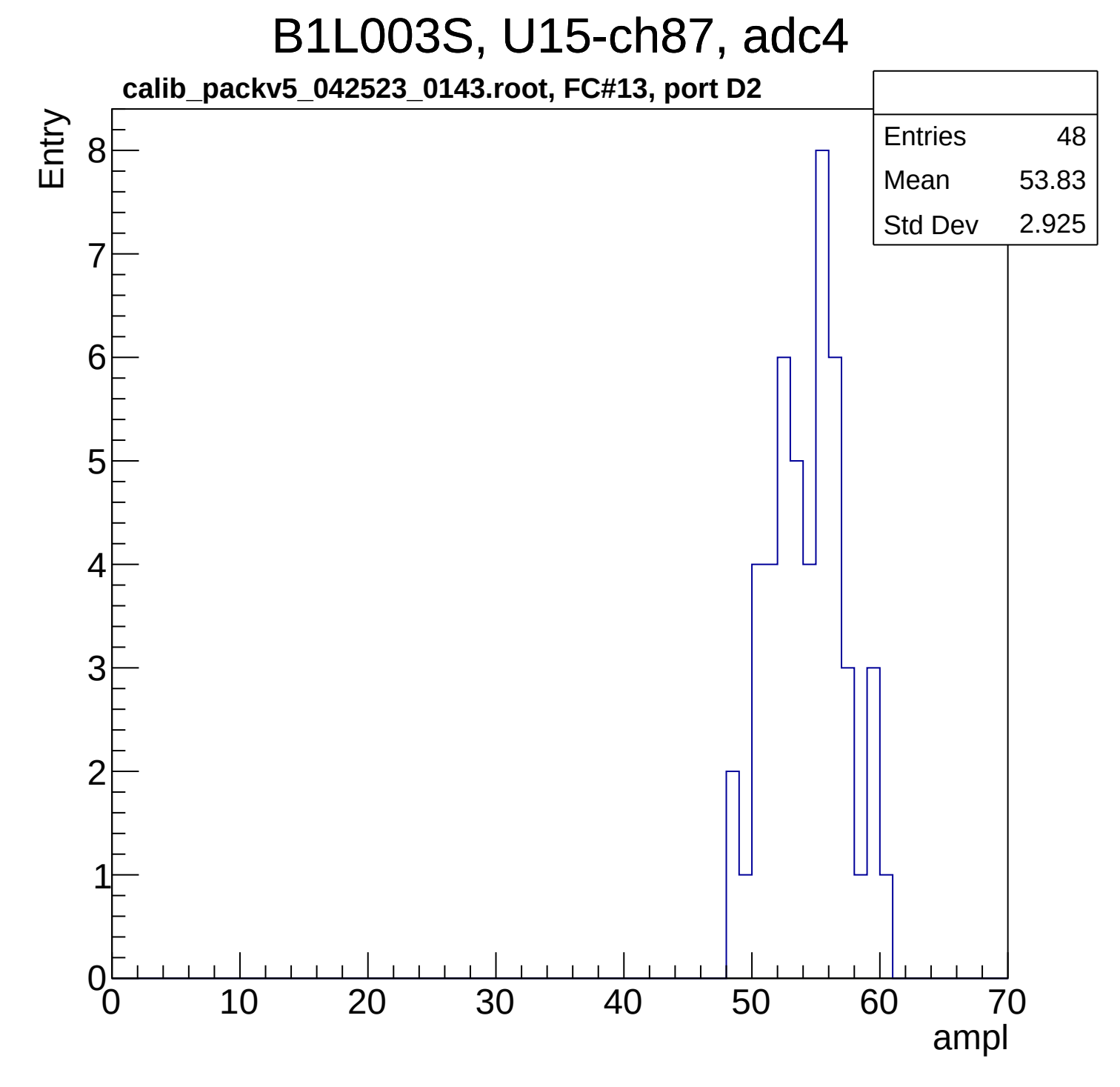

B1L003S, U15-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.28
Std Dev 7.623
Gaus mean : 28.8869
Gaus Width: 5.1624
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 34.95
Std Dev 3.531
Gaus mean : 34.8432
Gaus Width: 3.7874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.49
Std Dev 3.038
Gaus mean : 41.5765
Gaus Width: 3.1194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 47.93
Std Dev 3.639
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 53.83
Std Dev 2.925
Entry
ampl
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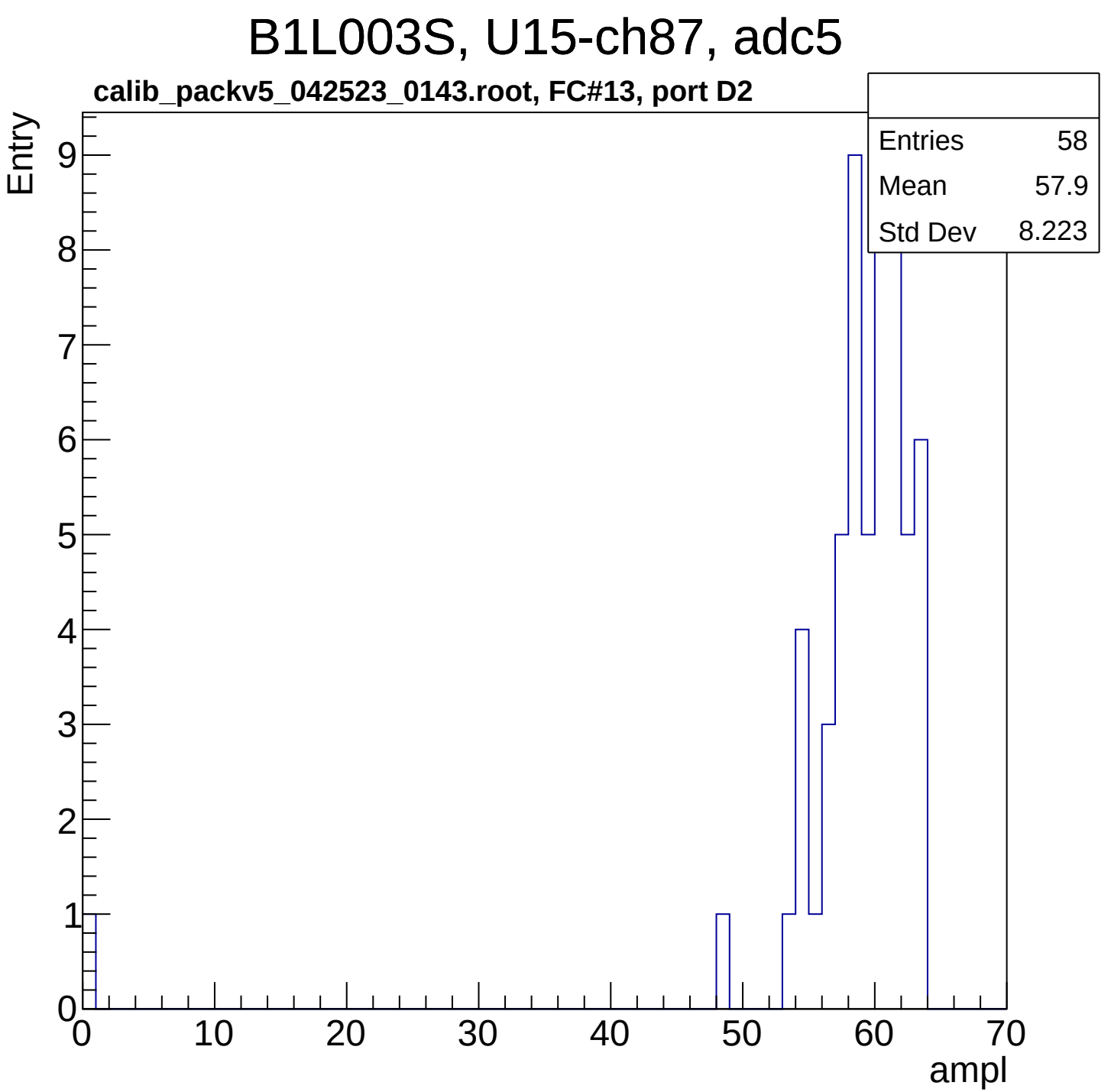
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 57.9
Std Dev 8.223
Entry
ampl
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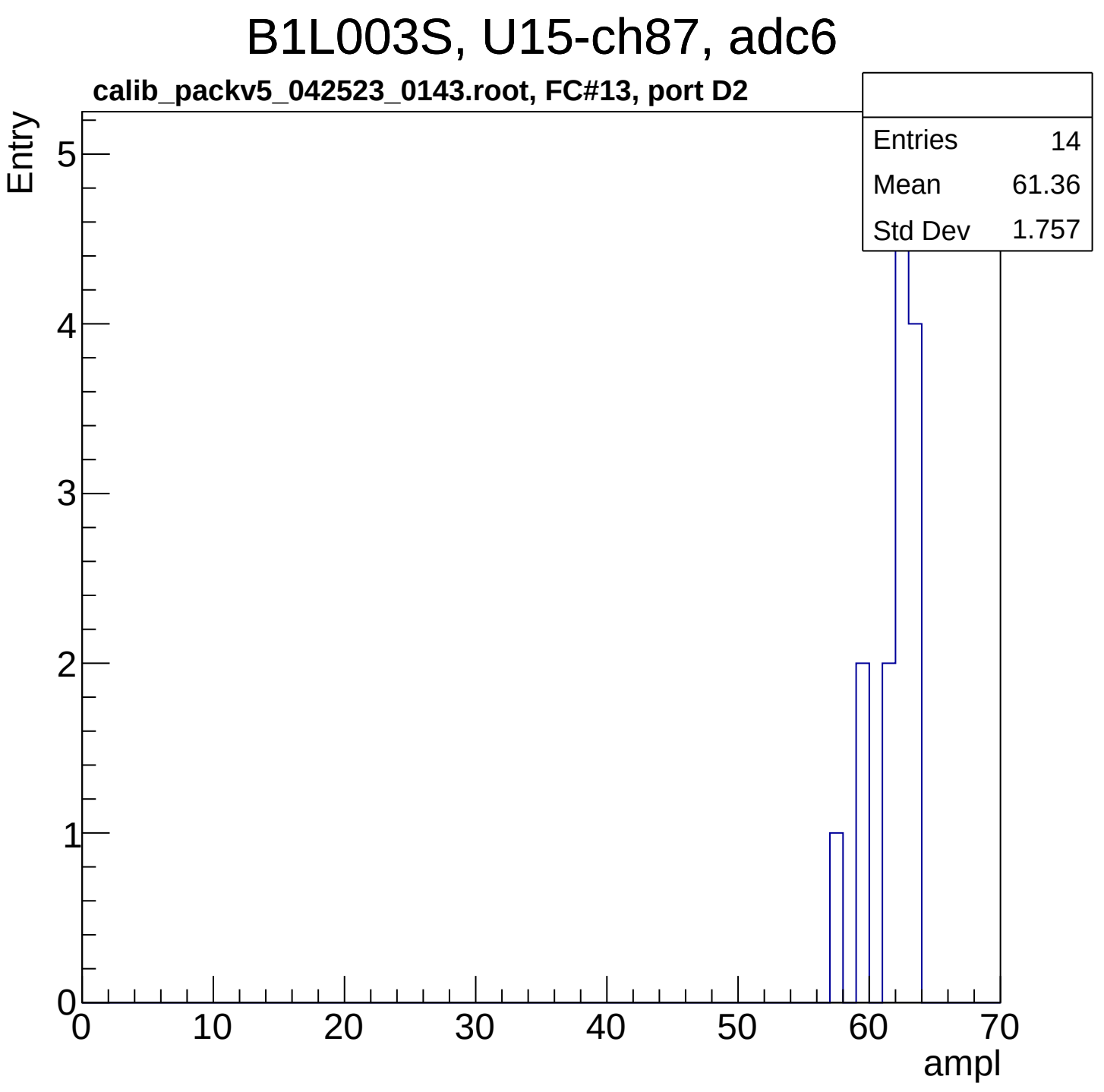
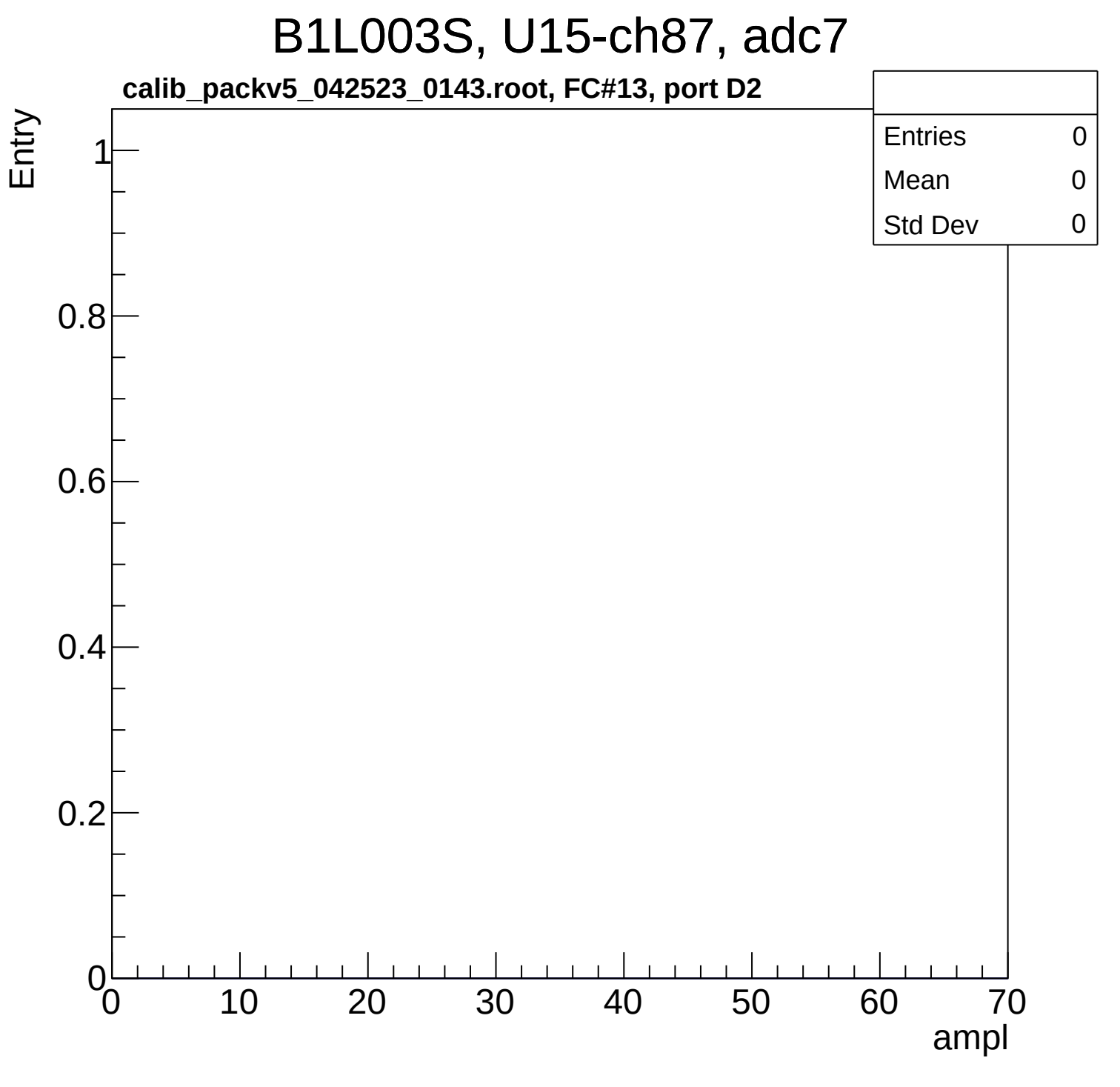
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.36
Std Dev 1.757
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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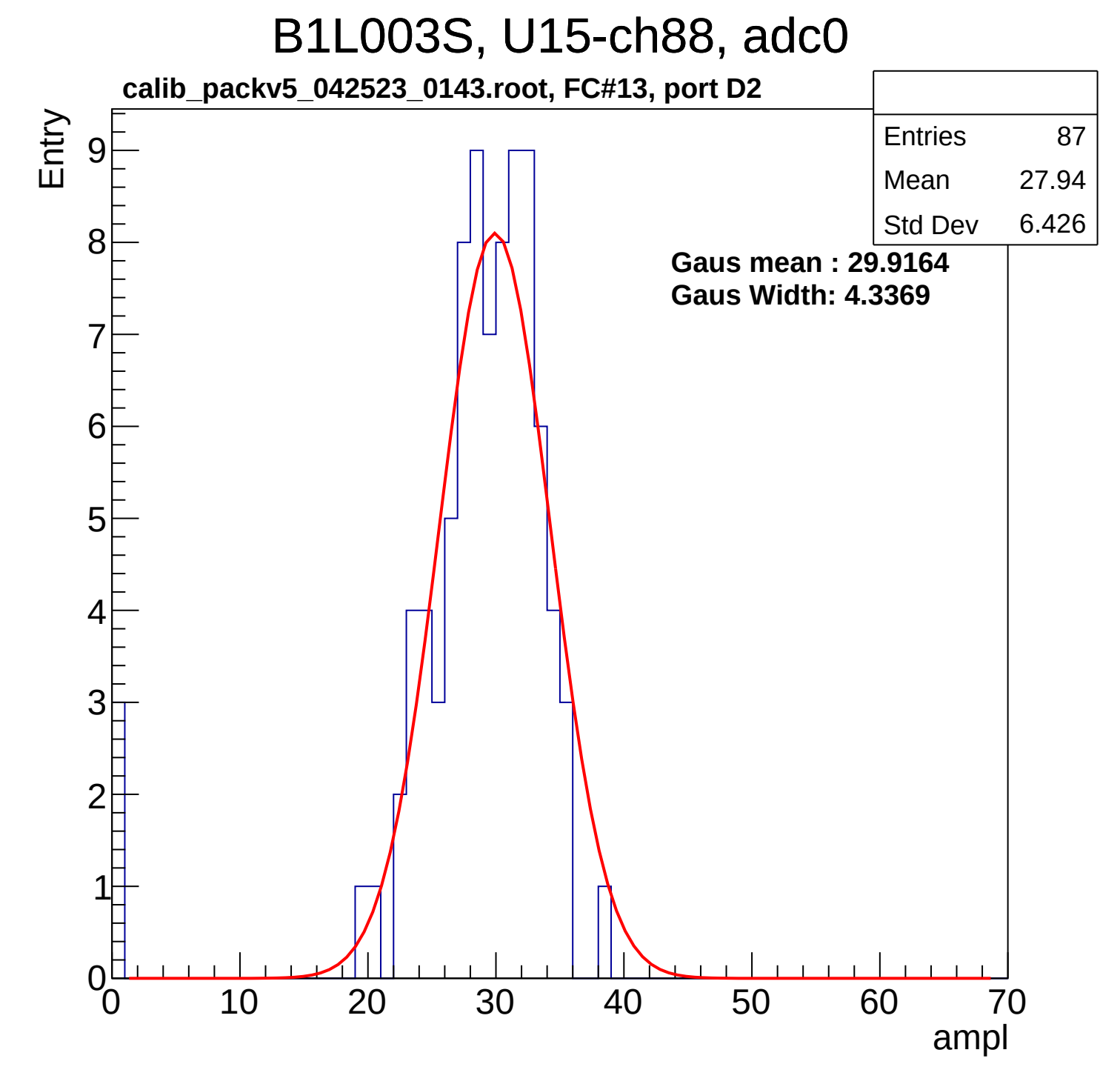
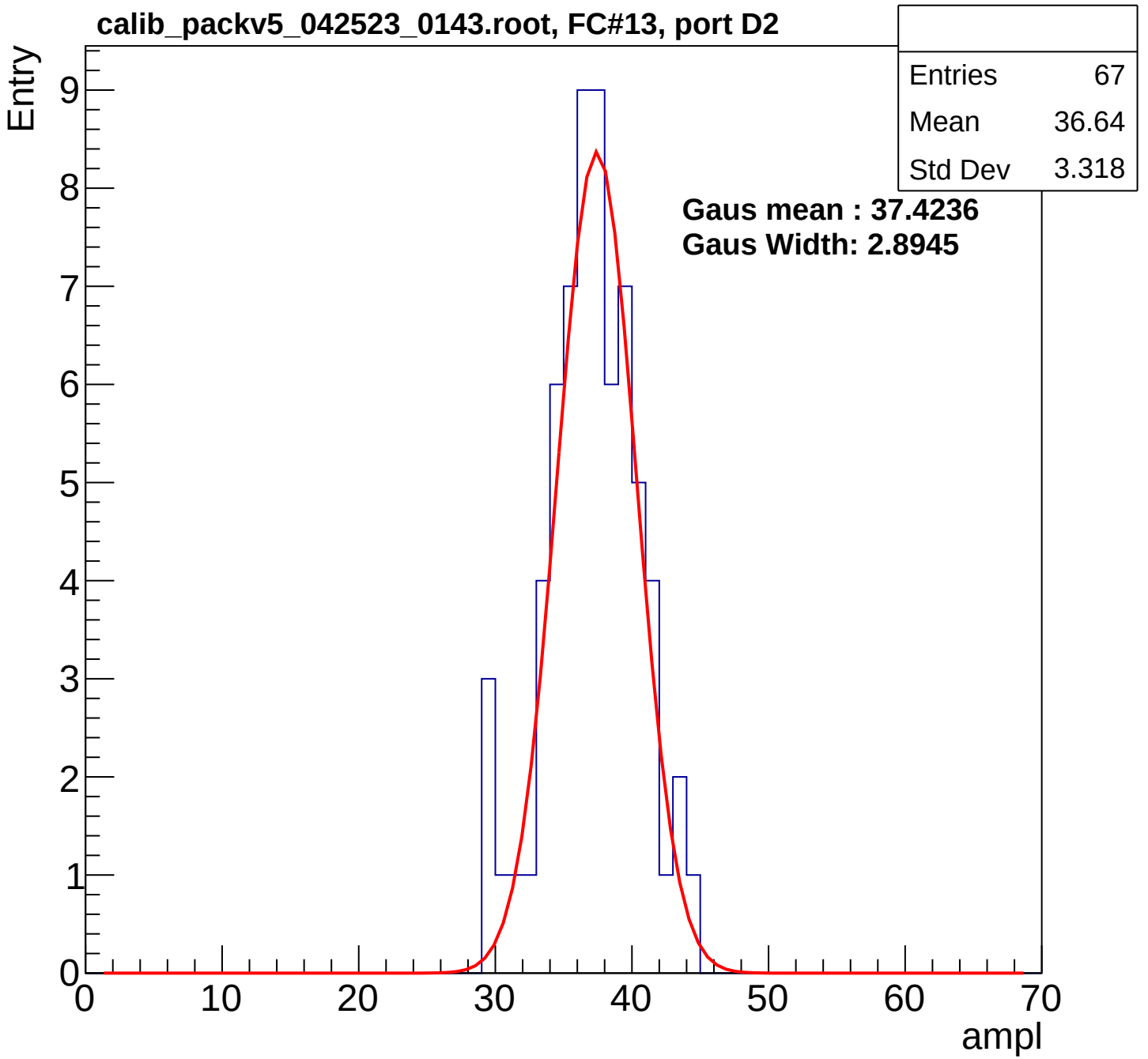
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 87
Mean 27.94
Std Dev 6.426
Gaus mean : 29.9164
Gaus Width: 4.3369
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.64
Std Dev 3.318
Gaus mean : 37.4236
Gaus Width: 2.8945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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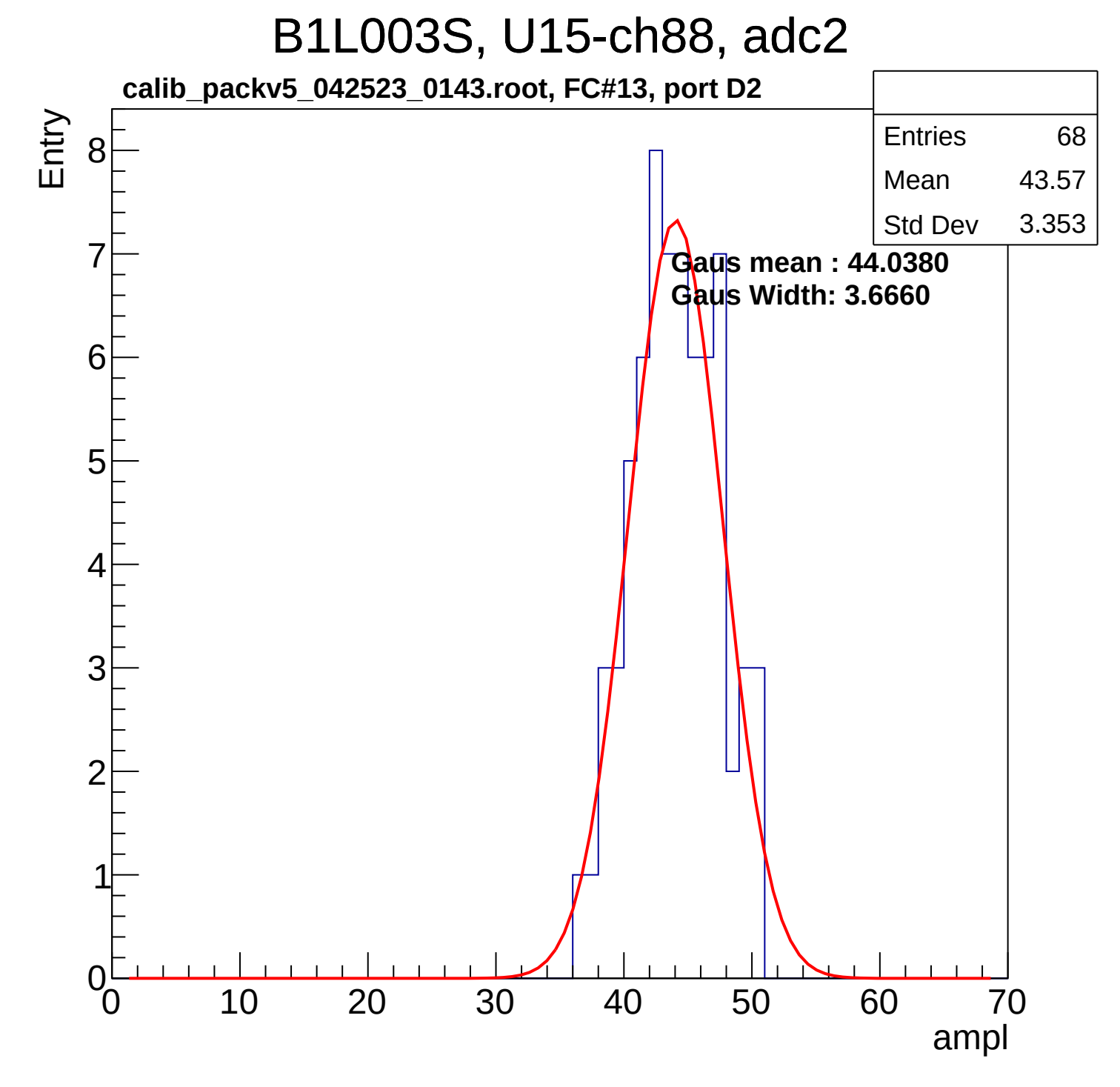
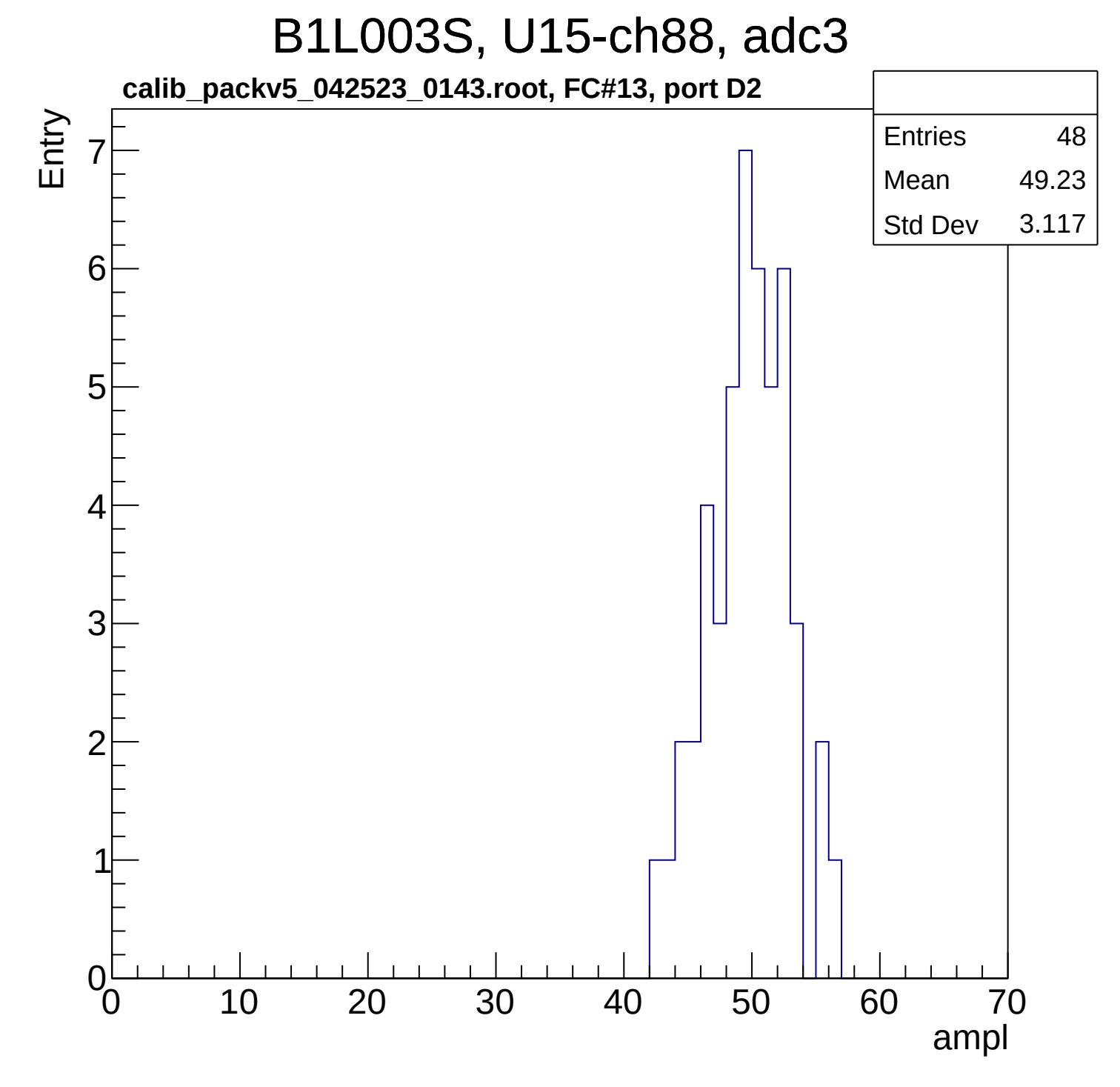
60
70


B1L003S, U15-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 43.57
Std Dev 3.353
Gaus mean : 44.0380
Gaus Width: 3.6660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 49.23
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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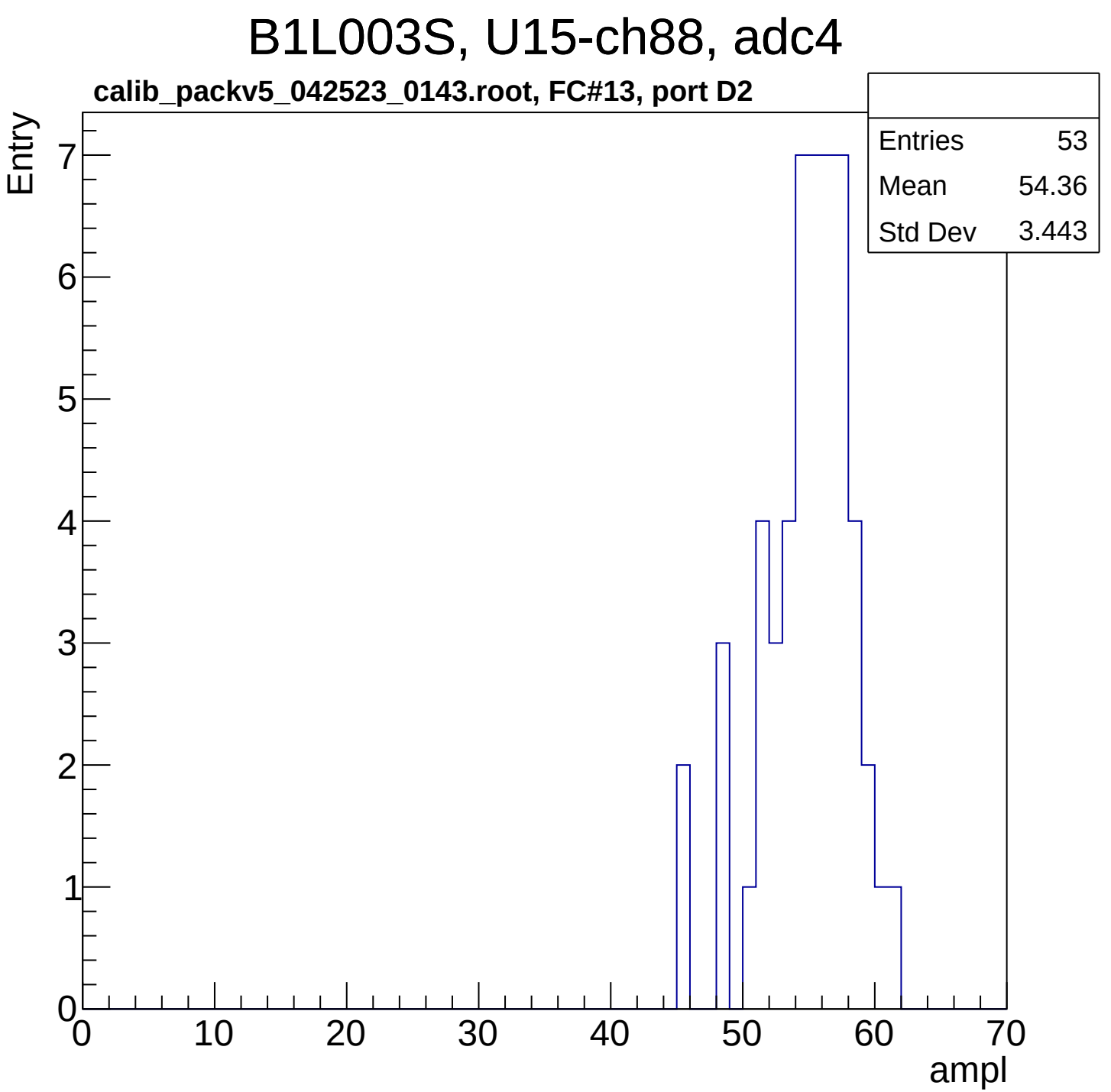
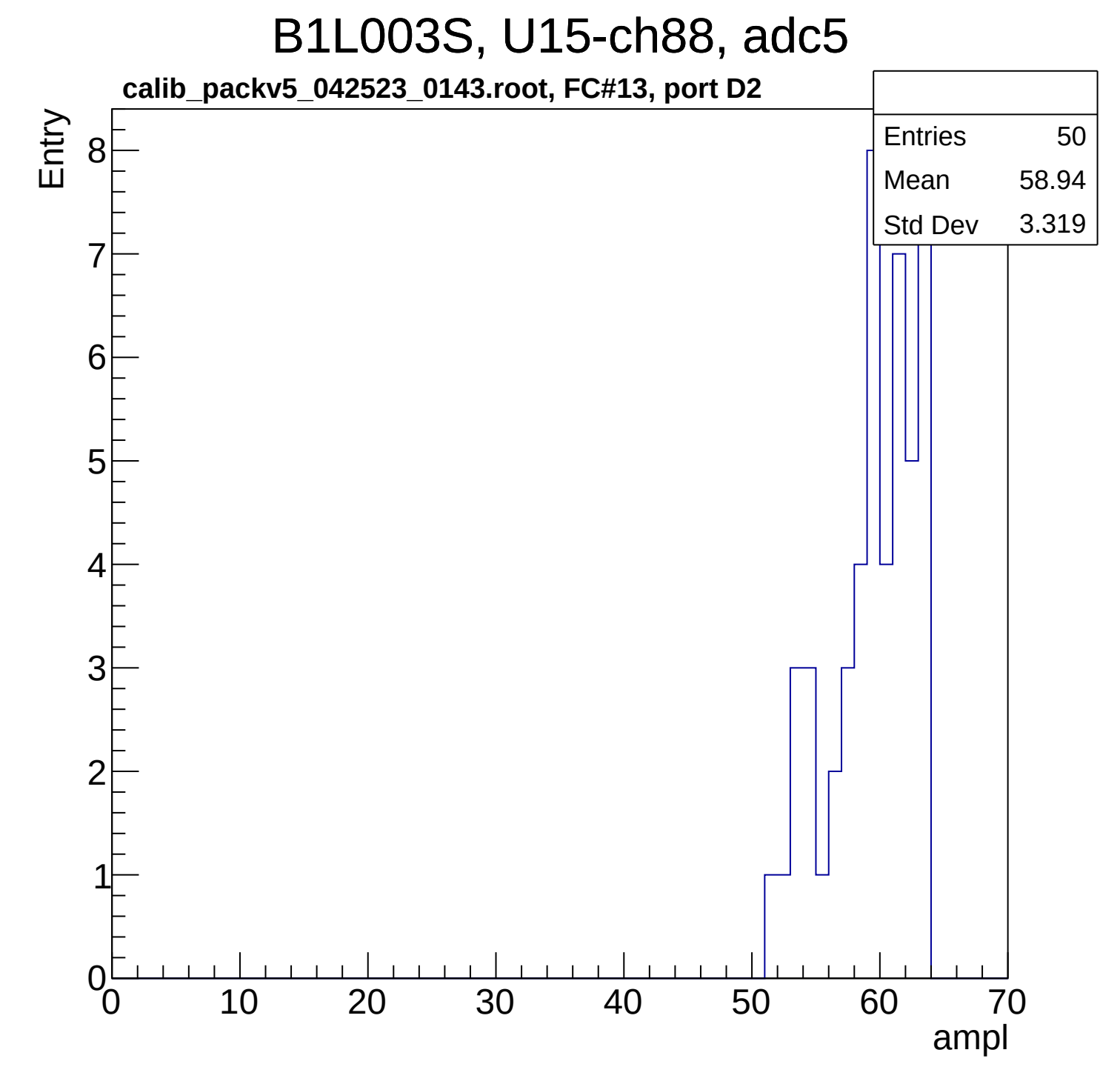
40
50
60
70


B1L003S, U15-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.36
Std Dev 3.443
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.94
Std Dev 3.319
Entry
ampl
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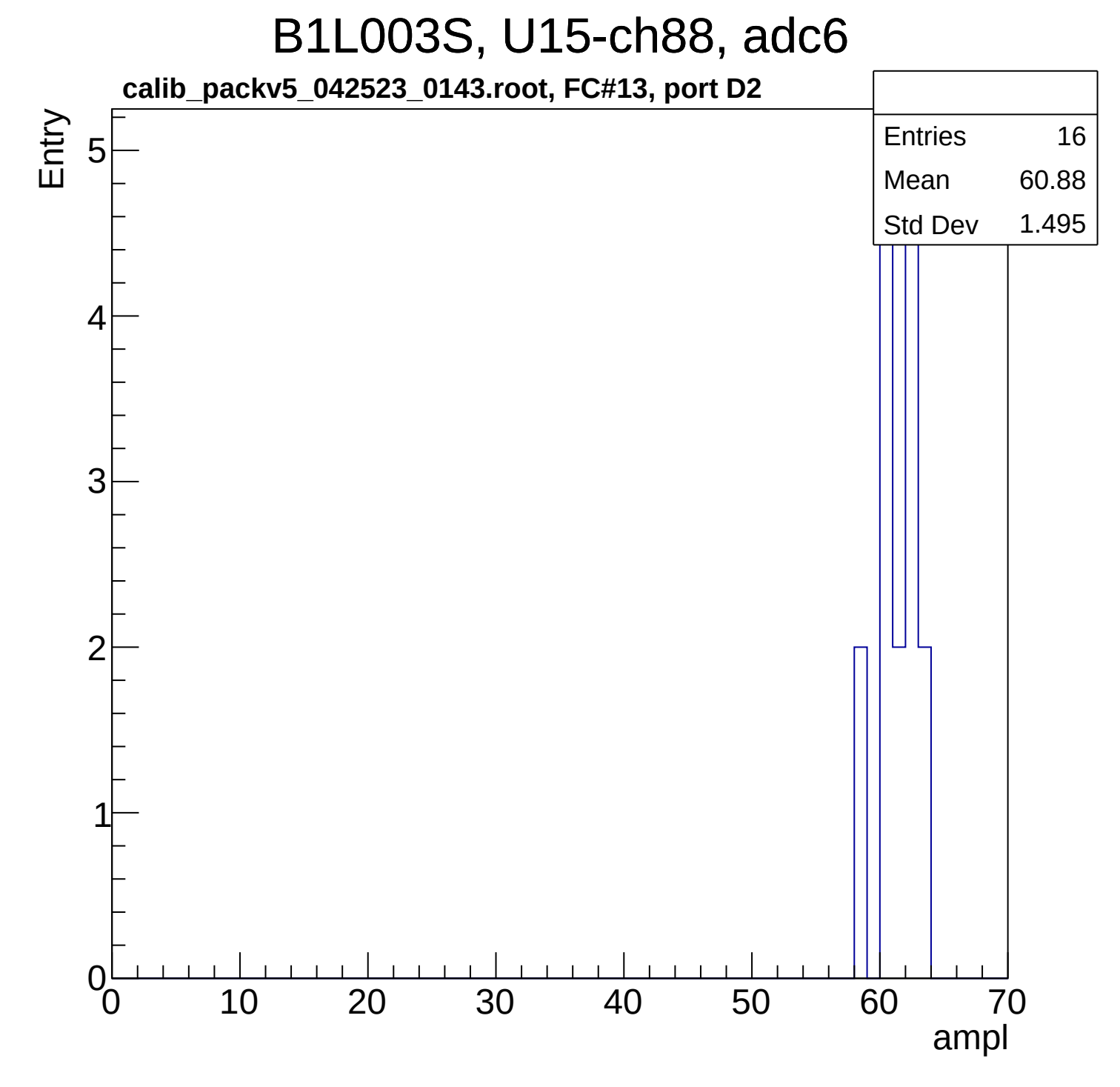
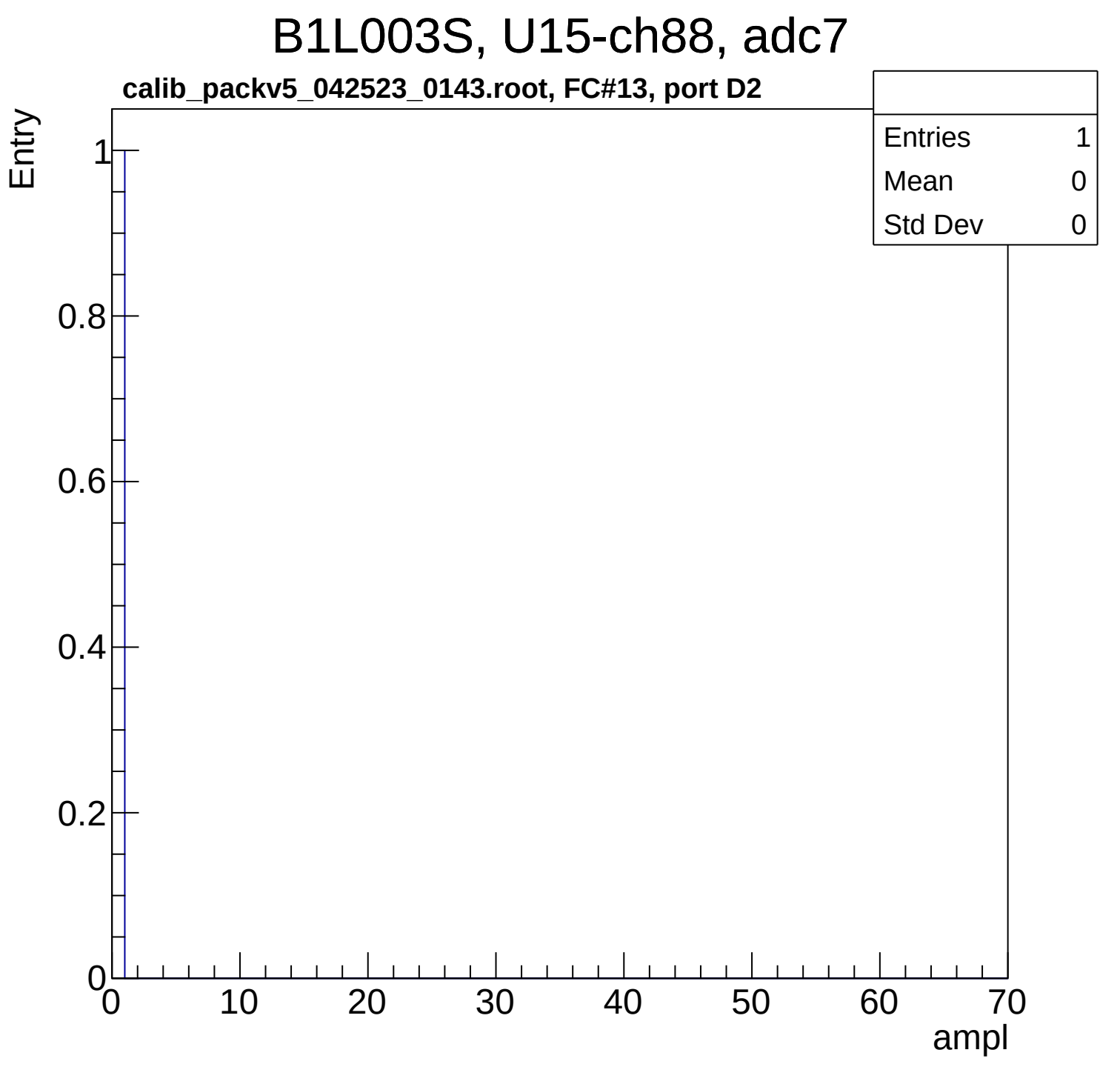
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 60.88
Std Dev 1.495
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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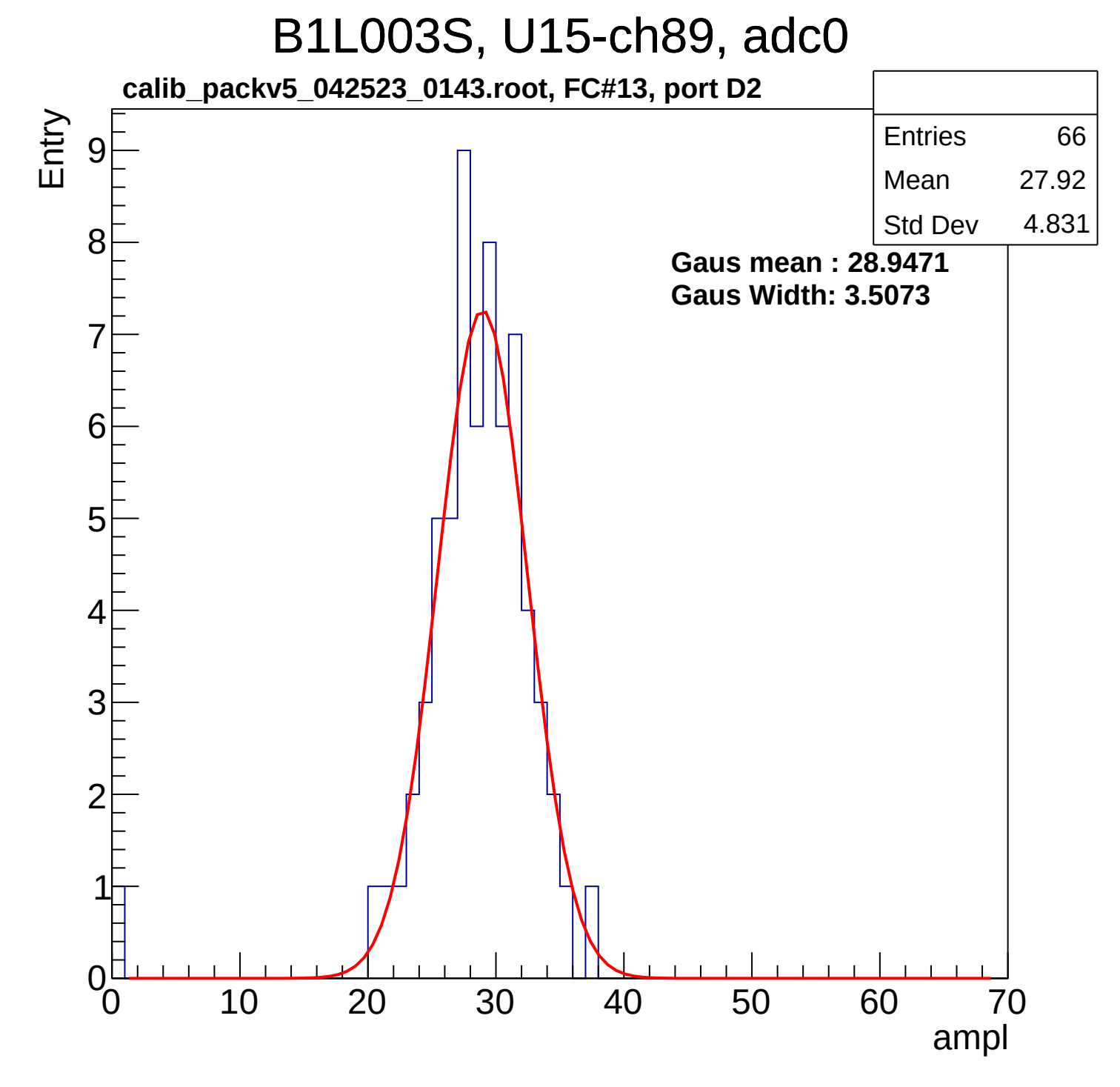
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 27.92
Std Dev 4.831
Gaus mean : 28.9471
Gaus Width: 3.5073
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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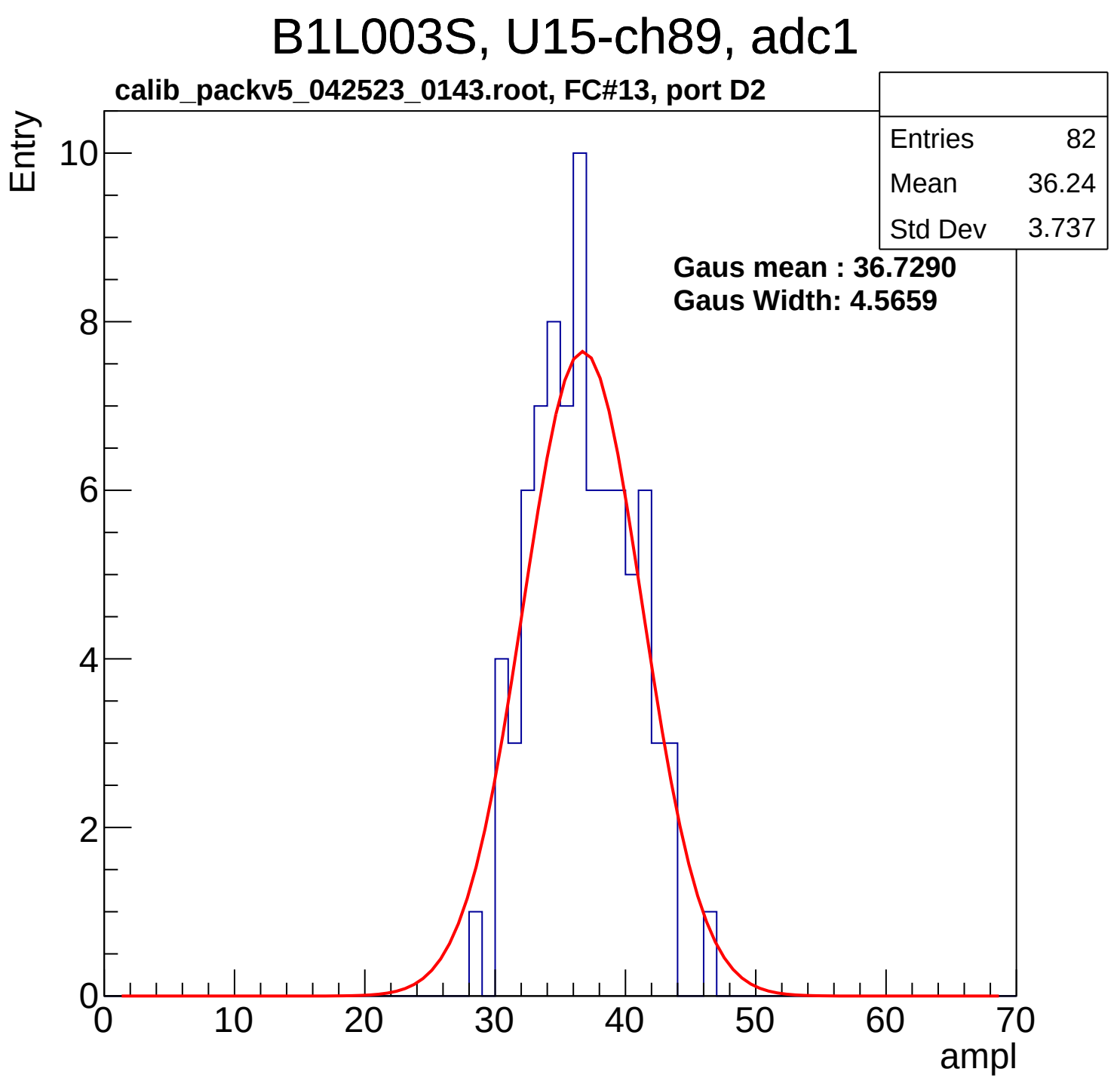
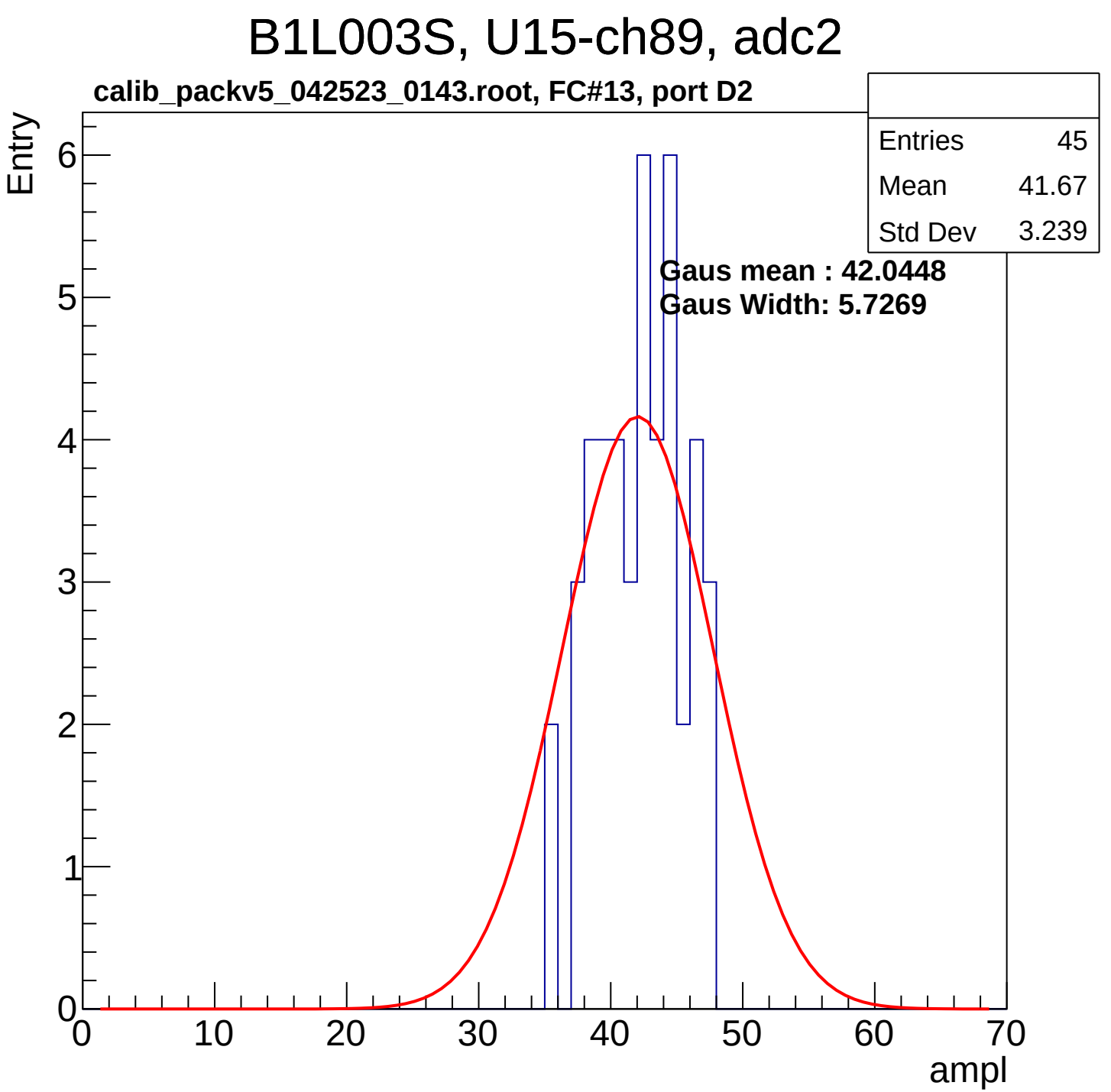
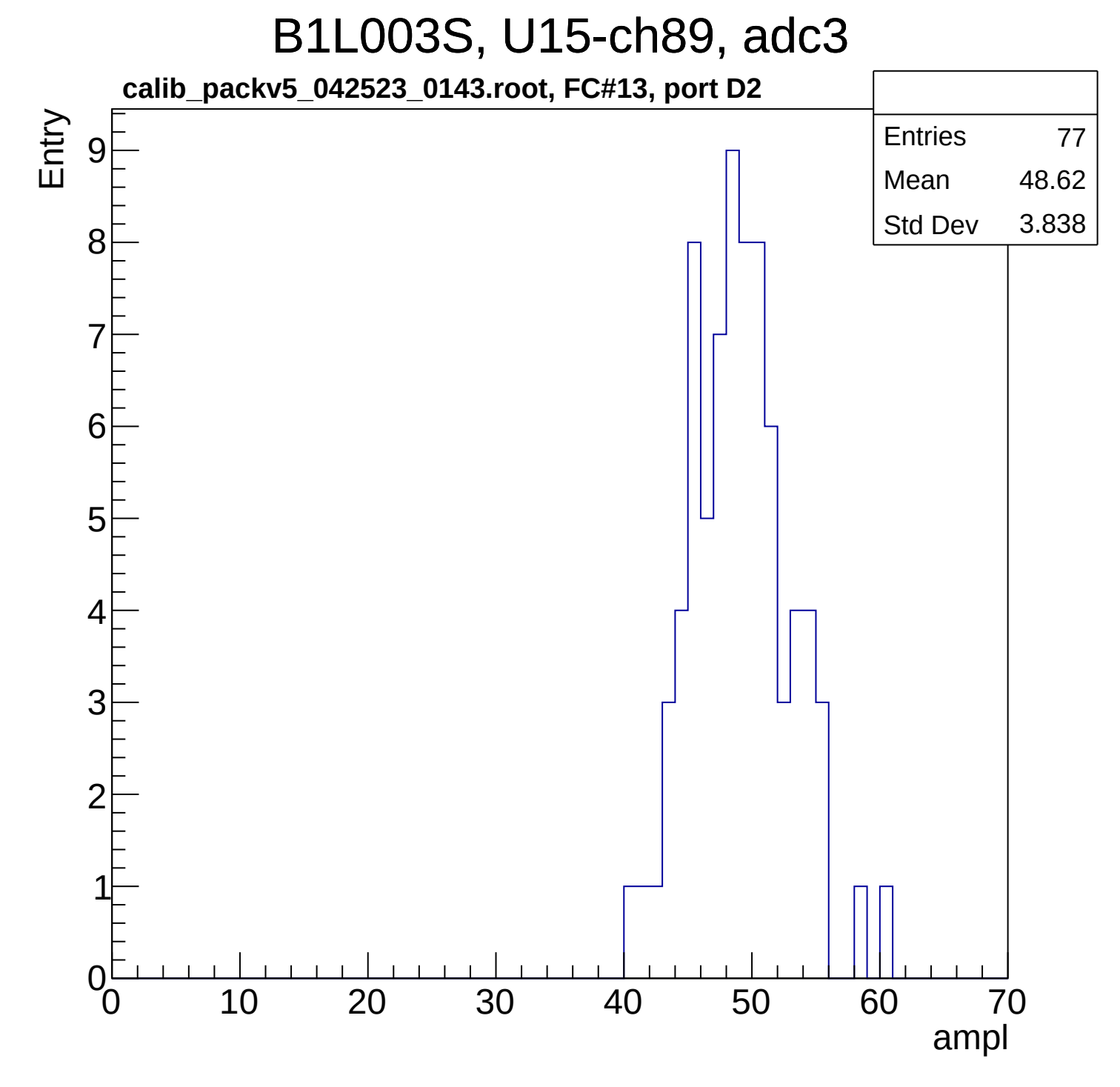
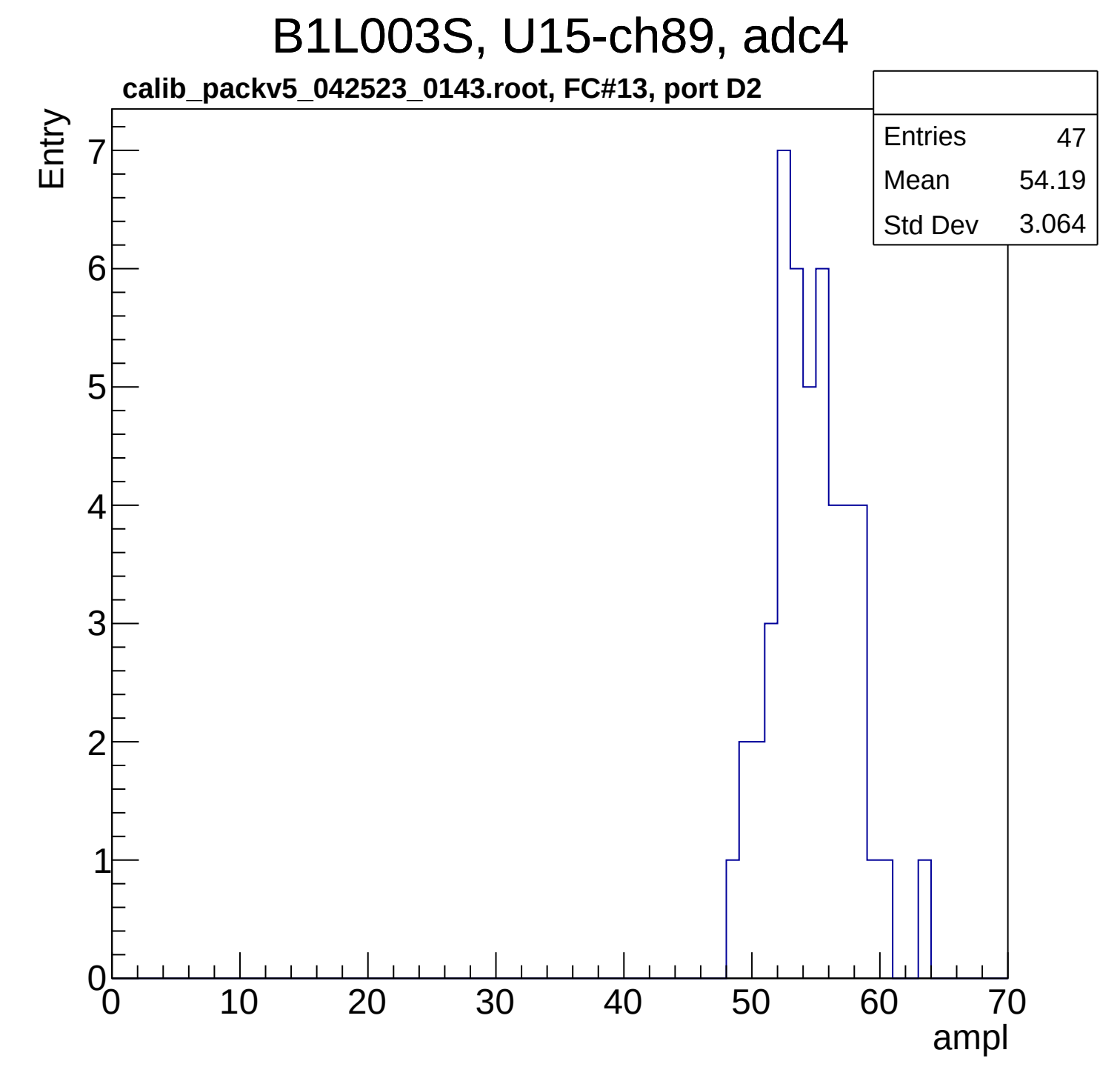
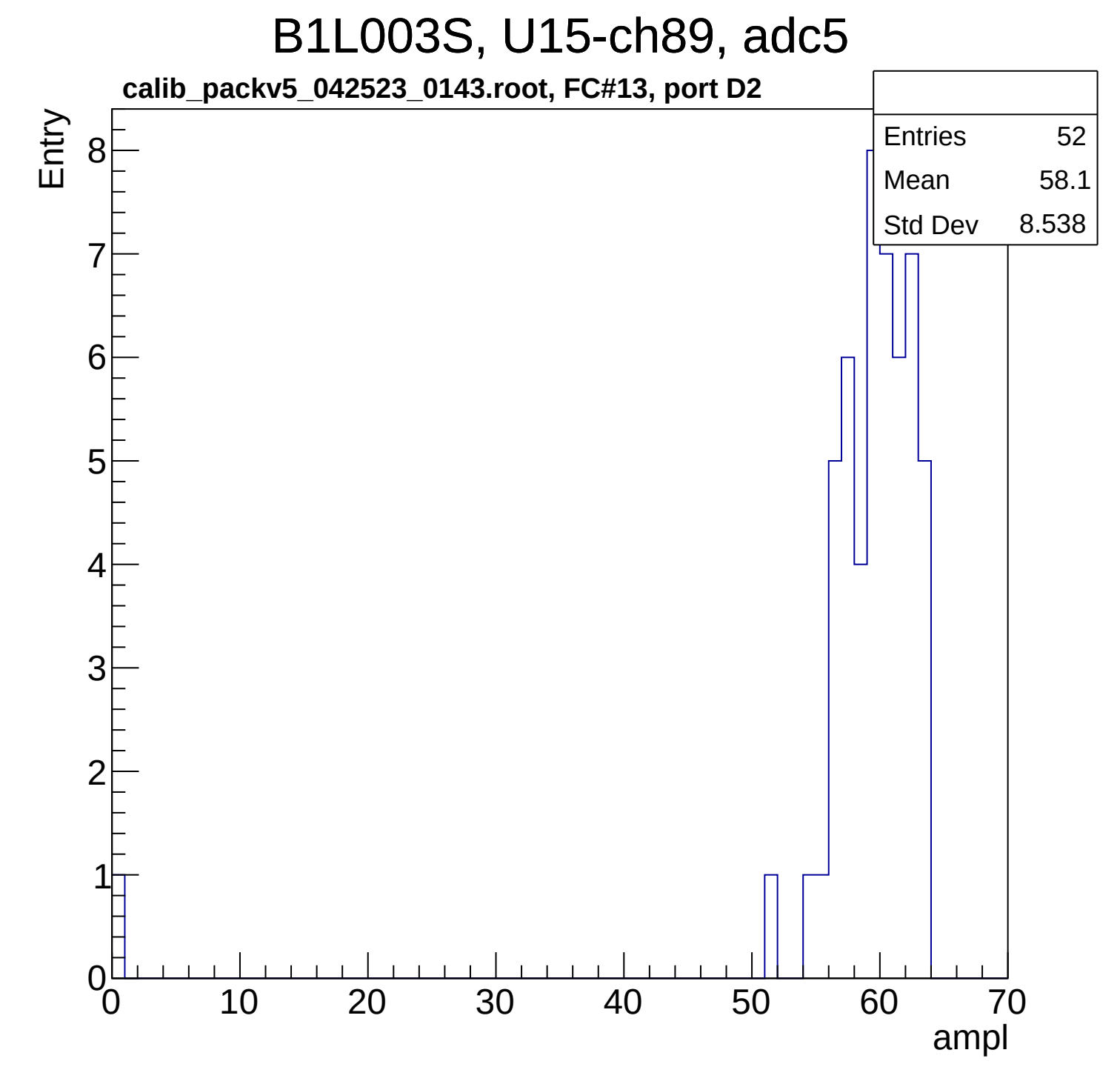

B1L003S, U15-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 36.24
Std Dev 3.737
Gaus mean : 36.7290
Gaus Width: 4.5659
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 41.67
Std Dev 3.239
Gaus mean : 42.0448
Gaus Width: 5.7269
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U15-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 48.62
Std Dev 3.838
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.19
Std Dev 3.064
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.1
Std Dev 8.538
Entry
ampl
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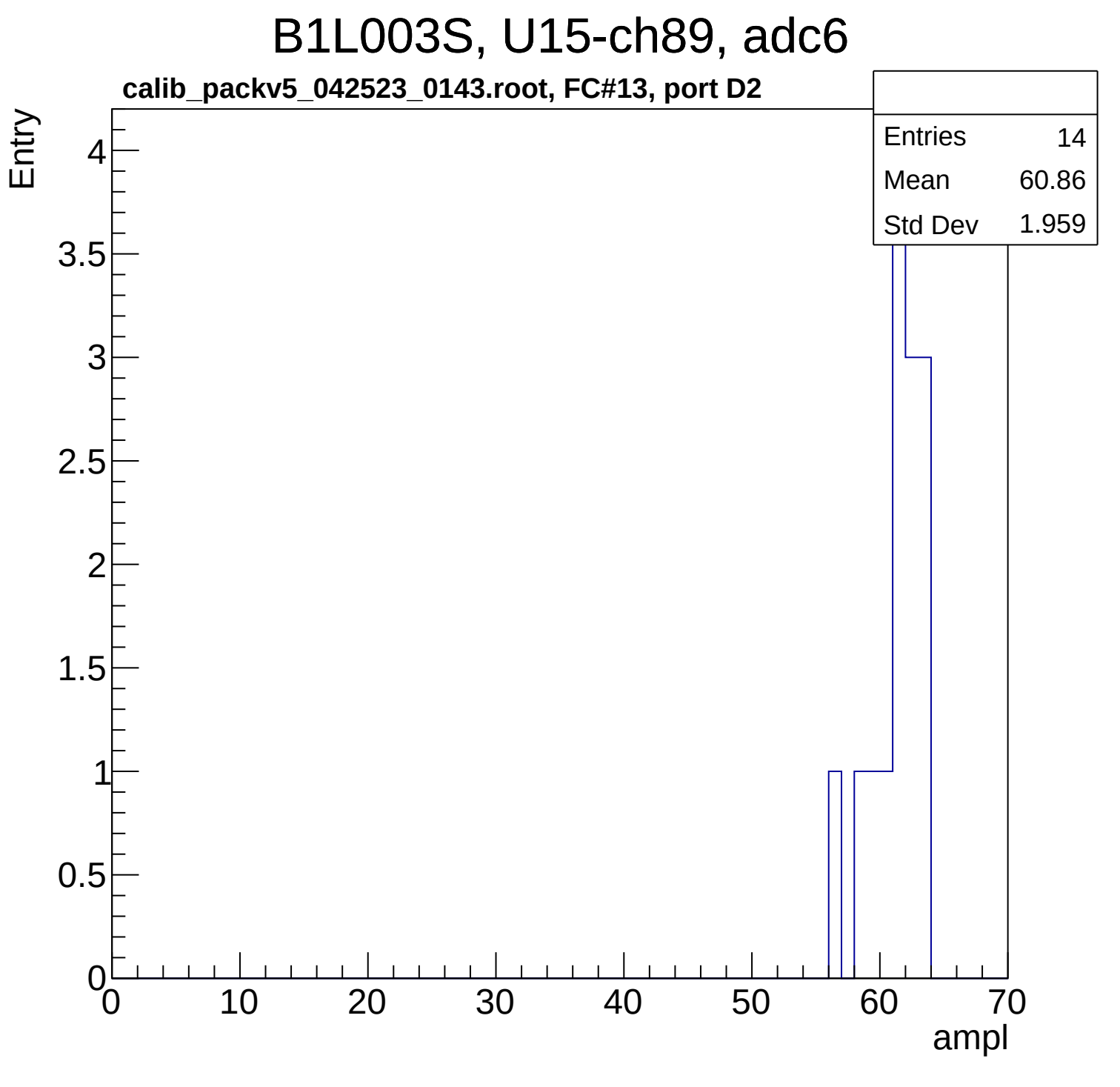
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 60.86
Std Dev 1.959
Entry
ampl
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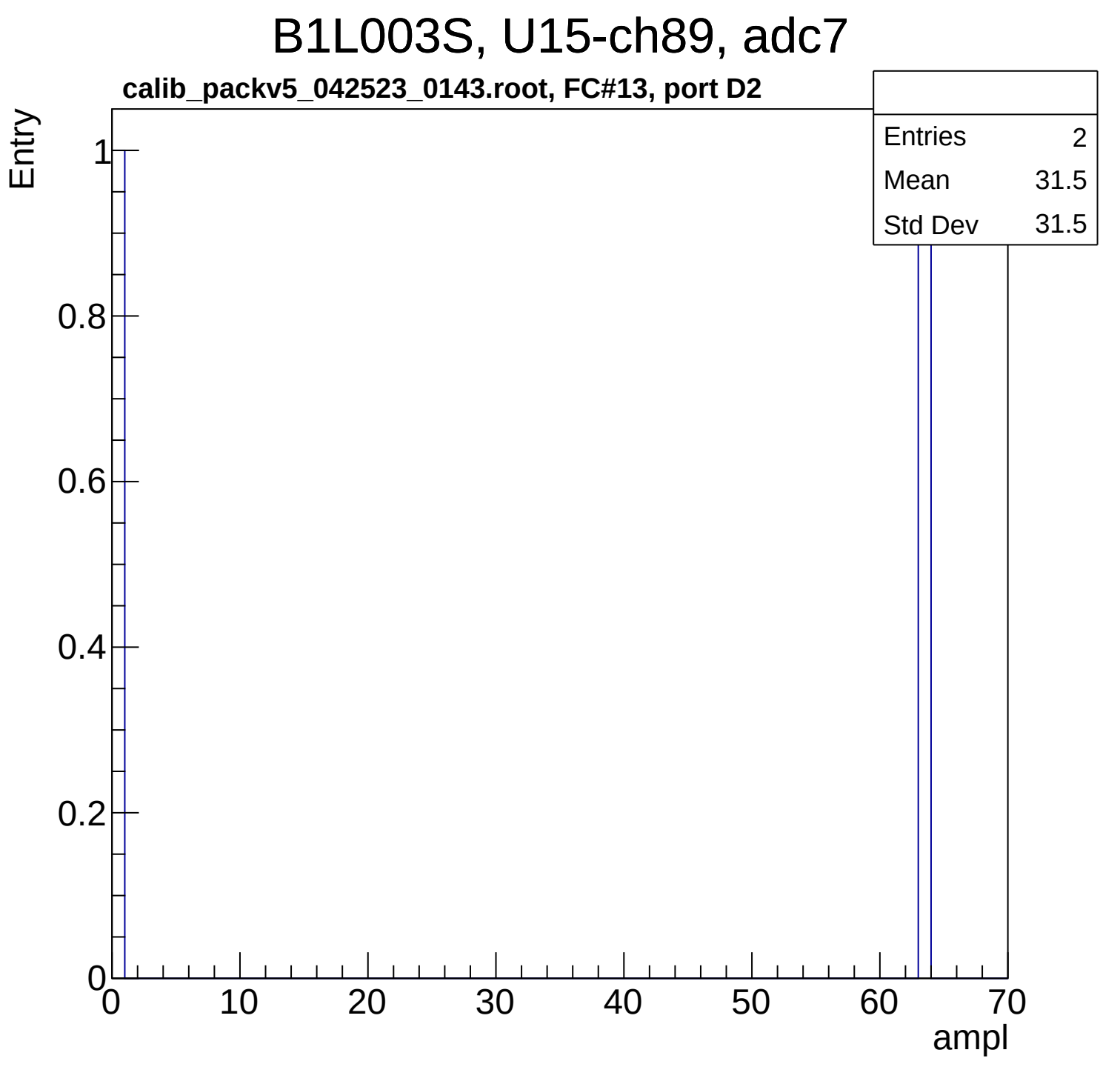
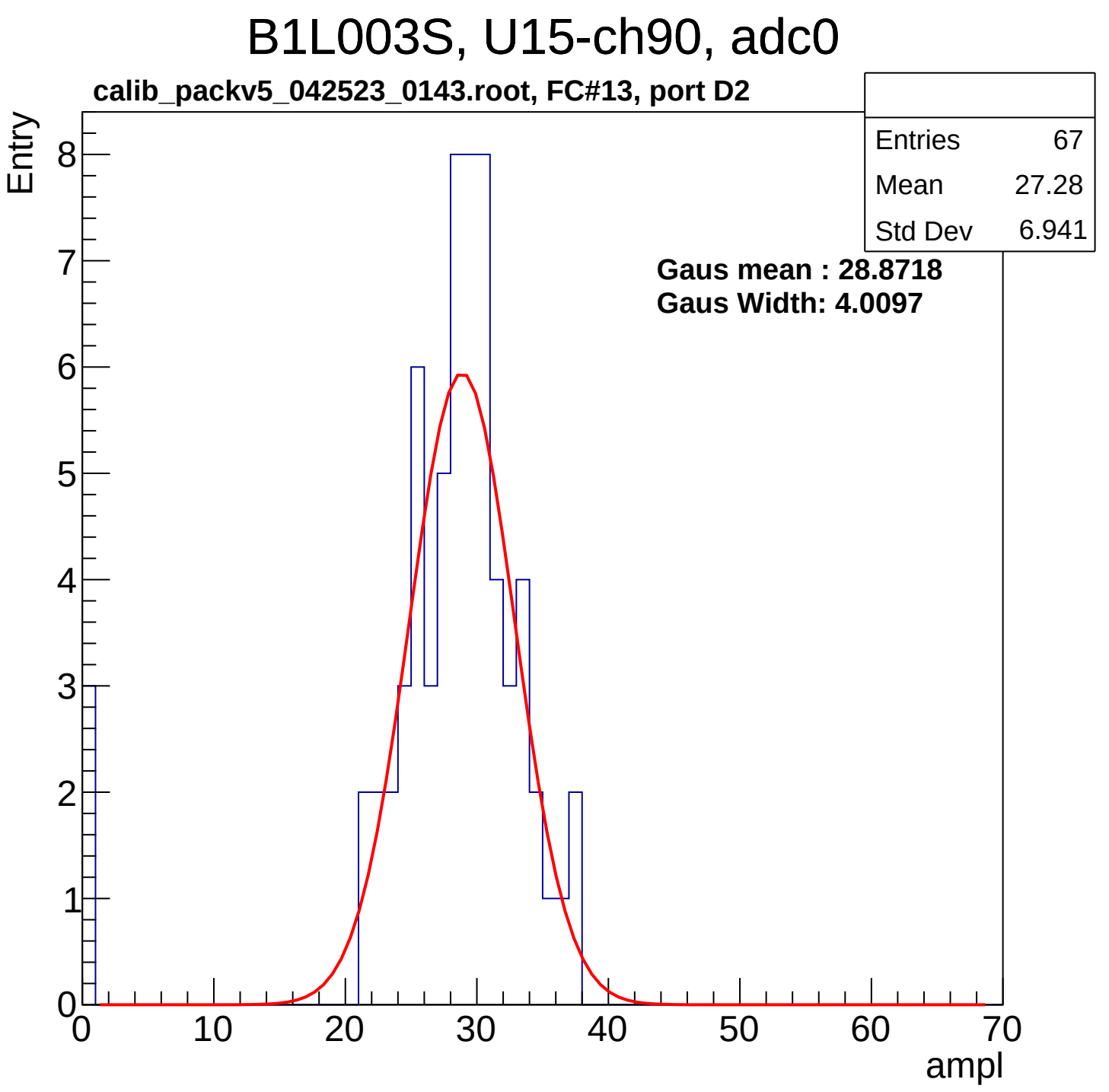
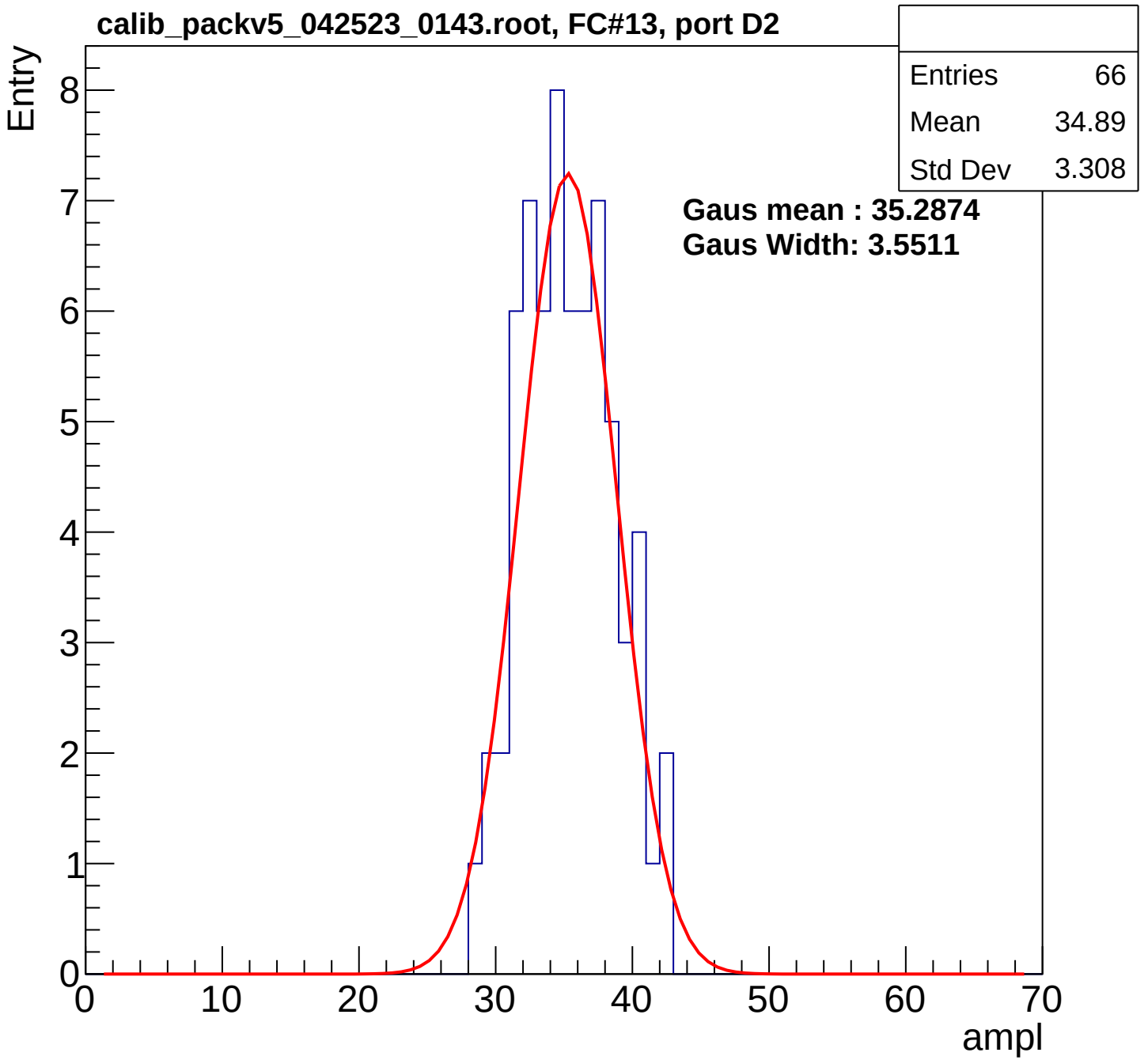
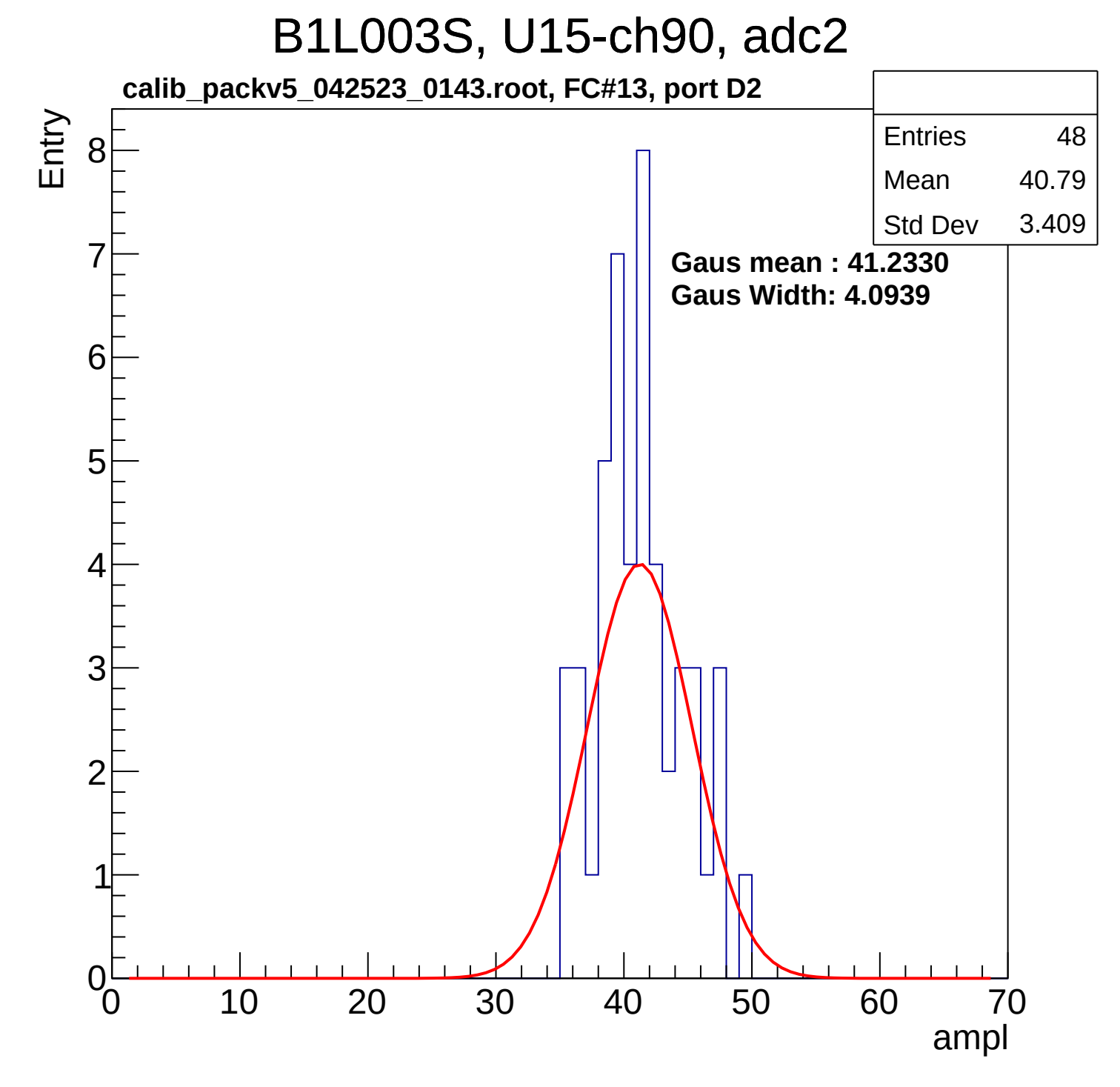
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 27.28
Std Dev 6.941
Gaus mean : 28.8718
Gaus Width: 4.0097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 34.89
Std Dev 3.308
Gaus mean : 35.2874
Gaus Width: 3.5511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 40.79
Std Dev 3.409
Gaus mean : 41.2330
Gaus Width: 4.0939
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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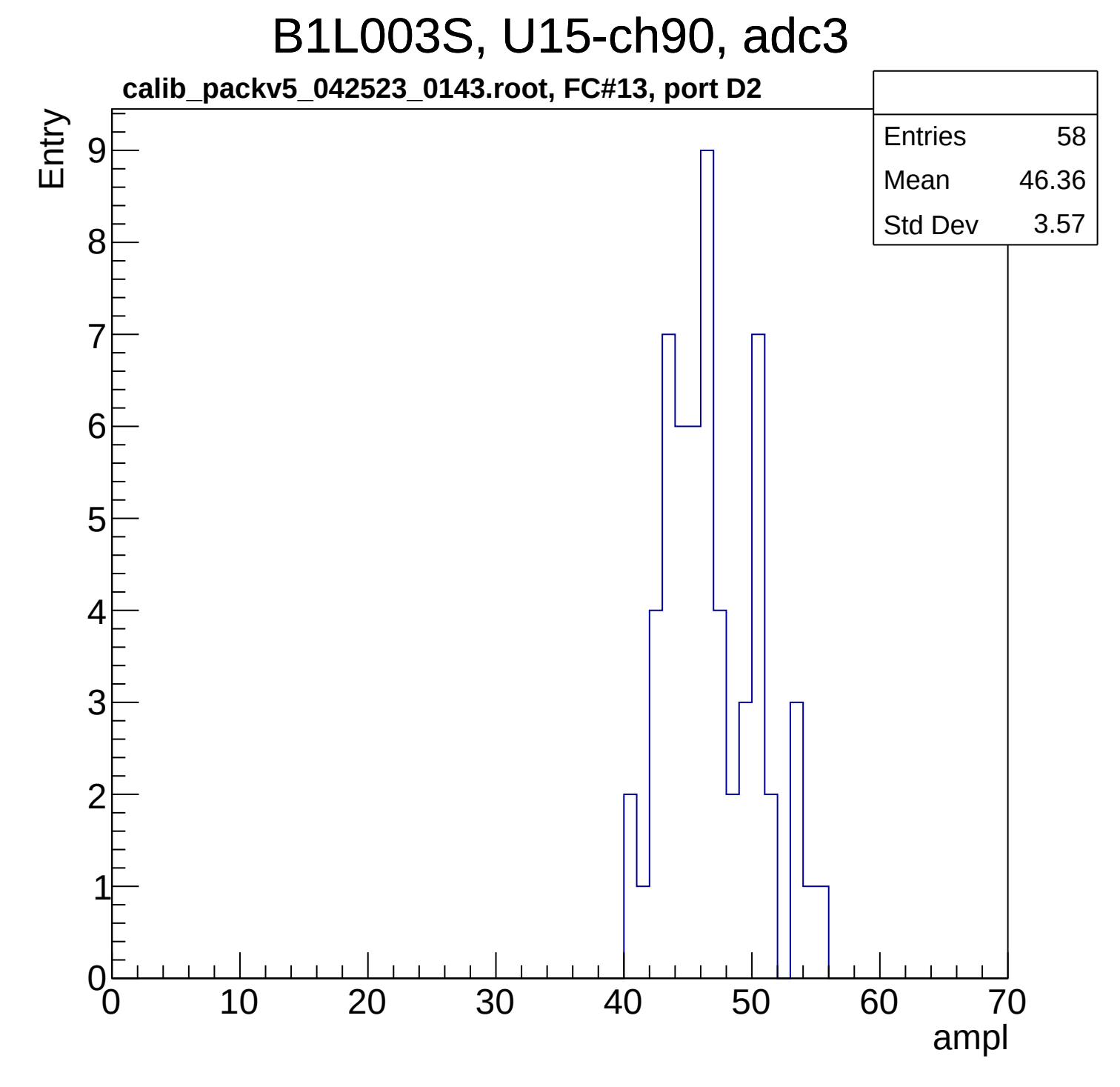
B1L003S, U15-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 46.36
Std Dev 3.57
Entry
ampl
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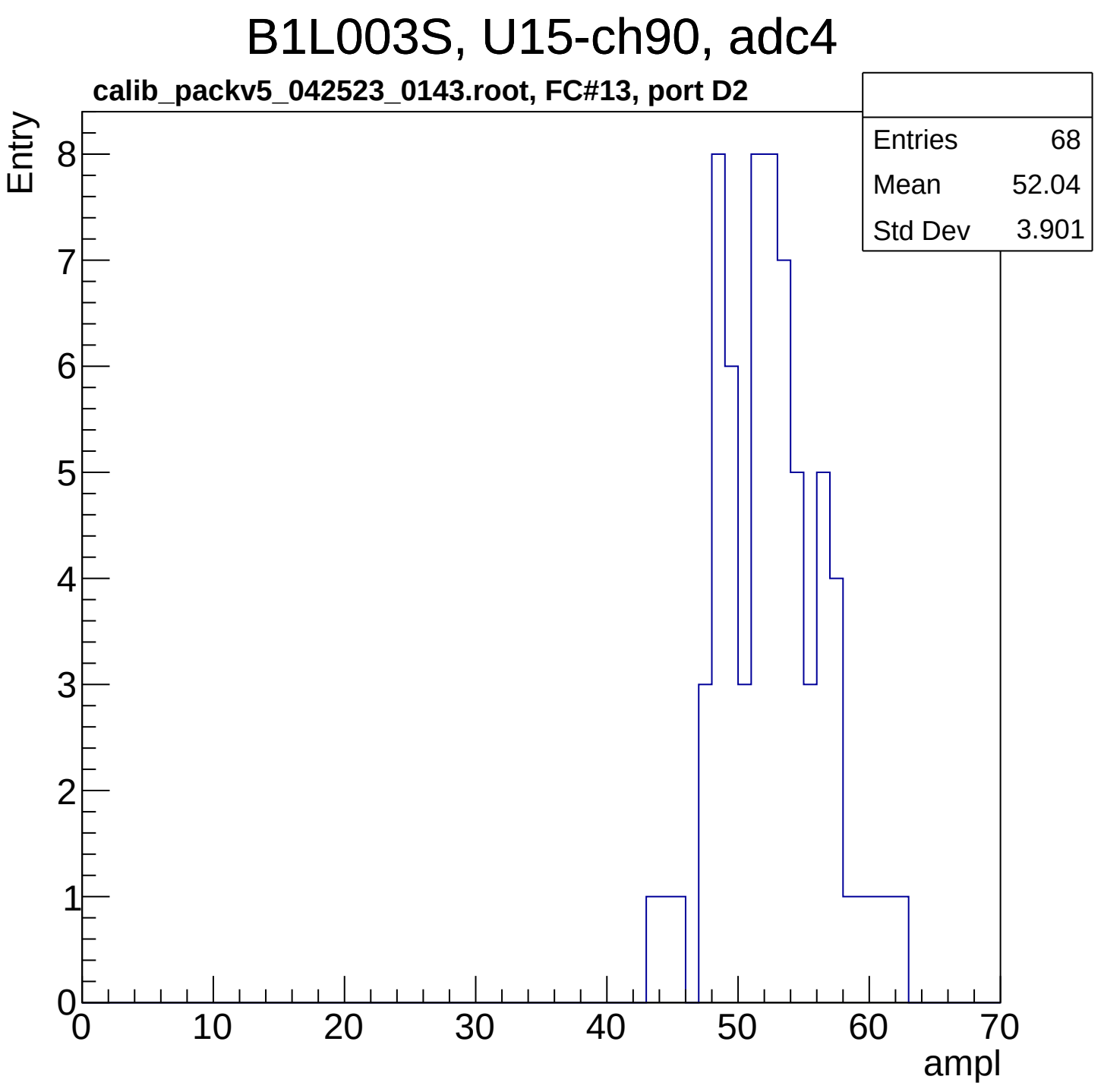
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 52.04
Std Dev 3.901
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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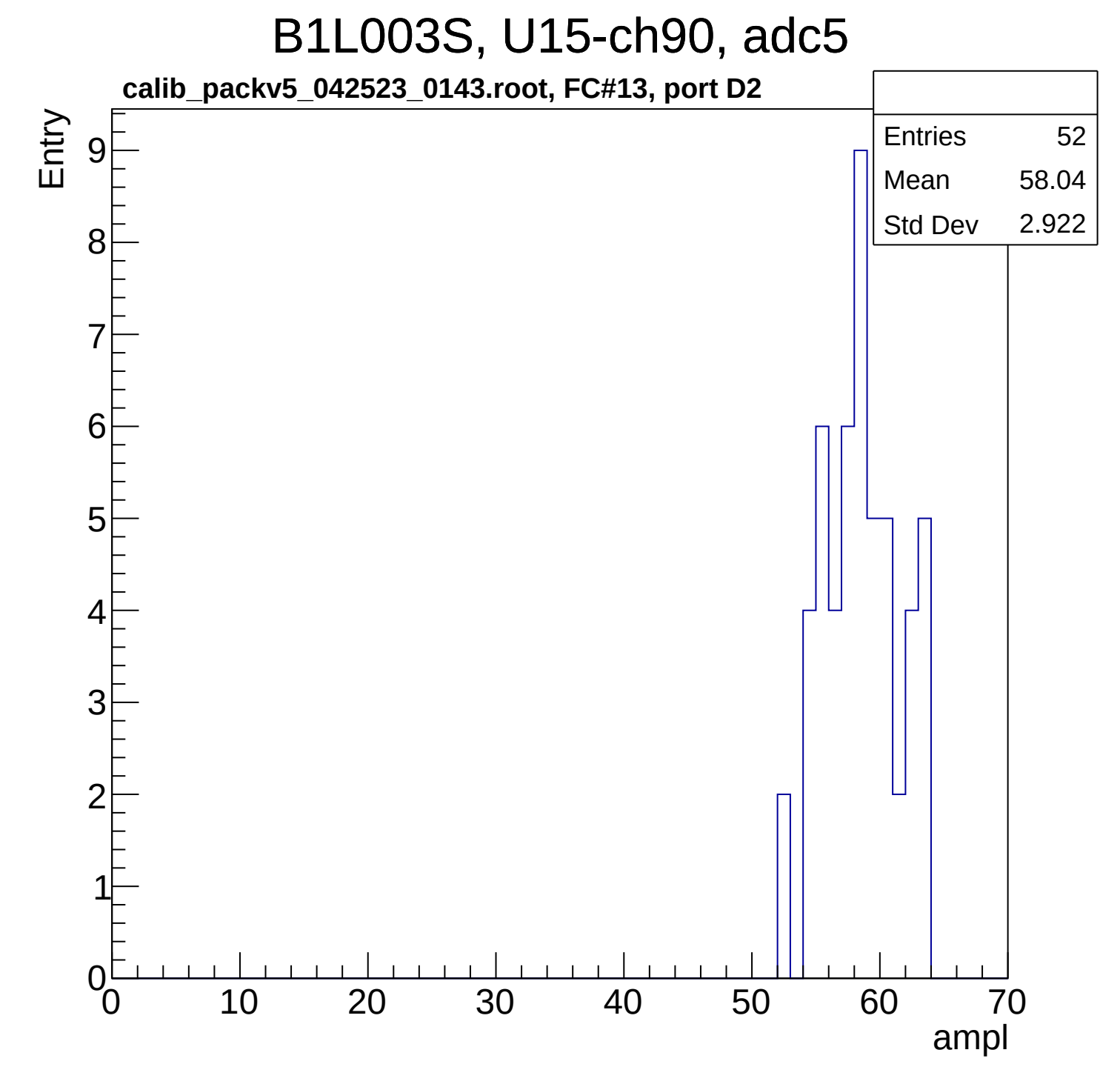
B1L003S, U15-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.04
Std Dev 2.922
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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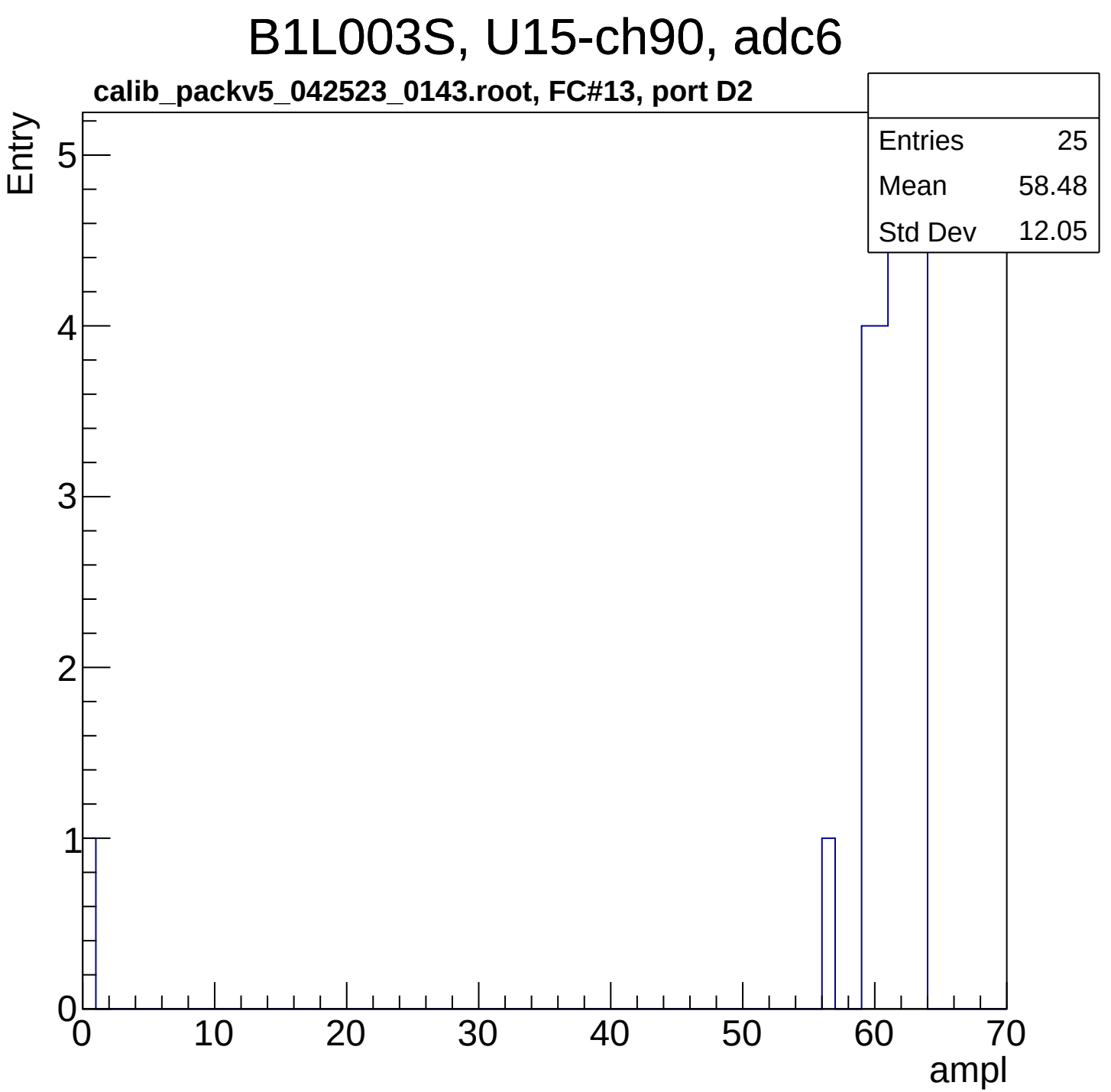
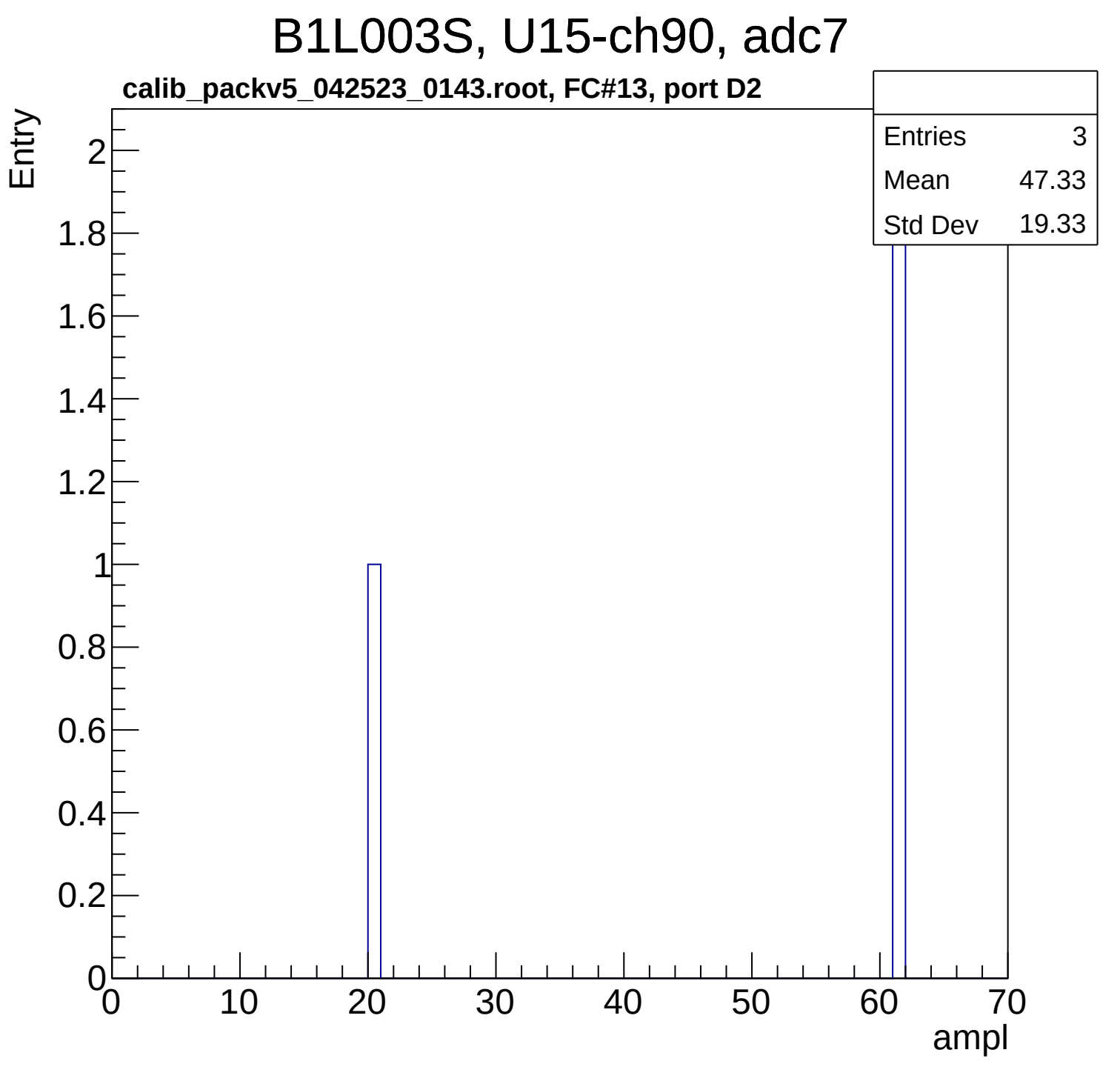
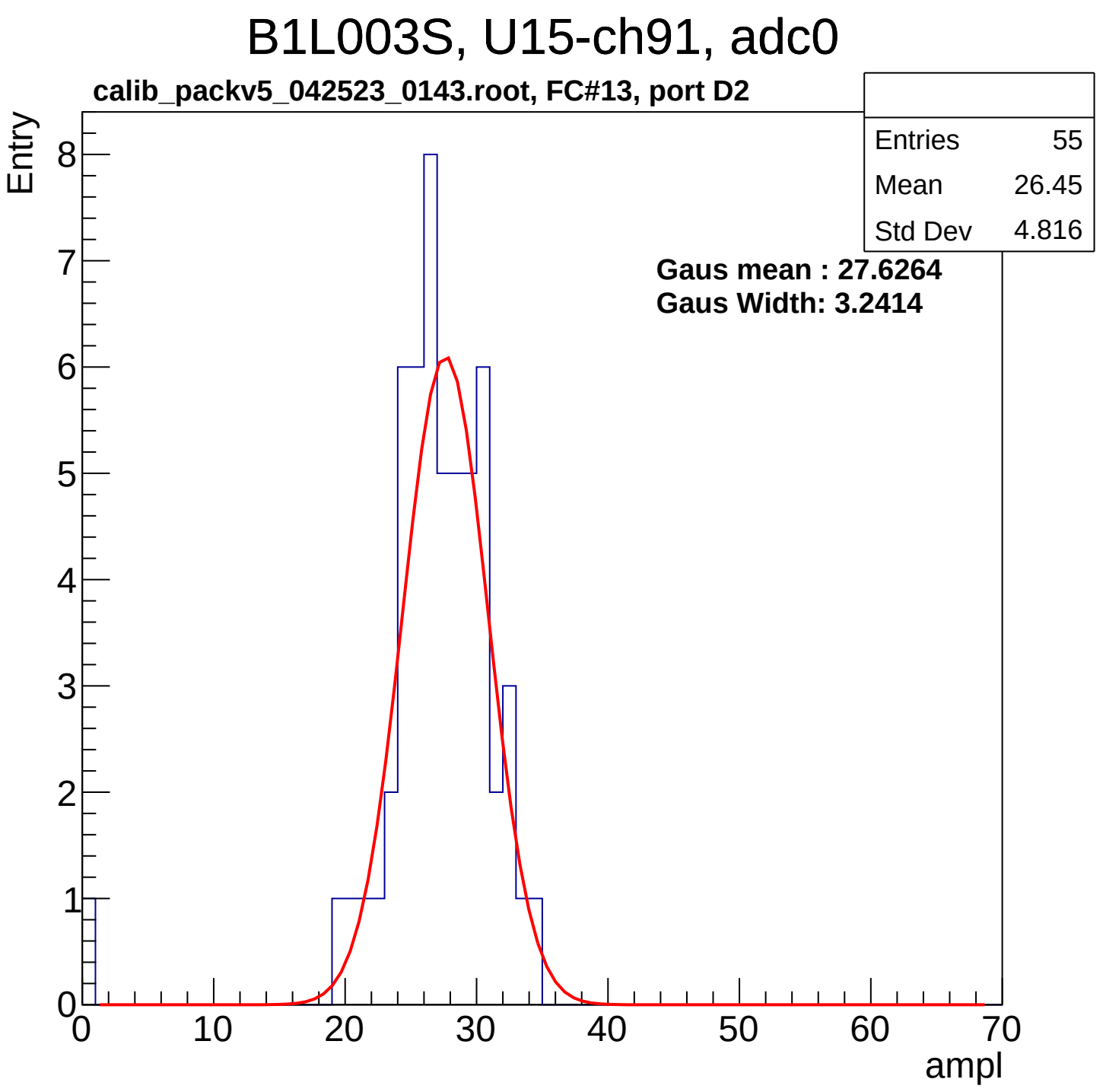
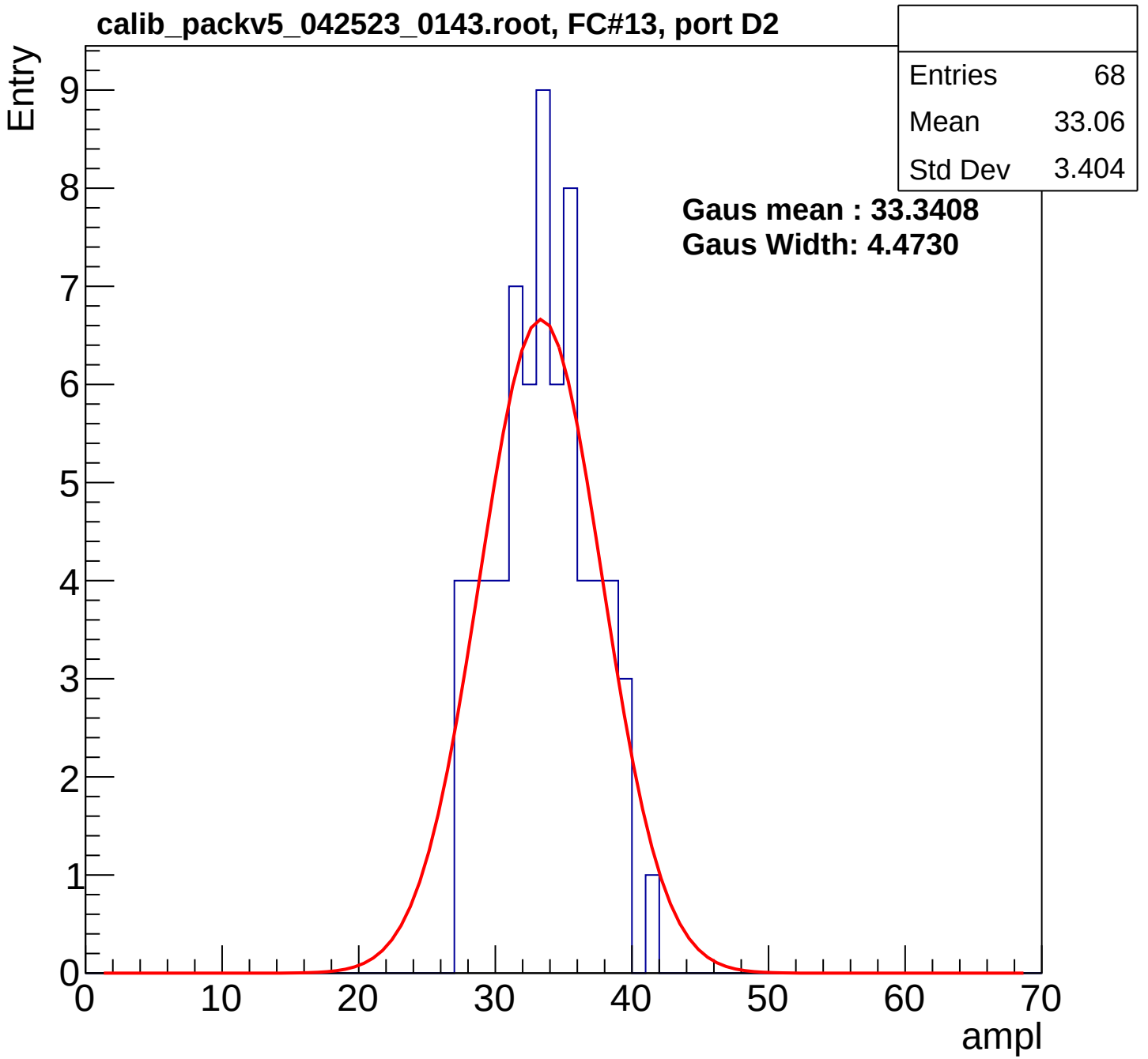
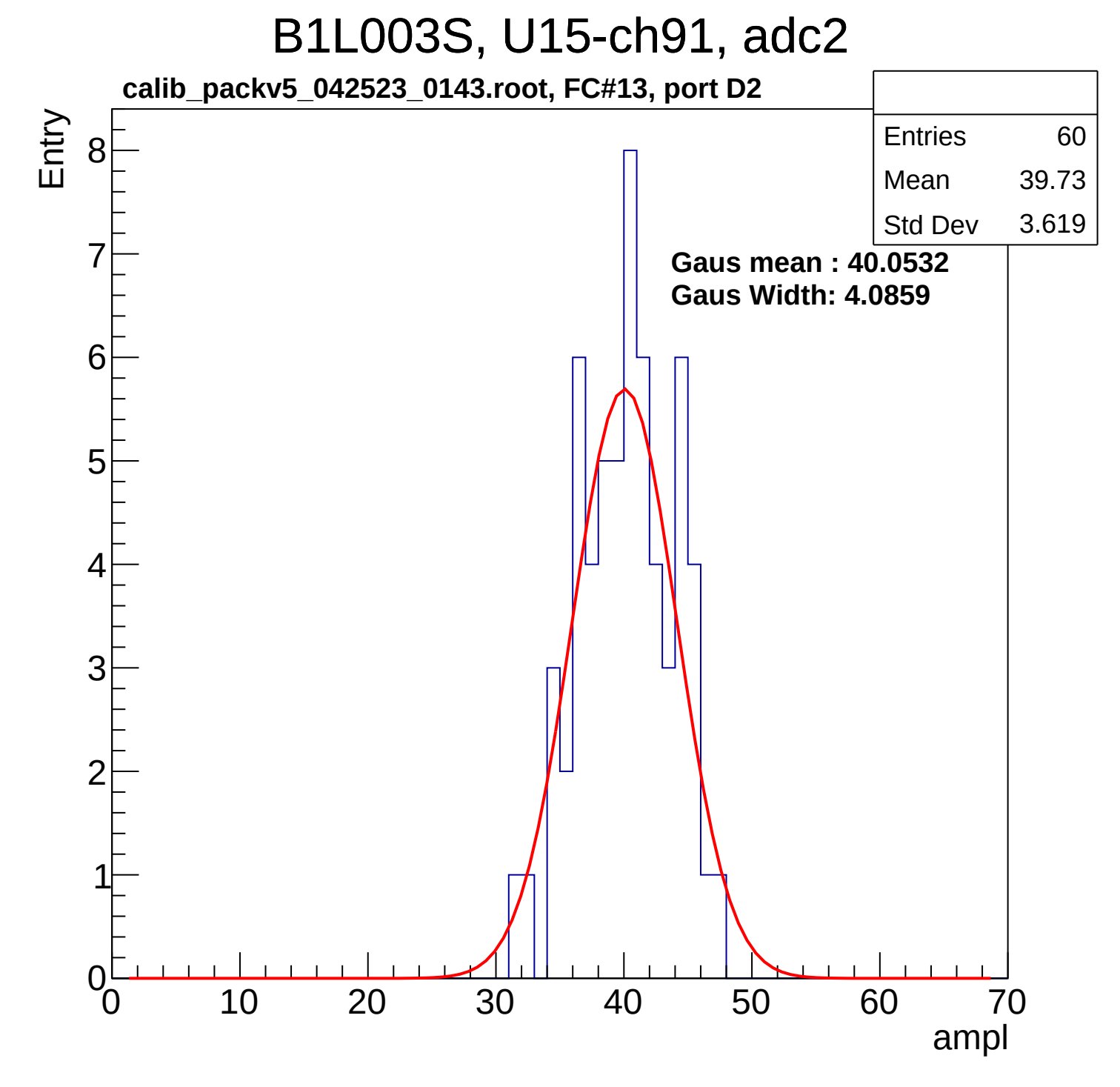

B1L003S, U15-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 25
Mean 58.48
Std Dev 12.05
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U15-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 47.33
Std Dev 19.33
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 26.45
Std Dev 4.816
Gaus mean : 27.6264
Gaus Width: 3.2414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 33.06
Std Dev 3.404
Gaus mean : 33.3408
Gaus Width: 4.4730
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 39.73
Std Dev 3.619
Gaus mean : 40.0532
Gaus Width: 4.0859
Entry
ampl
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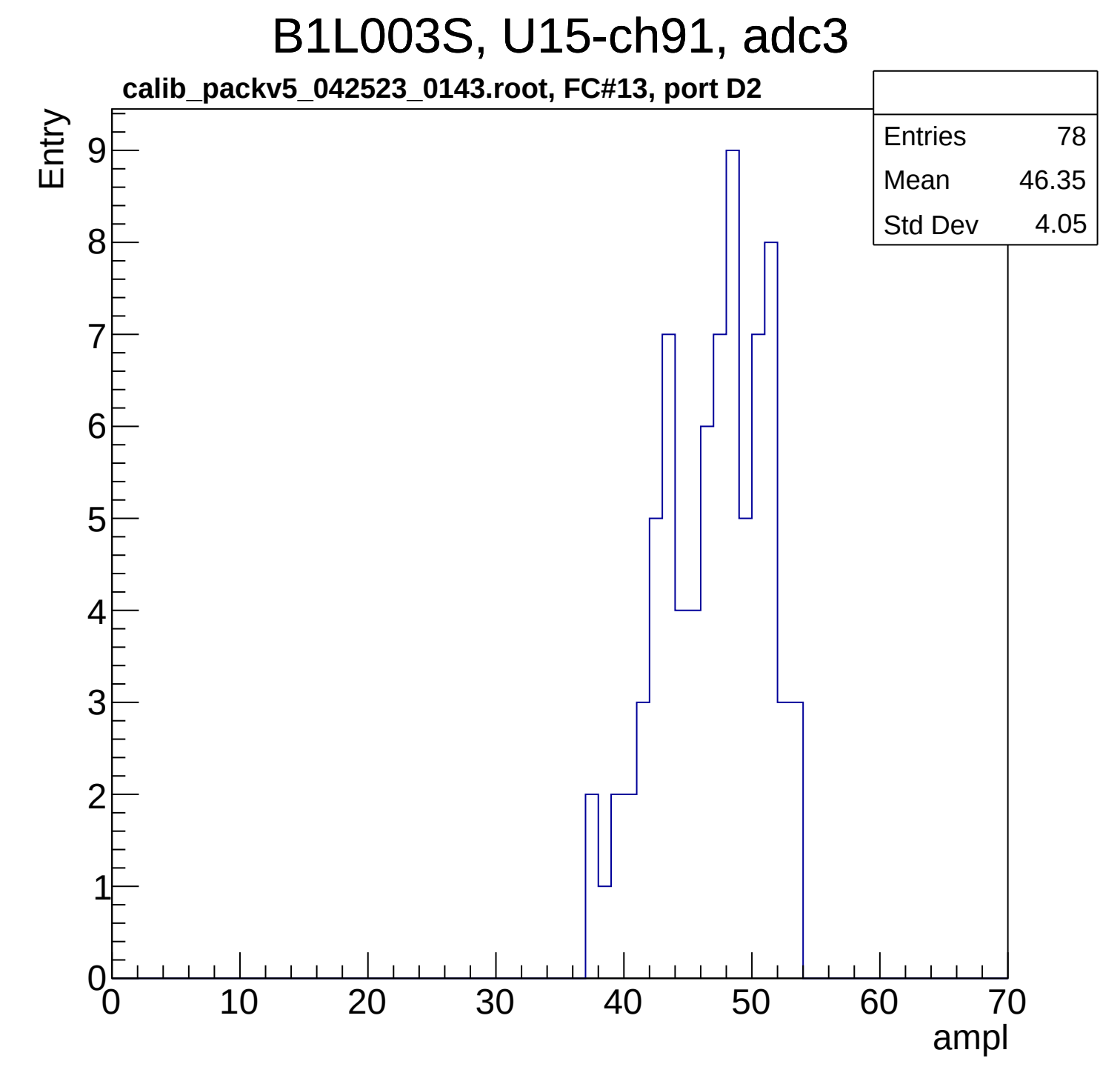
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 46.35
Std Dev 4.05
Entry
ampl
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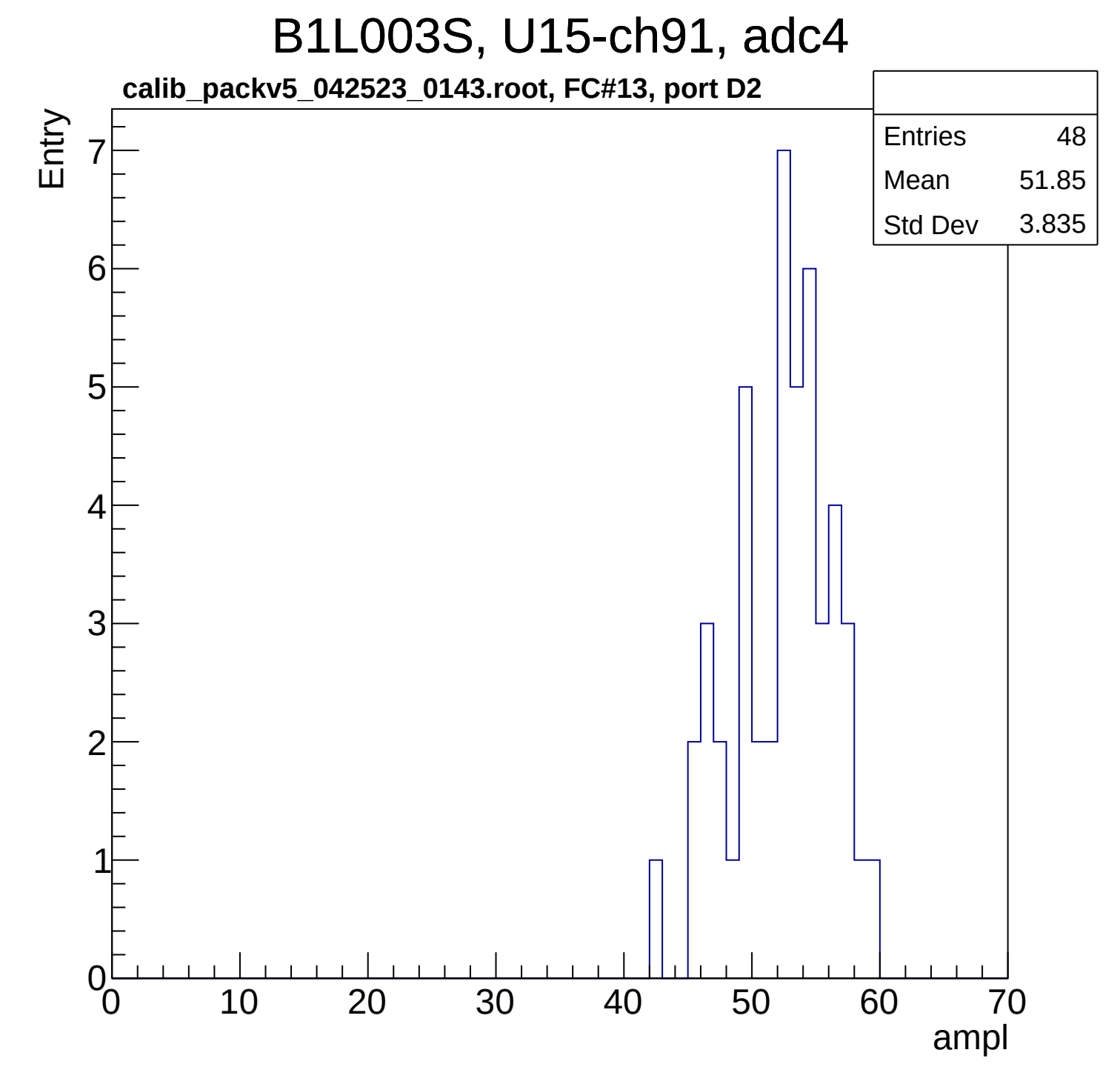
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 51.85
Std Dev 3.835
Entry
ampl
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1
2
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4
5
6
7
0
10
20
30
40
50
60
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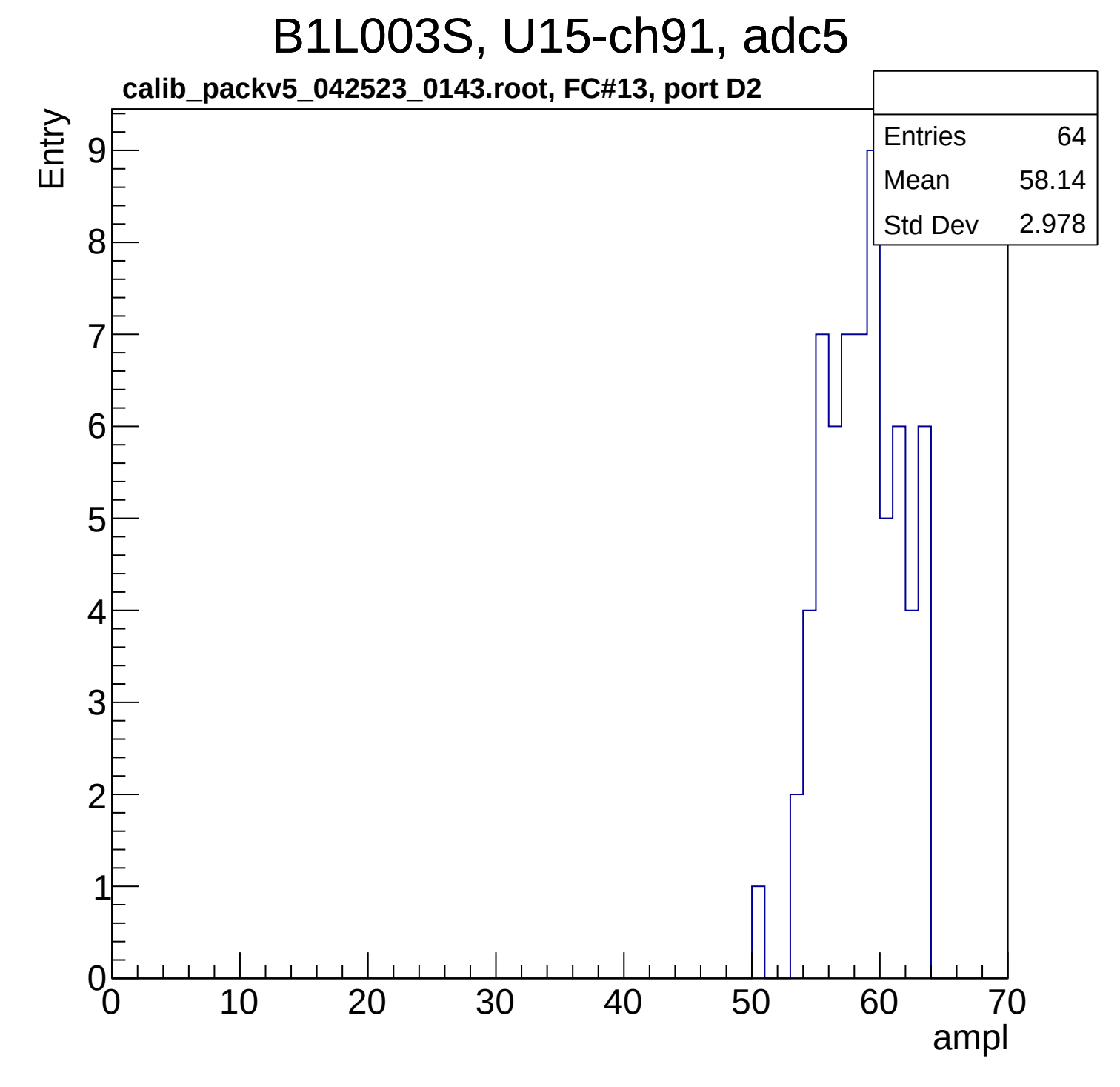
B1L003S, U15-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 58.14
Std Dev 2.978
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
50
60
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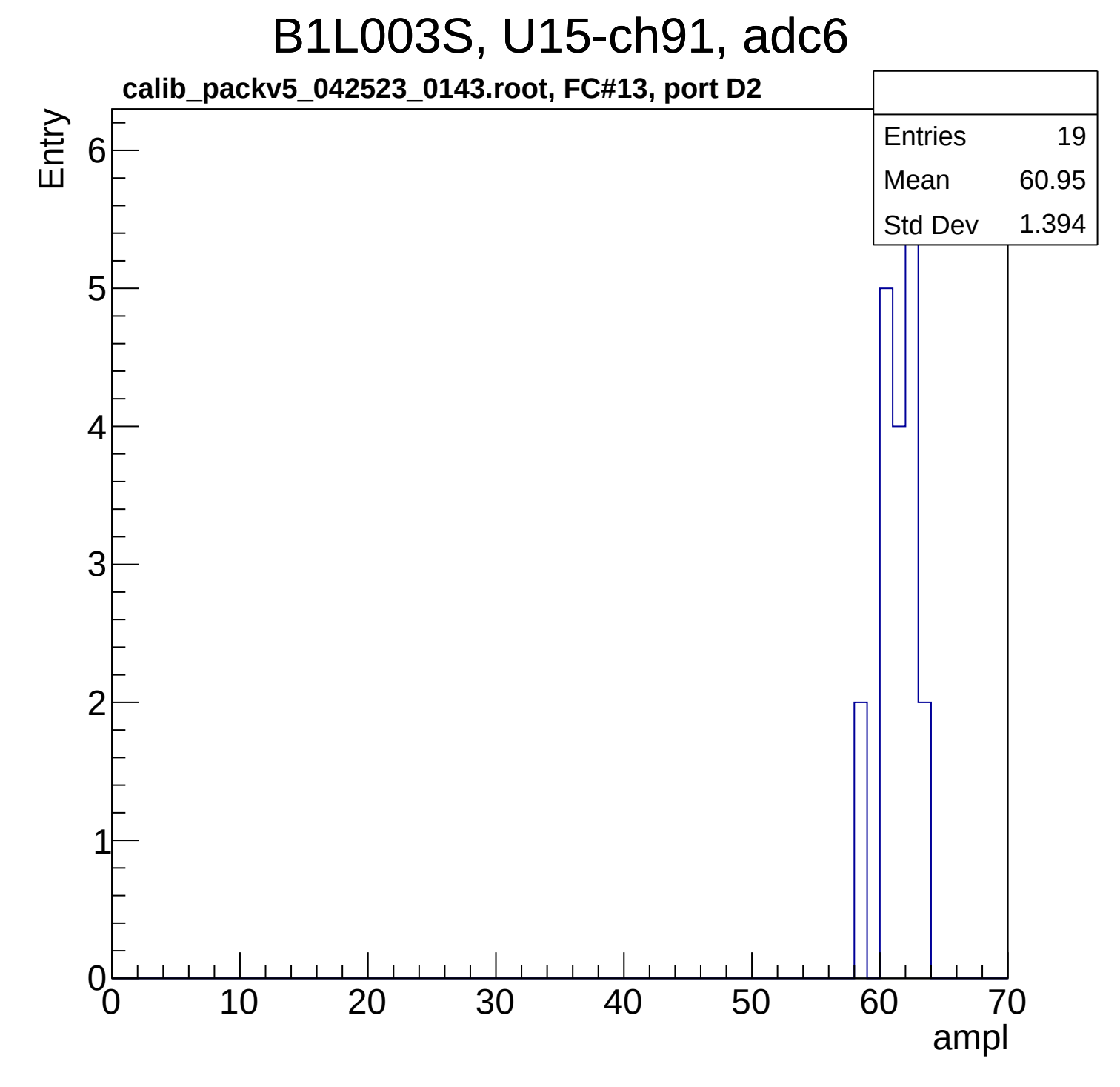
B1L003S, U15-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 60.95
Std Dev 1.394
Entry
0
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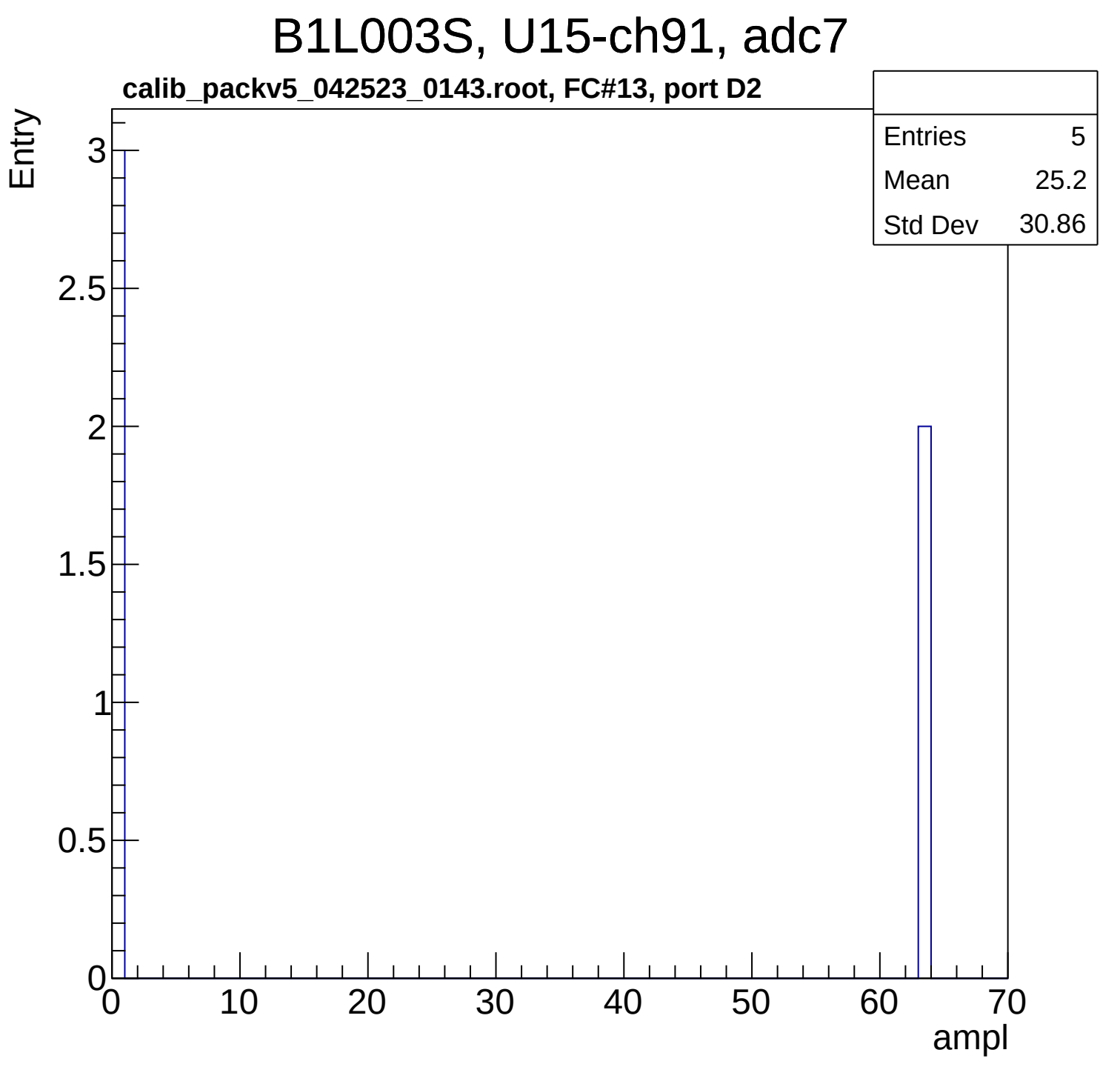
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
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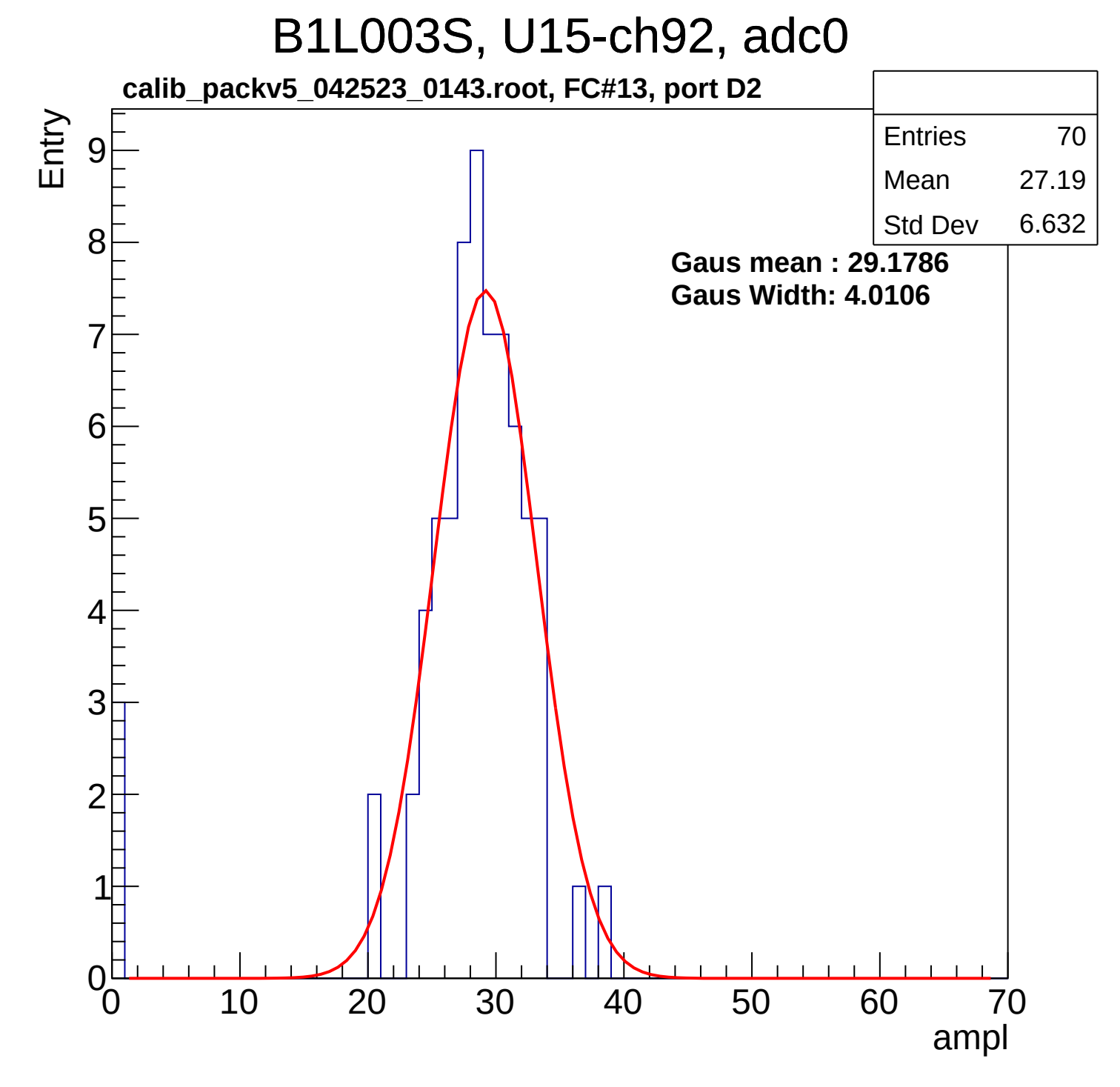
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.19
Std Dev 6.632
Gaus mean : 29.1786
Gaus Width: 4.0106
Entry
ampl
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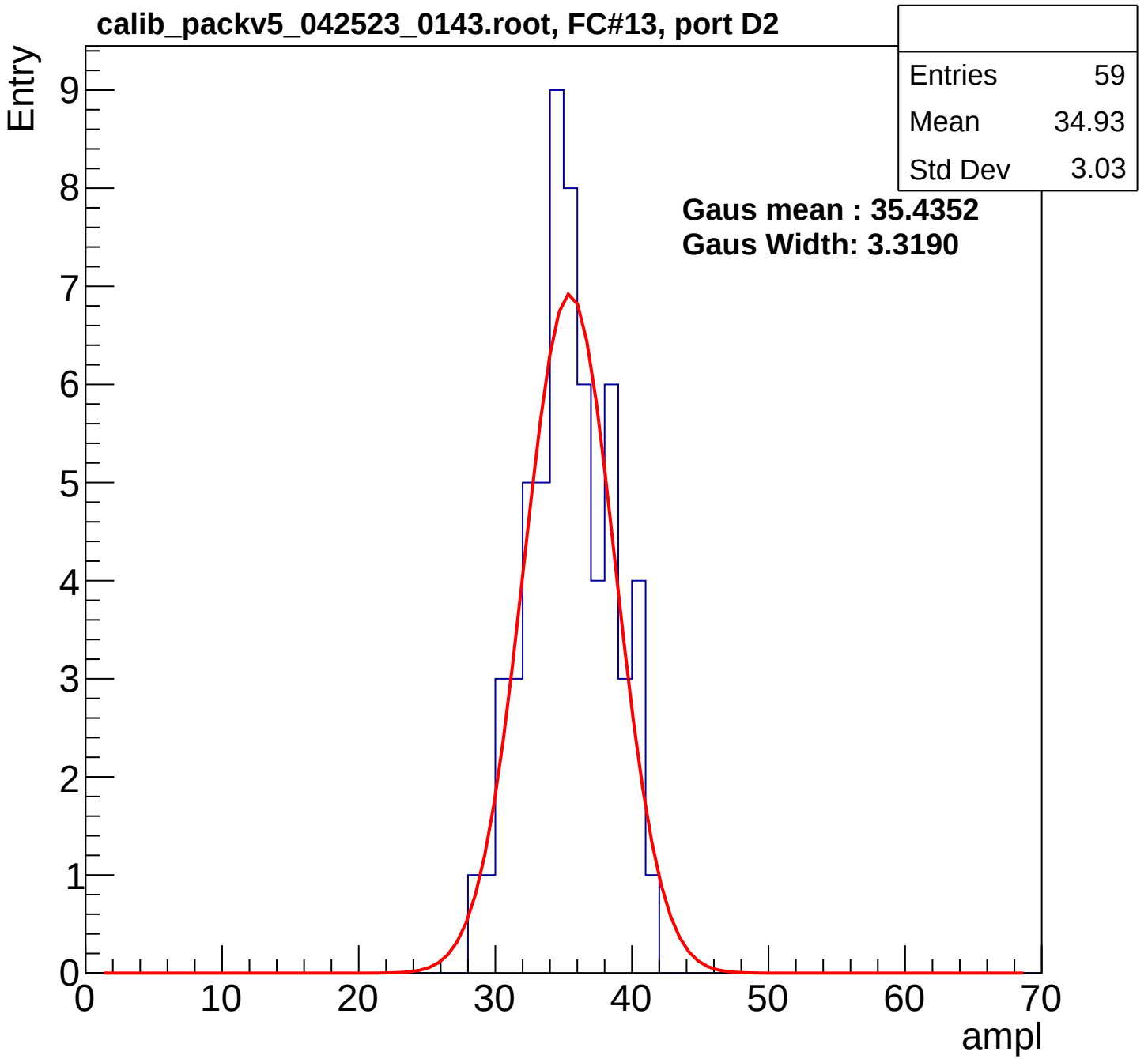
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 34.93
Std Dev 3.03
Gaus mean : 35.4352
Gaus Width: 3.3190
Entry
ampl
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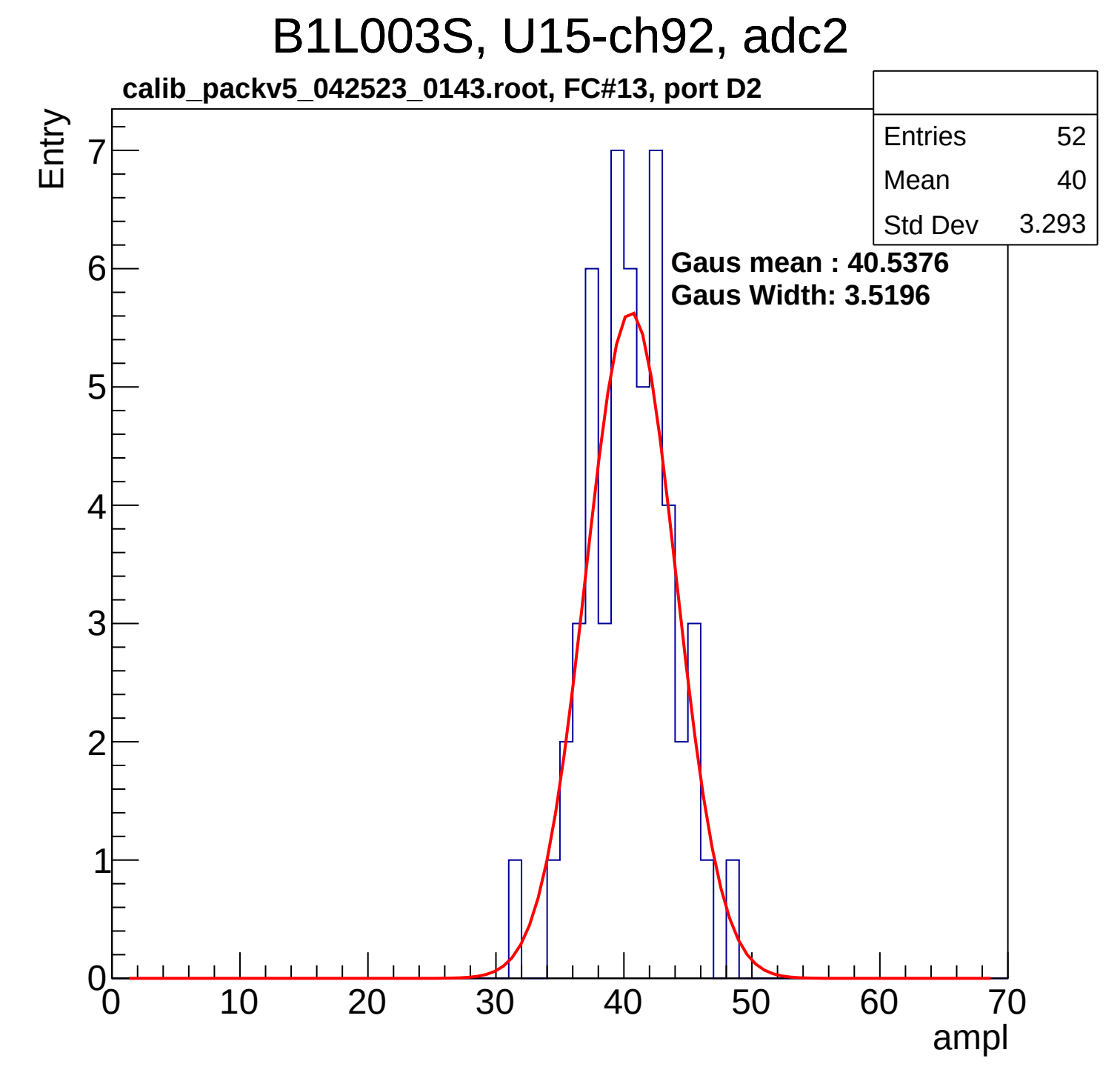
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 40
Std Dev 3.293
Gaus mean : 40.5376
Gaus Width: 3.5196
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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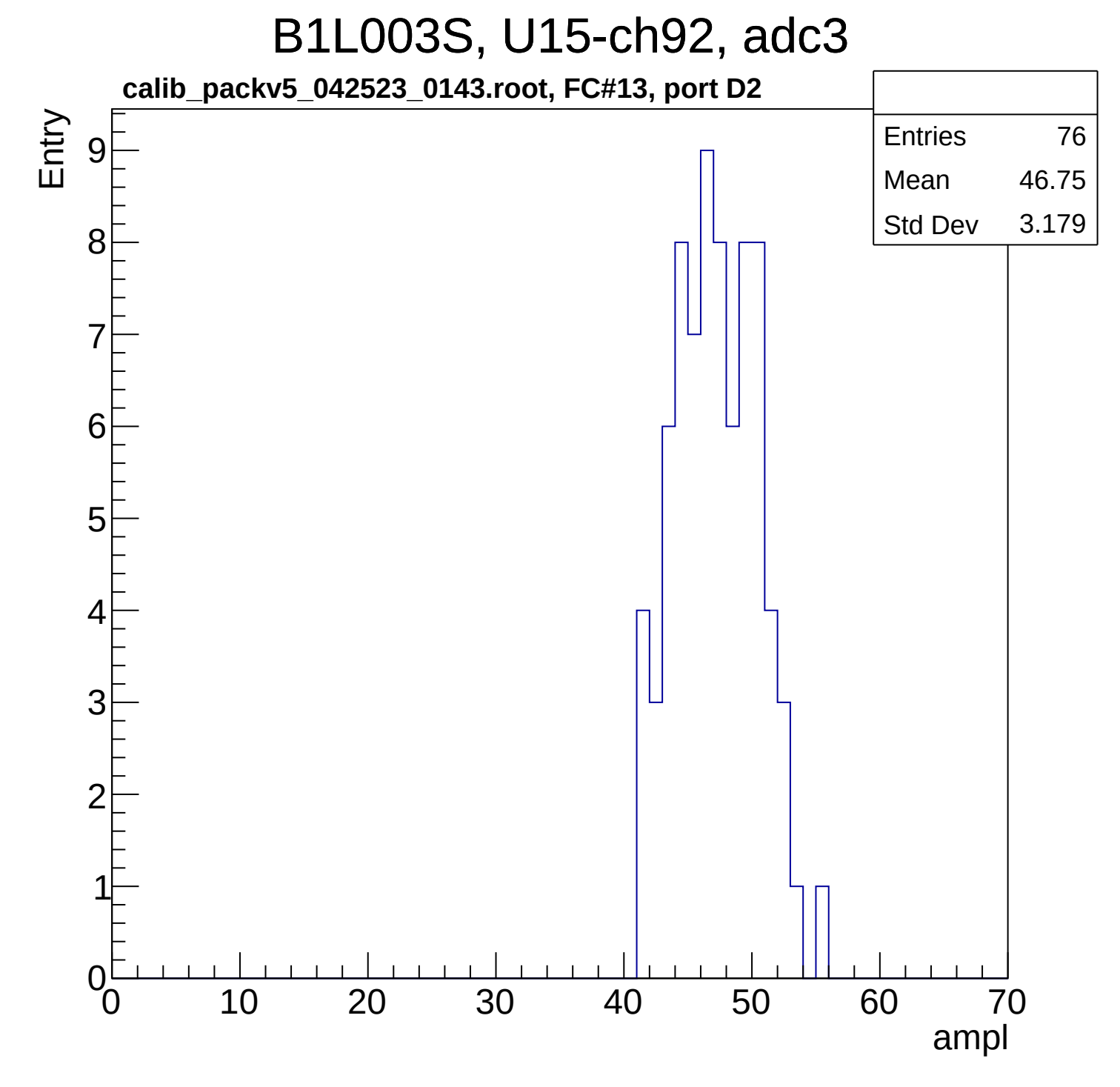
B1L003S, U15-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 46.75
Std Dev 3.179
Entry
ampl
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3
4
5
6
7
8
9
0
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40
50
60
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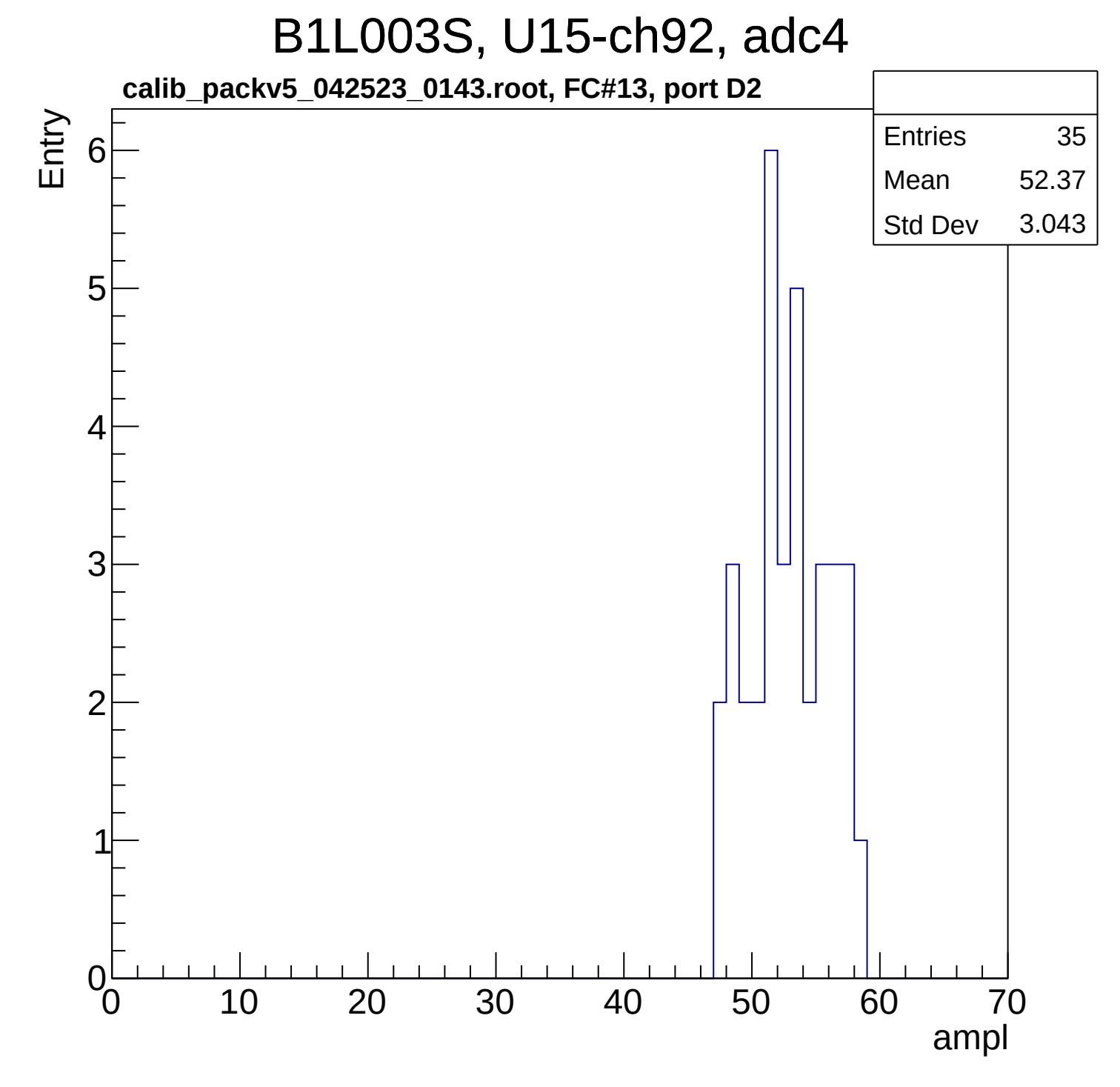
B1L003S, U15-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 52.37
Std Dev 3.043
Entry
0
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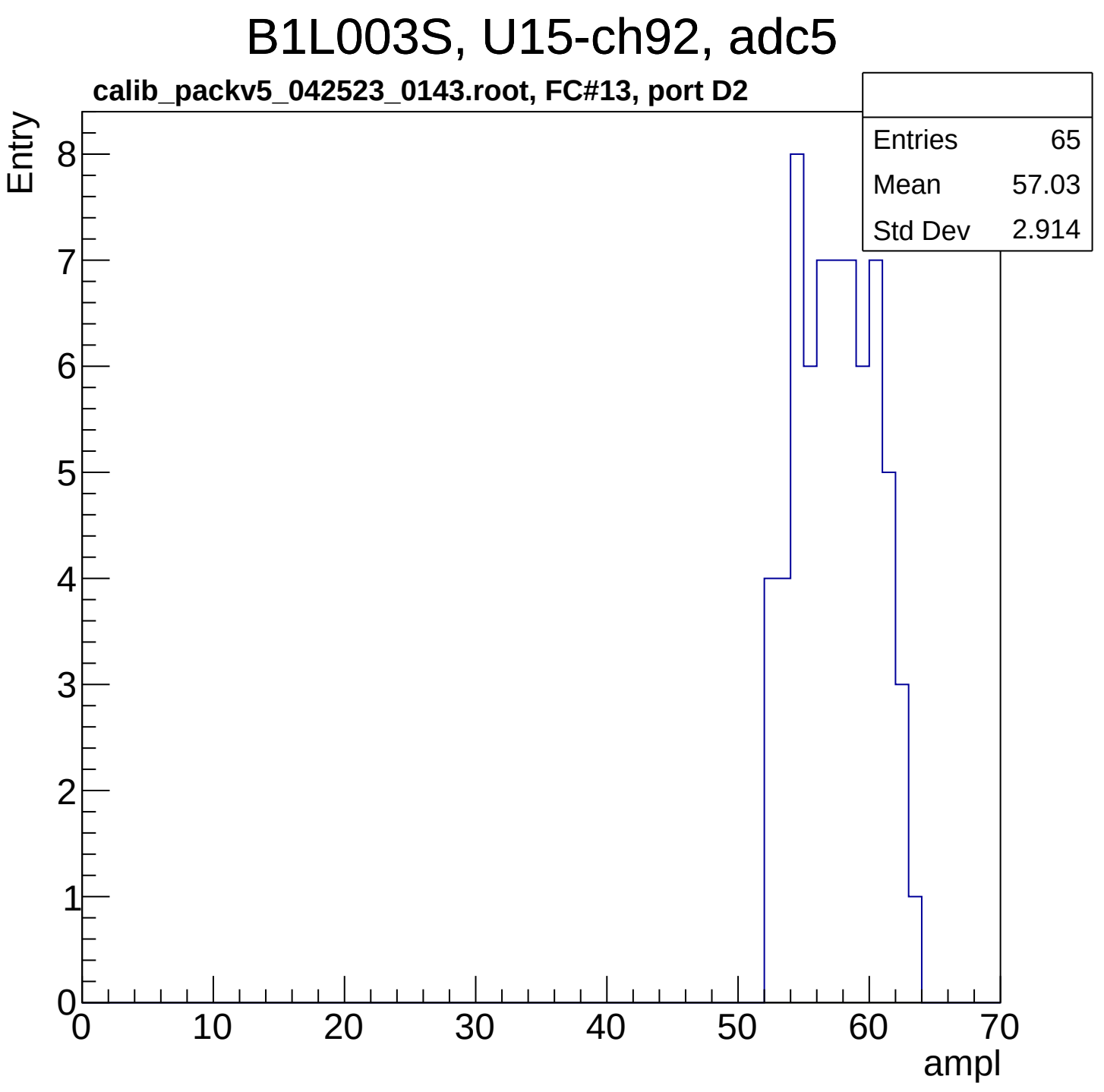
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 57.03
Std Dev 2.914
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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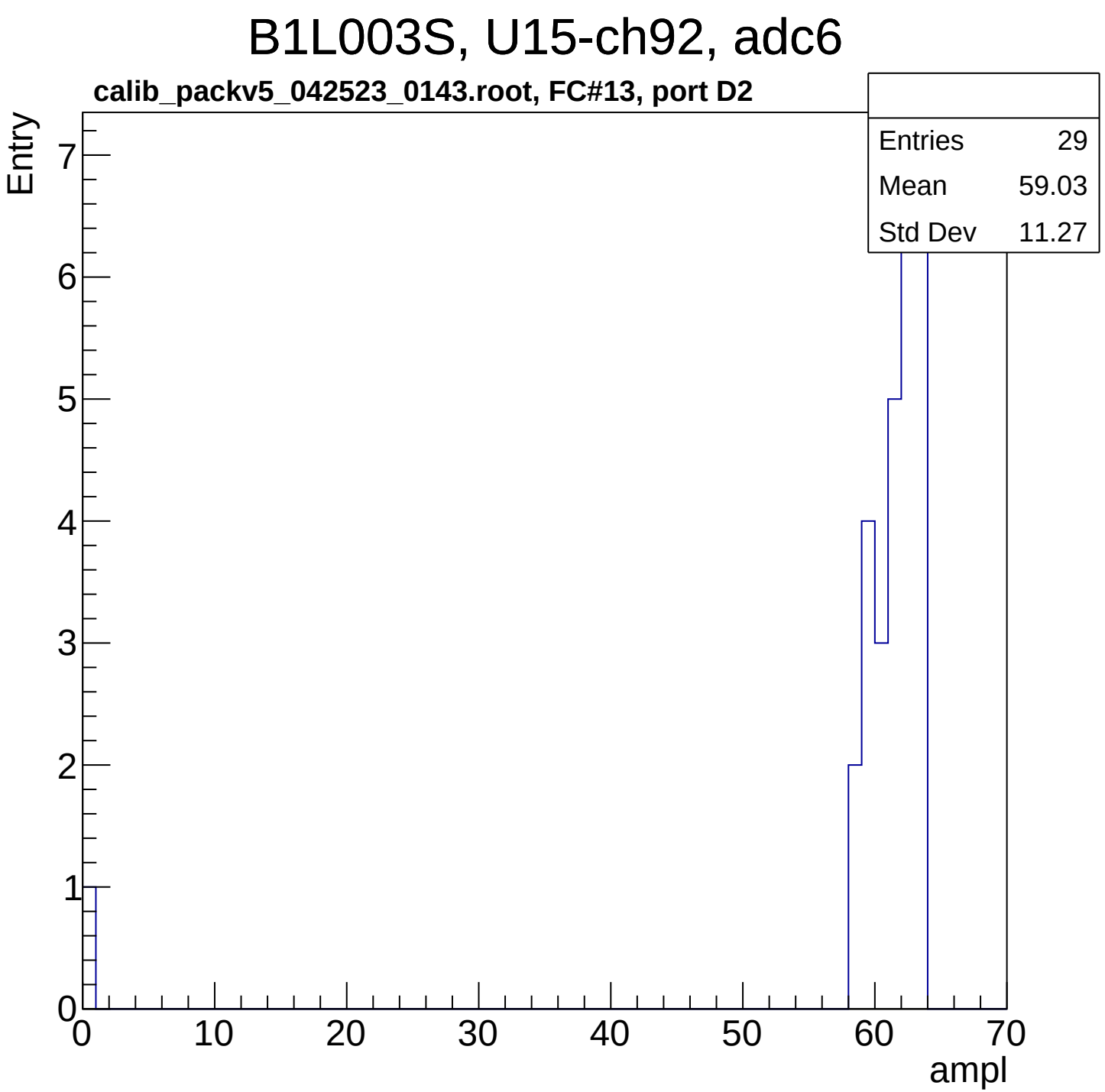
B1L003S, U15-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 29
Mean 59.03
Std Dev 11.27
Entry
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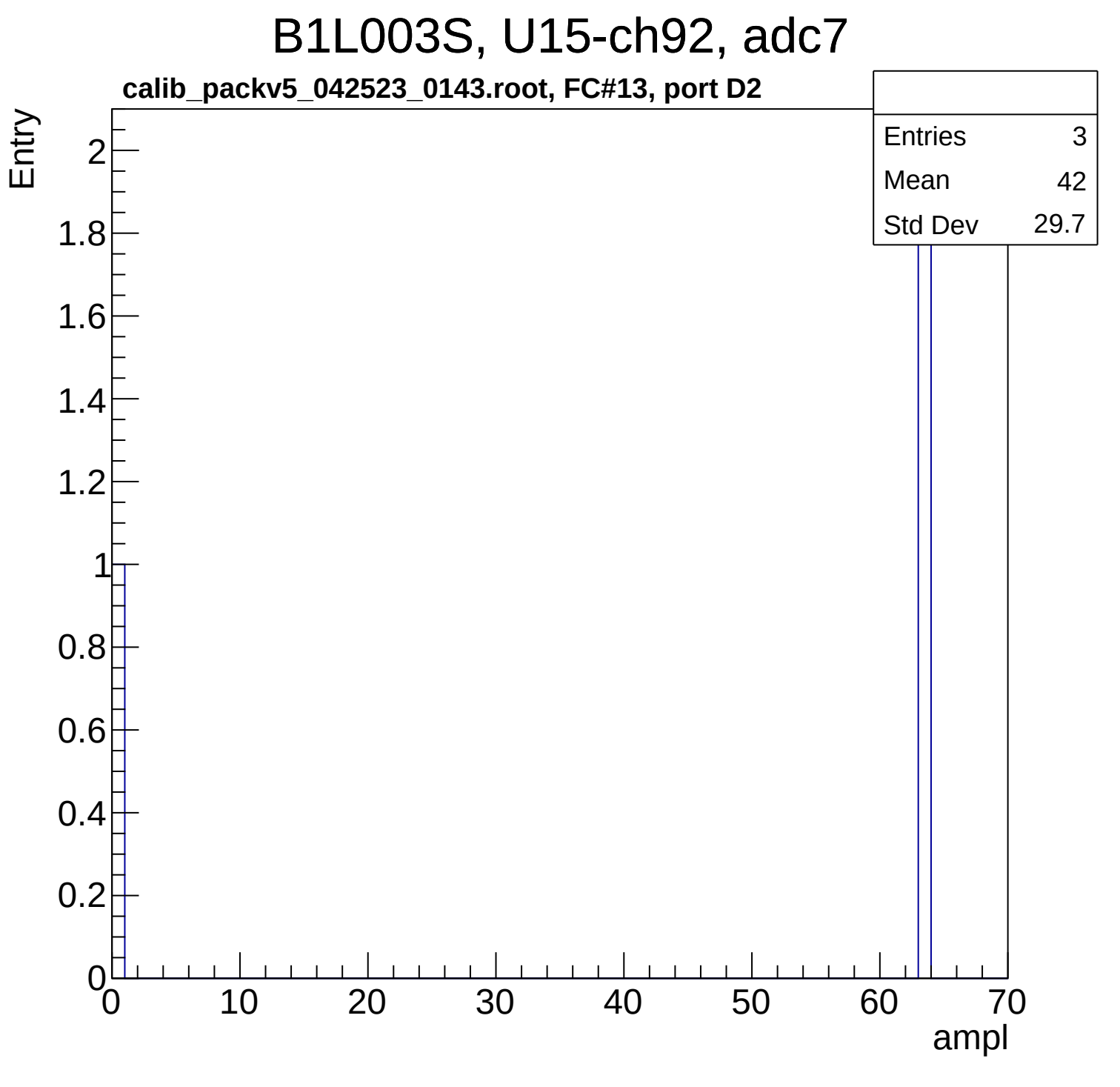
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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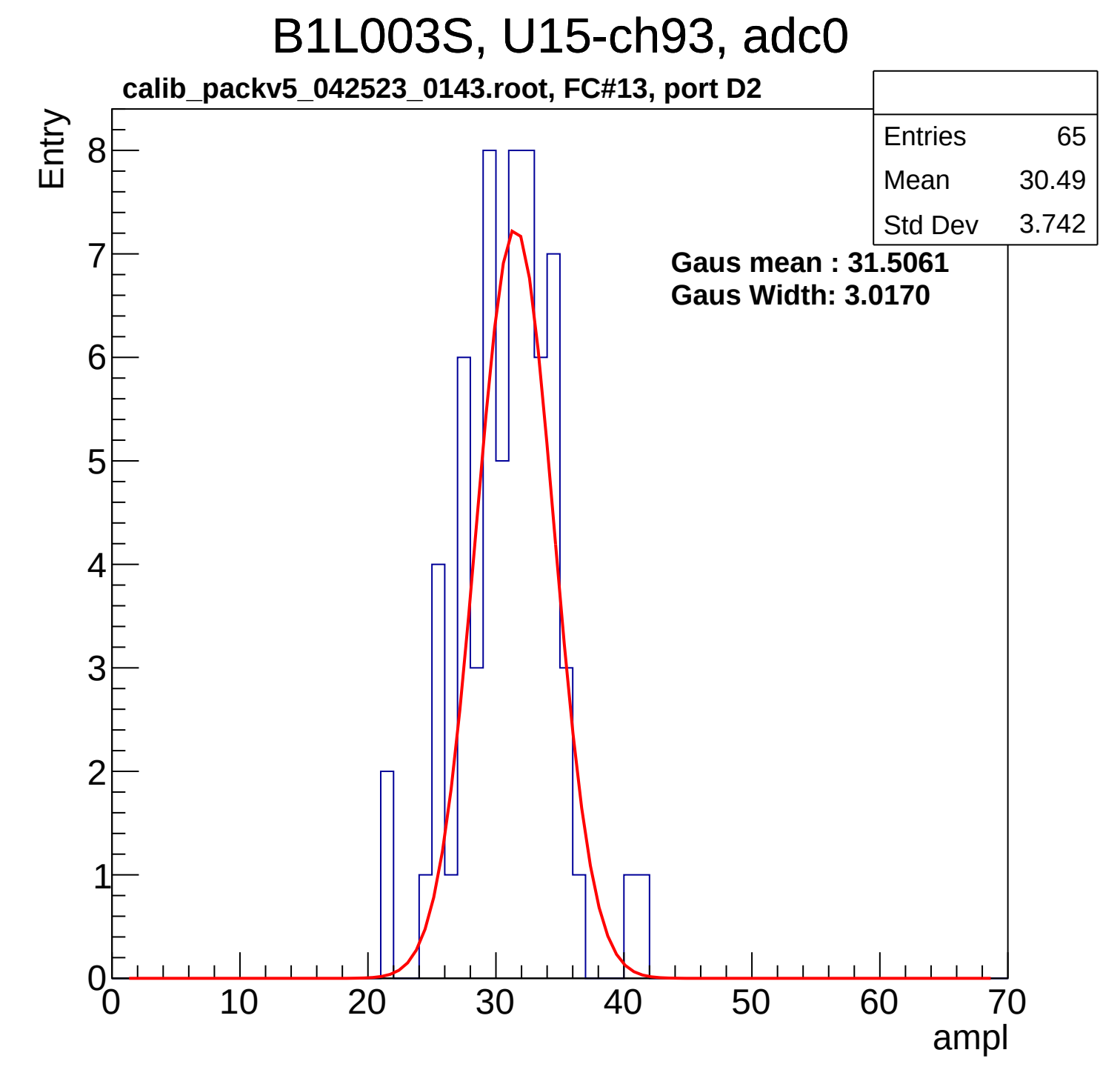
B1L003S, U15-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.49
Std Dev 3.742
Gaus mean : 31.5061
Gaus Width: 3.0170
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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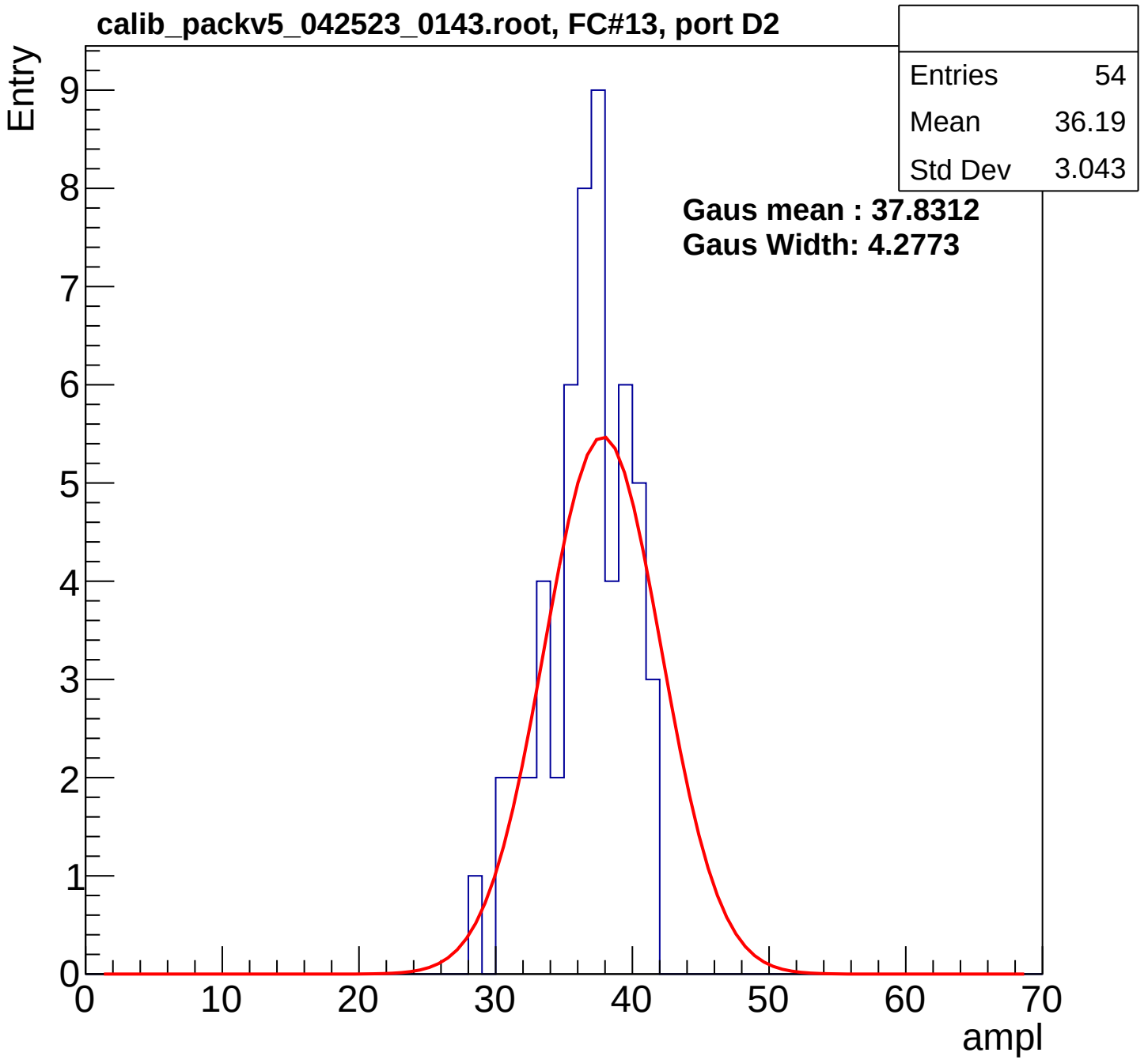
B1L003S, U15-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 36.19
Std Dev 3.043
Gaus mean : 37.8312
Gaus Width: 4.2773
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
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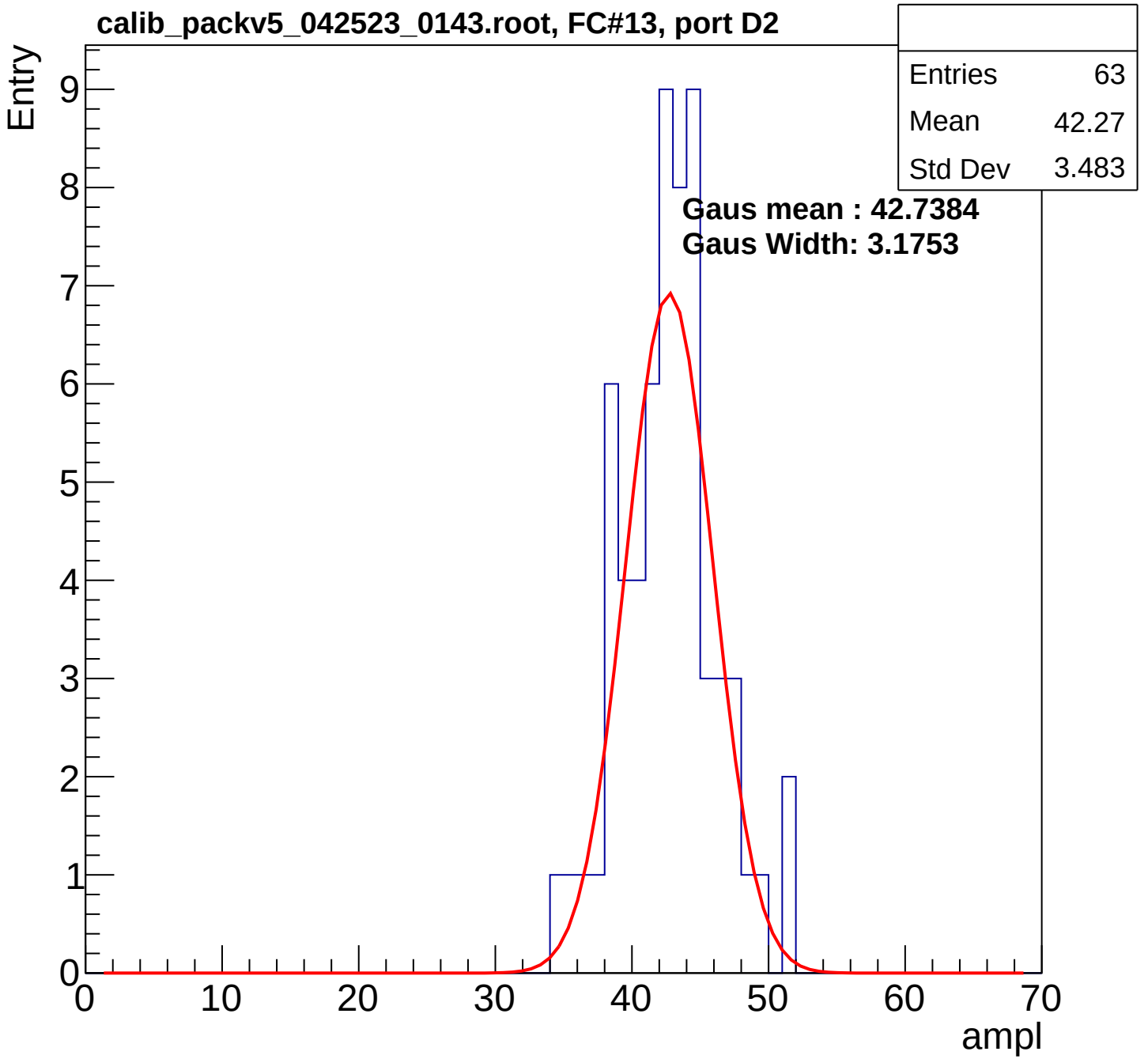
6
7
8
9


B1L003S, U15-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.27
Std Dev 3.483
Gaus mean : 42.7384
Gaus Width: 3.1753
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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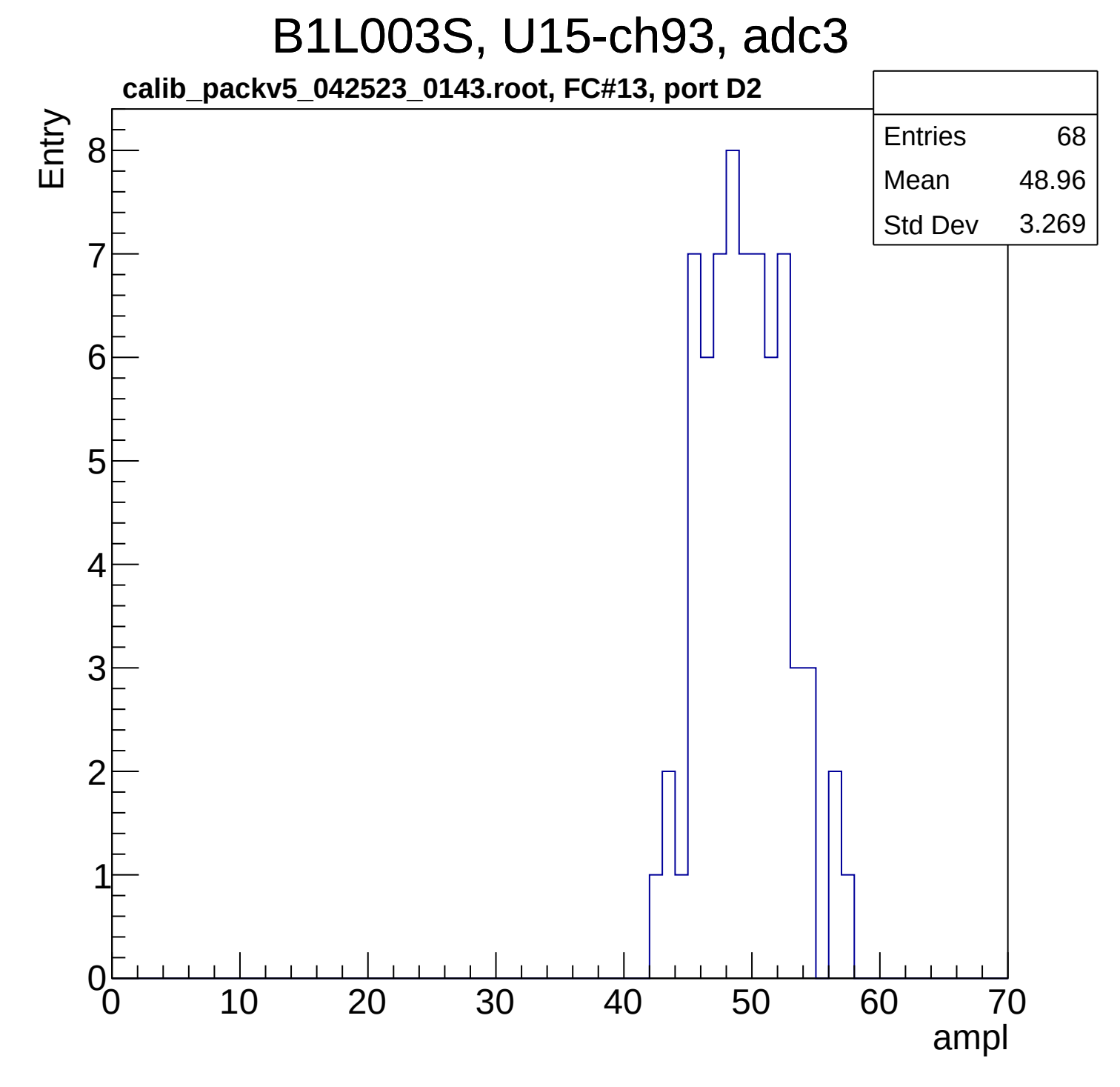
B1L003S, U15-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.96
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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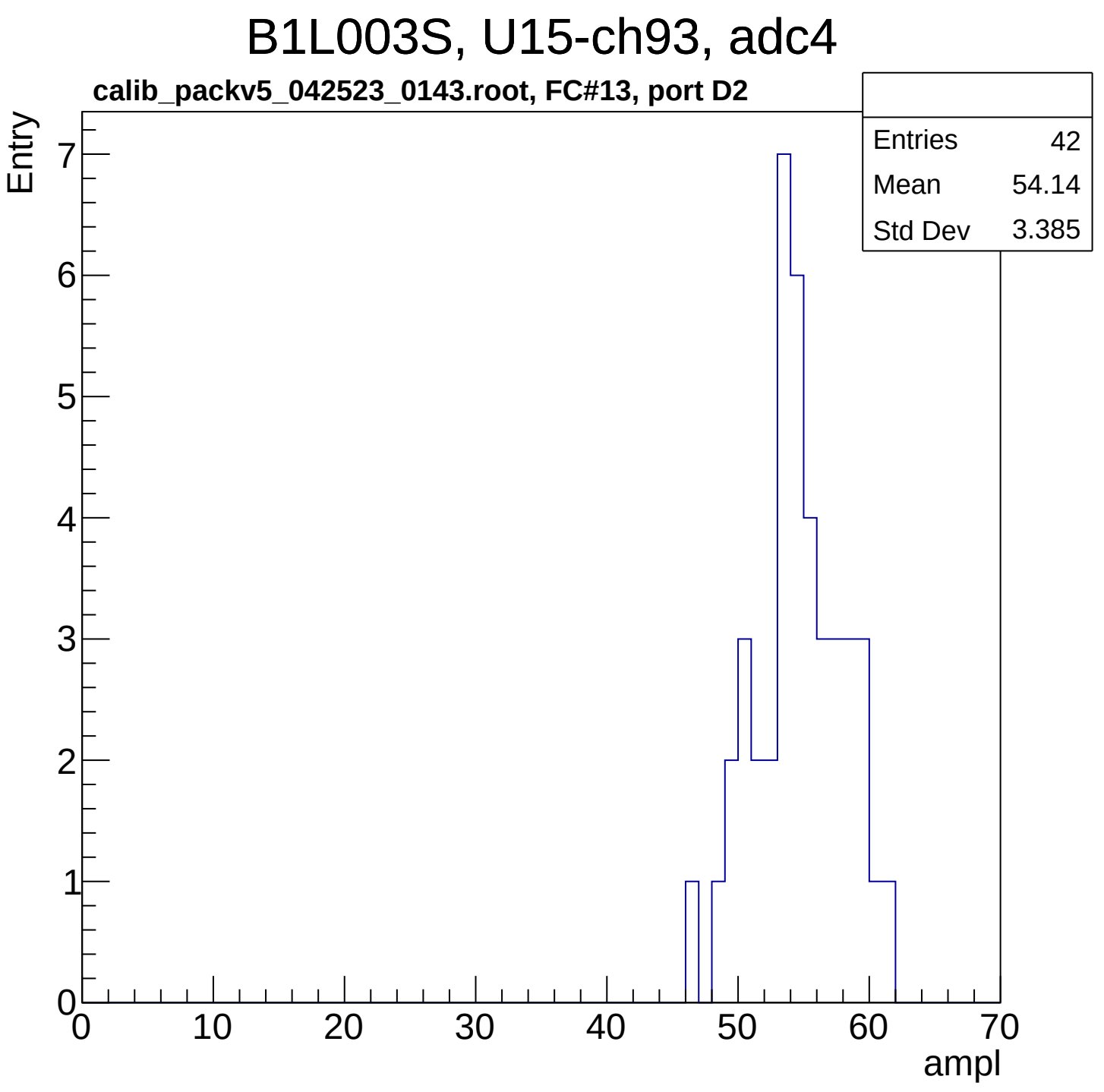
B1L003S, U15-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 54.14
Std Dev 3.385
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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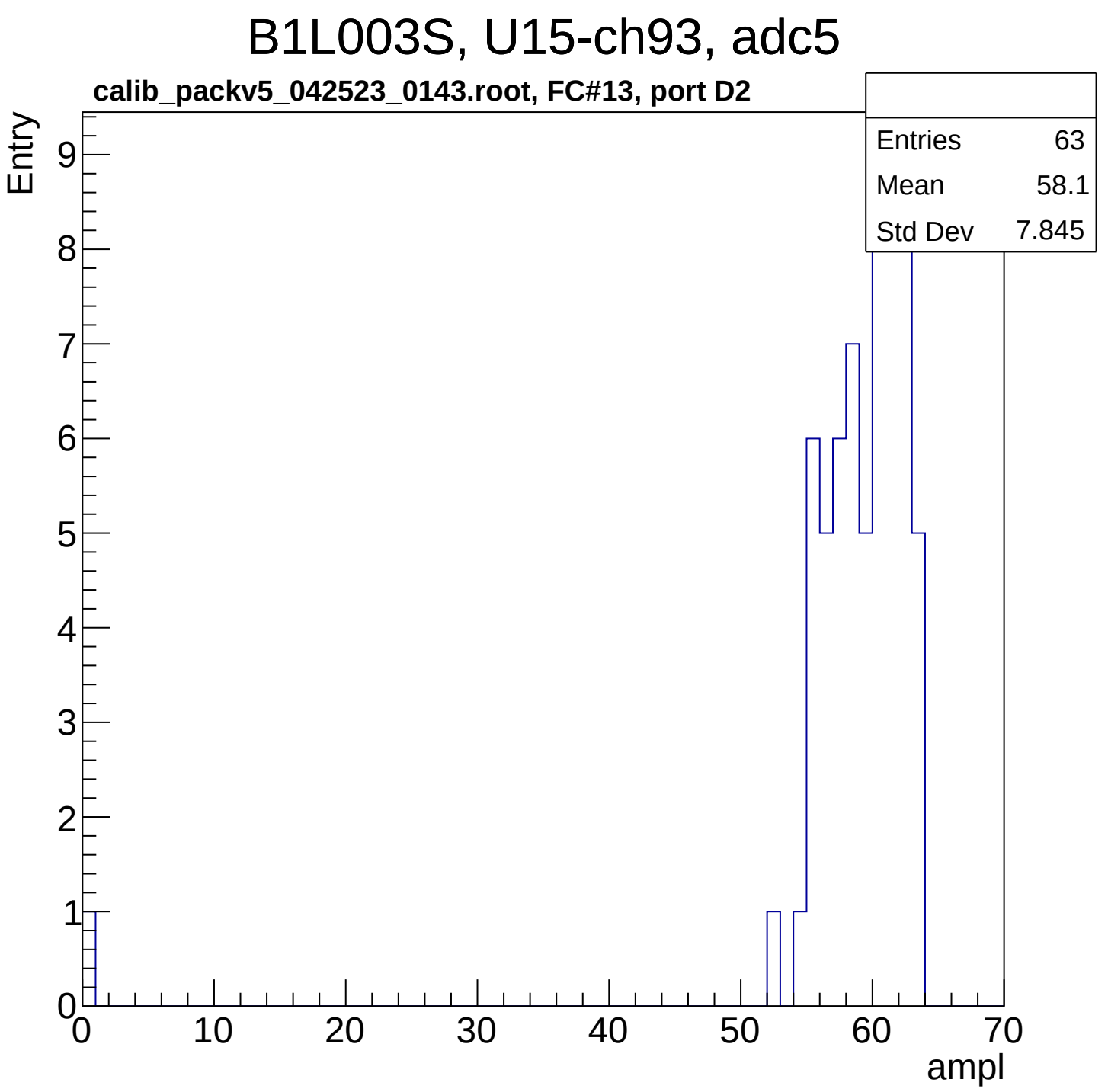
B1L003S, U15-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 58.1
Std Dev 7.845
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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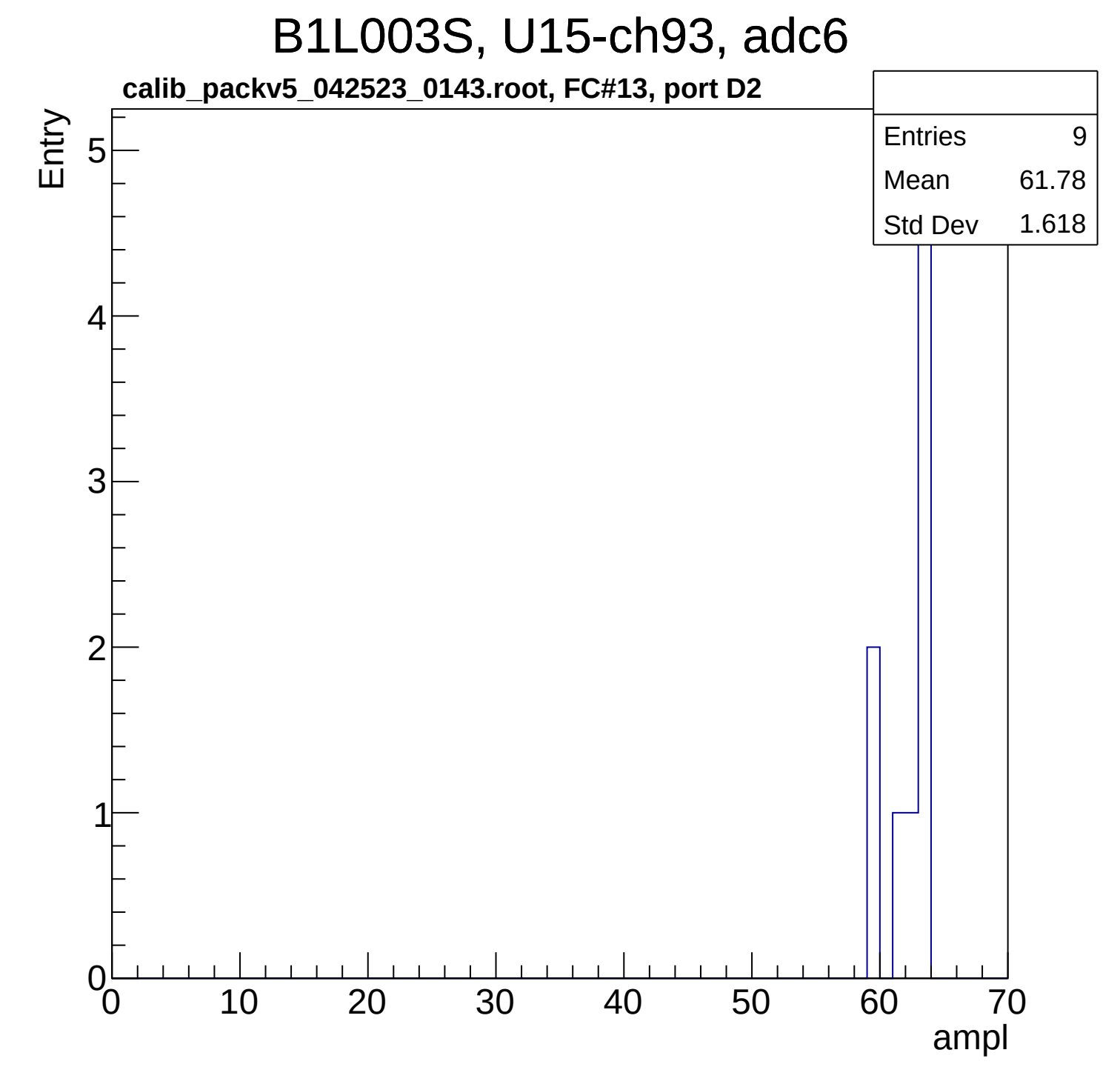
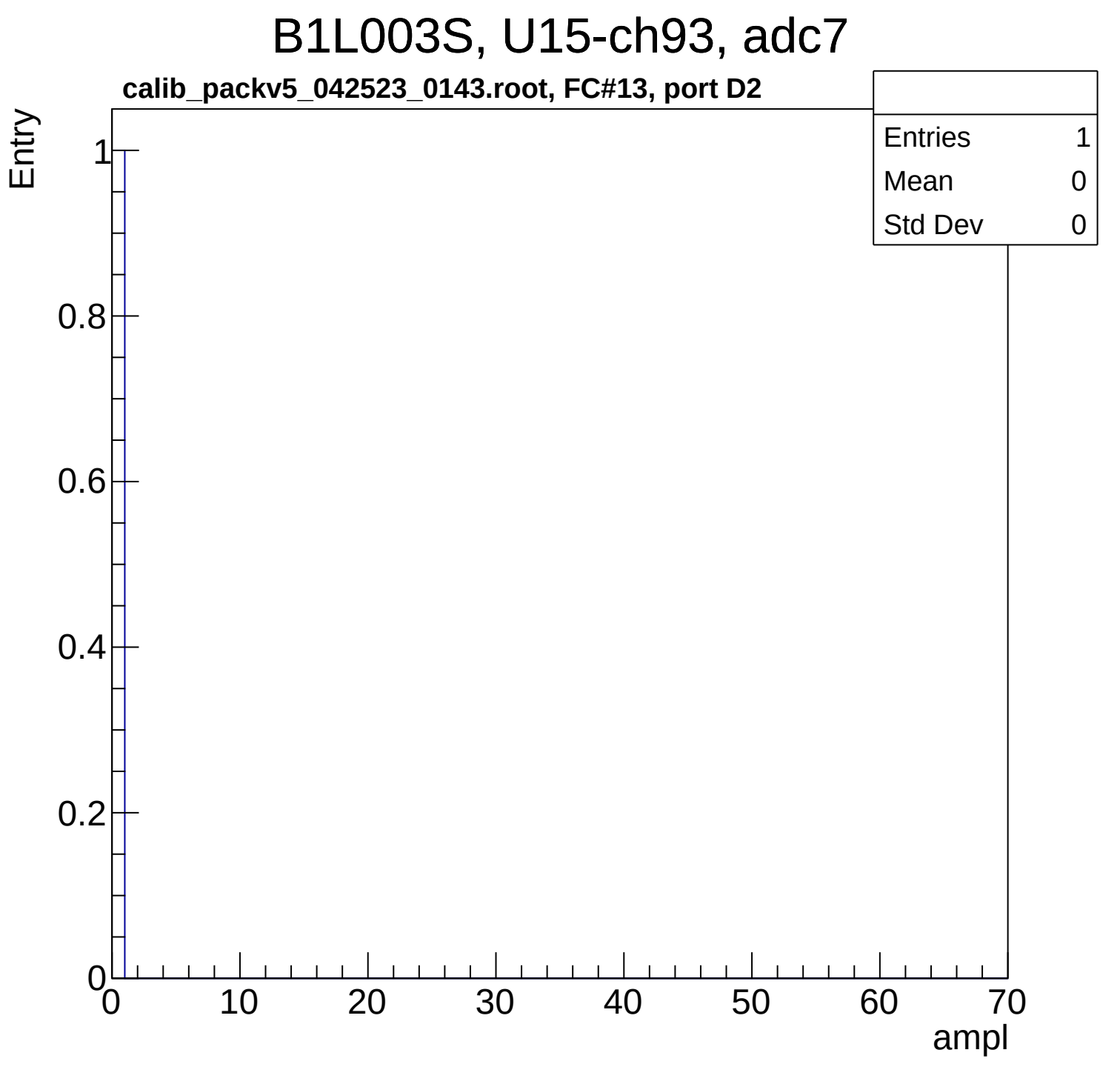
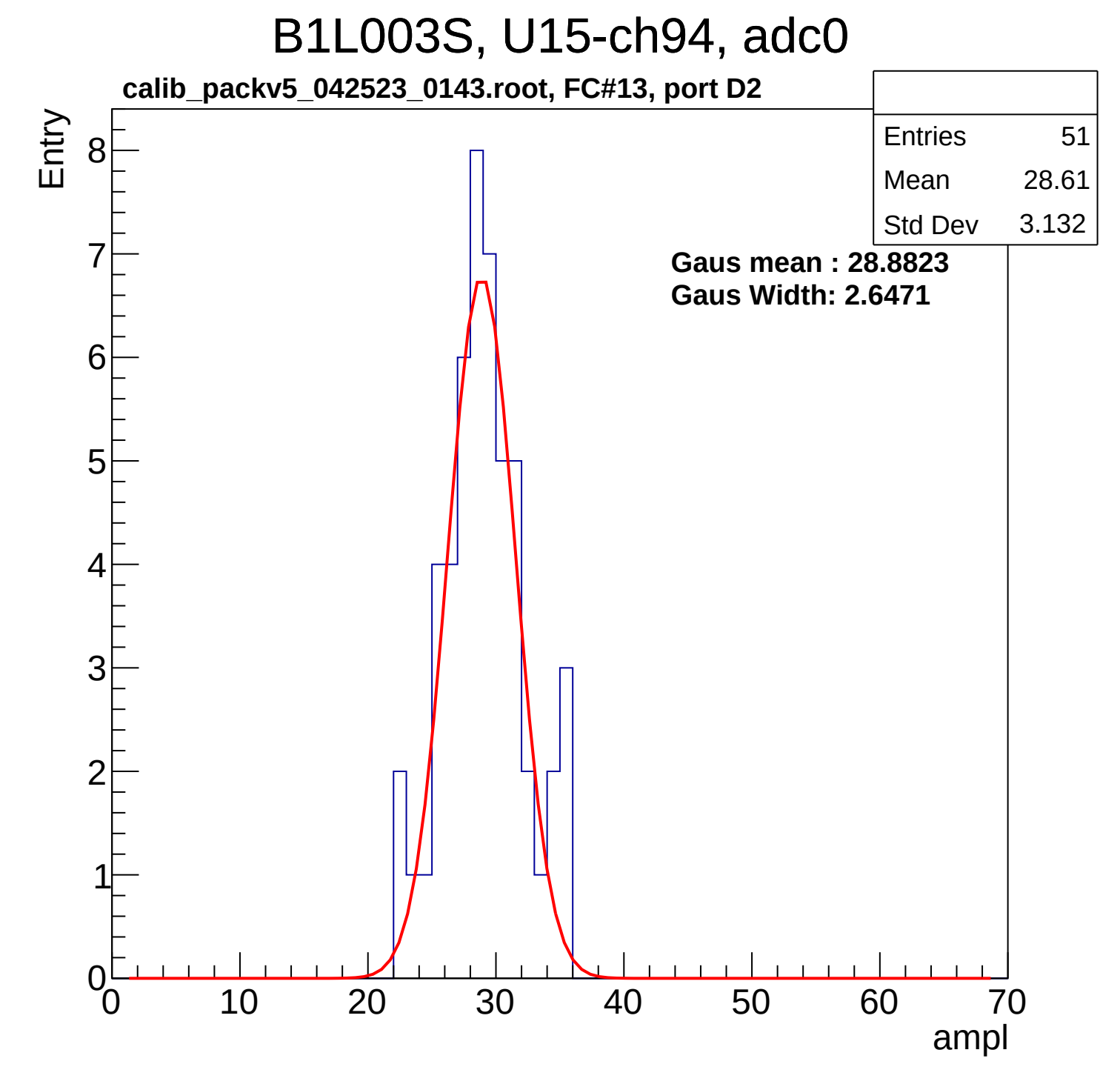
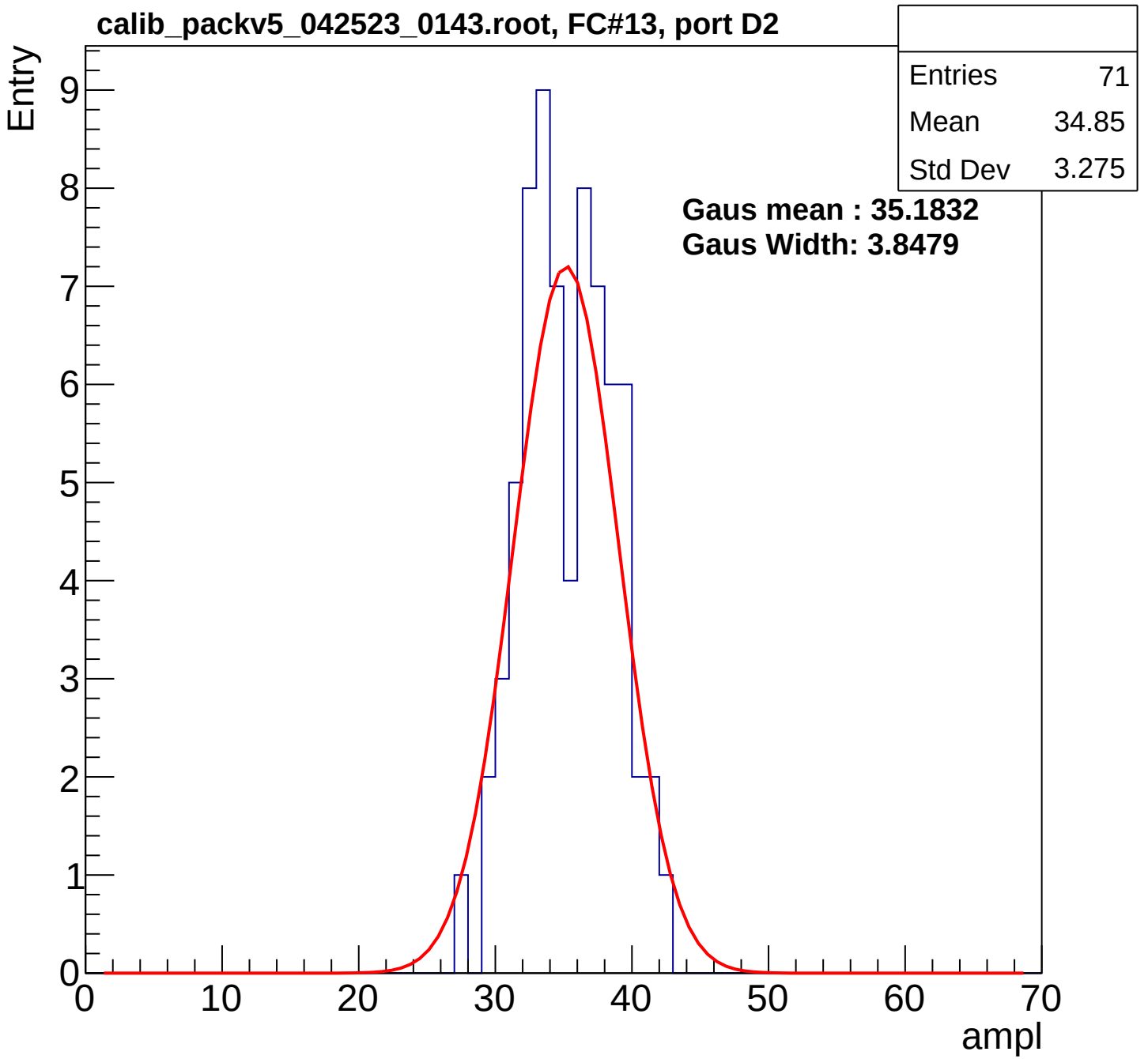
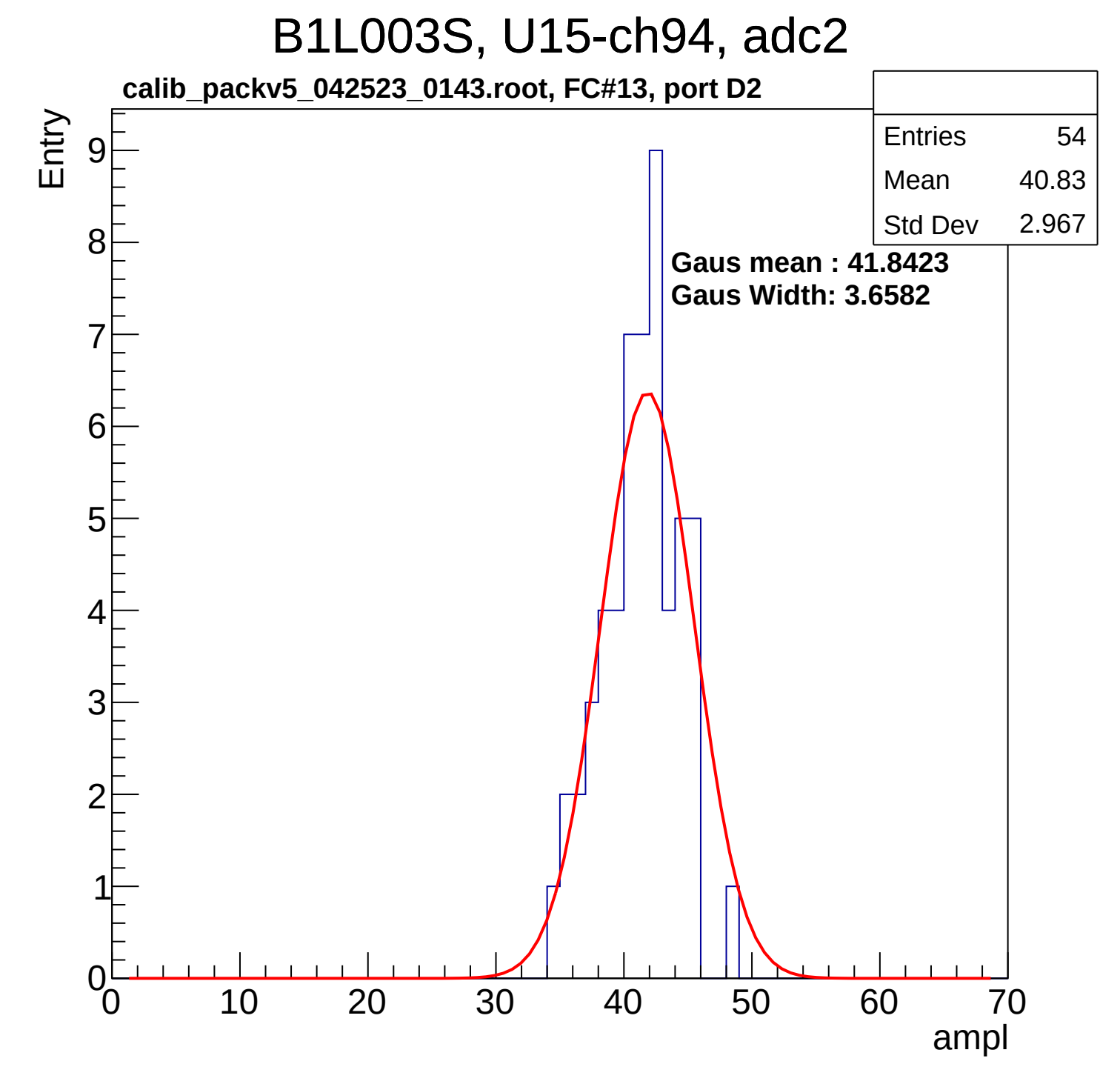

B1L003S, U15-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.78
Std Dev 1.618
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U15-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.61
Std Dev 3.132
Gaus mean : 28.8823
Gaus Width: 2.6471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 34.85
Std Dev 3.275
Gaus mean : 35.1832
Gaus Width: 3.8479
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.83
Std Dev 2.967
Gaus mean : 41.8423
Gaus Width: 3.6582
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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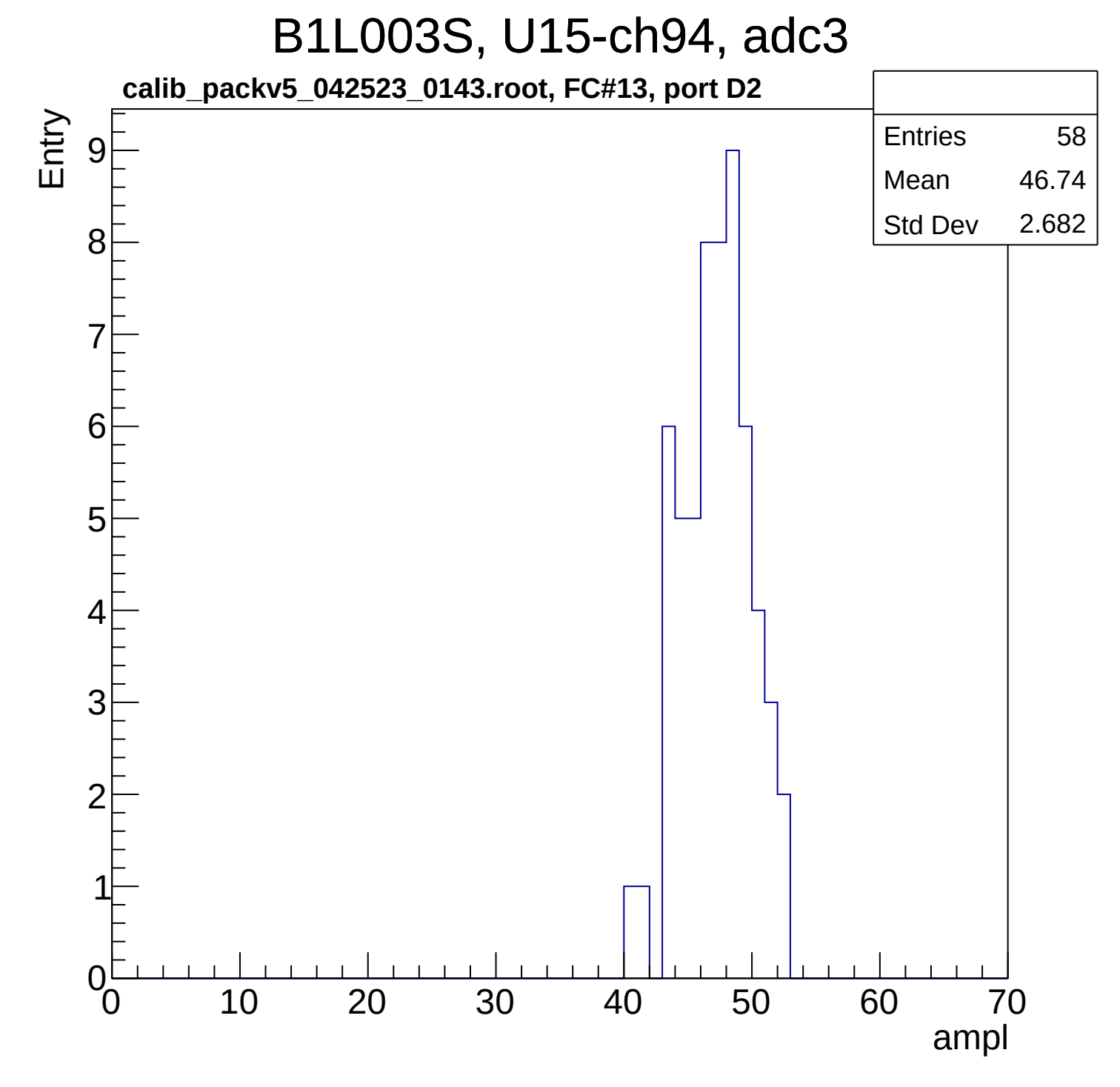
B1L003S, U15-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 46.74
Std Dev 2.682
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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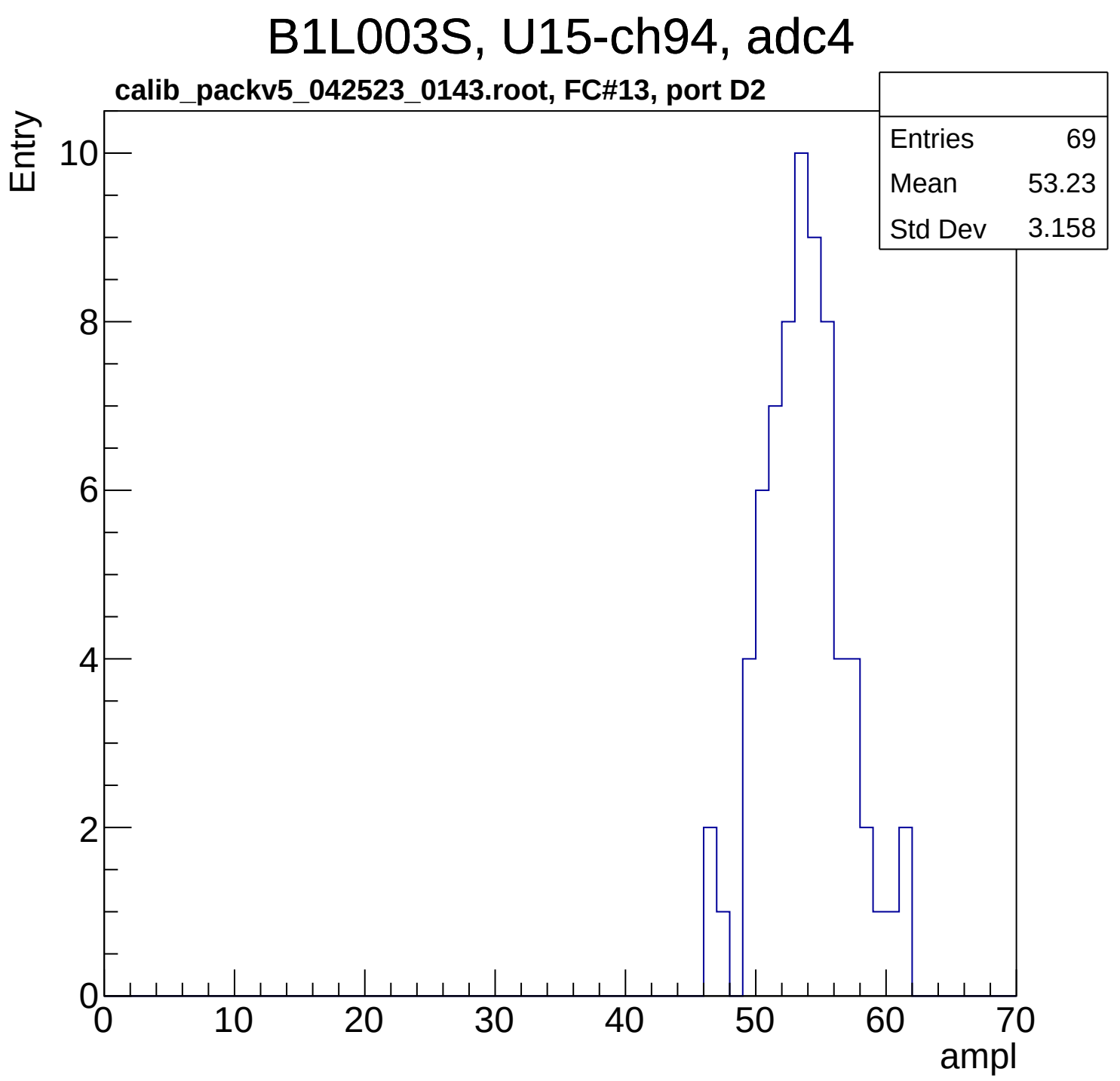
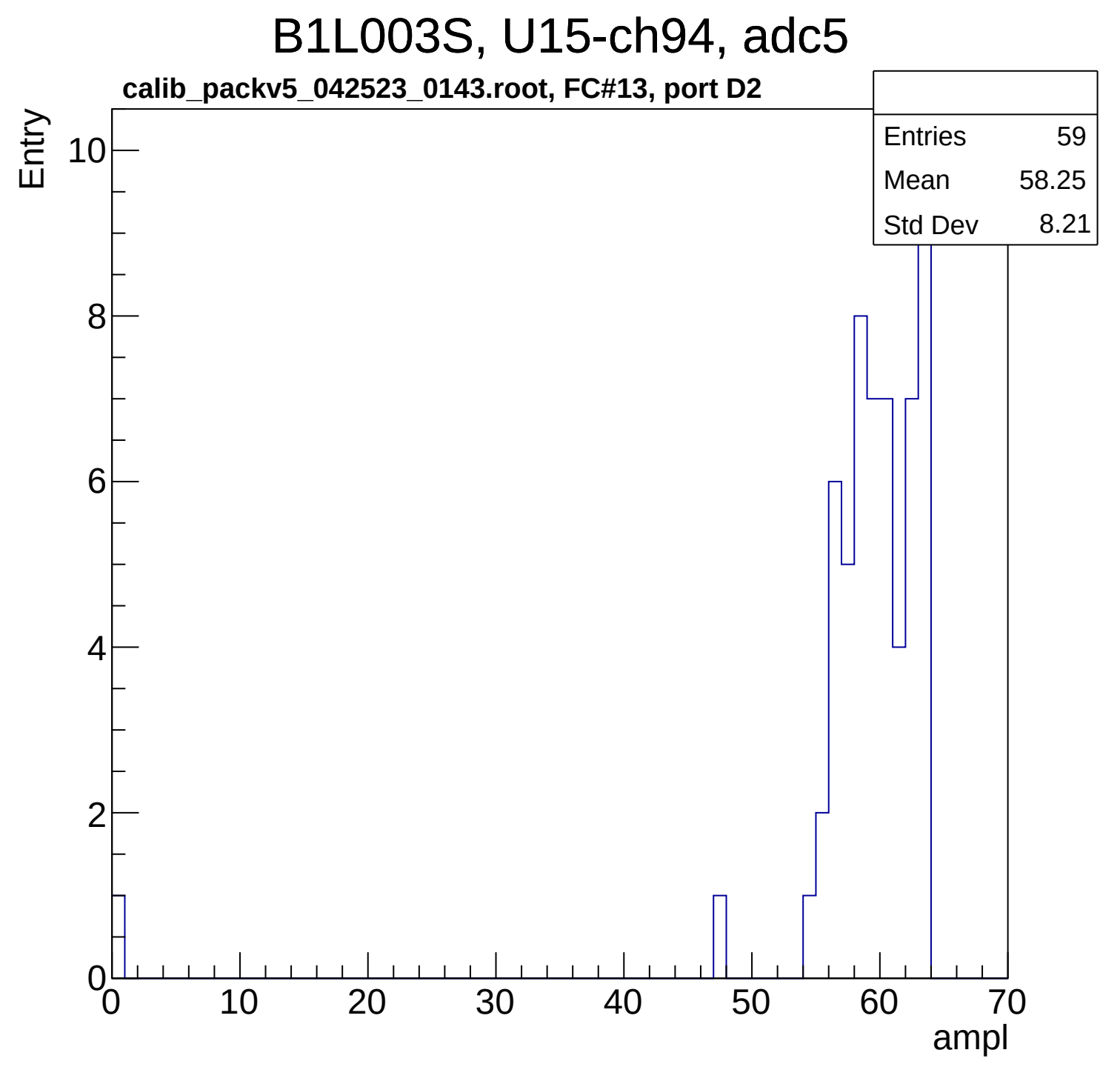
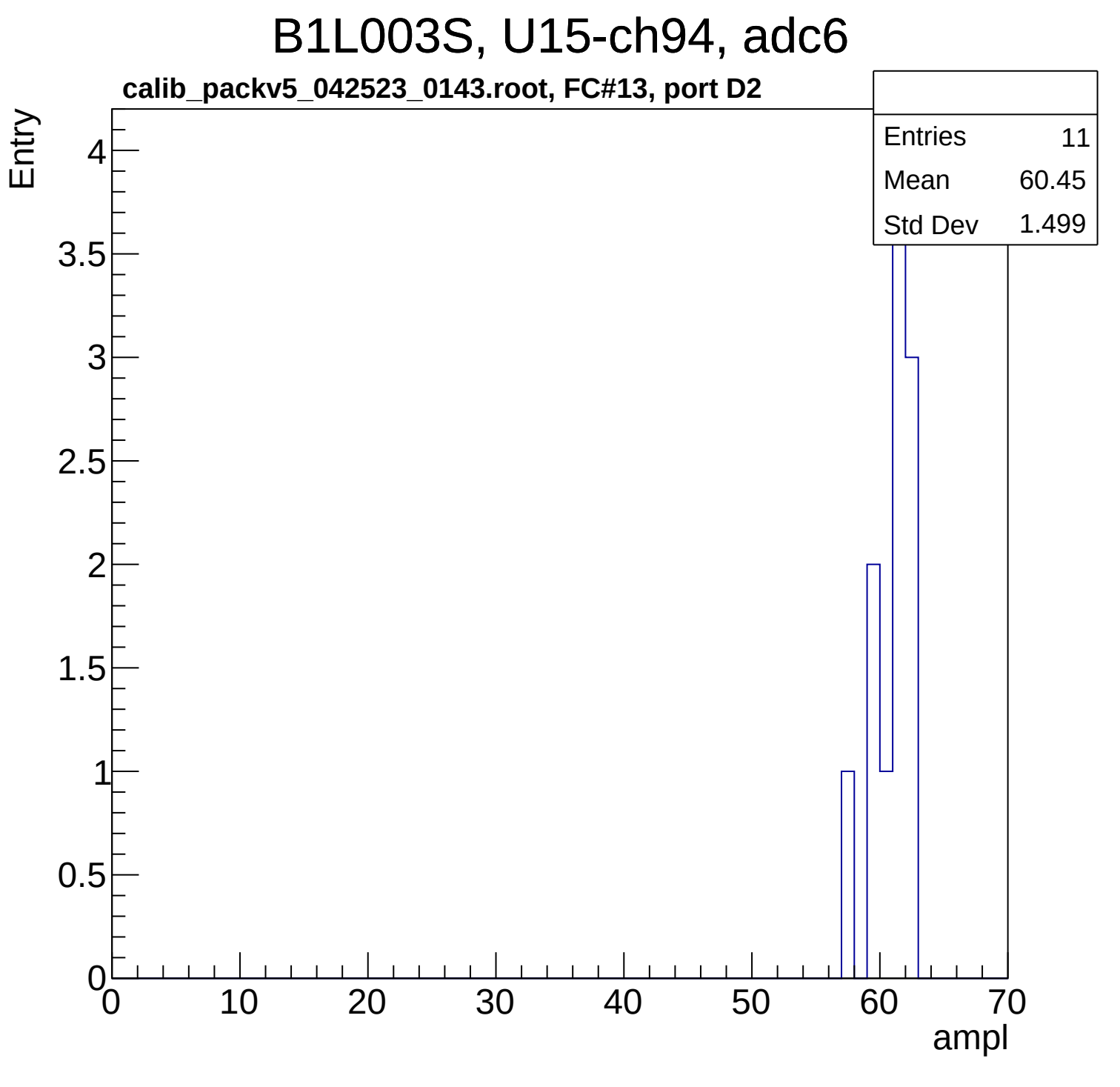

B1L003S, U15-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 53.23
Std Dev 3.158
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.25
Std Dev 8.21
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 60.45
Std Dev 1.499
Entry
ampl
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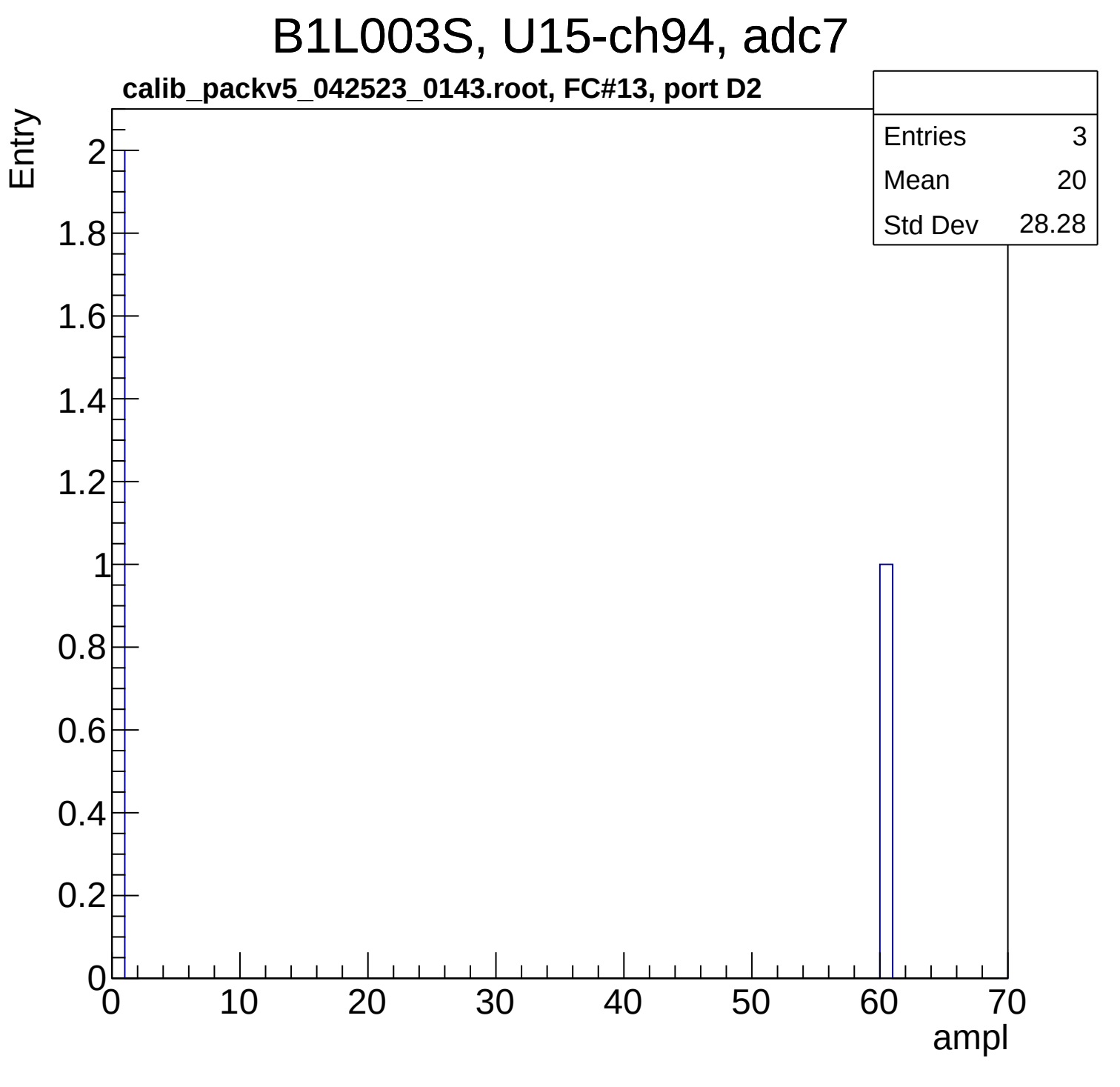
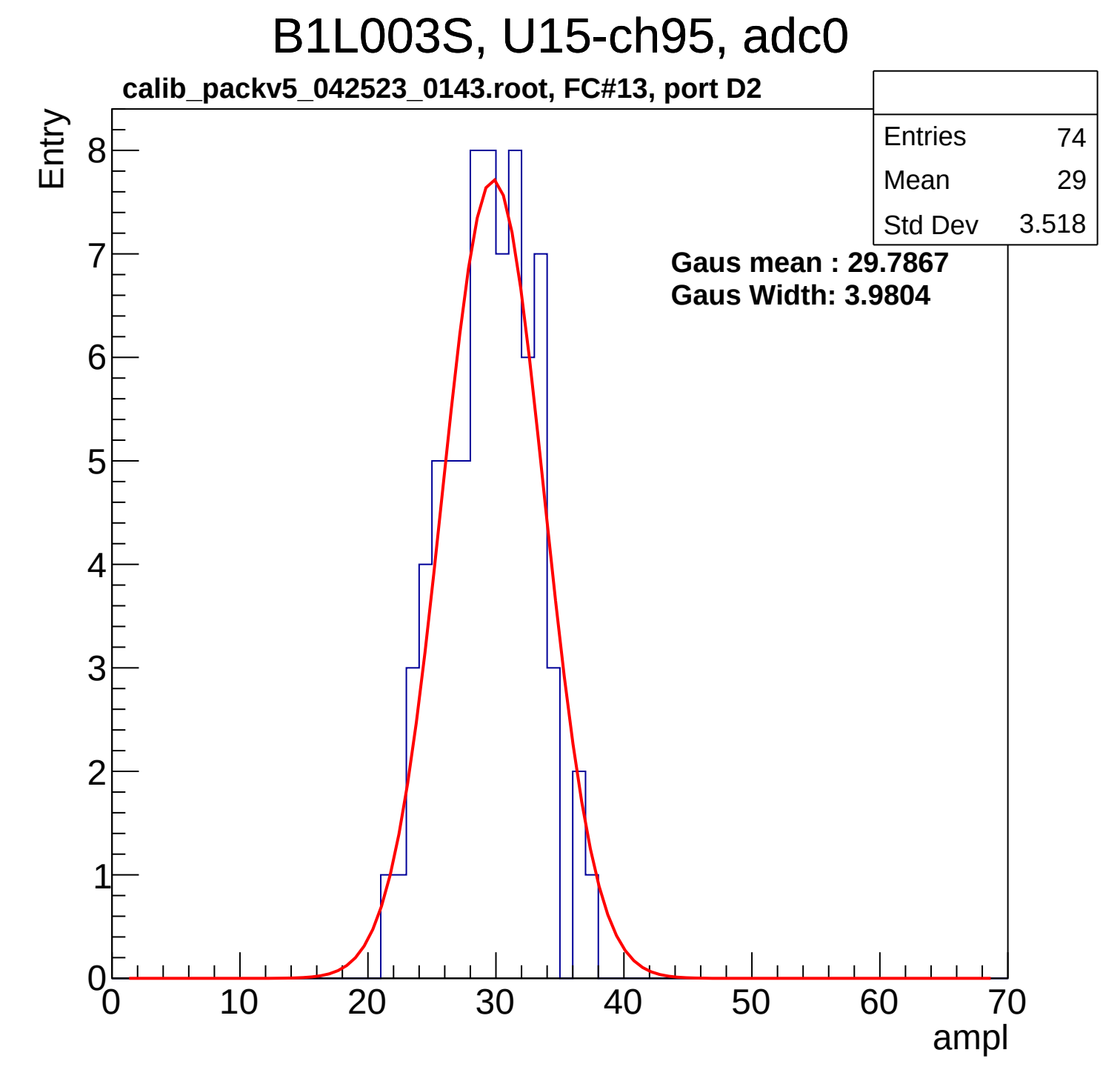
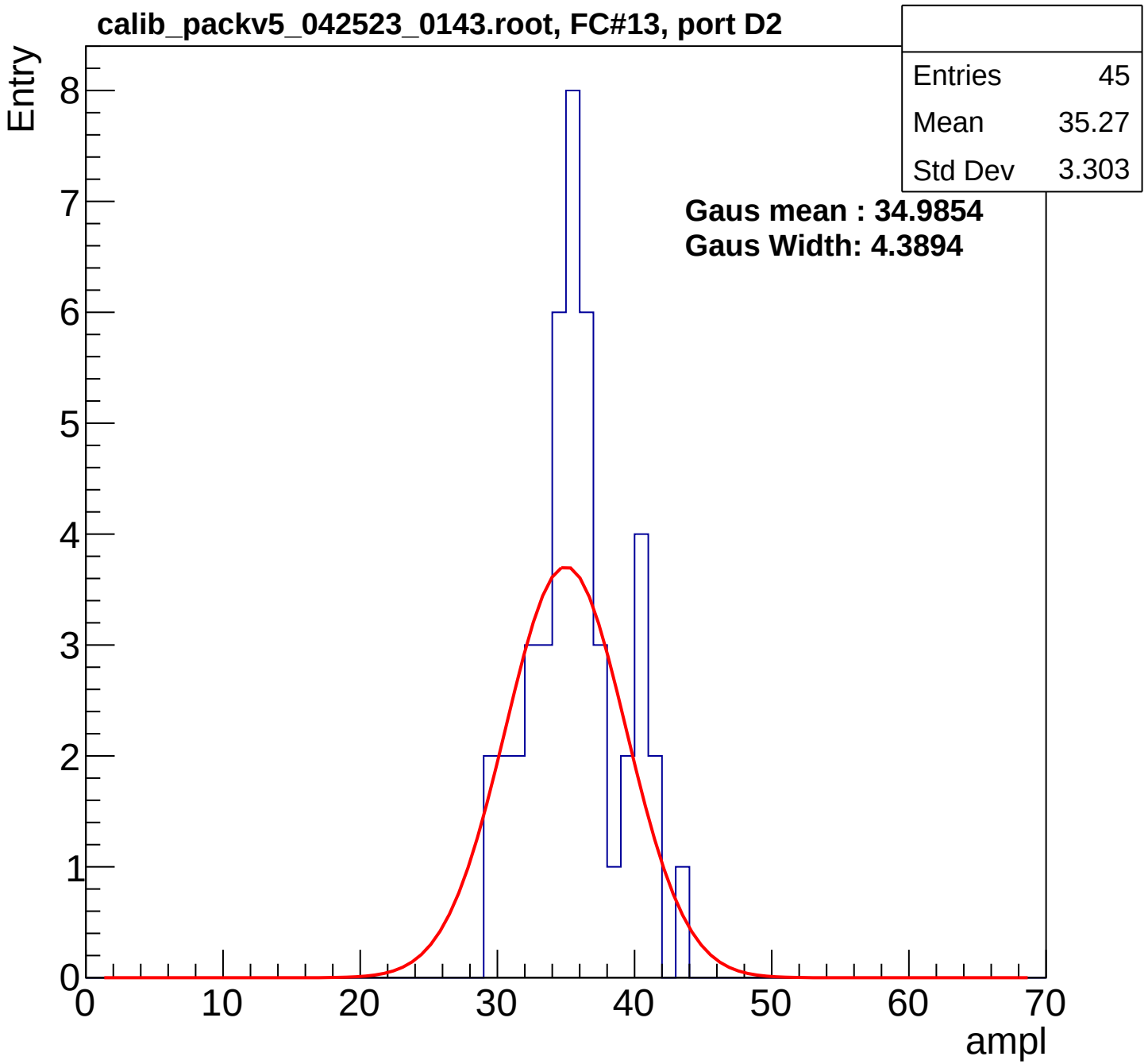
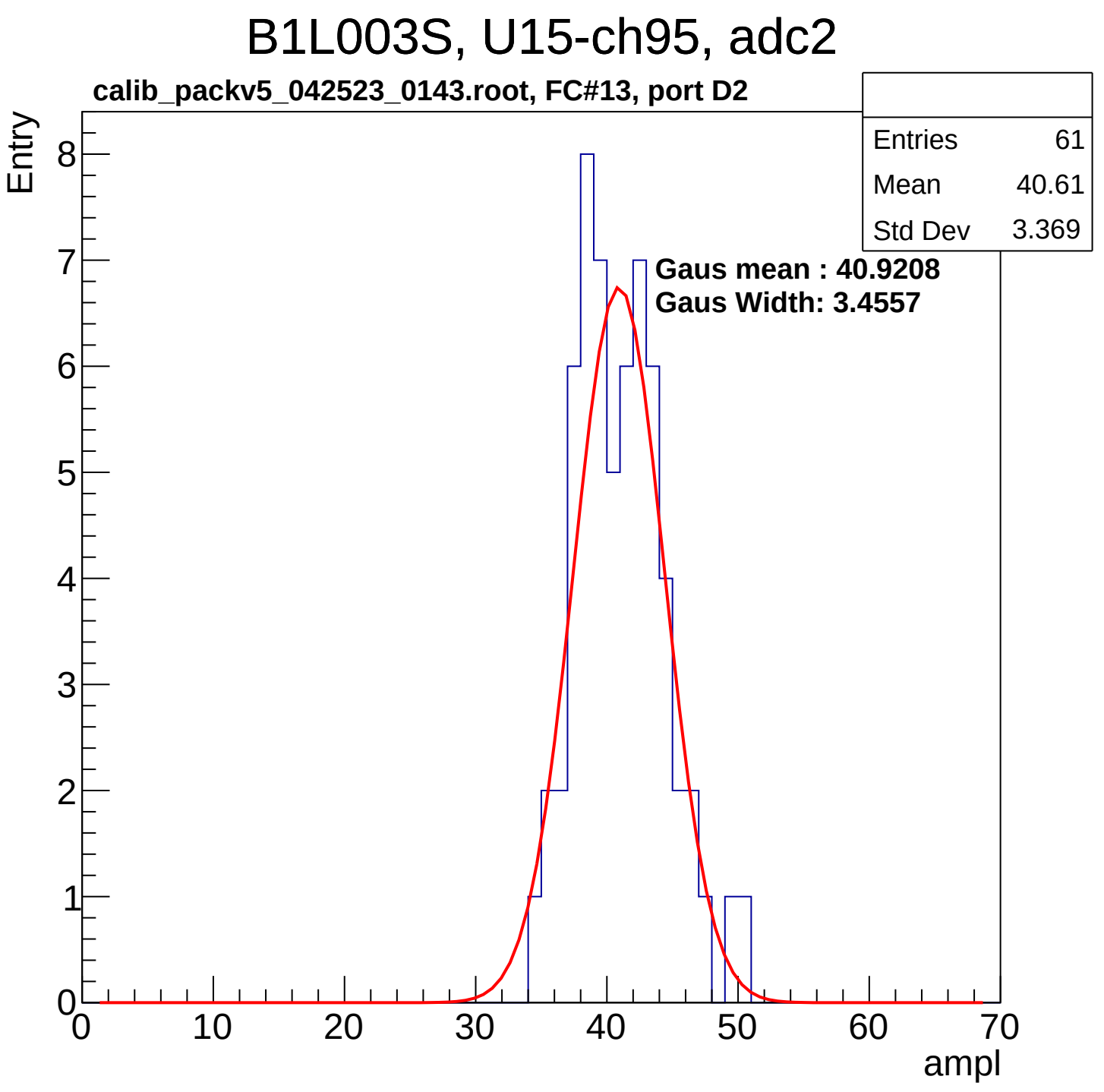
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20
Std Dev 28.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29
Std Dev 3.518
Gaus mean : 29.7867
Gaus Width: 3.9804
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 35.27
Std Dev 3.303
Gaus mean : 34.9854
Gaus Width: 4.3894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 40.61
Std Dev 3.369
Gaus mean : 40.9208
Gaus Width: 3.4557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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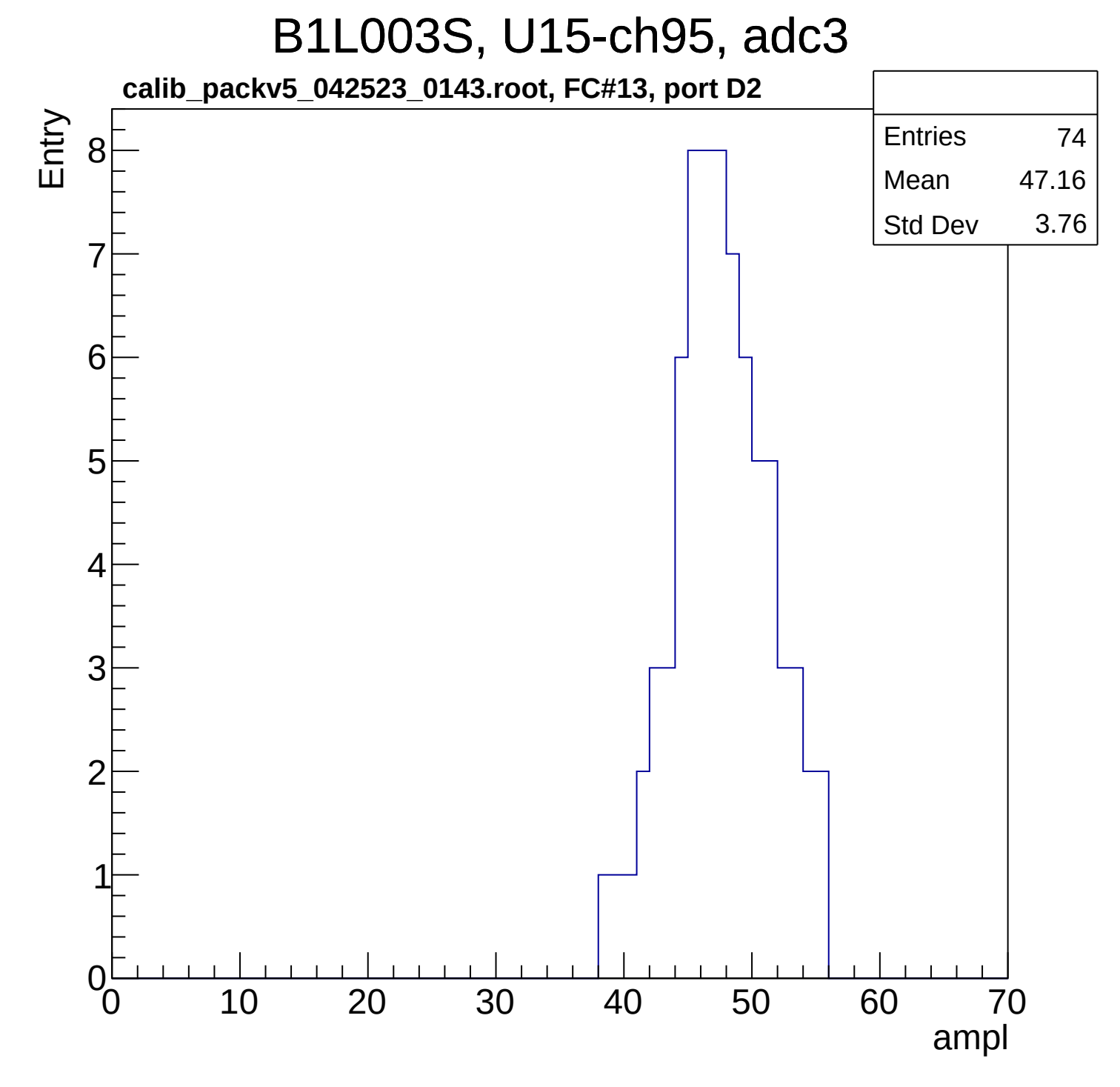
B1L003S, U15-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 47.16
Std Dev 3.76
Entry
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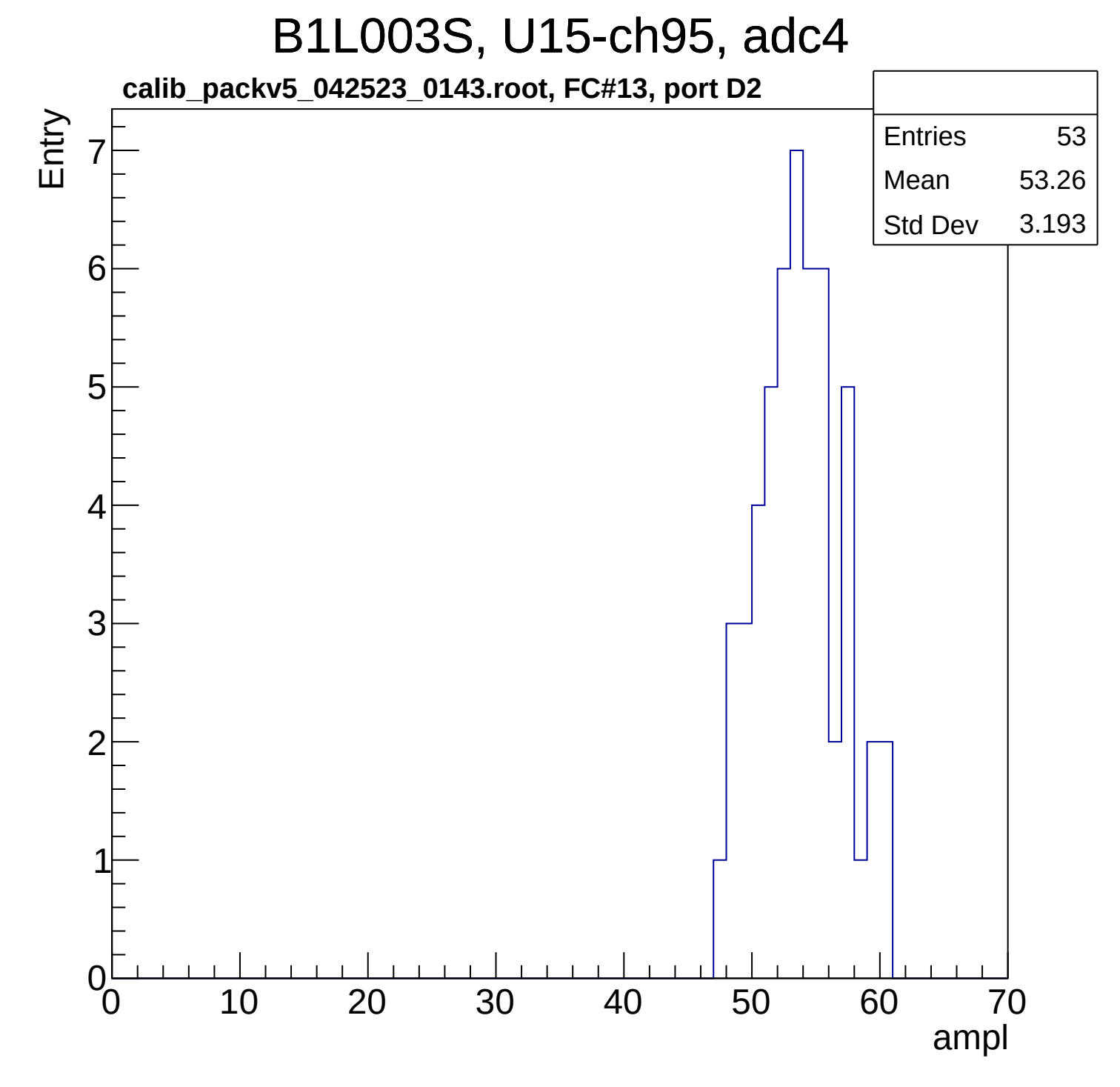
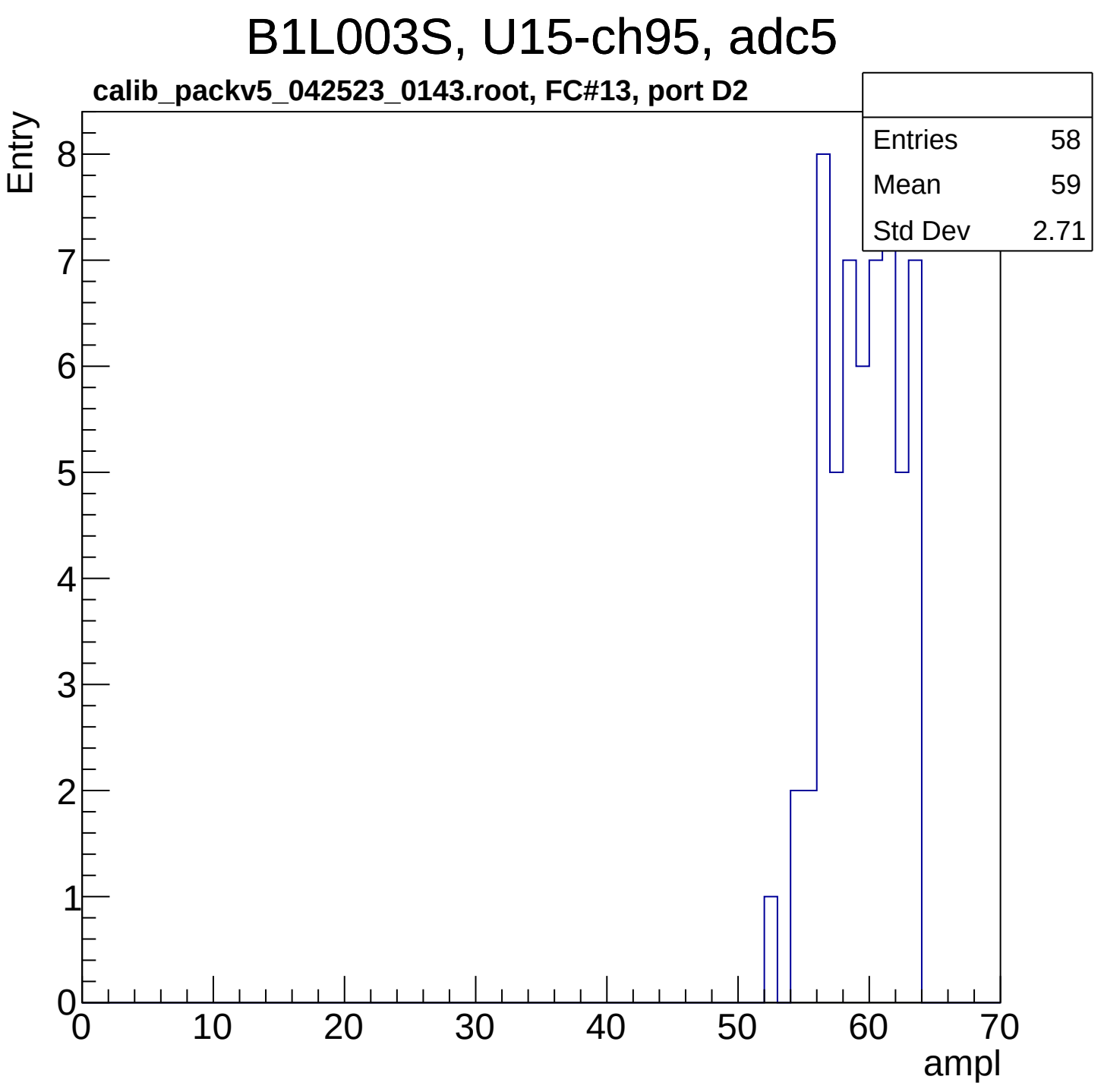
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 53.26
Std Dev 3.193
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 59
Std Dev 2.71
Entry
ampl
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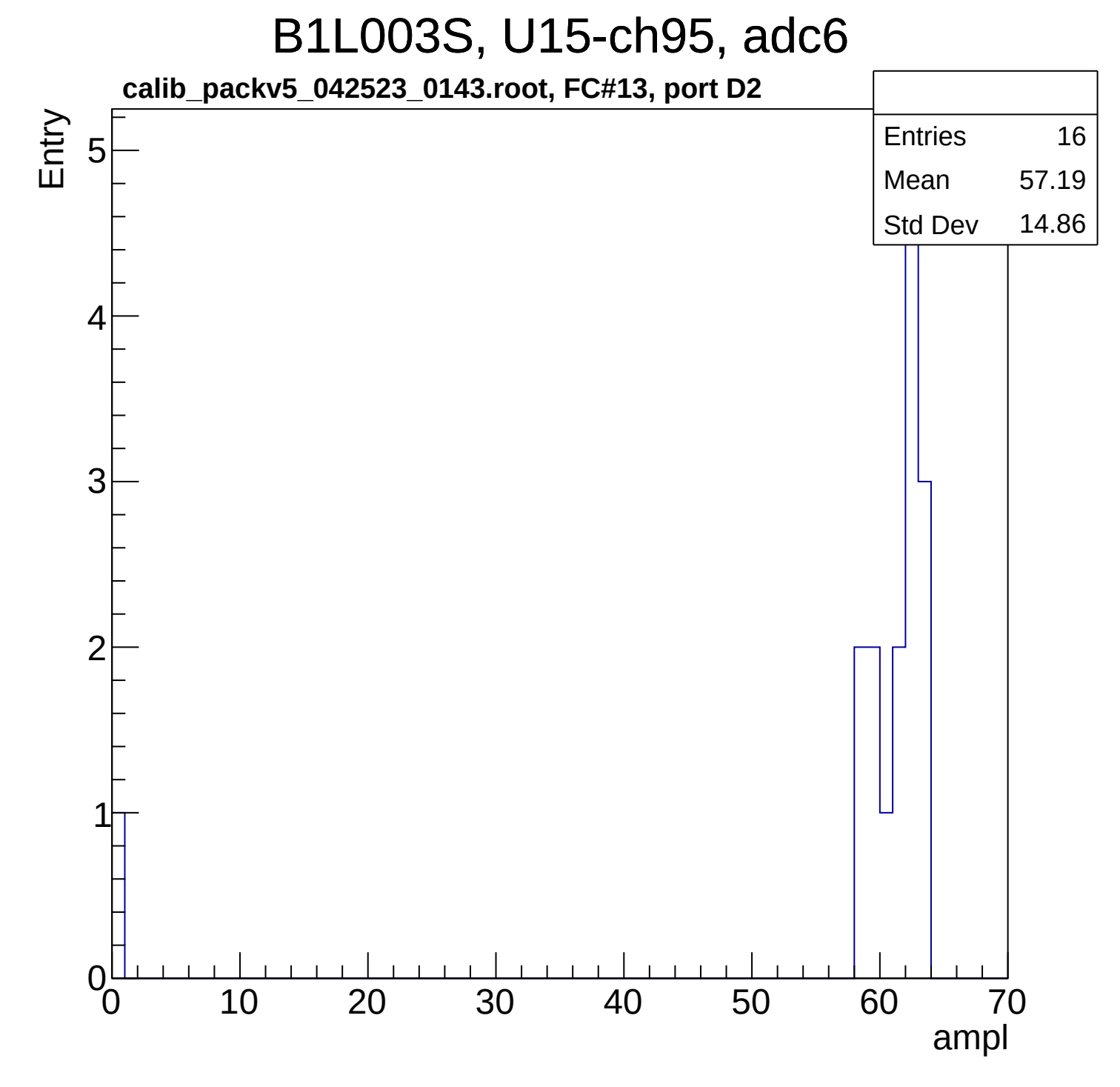
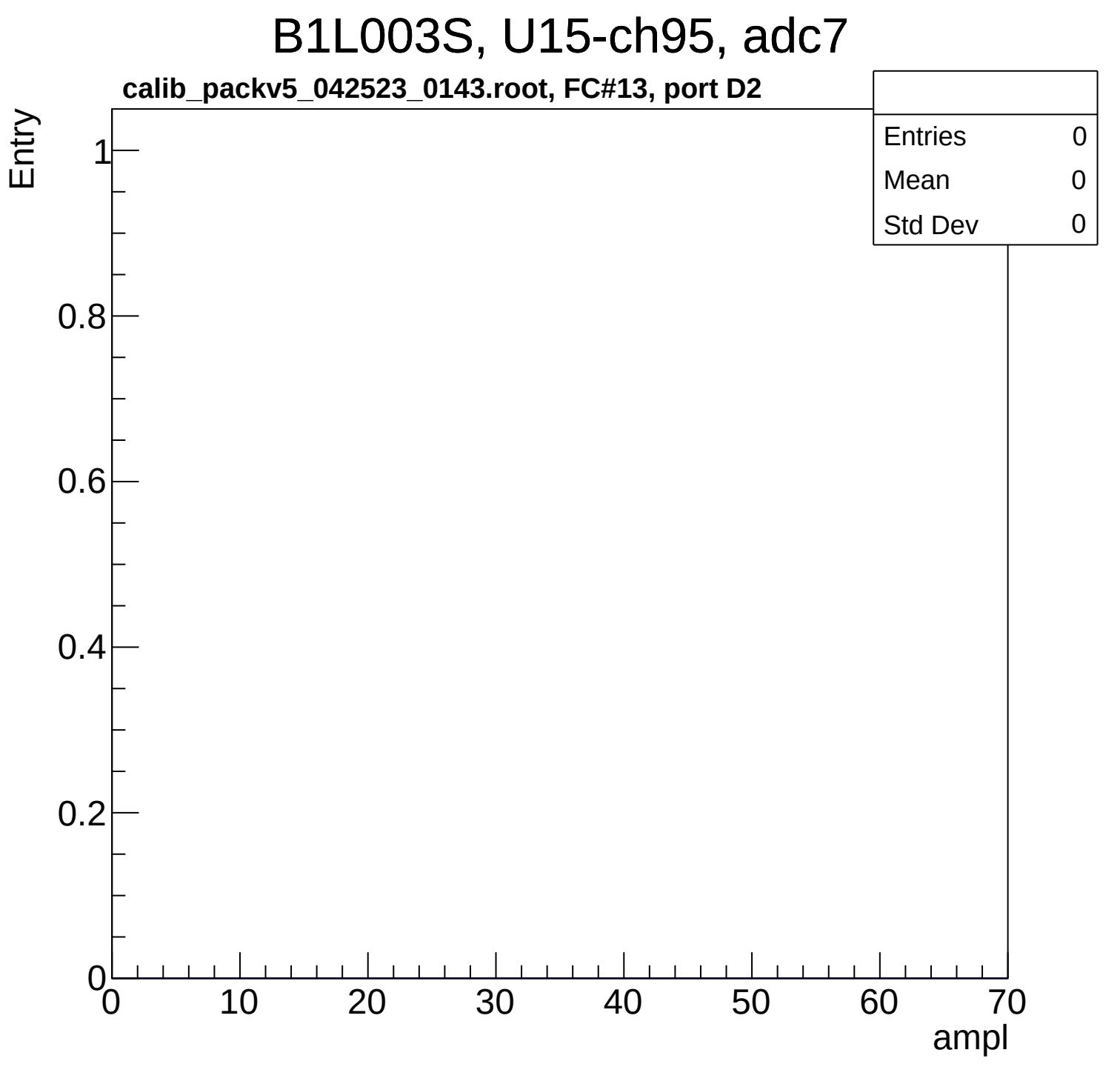
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 57.19
Std Dev 14.86
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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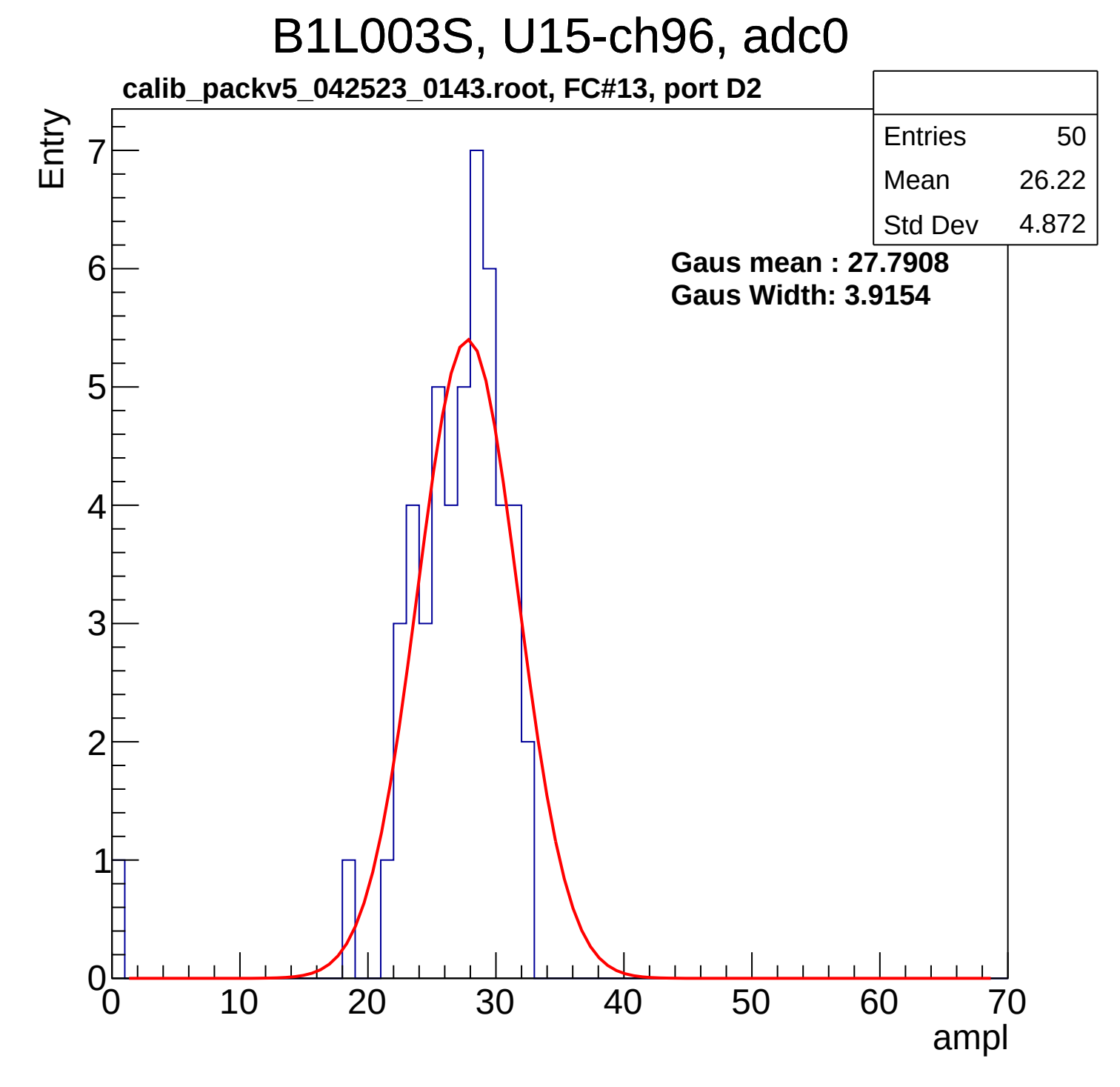
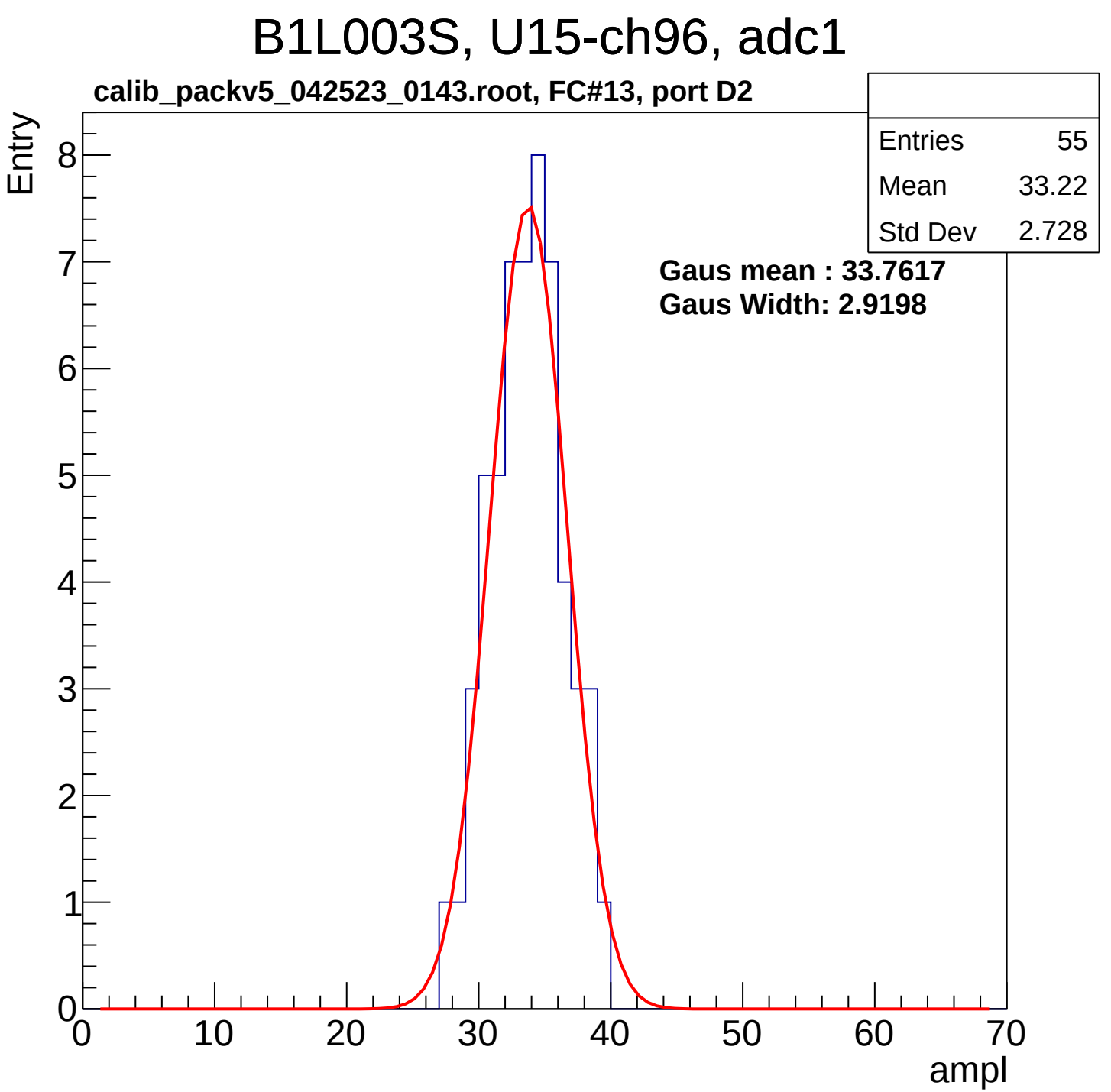
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 26.22
Std Dev 4.872
Gaus mean : 27.7908
Gaus Width: 3.9154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 33.22
Std Dev 2.728
Gaus mean : 33.7617
Gaus Width: 2.9198
Entry
ampl
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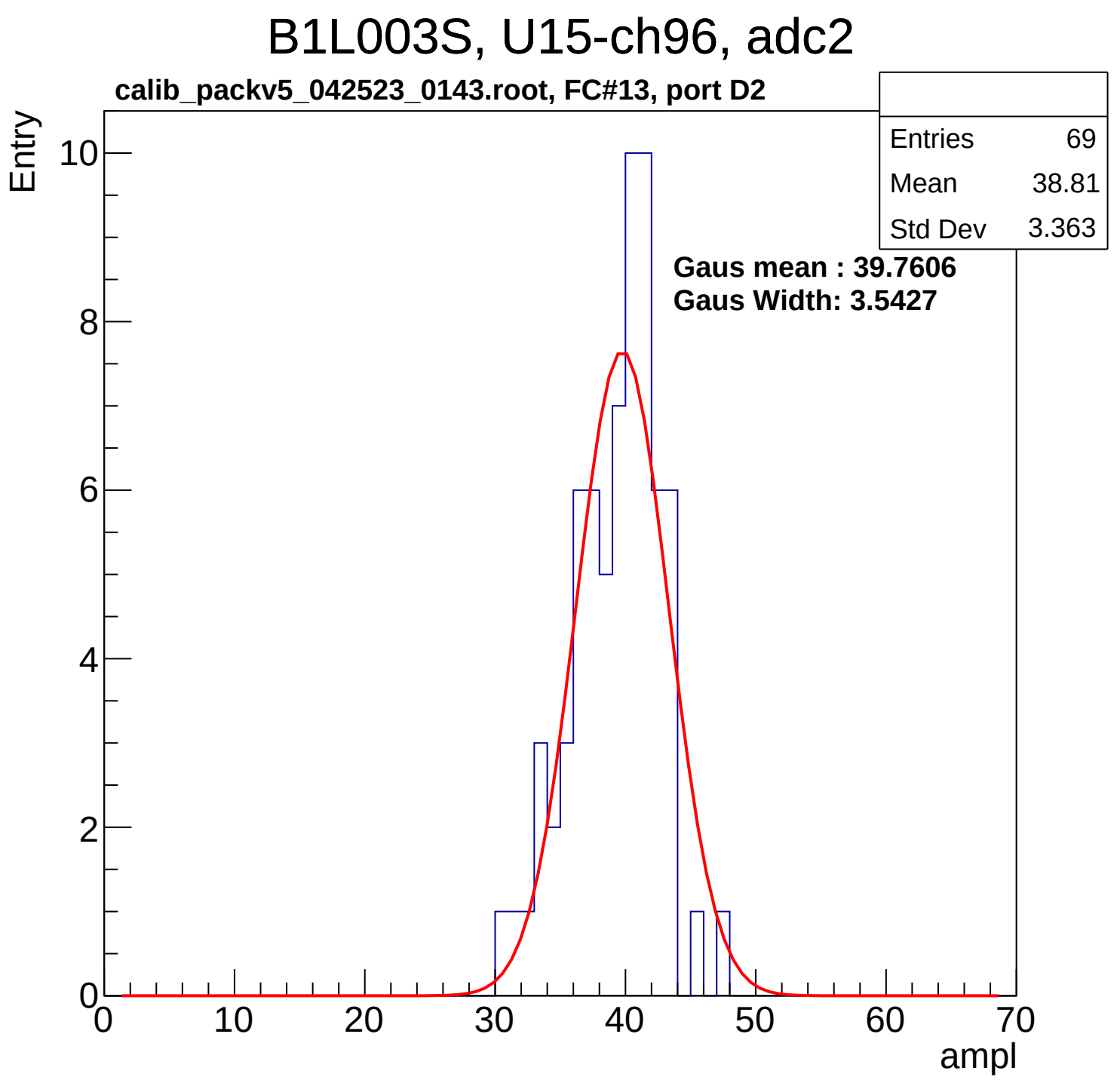
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 38.81
Std Dev 3.363
Gaus mean : 39.7606
Gaus Width: 3.5427
Entry
ampl
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4
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10
0
10
20
30
40
50
60
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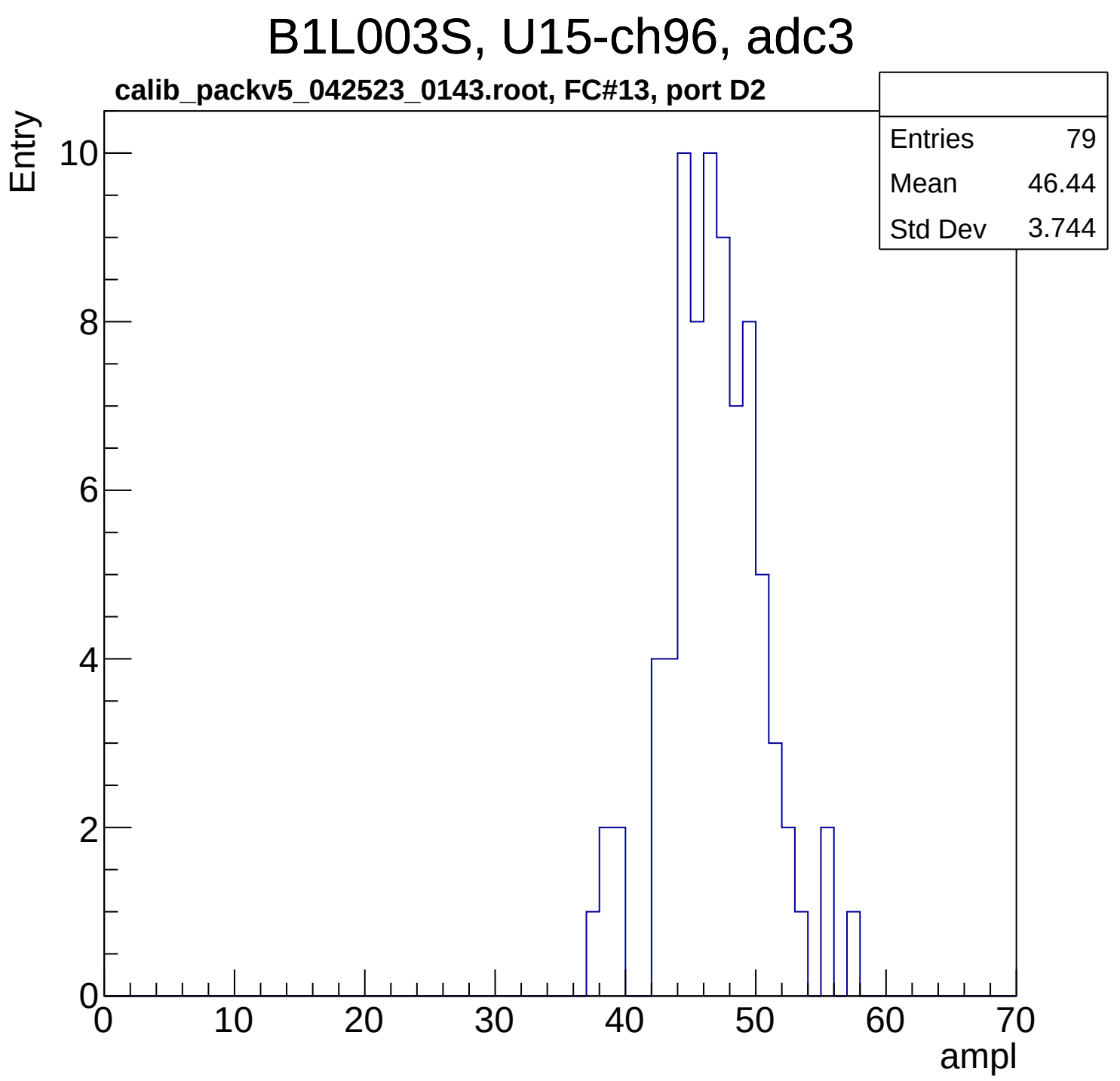
B1L003S, U15-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 46.44
Std Dev 3.744
Entry
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4
6
8
10
0
10
20
30
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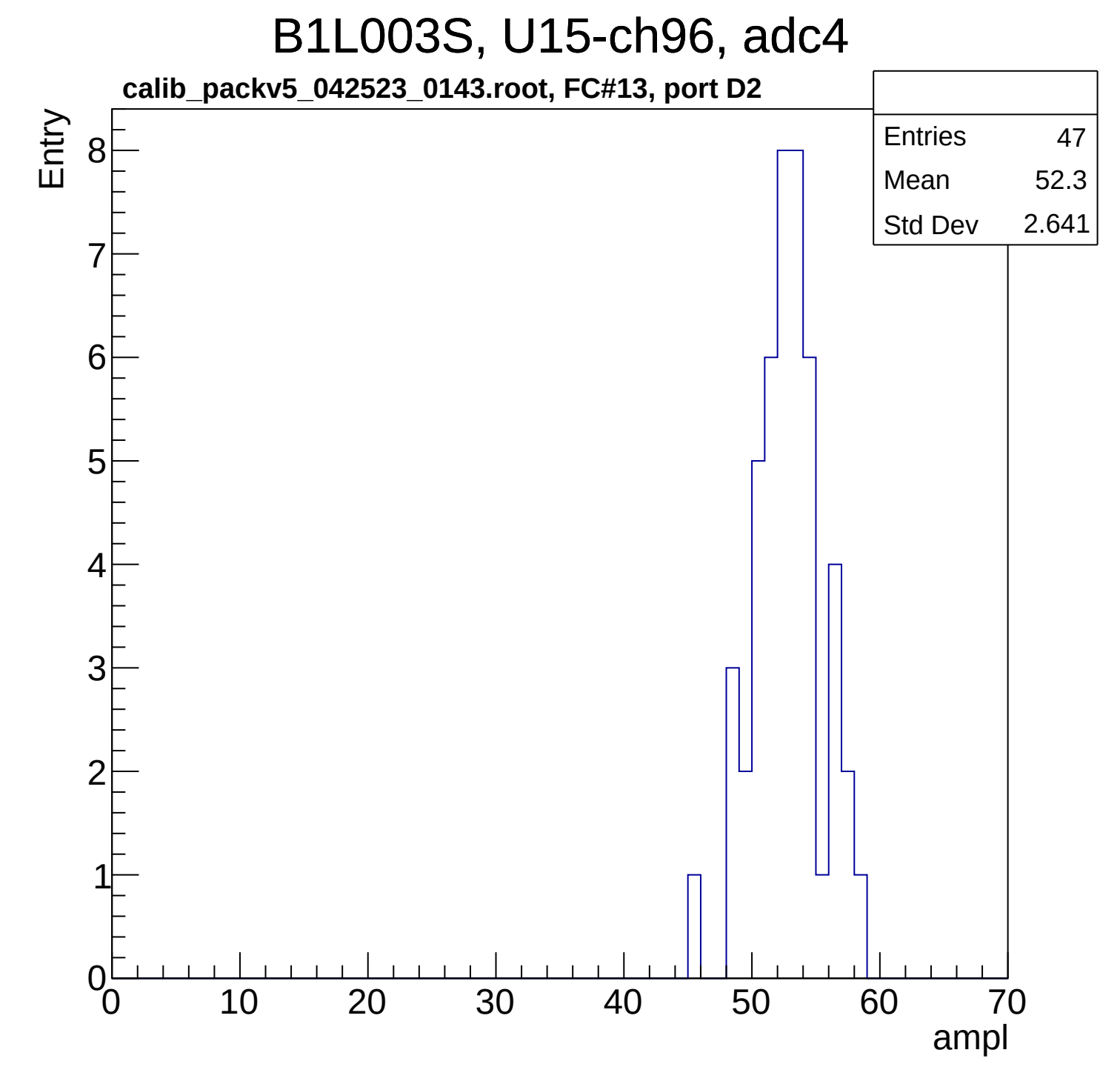
50
60
70
ampl


B1L003S, U15-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 52.3
Std Dev 2.641
Entry
ampl
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3
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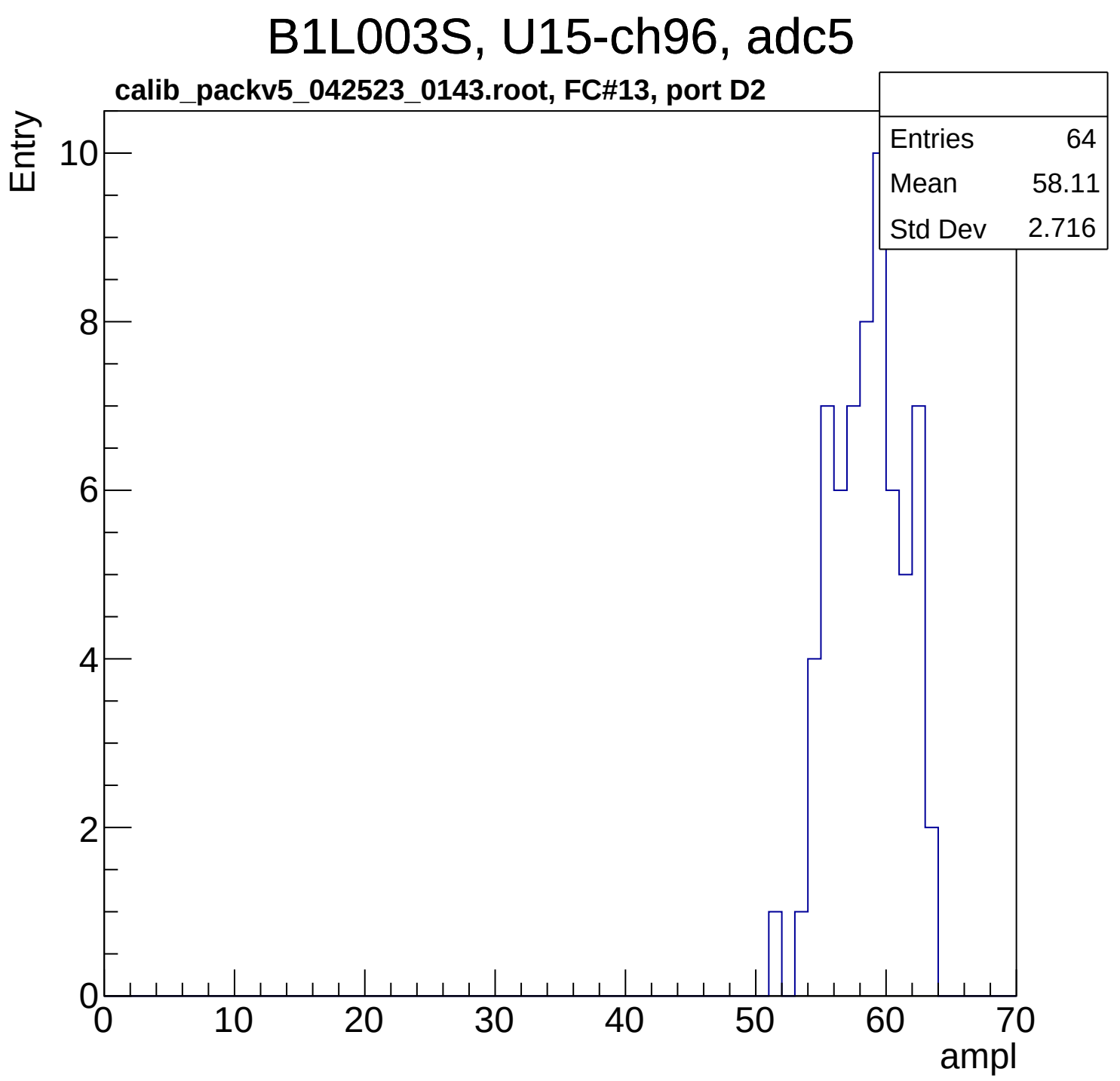
B1L003S, U15-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 58.11
Std Dev 2.716
Entry
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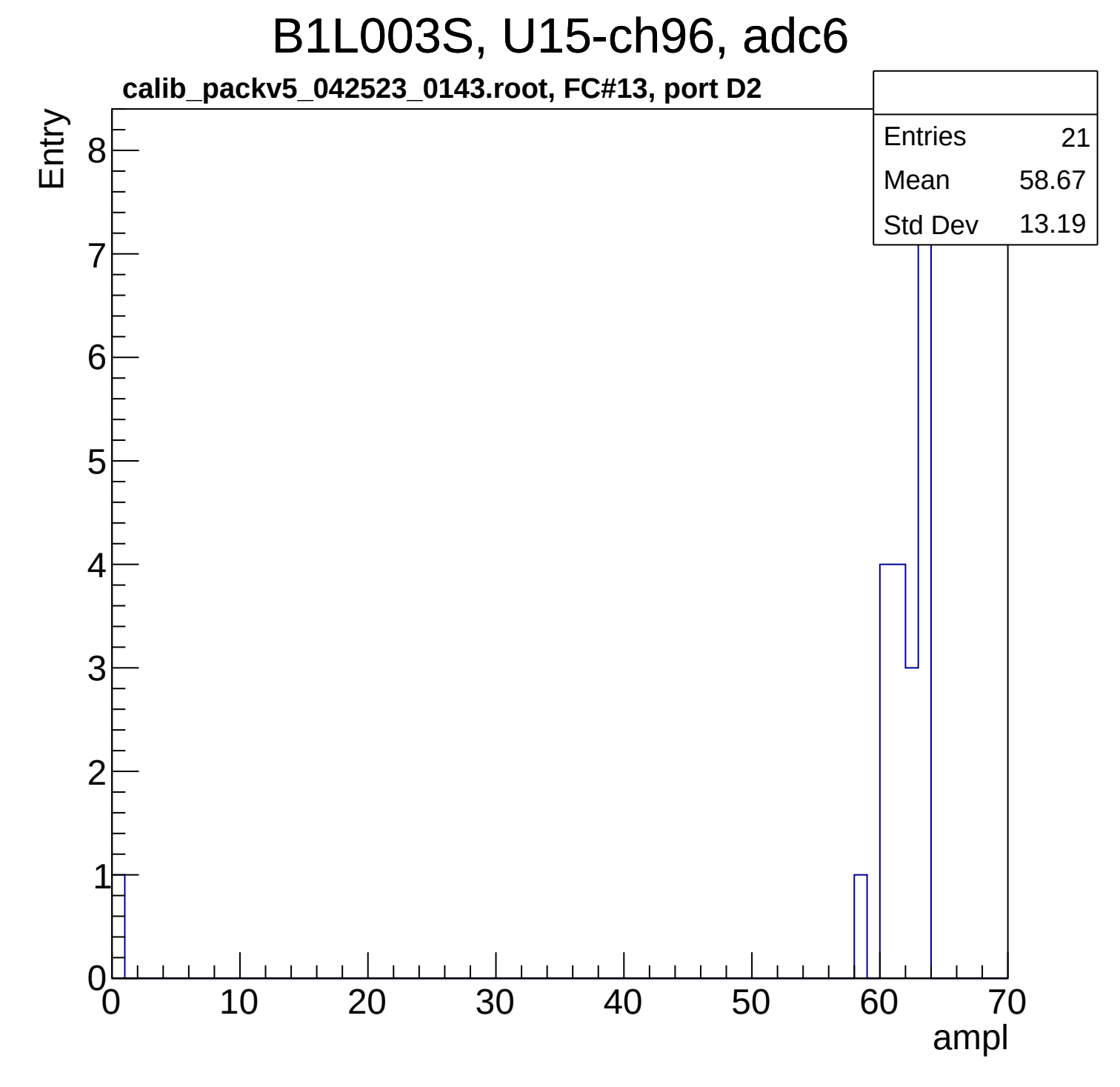
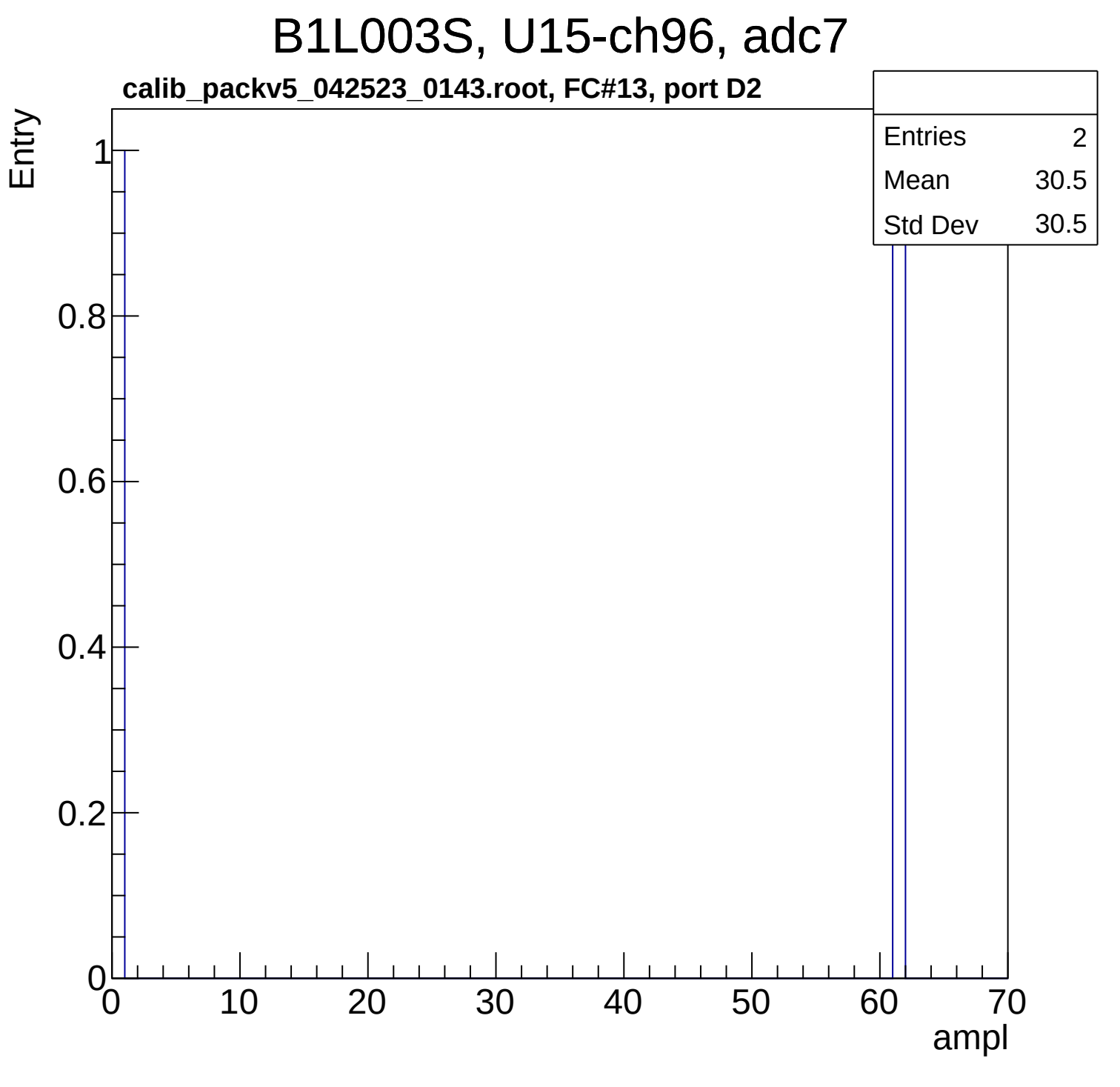
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 58.67
Std Dev 13.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
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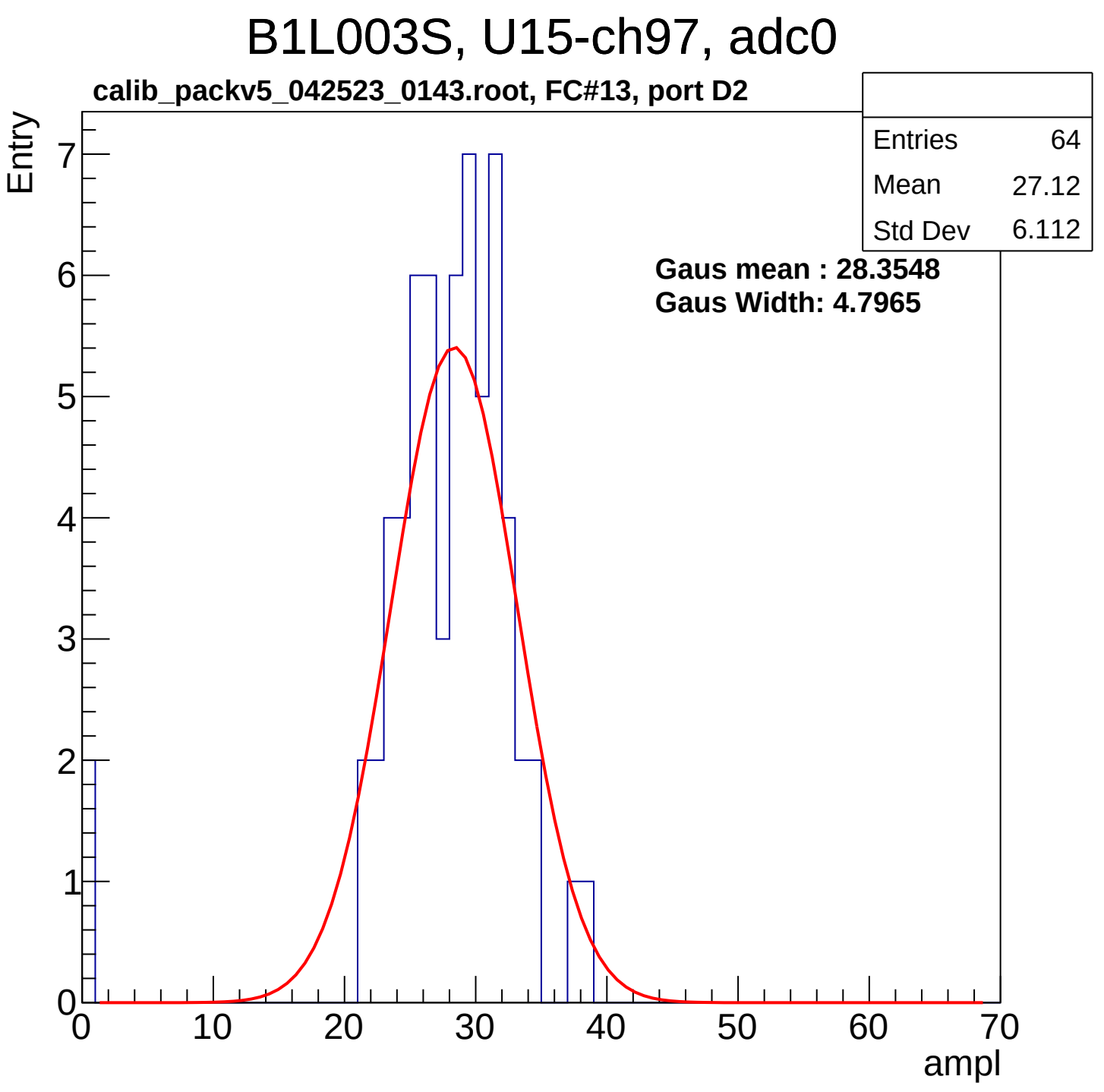
70
ampl


B1L003S, U15-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.12
Std Dev 6.112
Gaus mean : 28.3548
Gaus Width: 4.7965
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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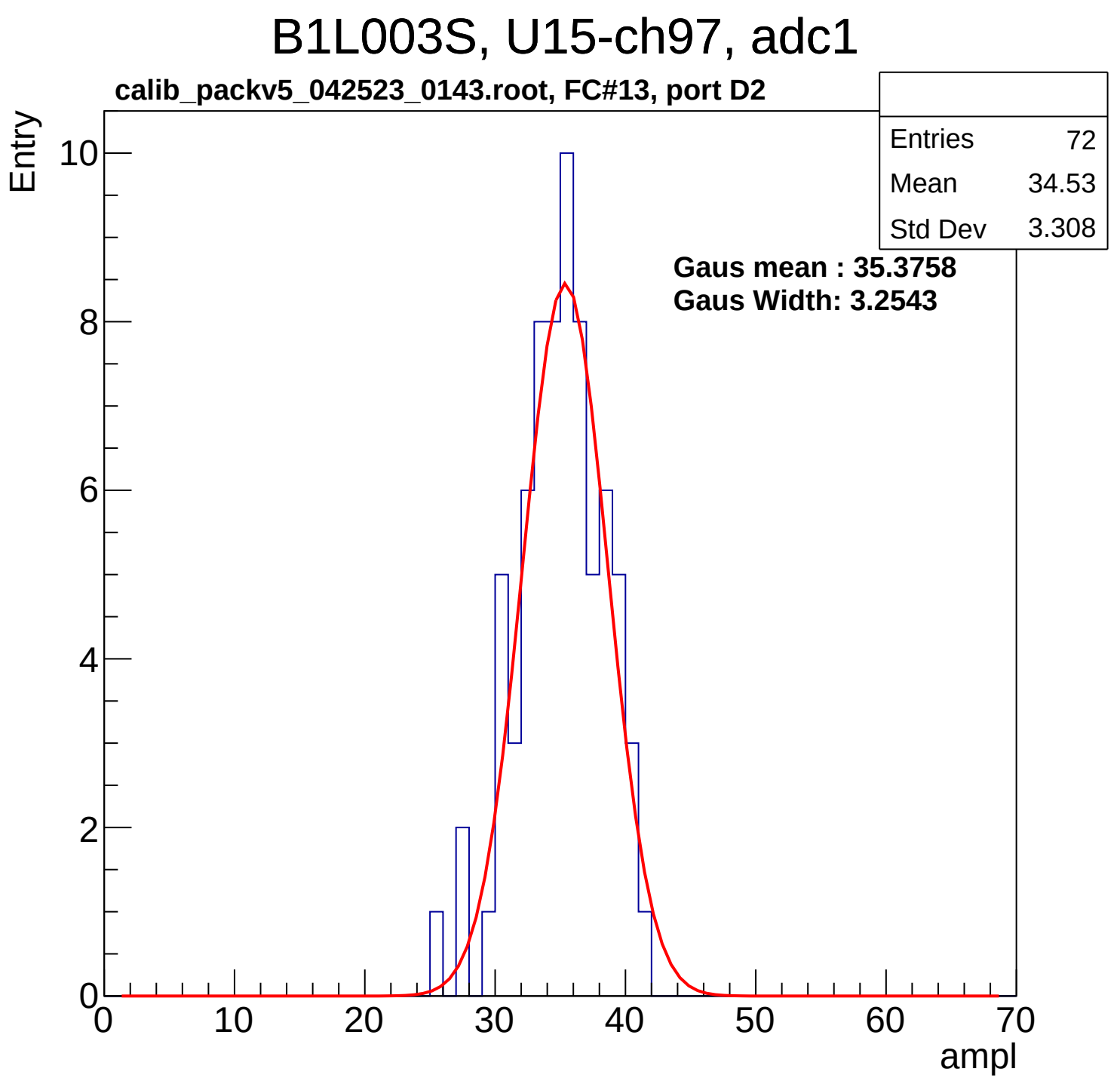
B1L003S, U15-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.53
Std Dev 3.308
Gaus mean : 35.3758
Gaus Width: 3.2543
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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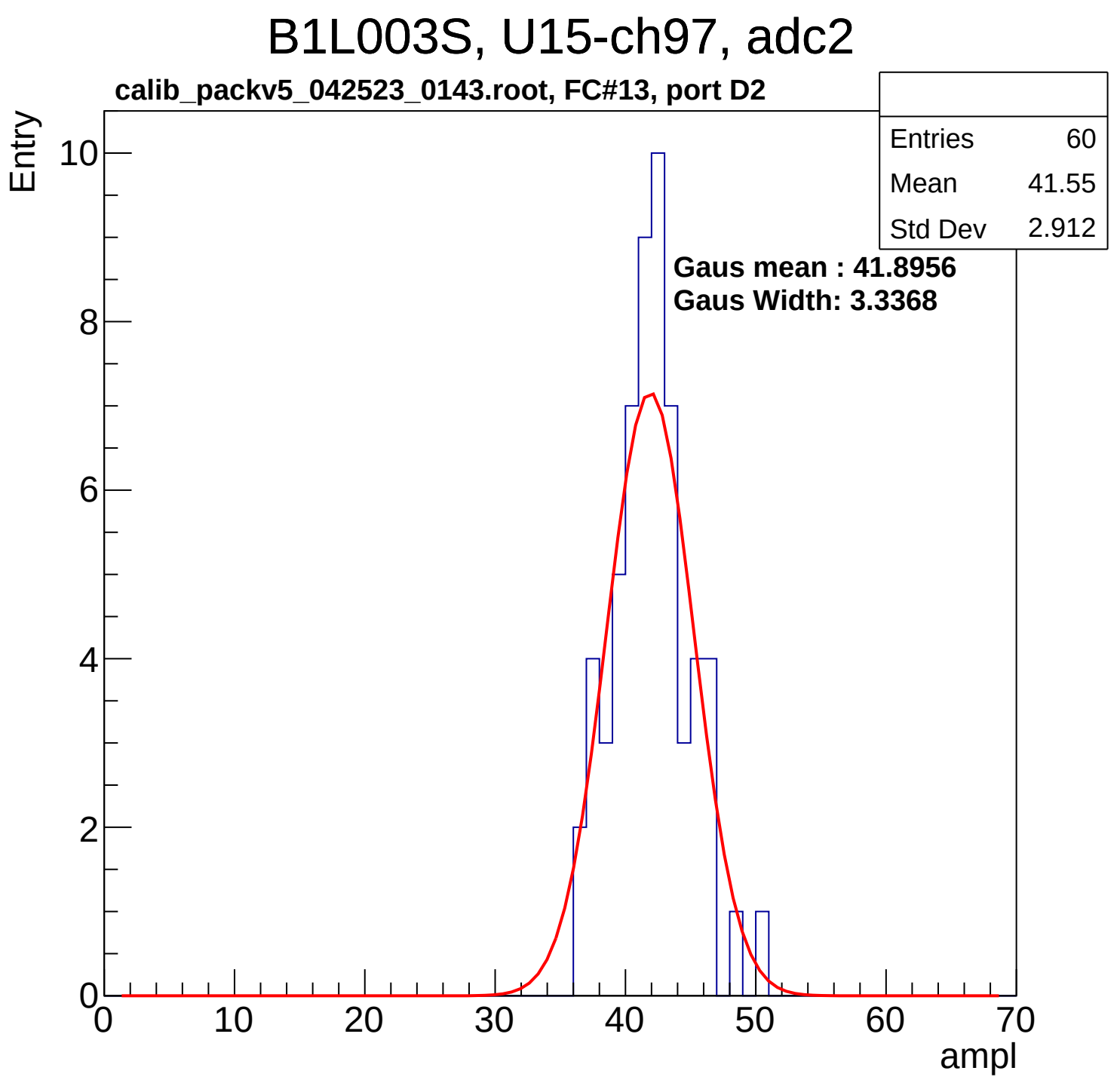
B1L003S, U15-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.55
Std Dev 2.912
Gaus mean : 41.8956
Gaus Width: 3.3368
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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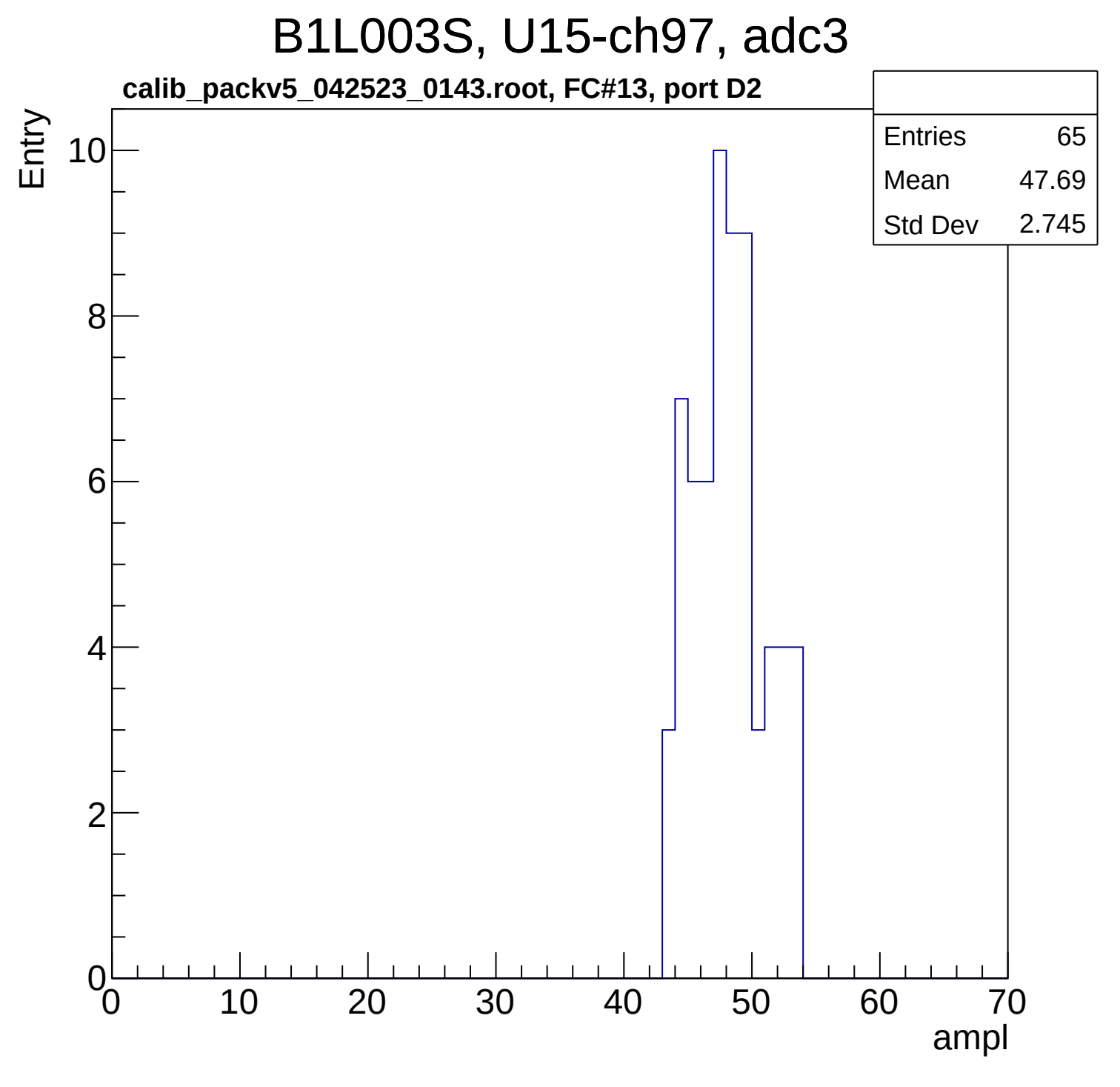
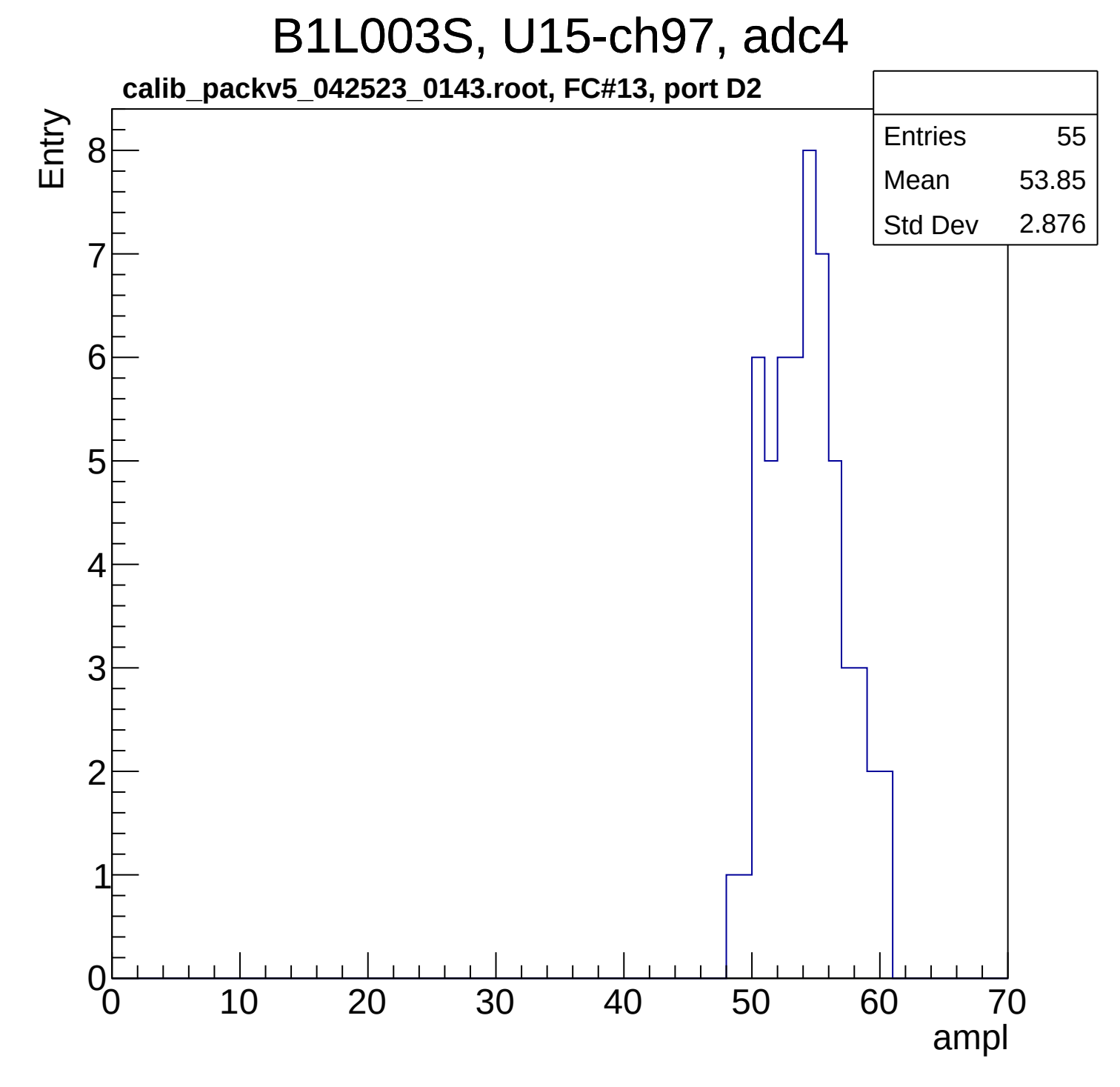

B1L003S, U15-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.69
Std Dev 2.745
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.85
Std Dev 2.876
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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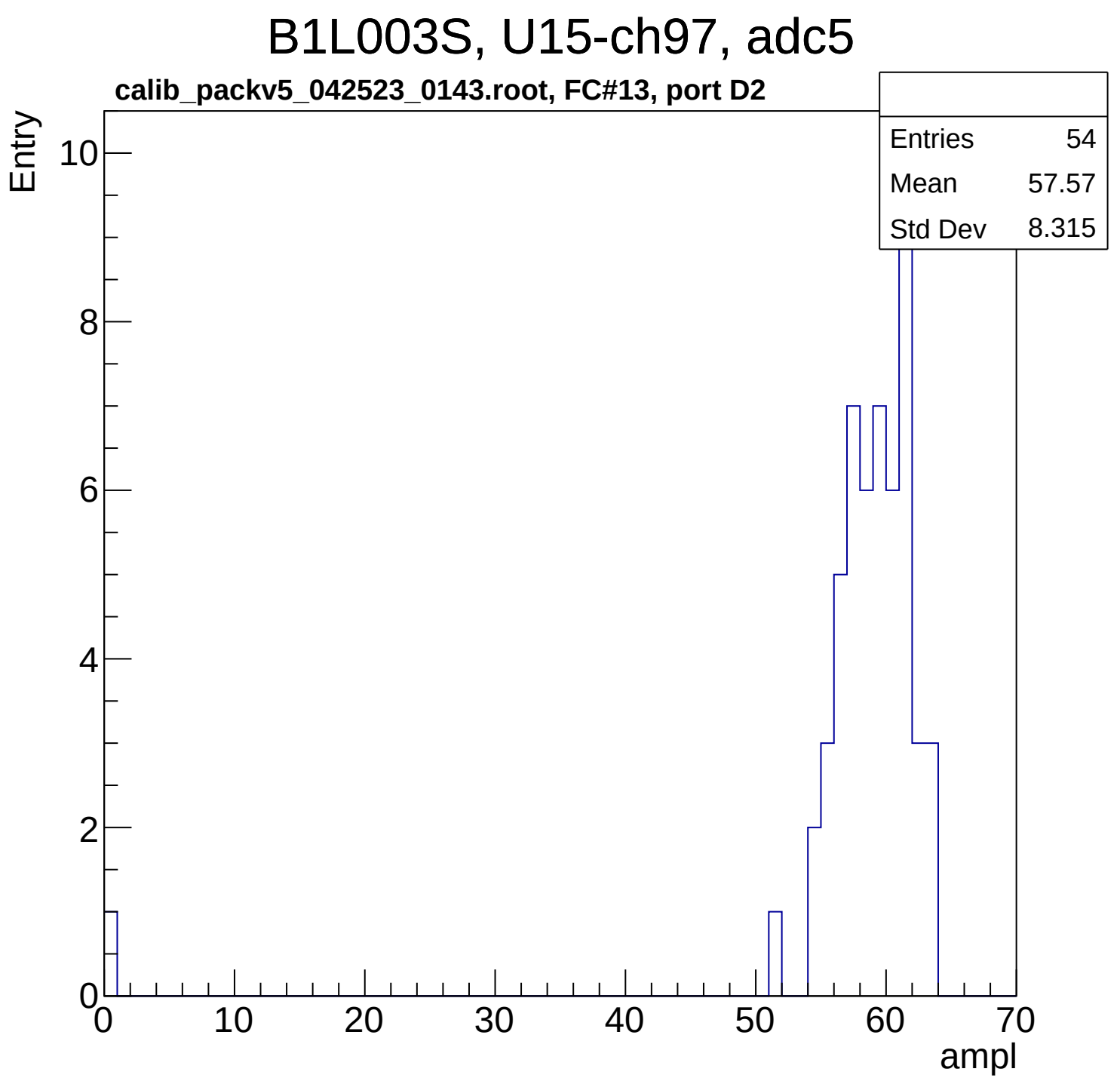
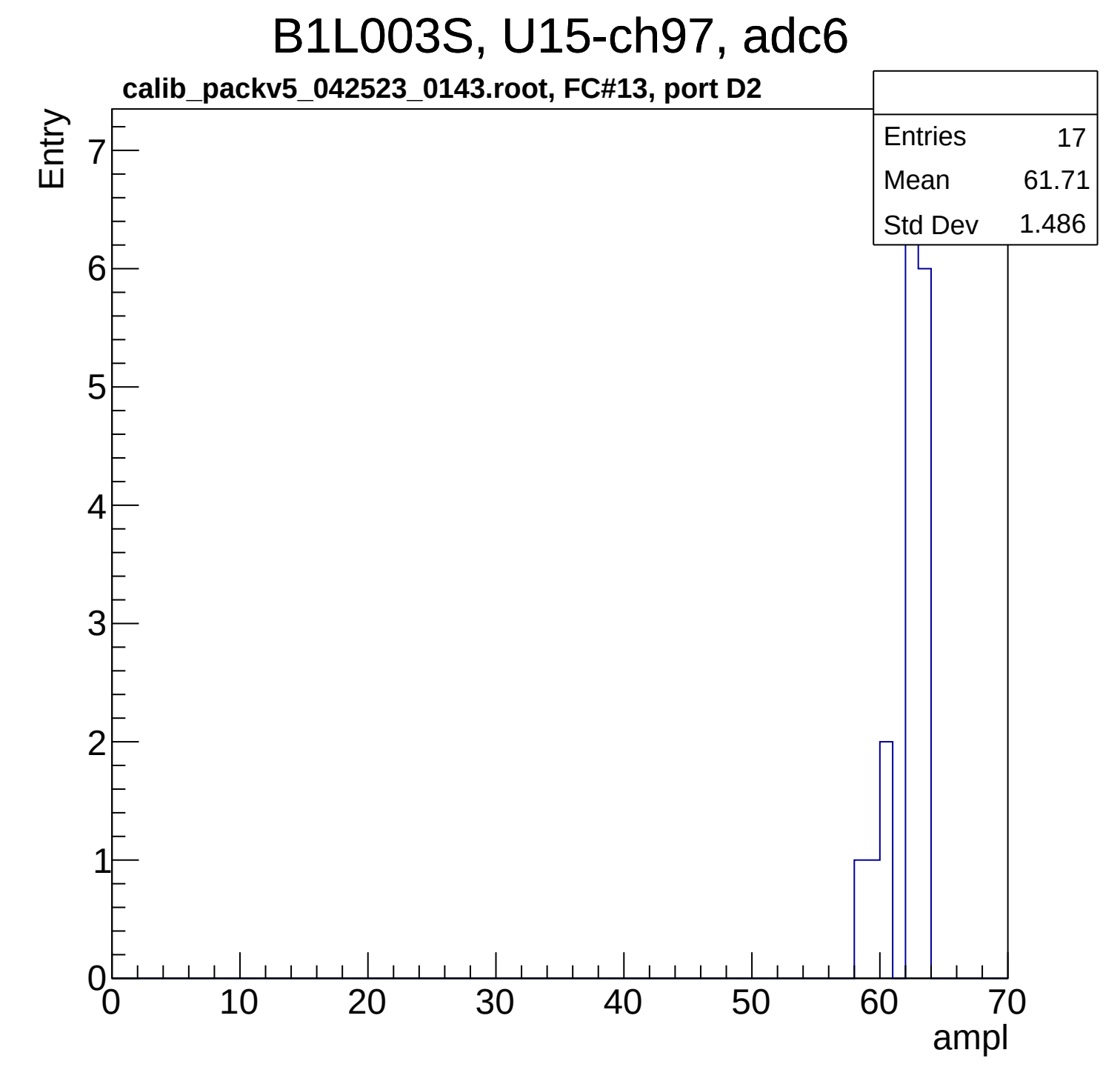

B1L003S, U15-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 57.57
Std Dev 8.315
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 61.71
Std Dev 1.486
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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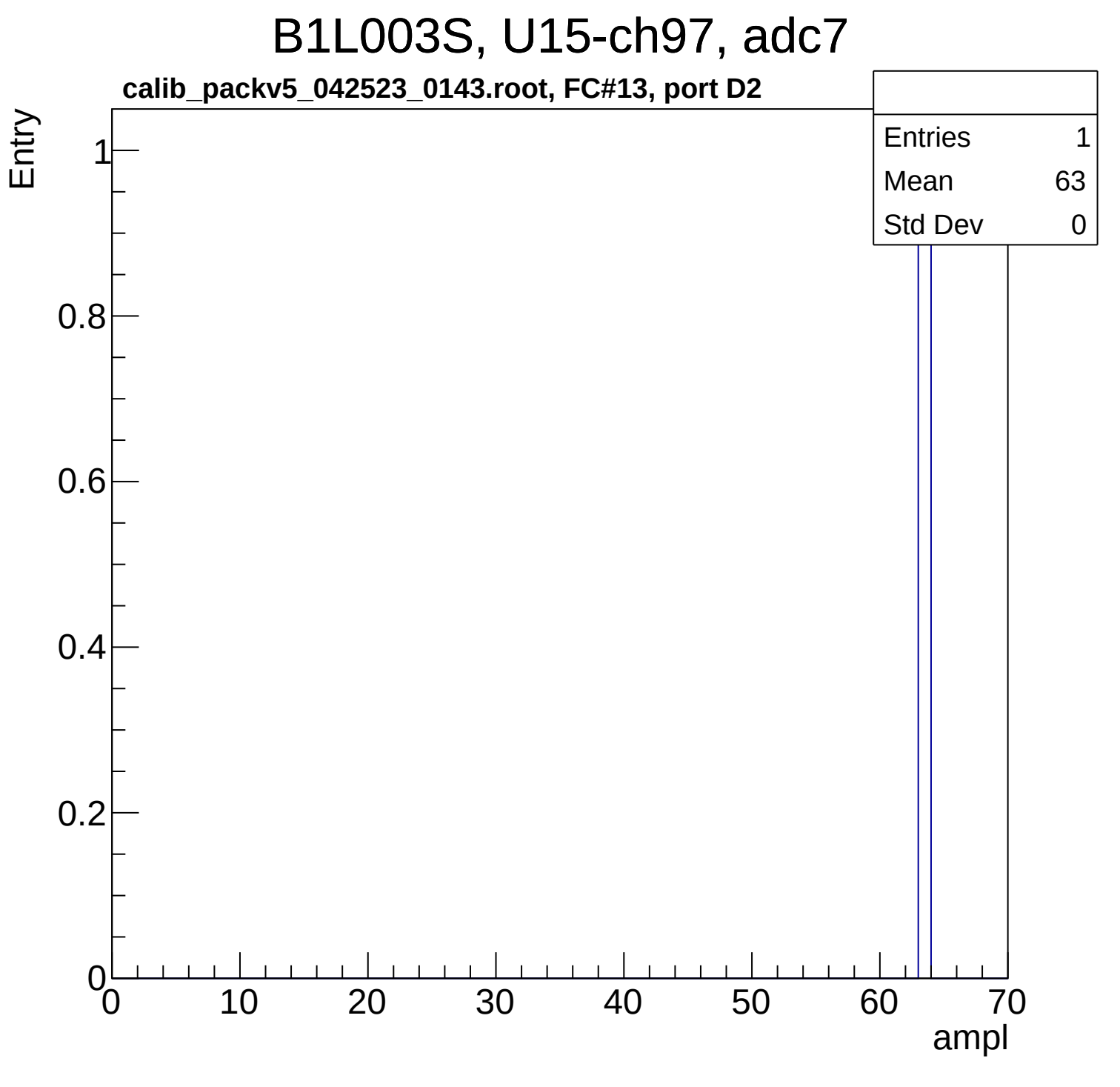
B1L003S, U15-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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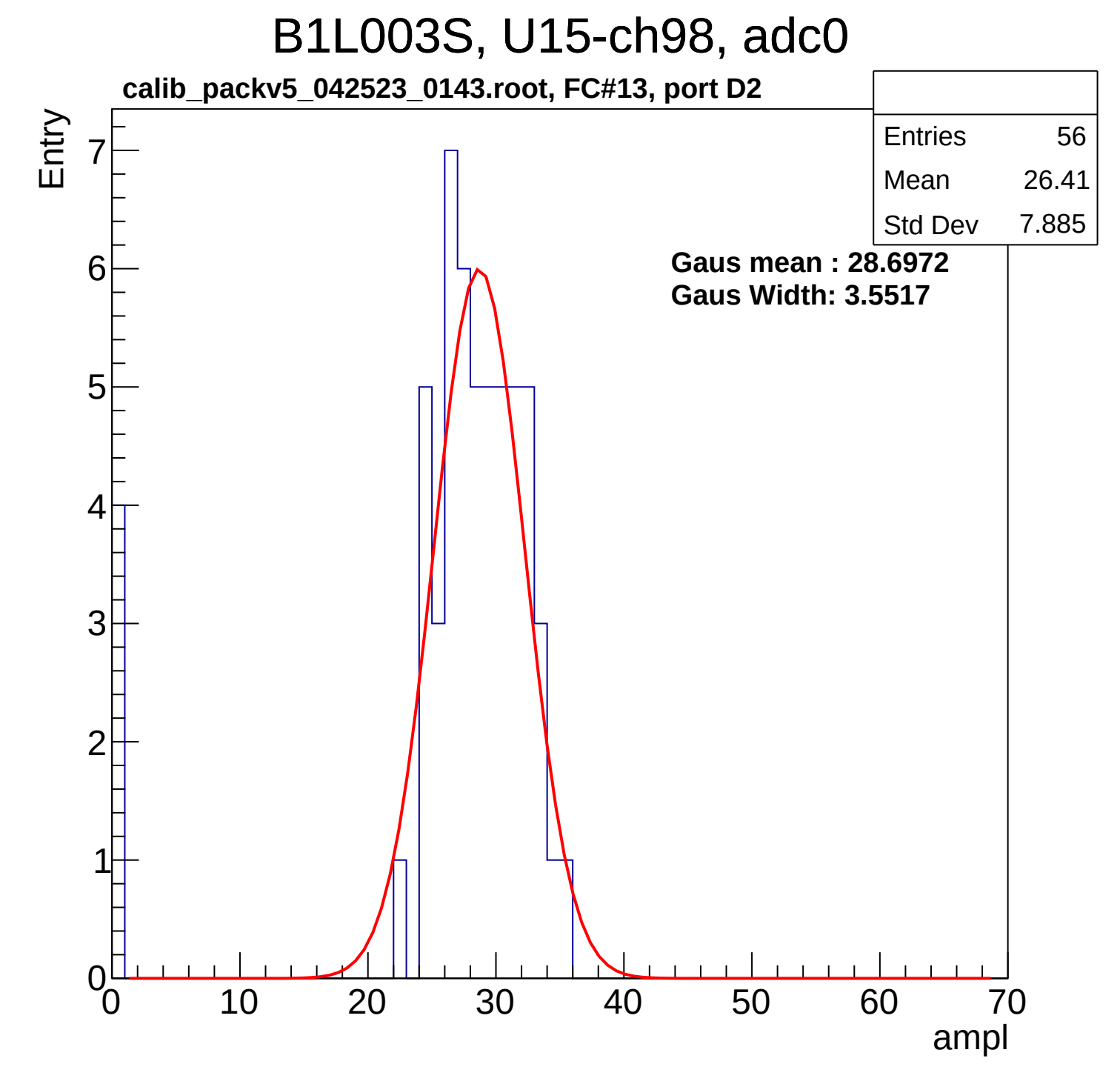
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 26.41
Std Dev 7.885
Gaus mean : 28.6972
Gaus Width: 3.5517
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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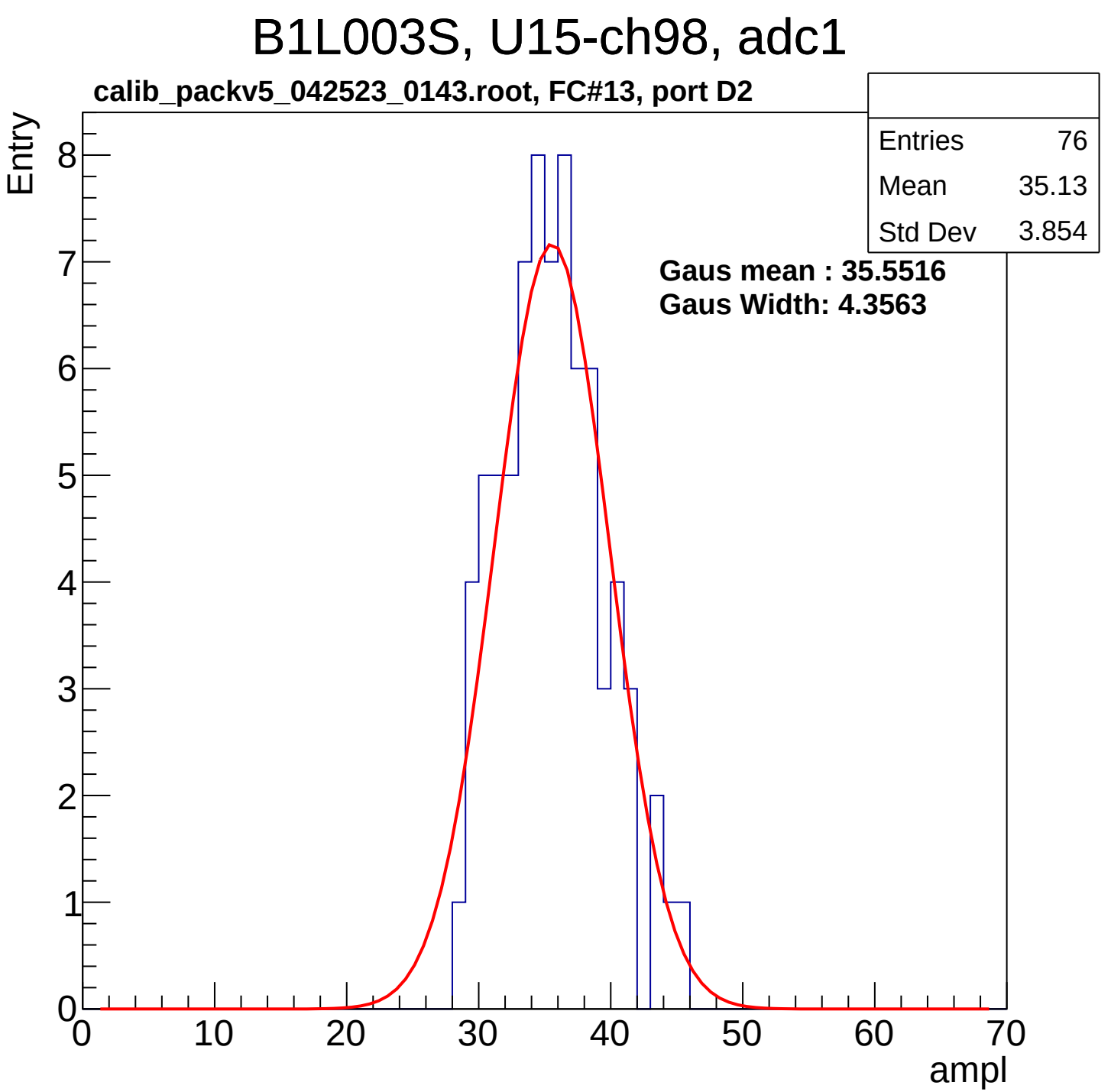
B1L003S, U15-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 35.13
Std Dev 3.854
Gaus mean : 35.5516
Gaus Width: 4.3563
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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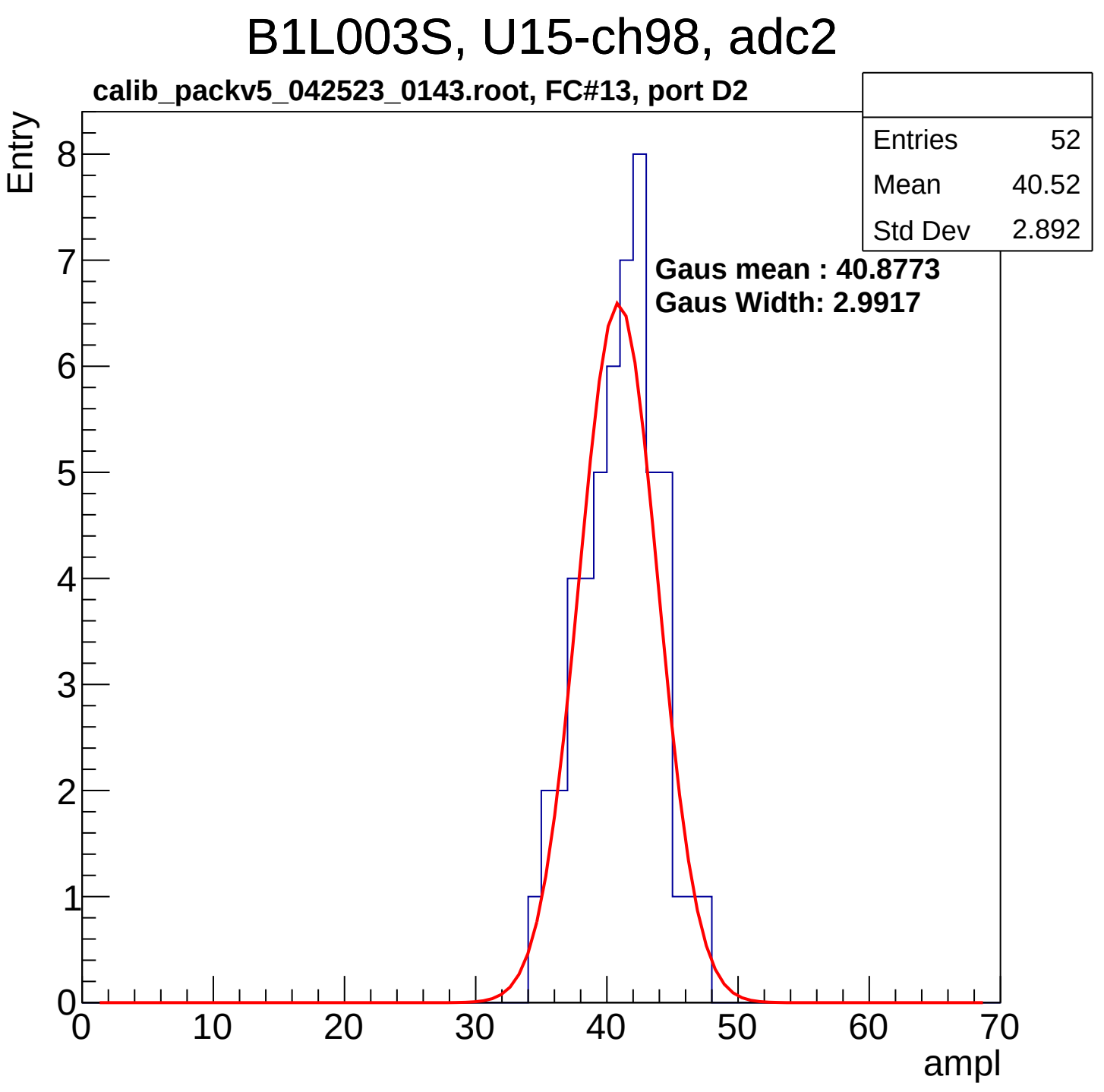
B1L003S, U15-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 40.52
Std Dev 2.892
Gaus mean : 40.8773
Gaus Width: 2.9917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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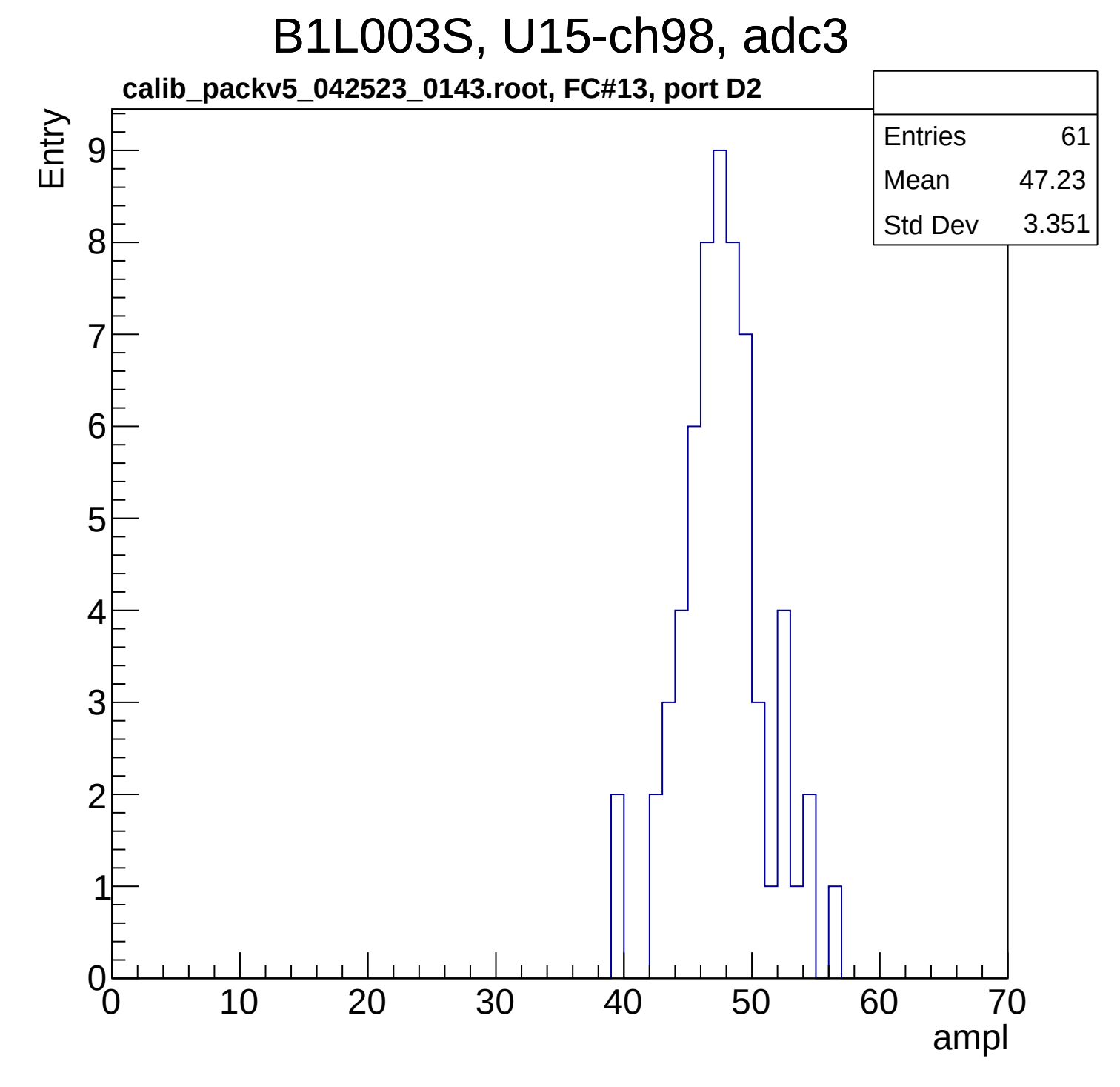
B1L003S, U15-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 47.23
Std Dev 3.351
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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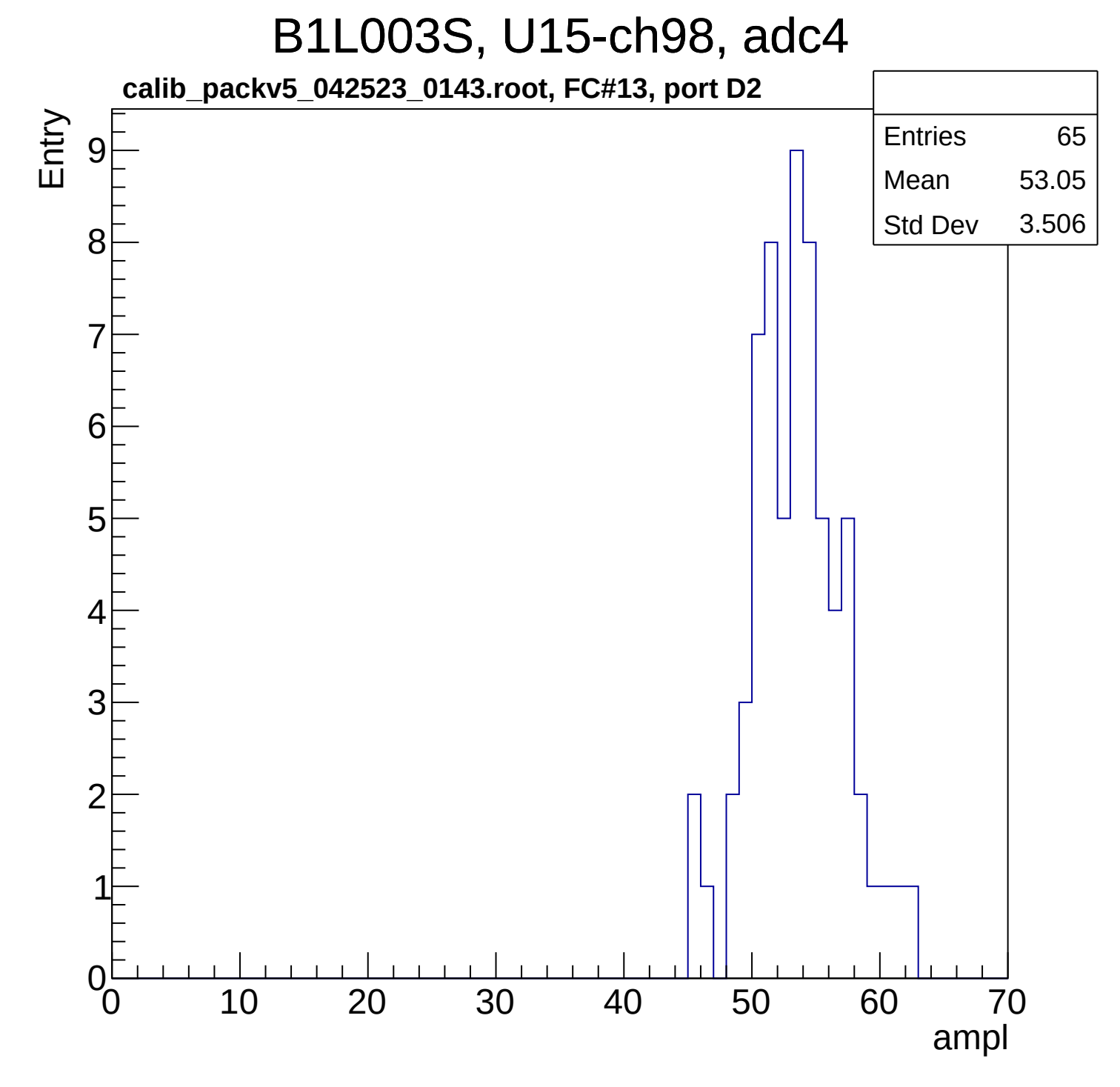
B1L003S, U15-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.05
Std Dev 3.506
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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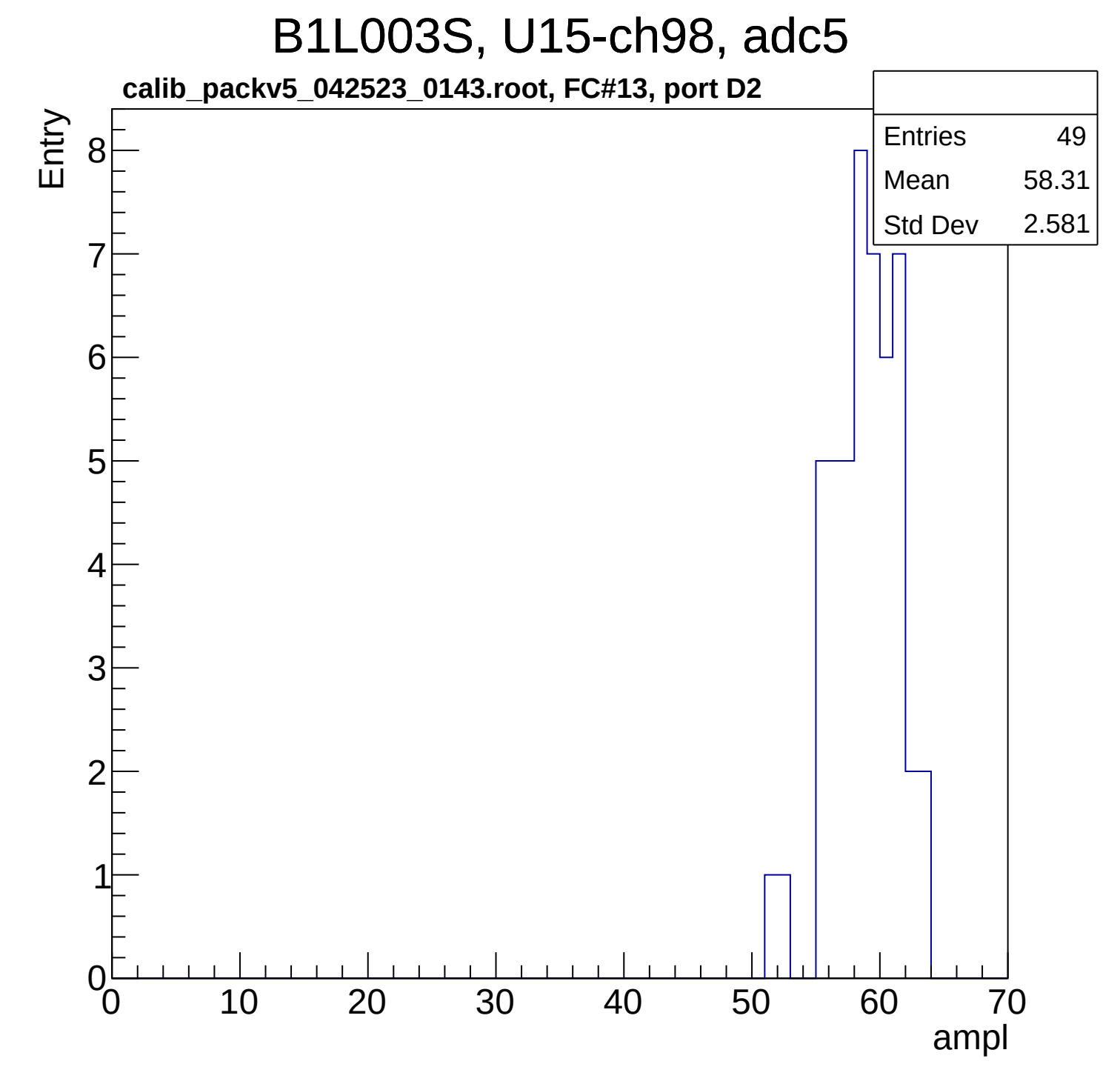
B1L003S, U15-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.31
Std Dev 2.581
Entry
ampl
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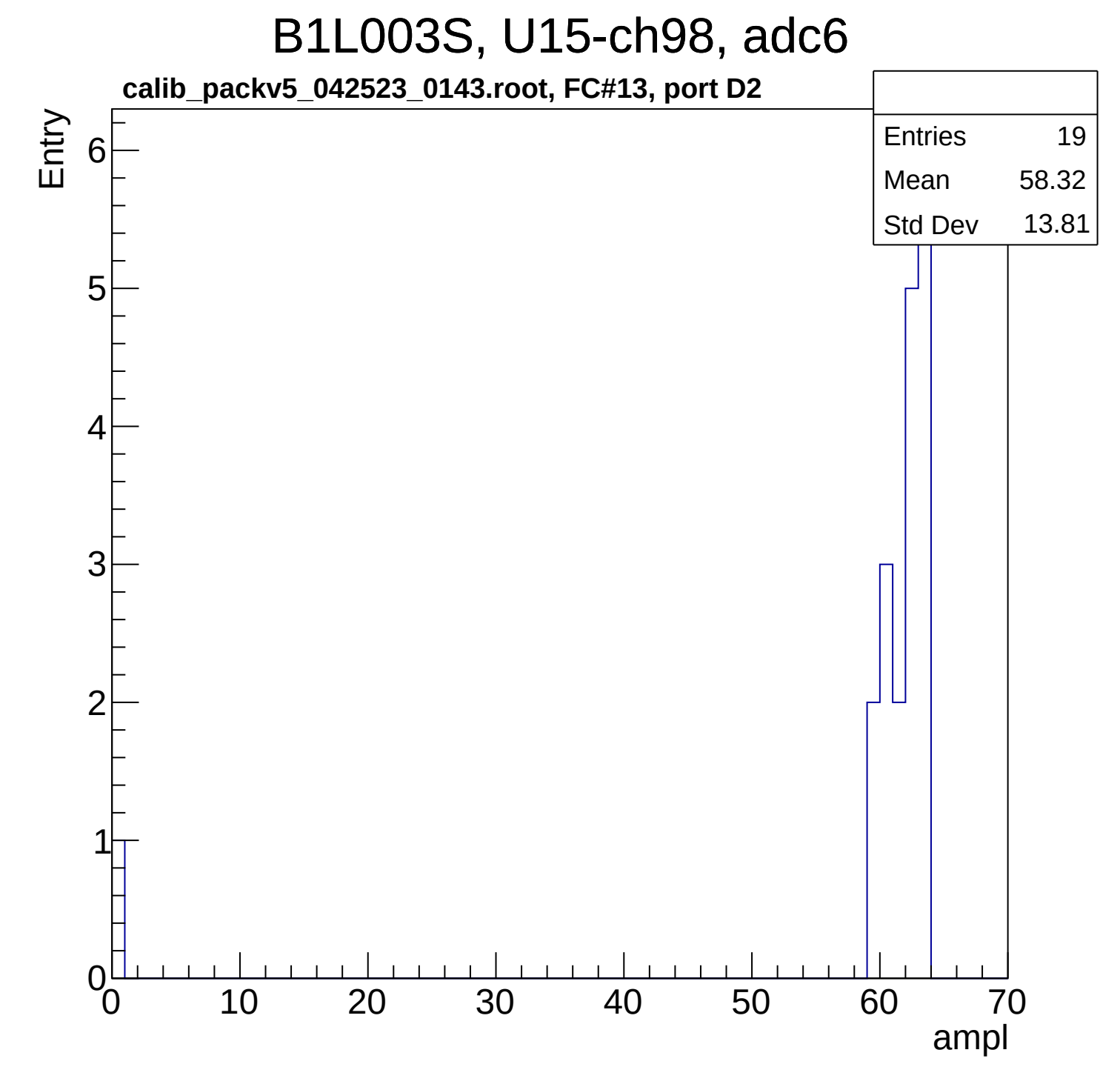
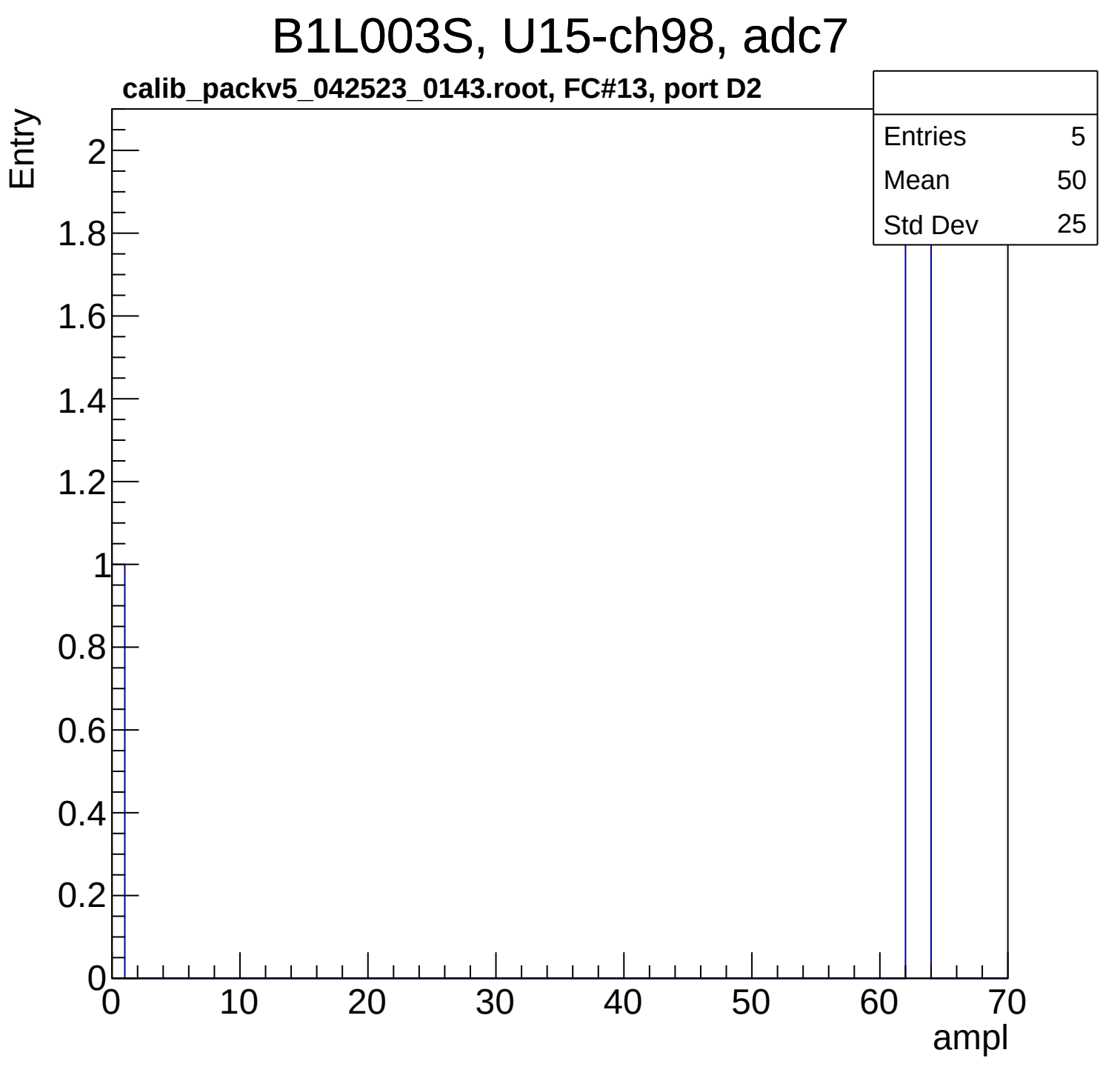
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.32
Std Dev 13.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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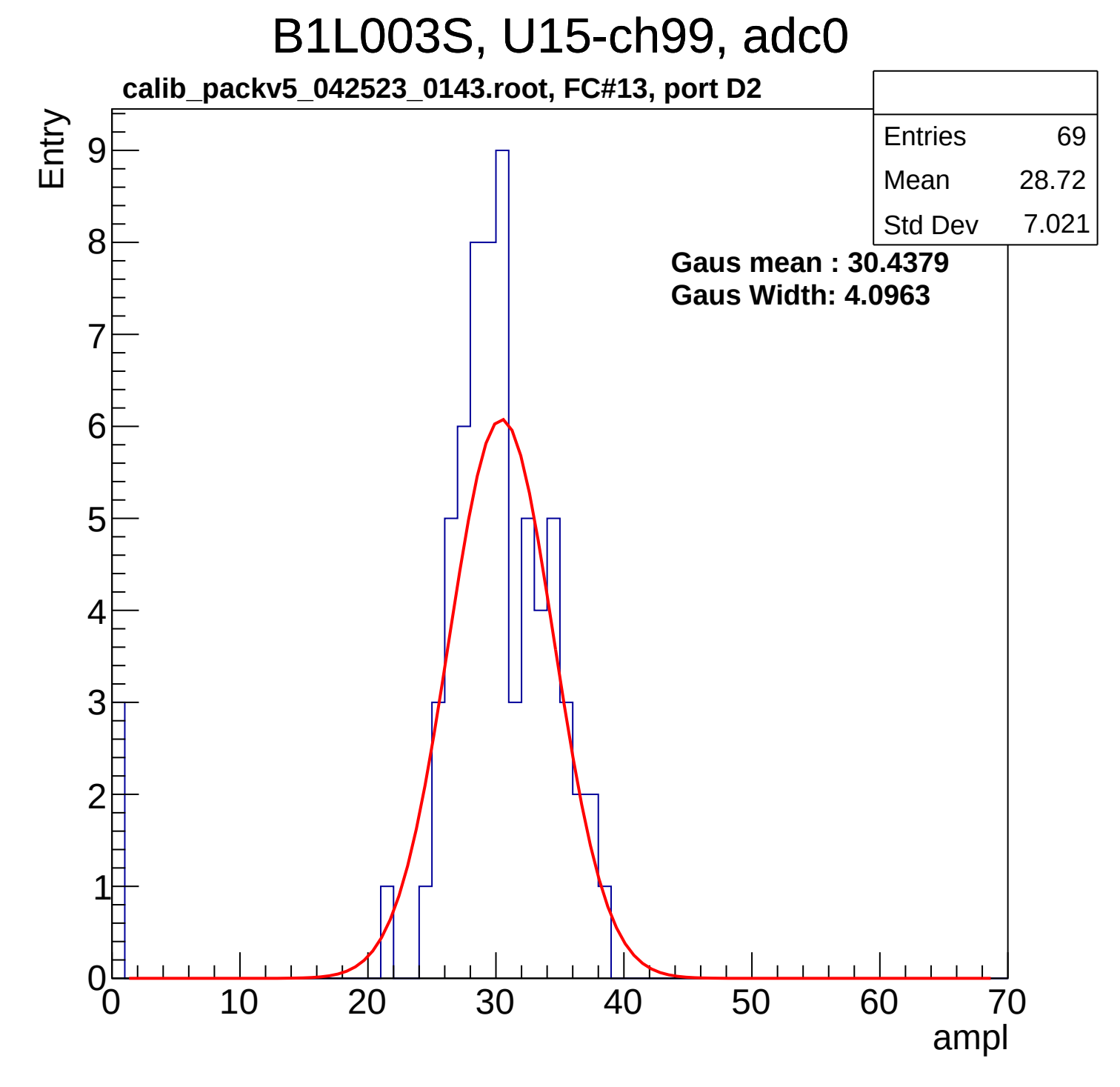
20
30
40
50
60
70


B1L003S, U15-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.72
Std Dev 7.021
Gaus mean : 30.4379
Gaus Width: 4.0963
Entry
ampl
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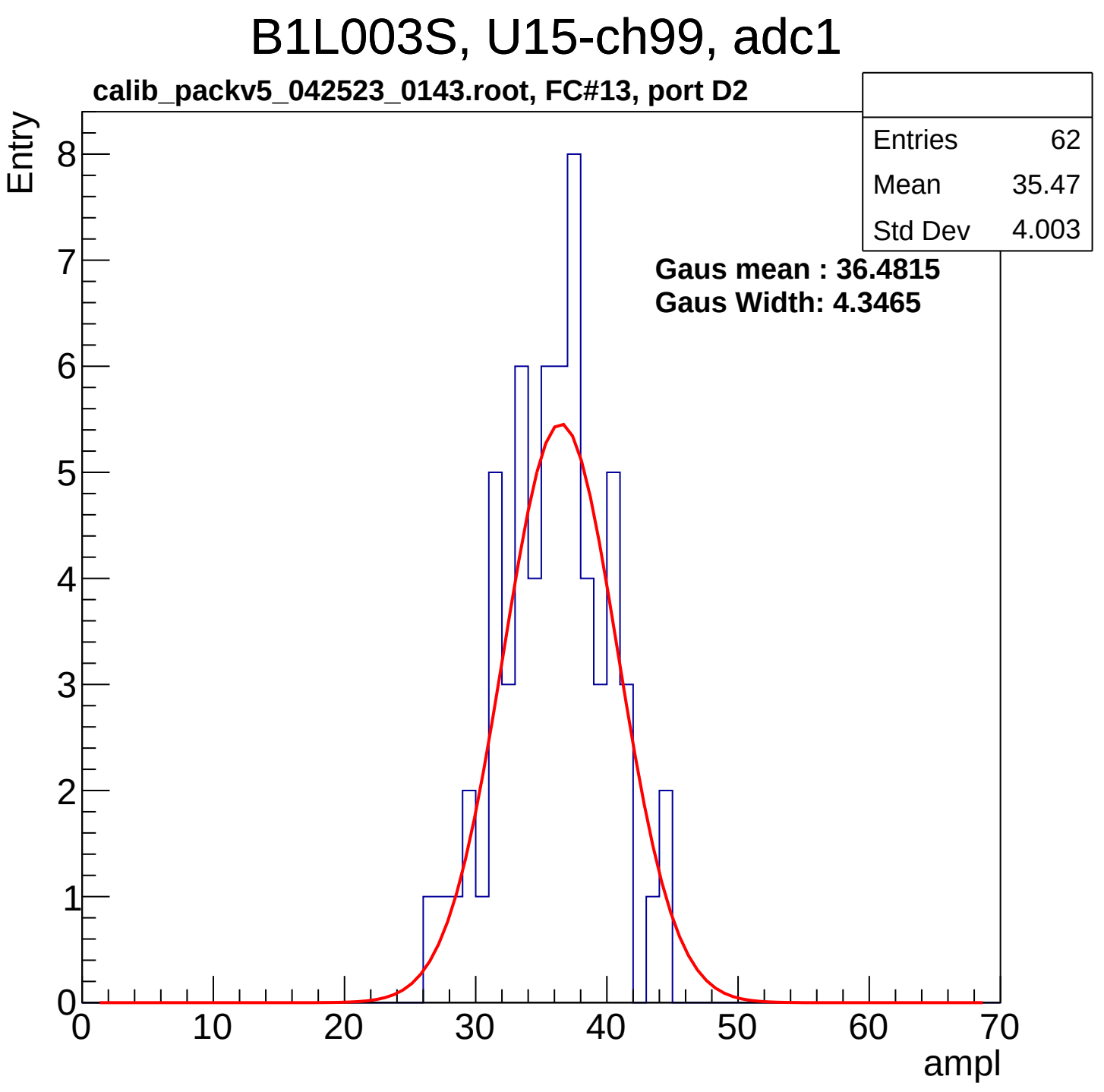
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 35.47
Std Dev 4.003
Gaus mean : 36.4815
Gaus Width: 4.3465
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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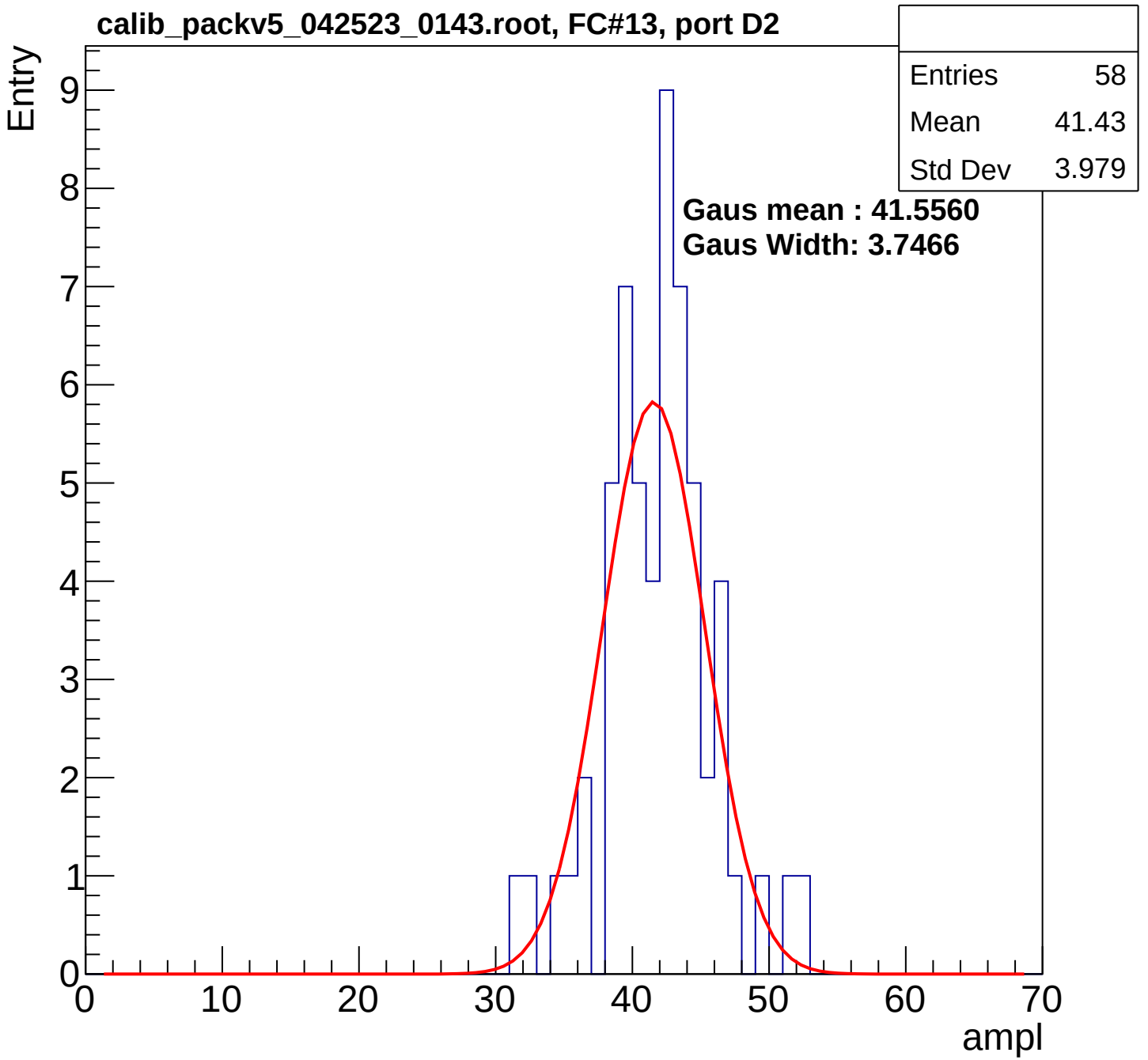
B1L003S, U15-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.43
Std Dev 3.979
Gaus mean : 41.5560
Gaus Width: 3.7466
Entry
ampl
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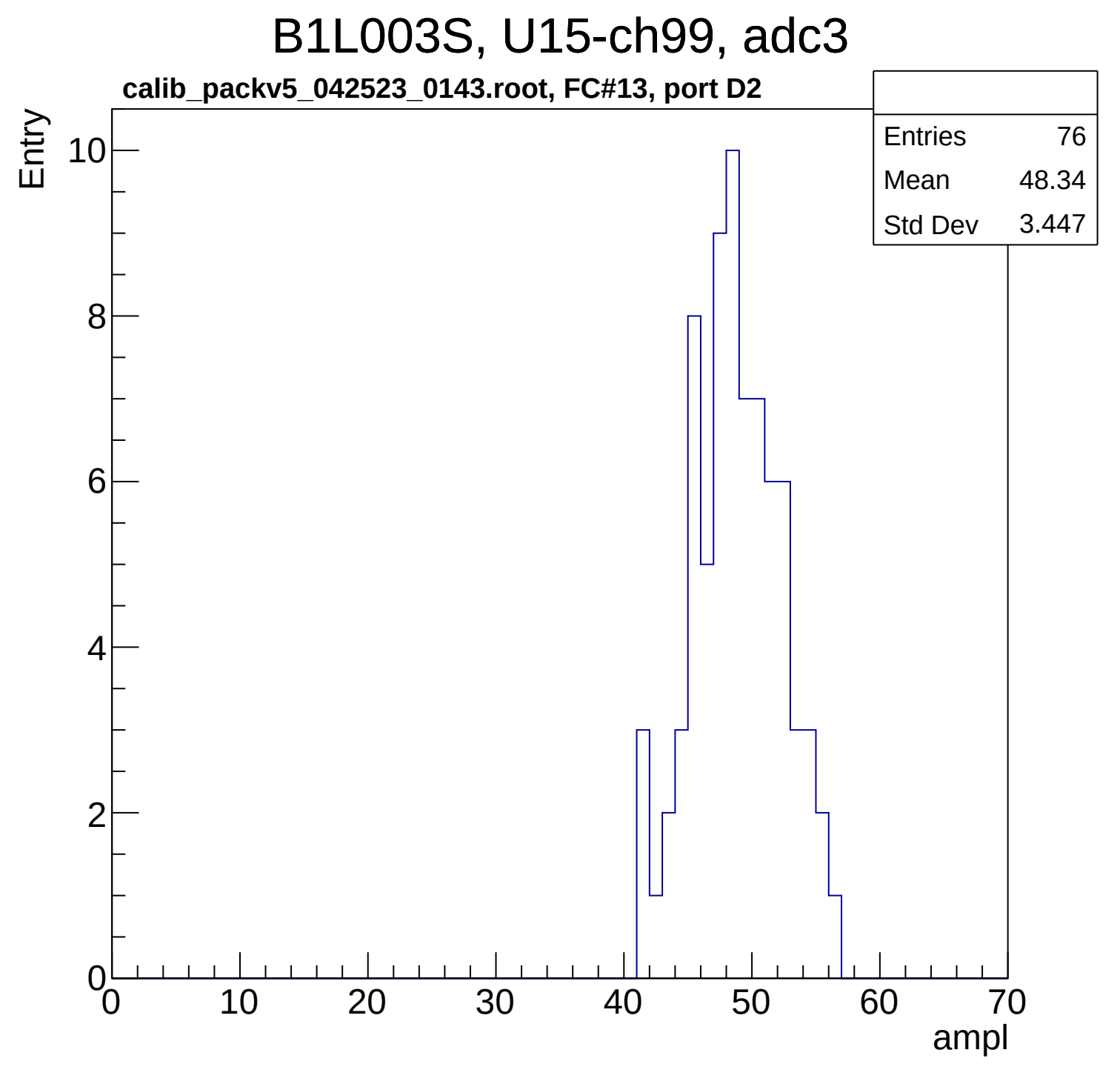
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 48.34
Std Dev 3.447
Entry
0
2
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6
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10
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10
20
30
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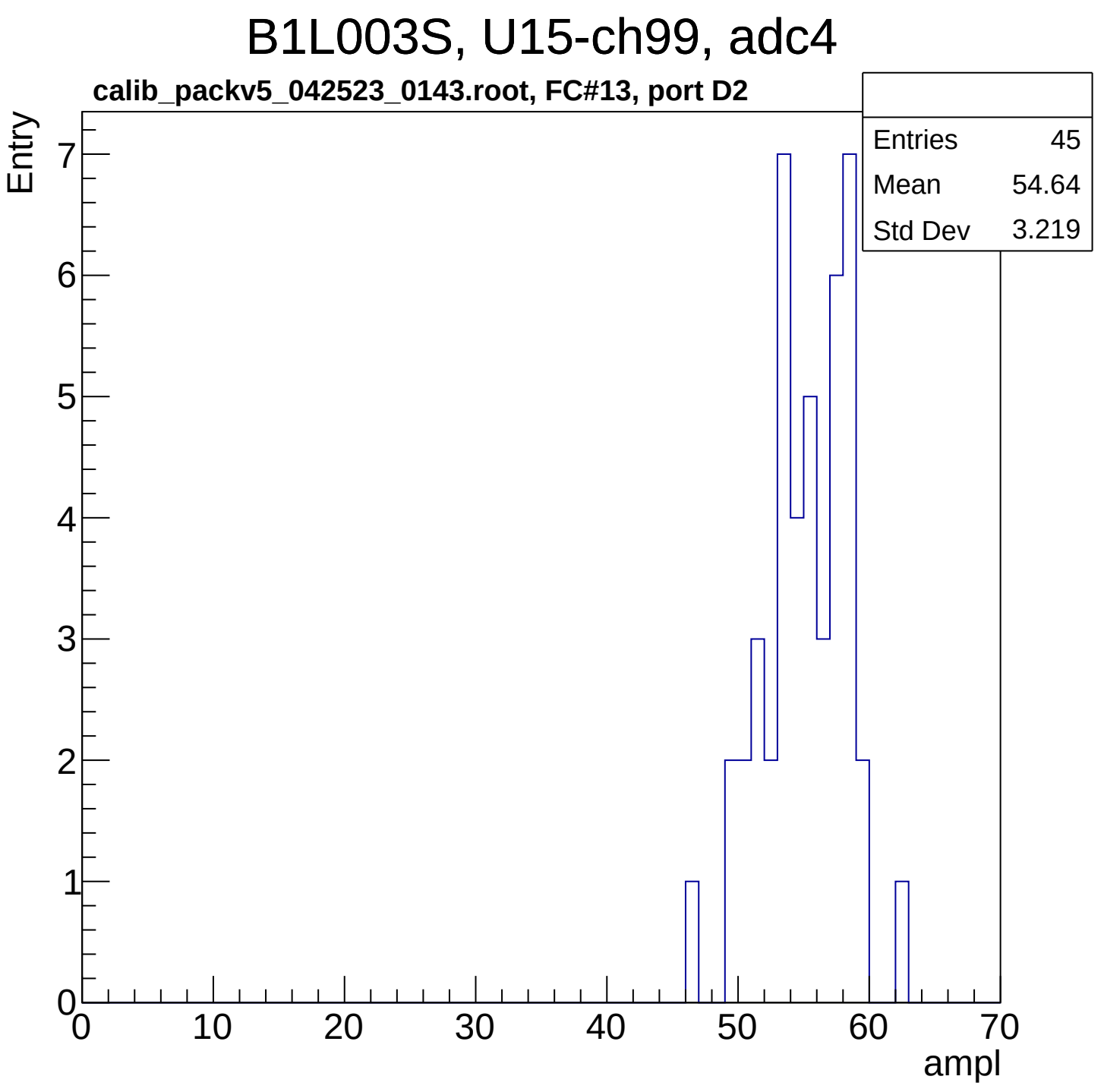
50
60
70
ampl


B1L003S, U15-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 54.64
Std Dev 3.219
Entry
ampl
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3
4
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6
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0
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40
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60
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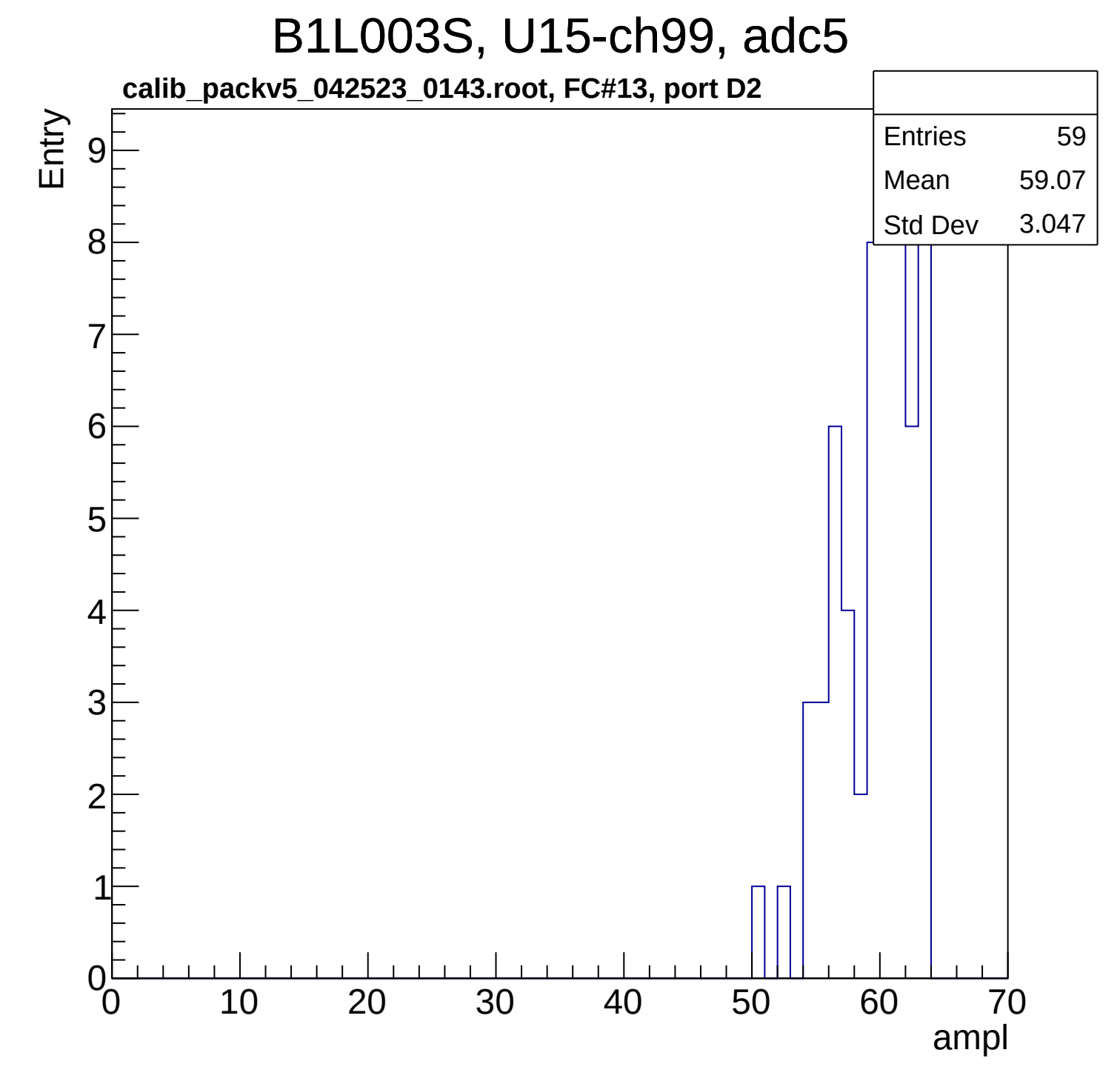
B1L003S, U15-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 59.07
Std Dev 3.047
Entry
ampl
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2
3
4
5
6
7
8
9
0
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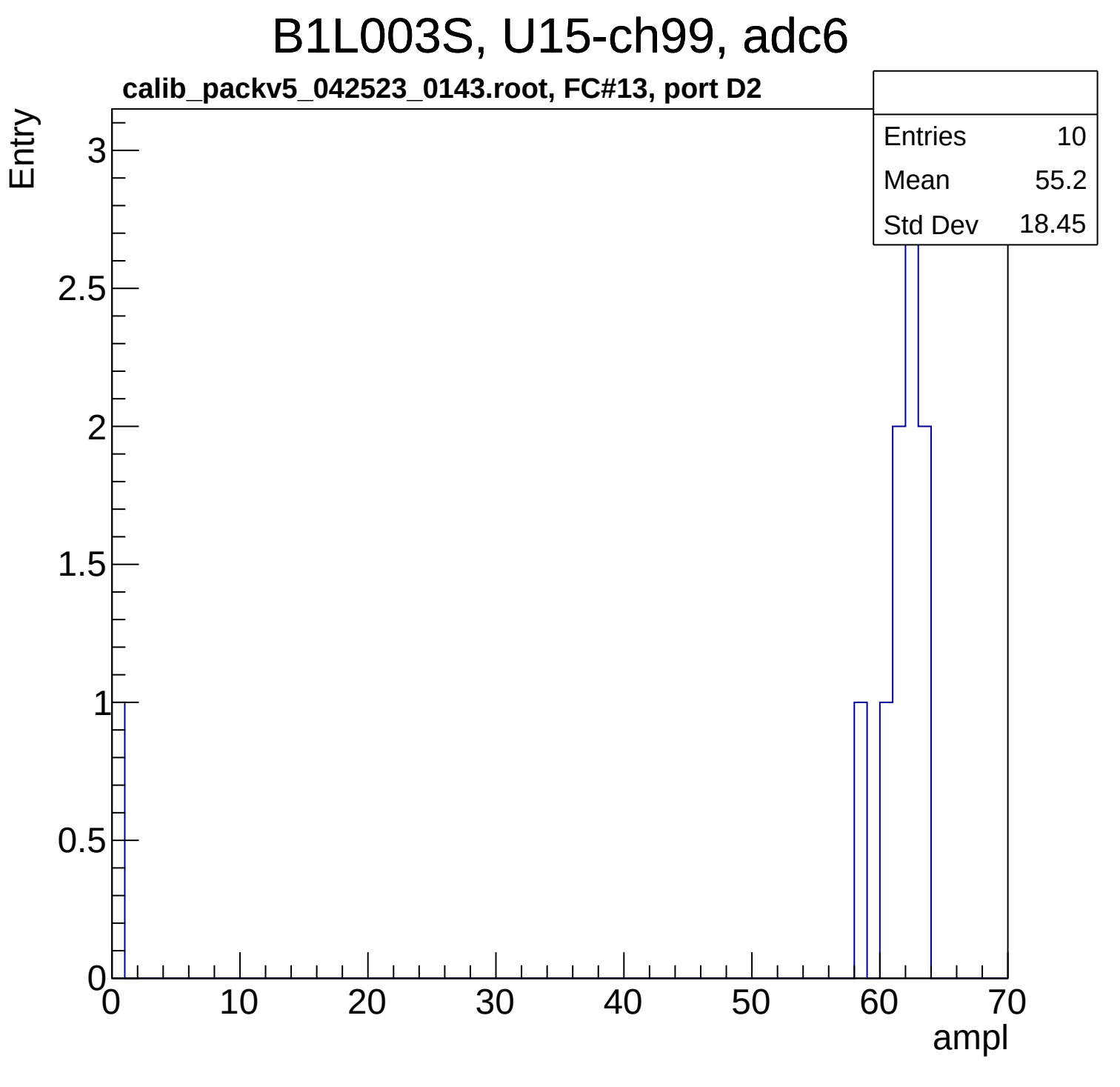
B1L003S, U15-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.2
Std Dev 18.45
Entry
ampl
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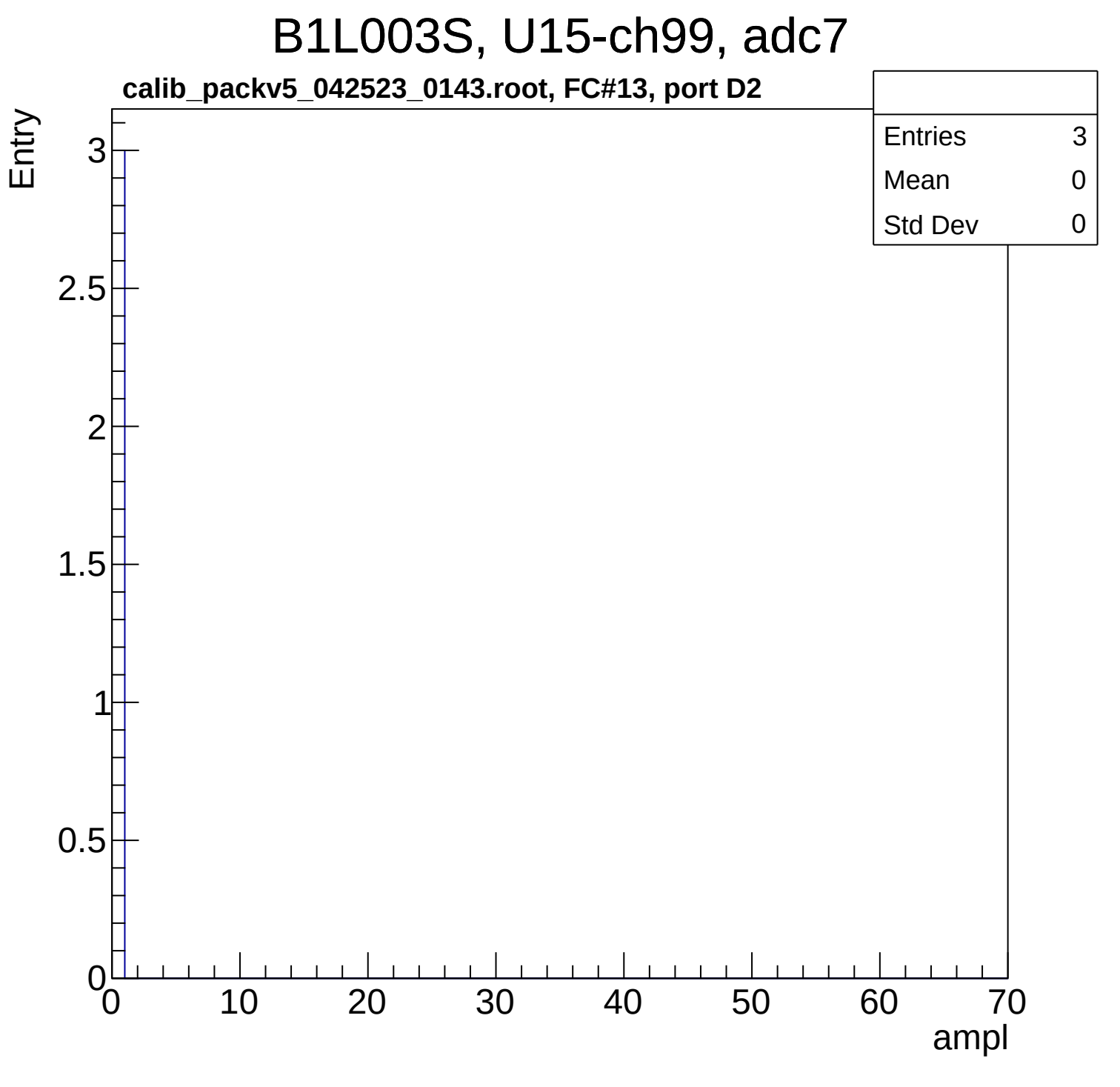
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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1.5
2
2.5
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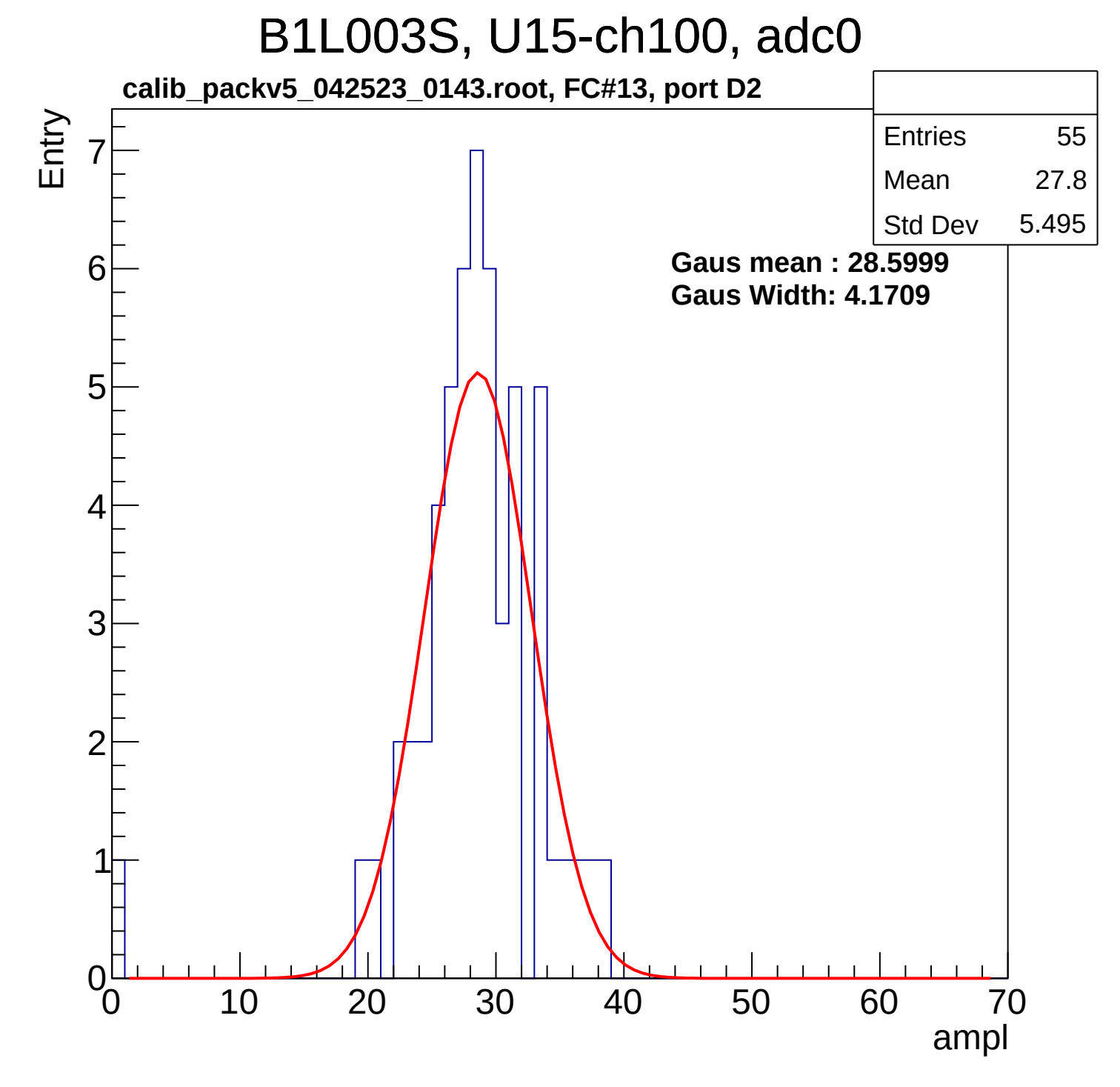
50
60
70
ampl


B1L003S, U15-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 27.8
Std Dev 5.495
Gaus mean : 28.5999
Gaus Width: 4.1709
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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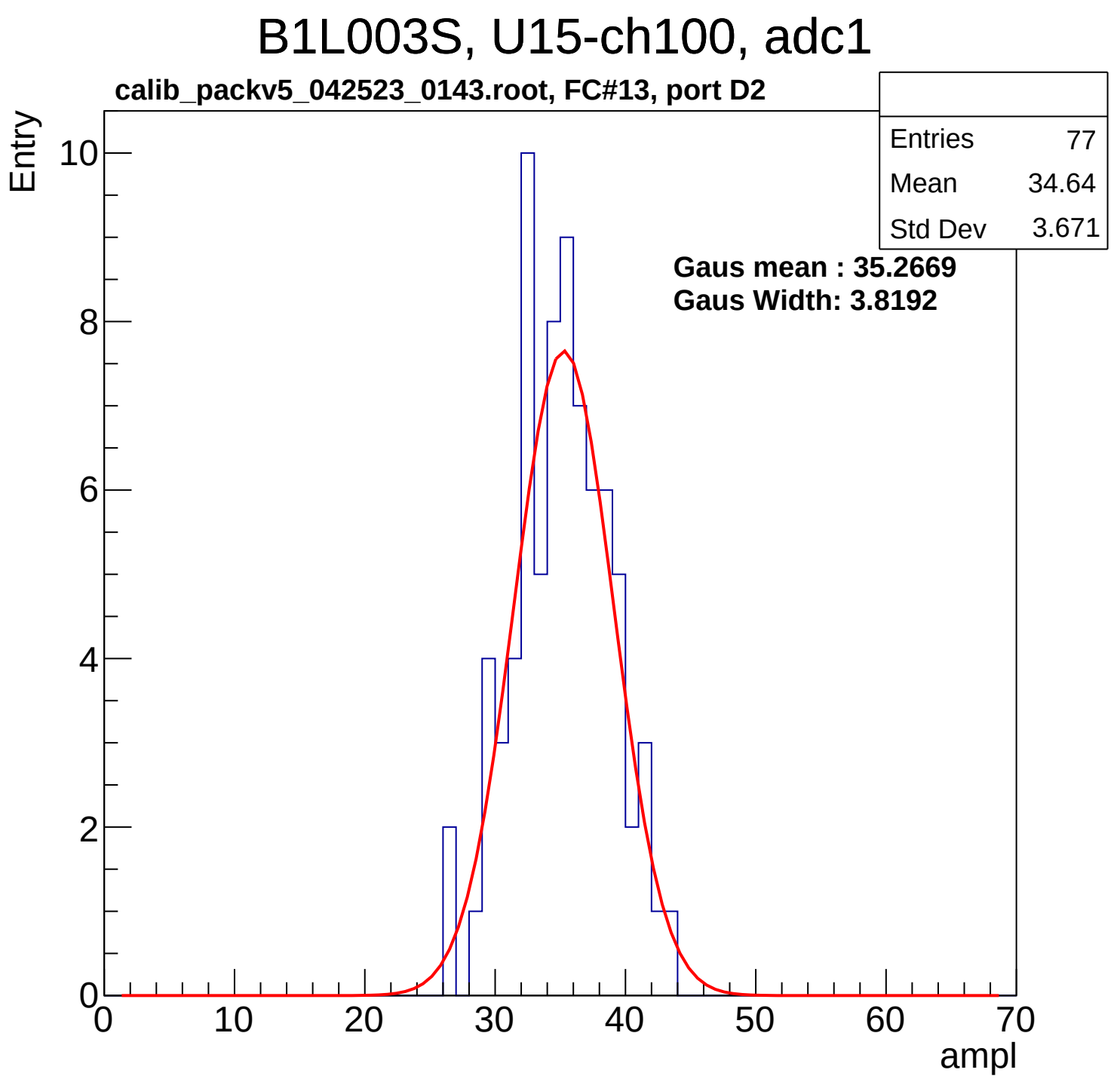
B1L003S, U15-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 34.64
Std Dev 3.671
Gaus mean : 35.2669
Gaus Width: 3.8192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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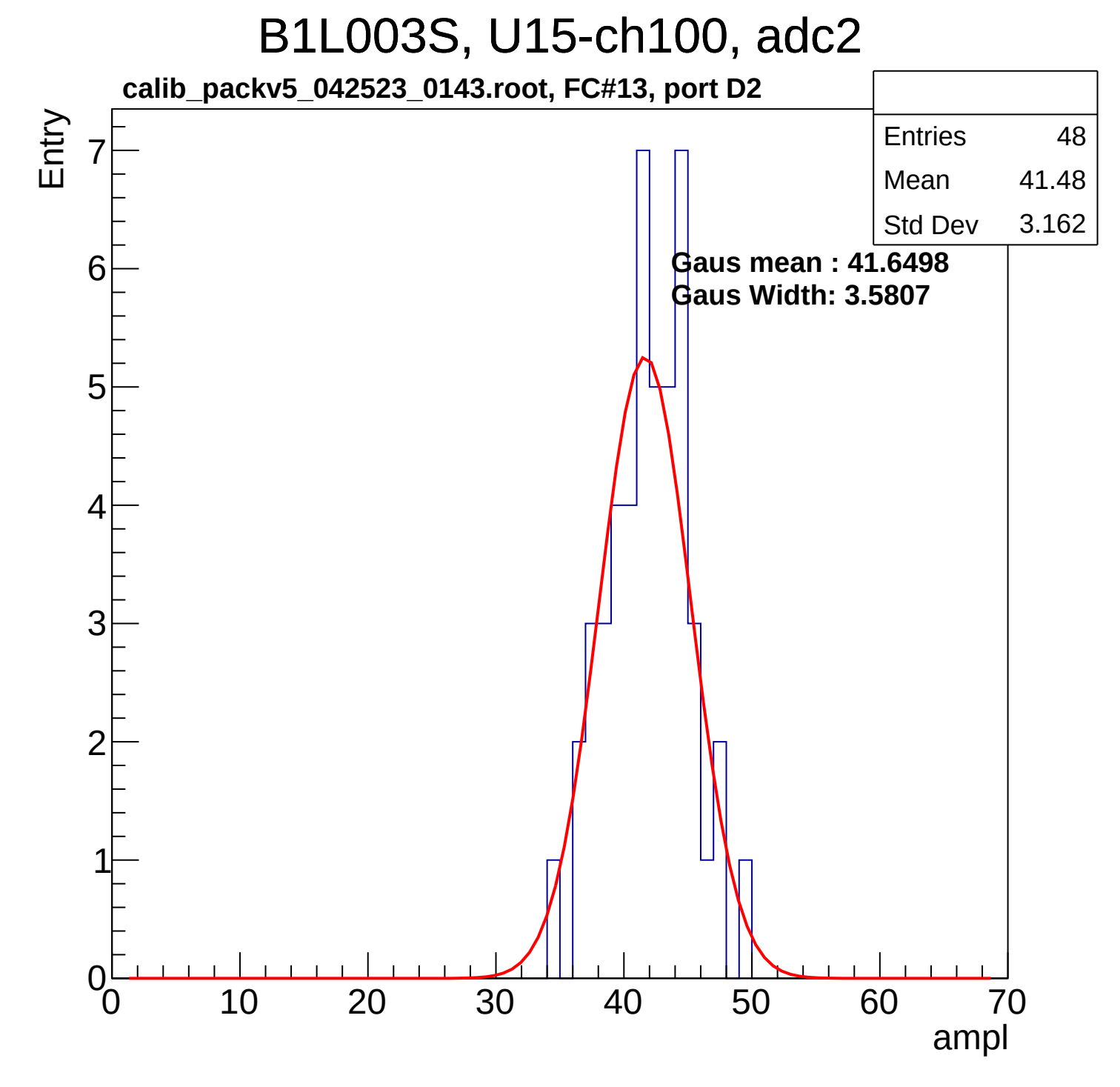
B1L003S, U15-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 41.48
Std Dev 3.162
Gaus mean : 41.6498
Gaus Width: 3.5807
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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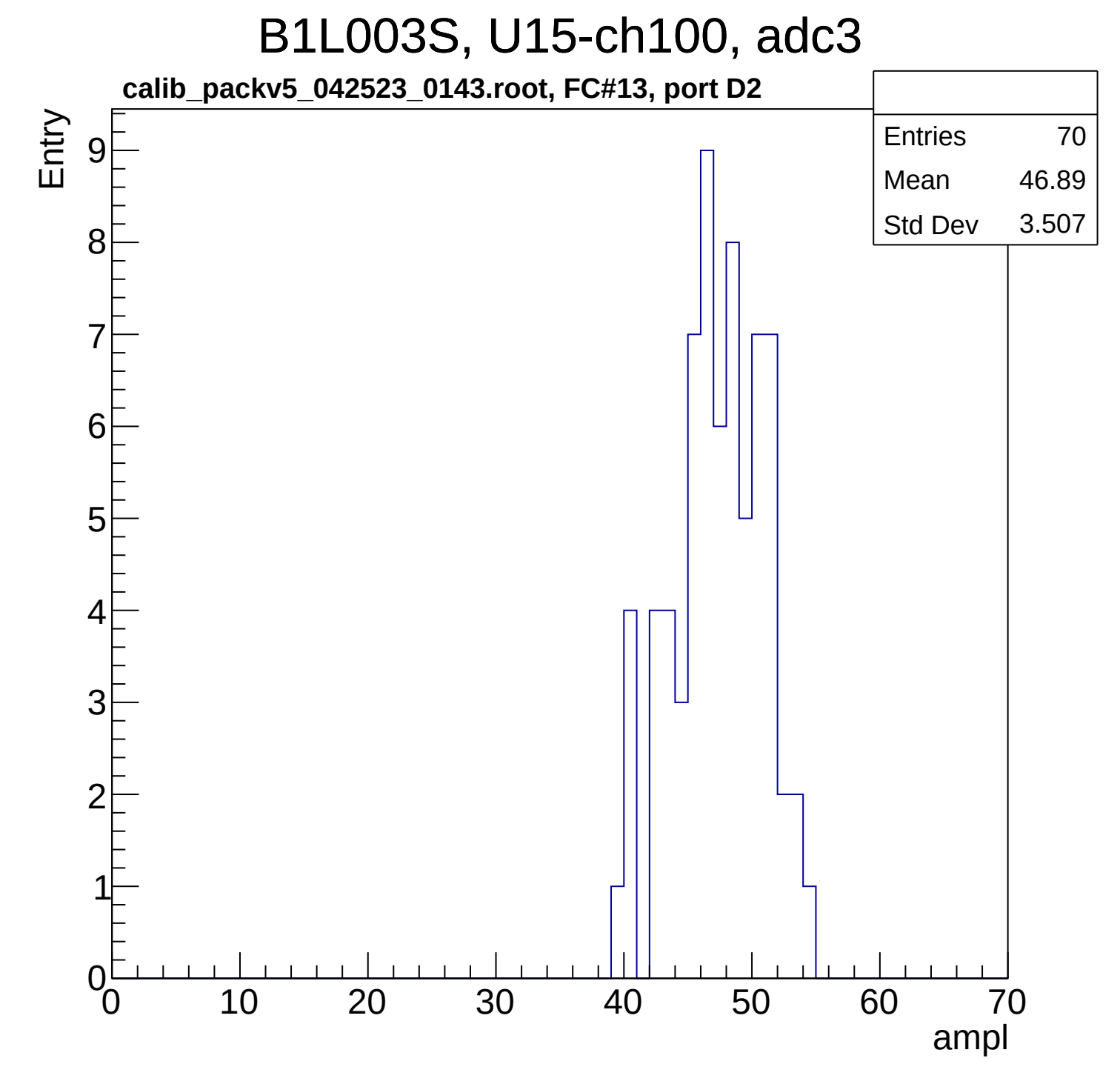
B1L003S, U15-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 46.89
Std Dev 3.507
Entry
ampl
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4
5
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7
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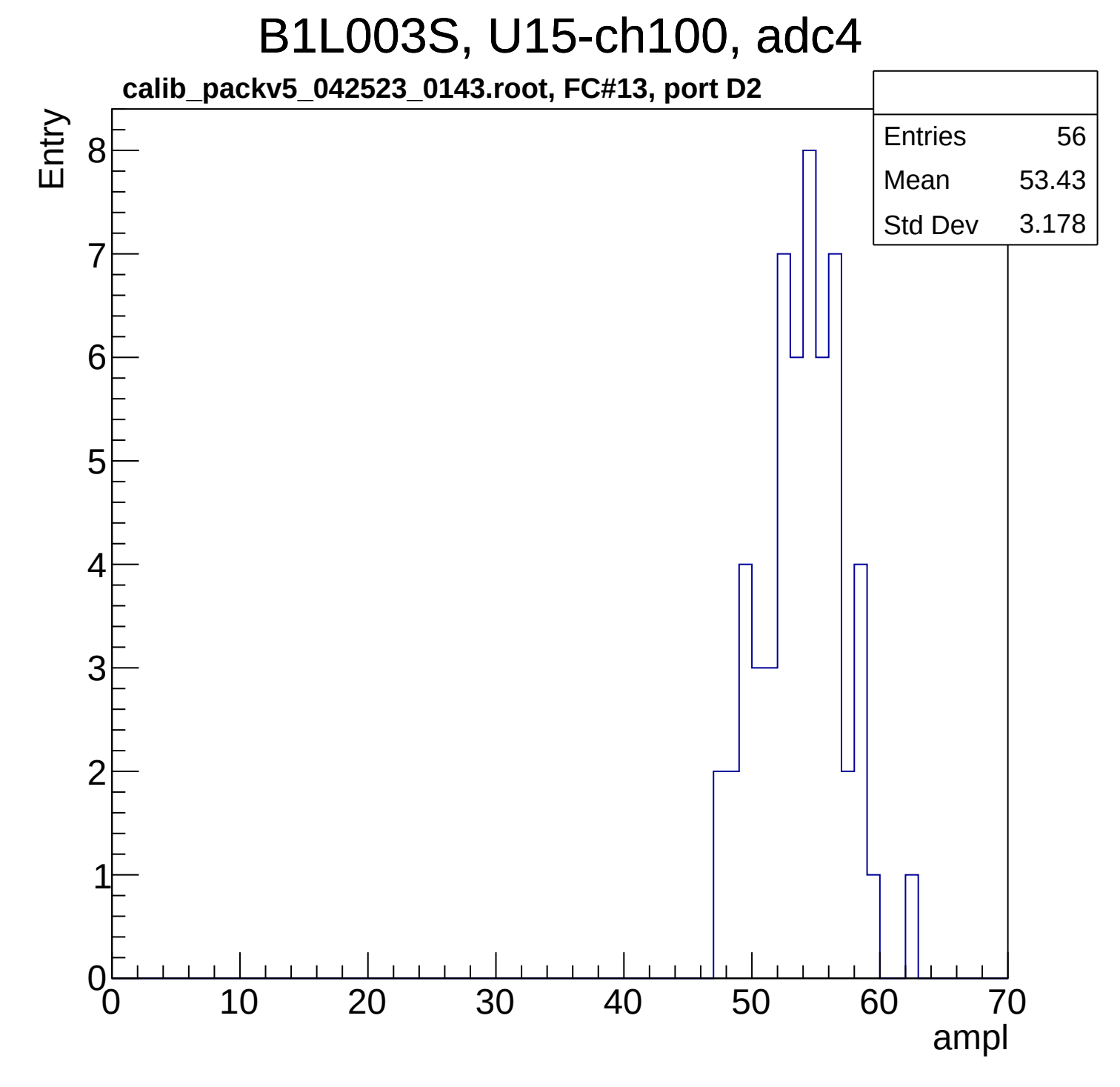
B1L003S, U15-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.43
Std Dev 3.178
Entry
ampl
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3
4
5
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0
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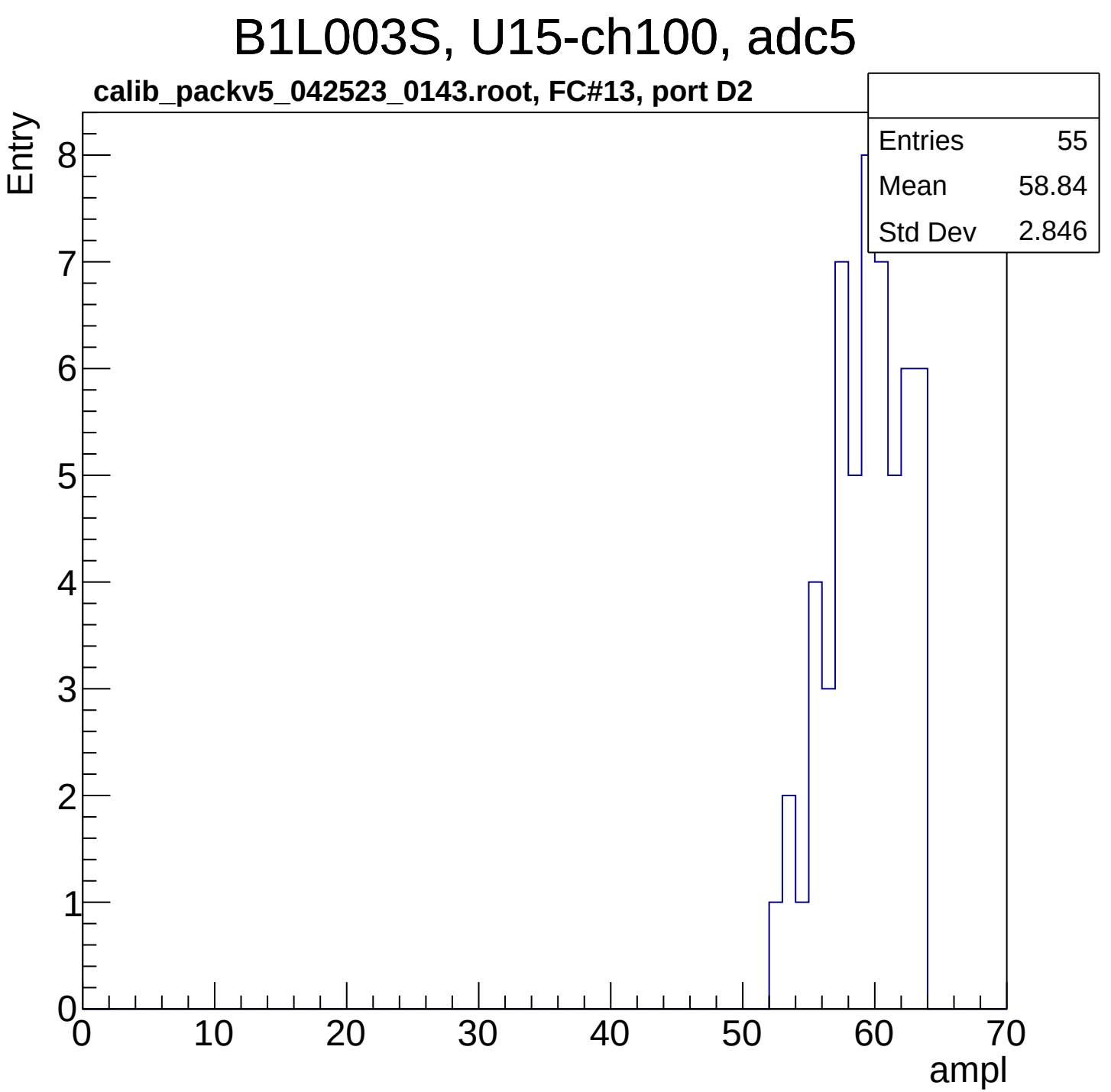
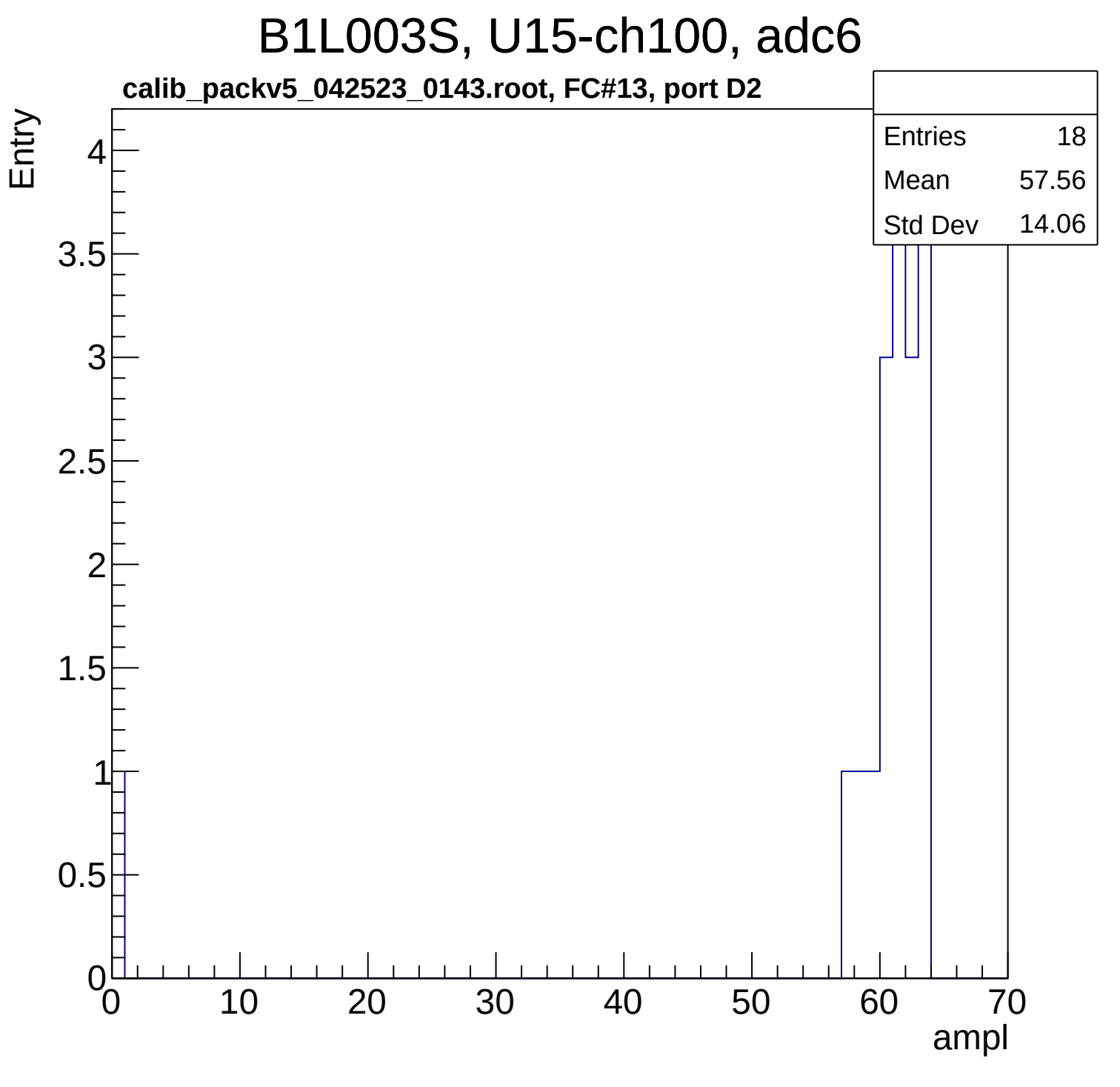
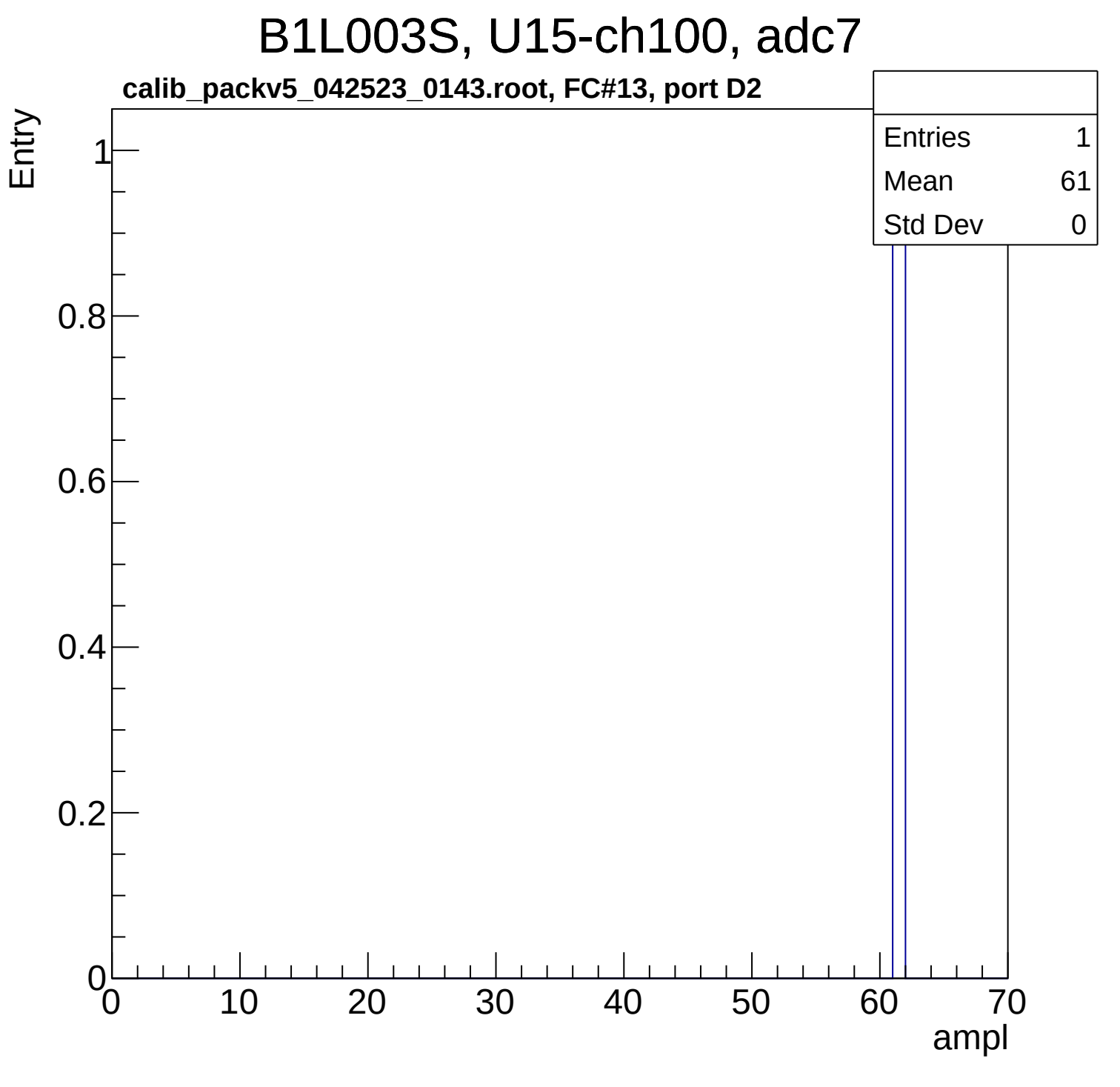
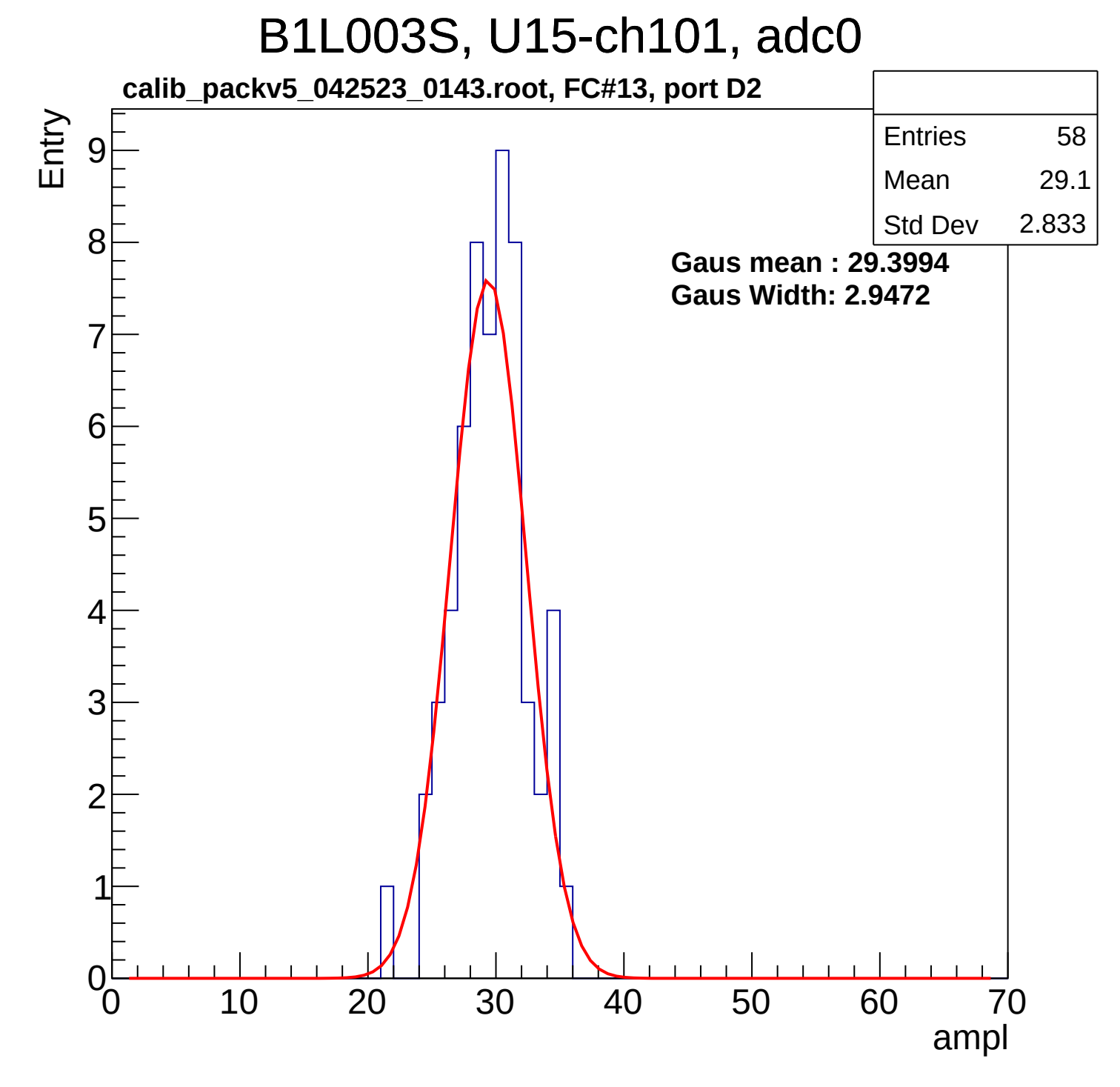
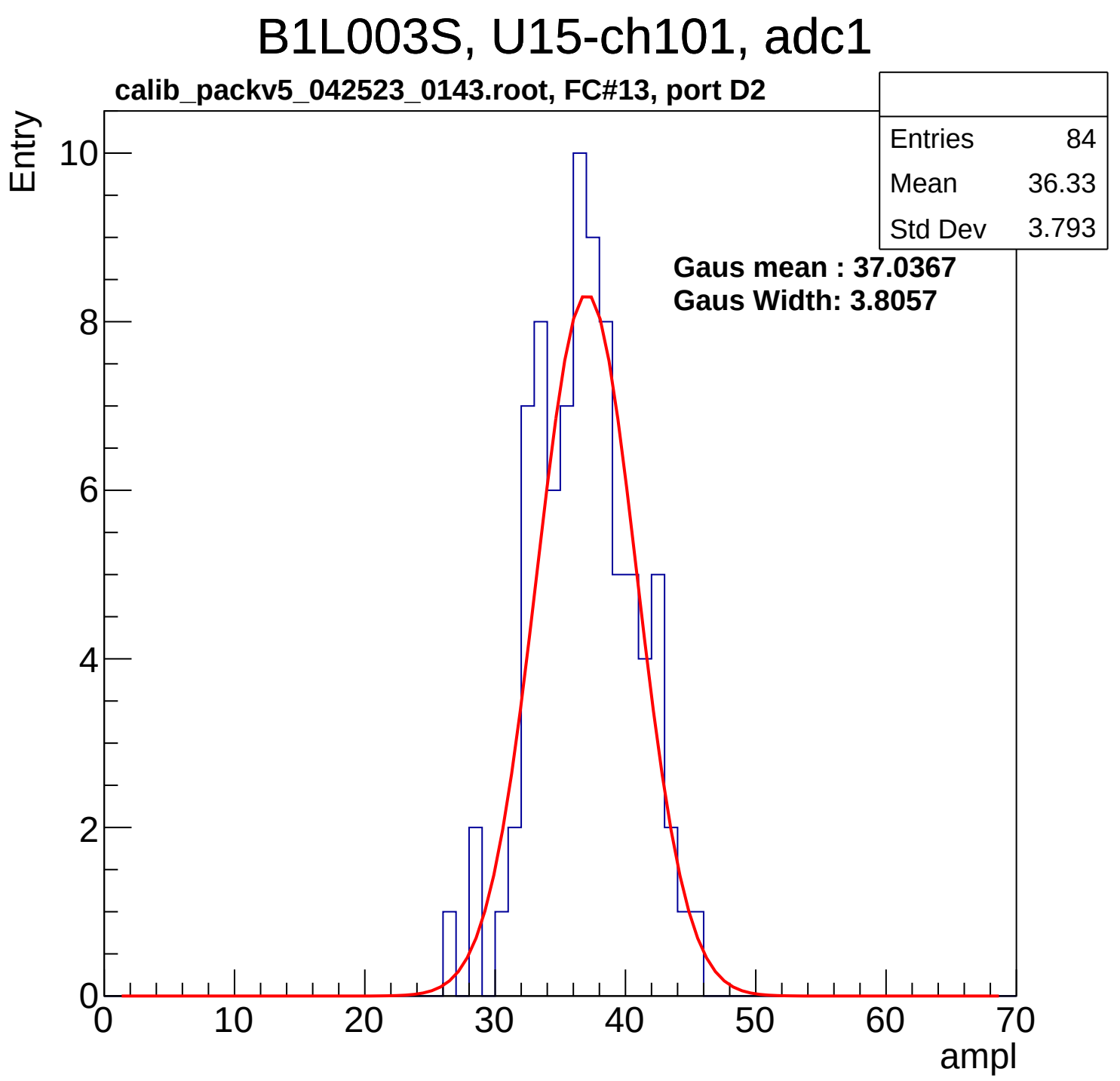
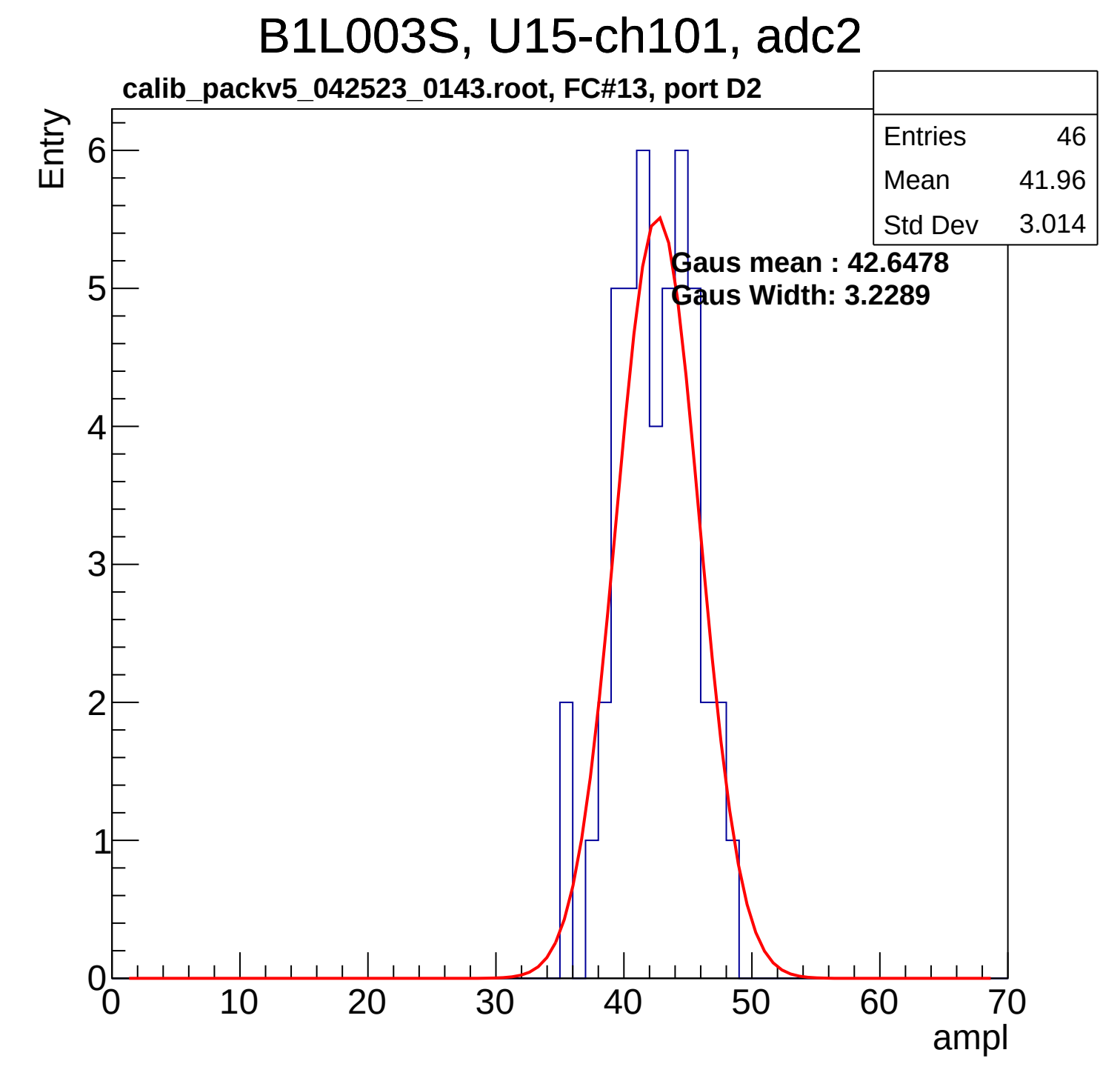

B1L003S, U15-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.84
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

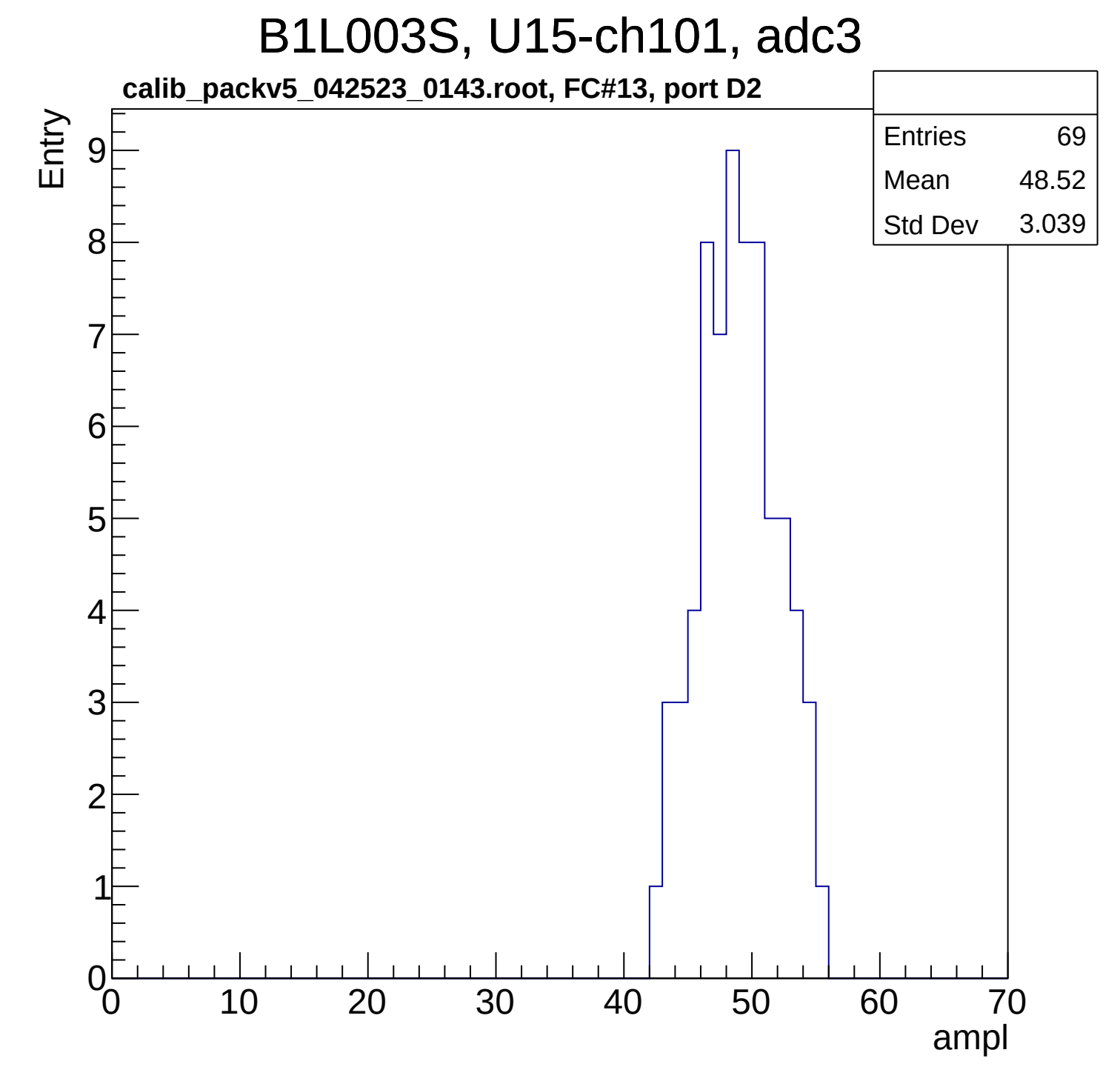

B1L003S, U15-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 57.56
Std Dev 14.06
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.1
Std Dev 2.833
Gaus mean : 29.3994
Gaus Width: 2.9472
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U15-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 36.33
Std Dev 3.793
Gaus mean : 37.0367
Gaus Width: 3.8057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 41.96
Std Dev 3.014
Gaus mean : 42.6478
Gaus Width: 3.2289
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U15-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.52
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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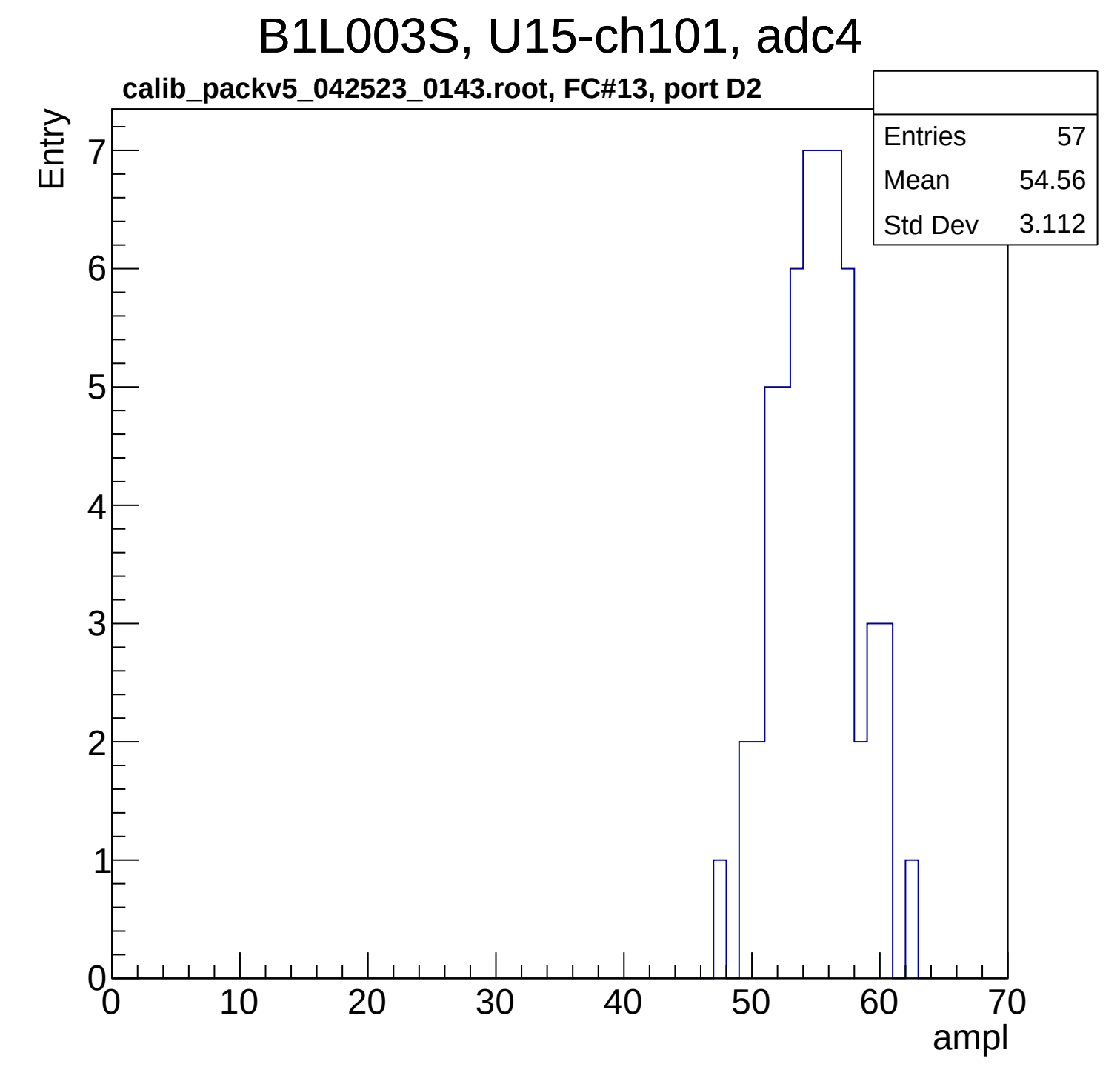
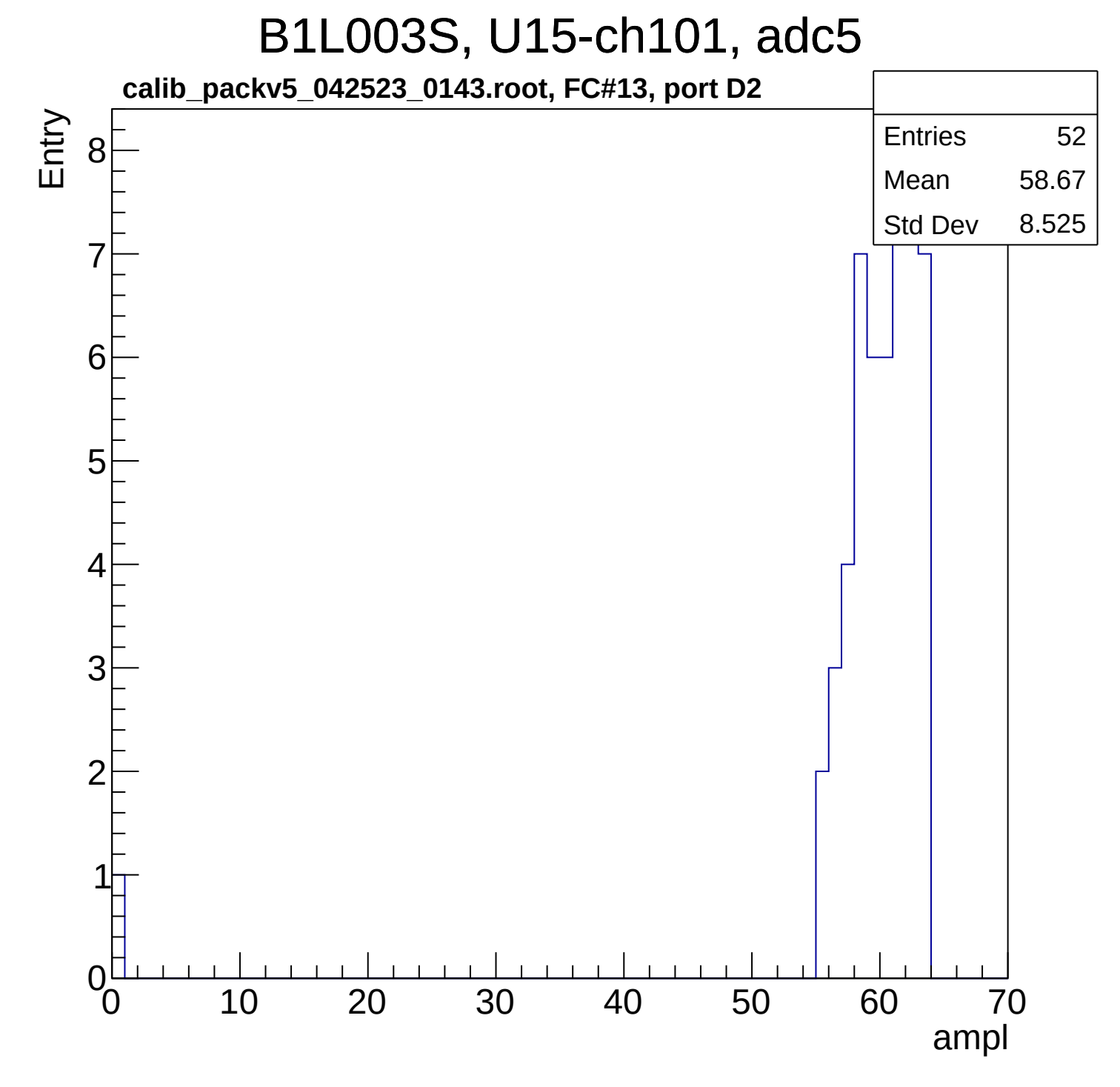
40
50
60
70


B1L003S, U15-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.56
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.67
Std Dev 8.525
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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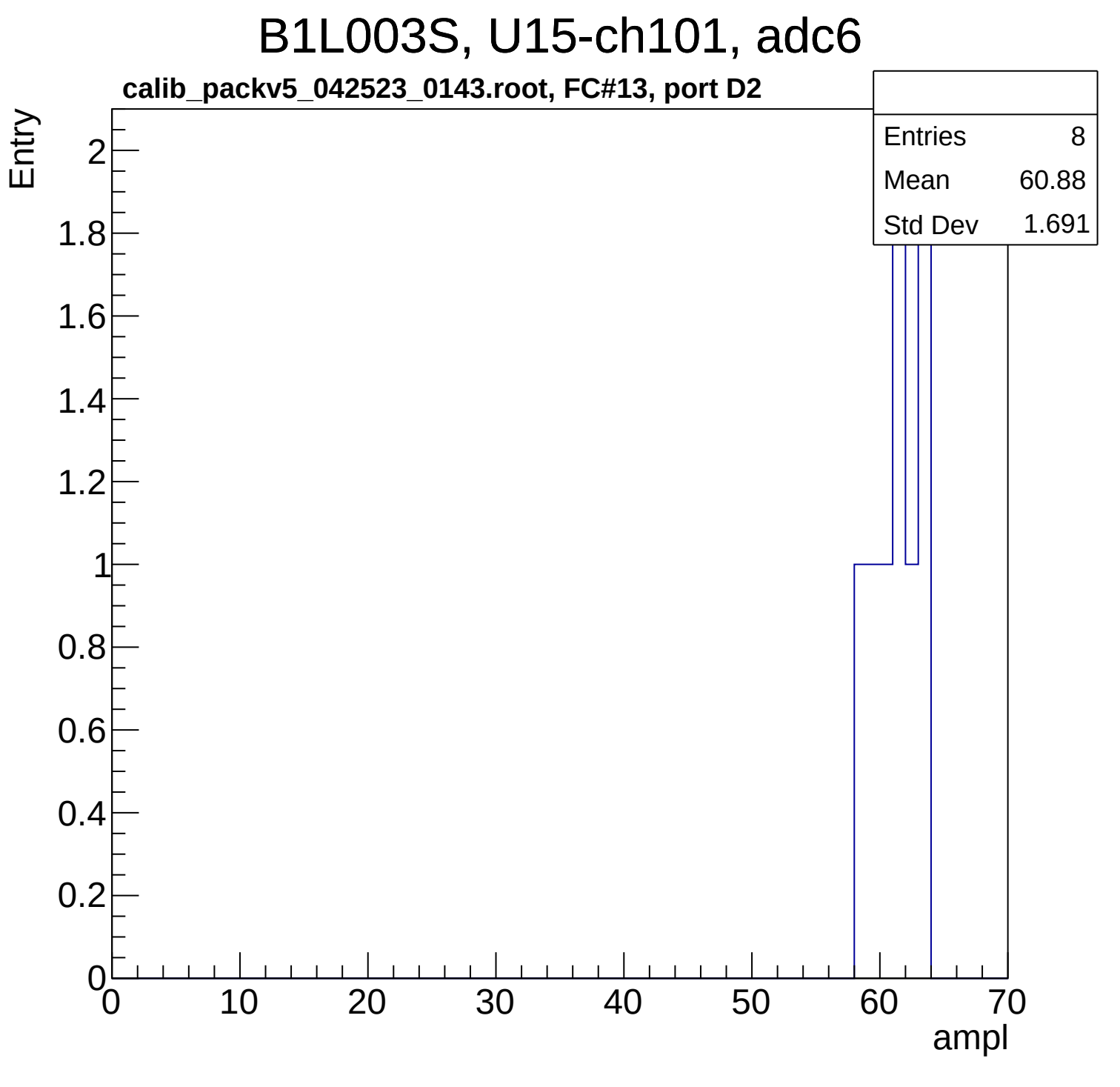
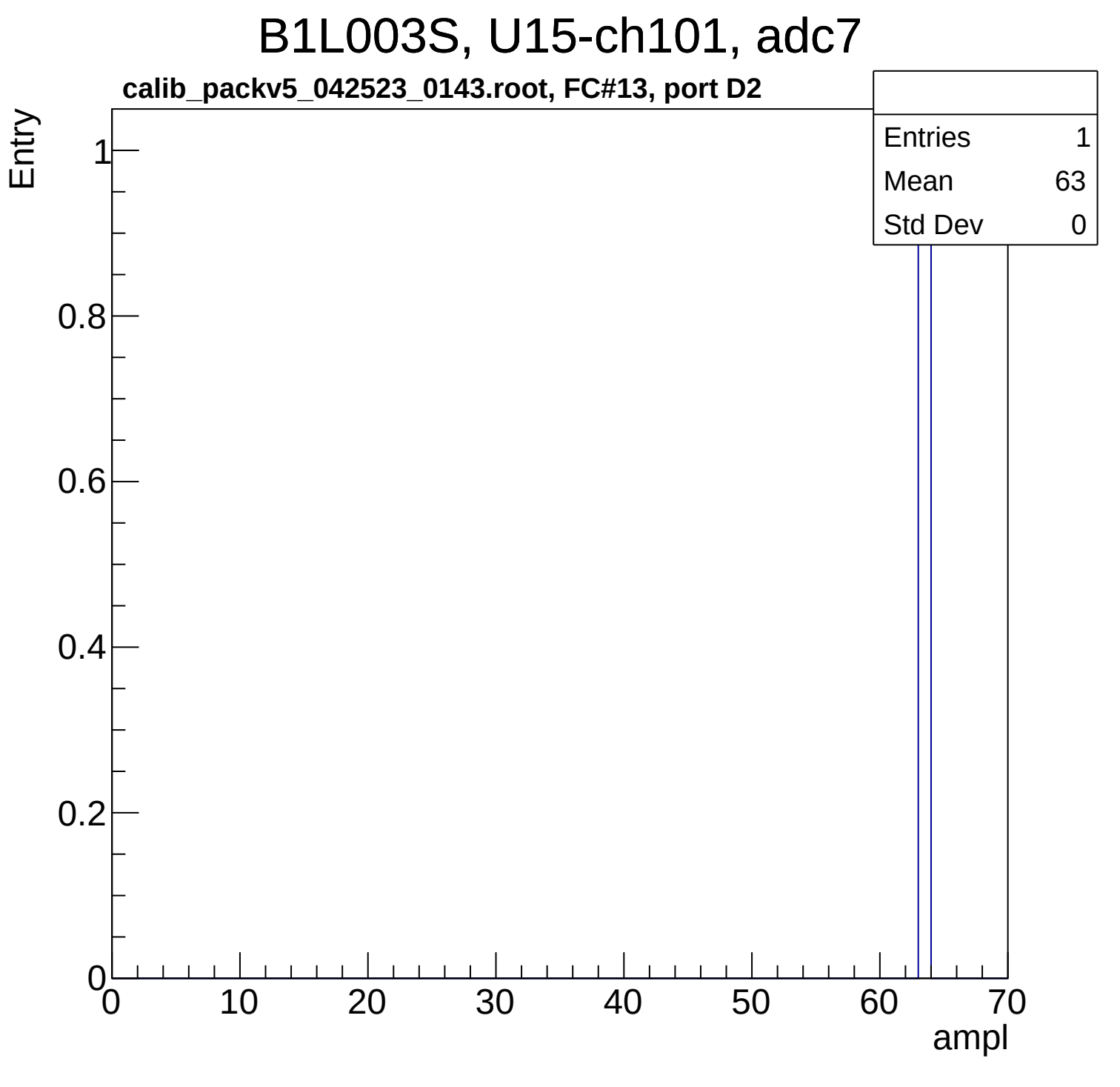
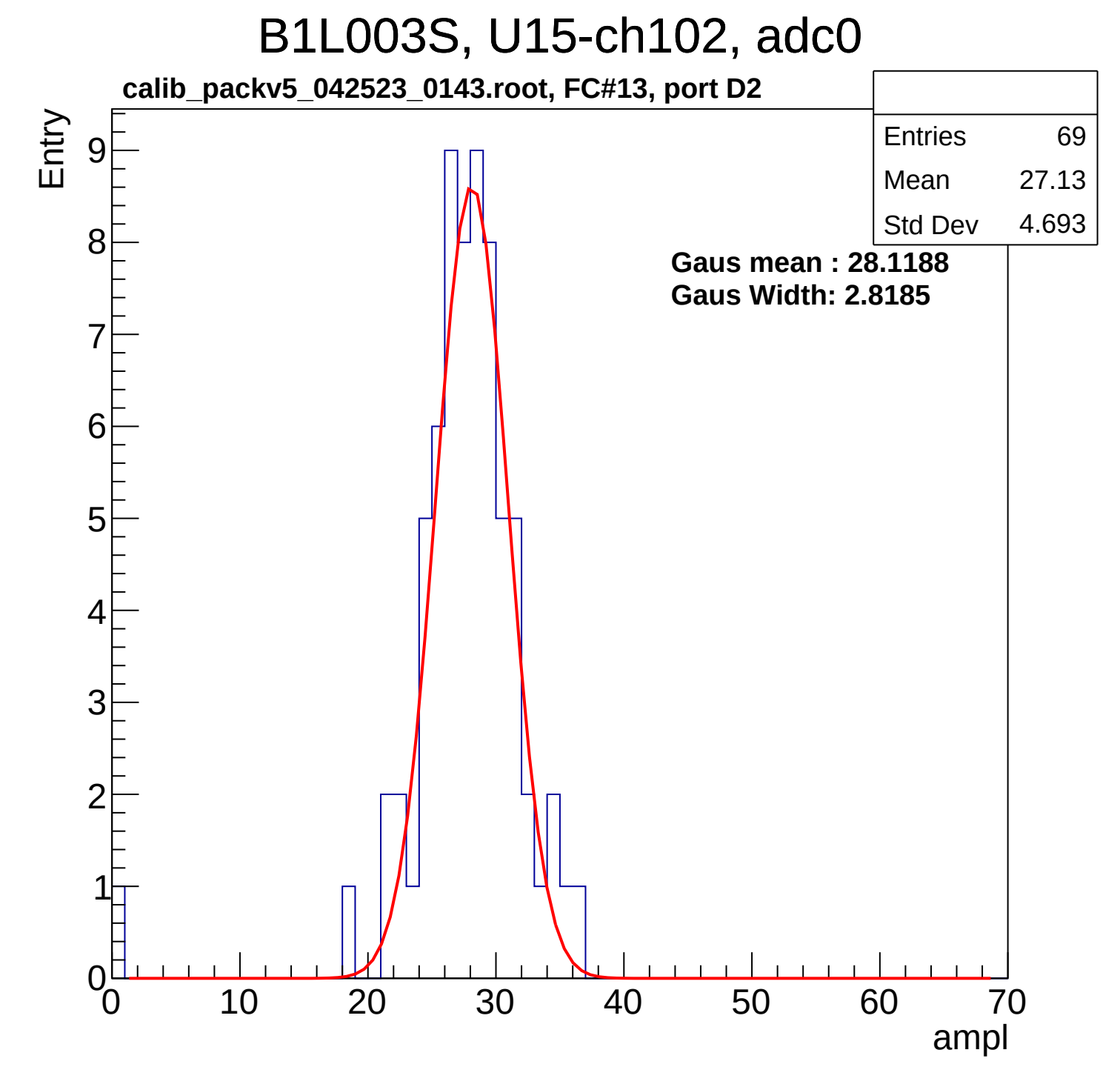
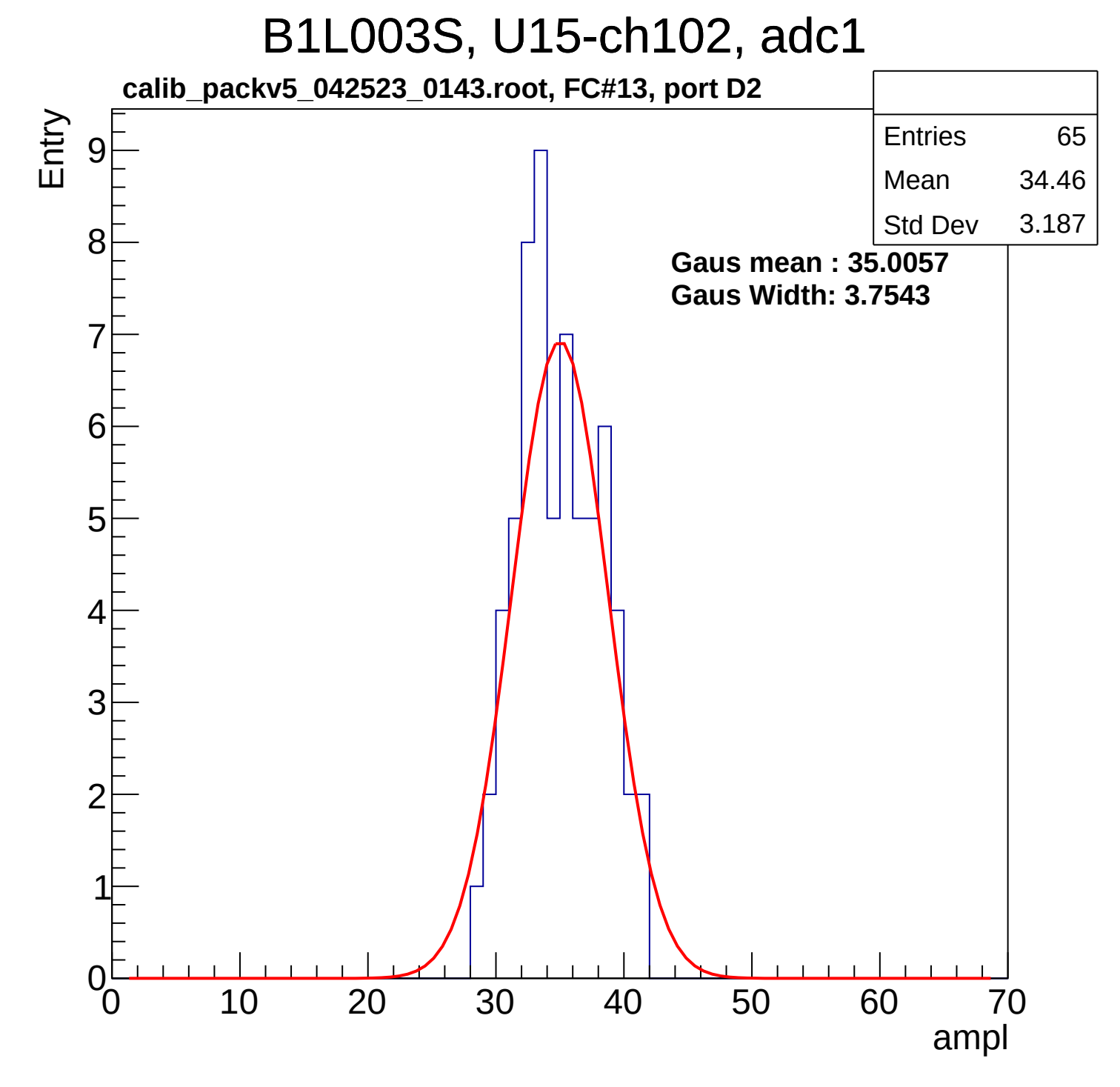
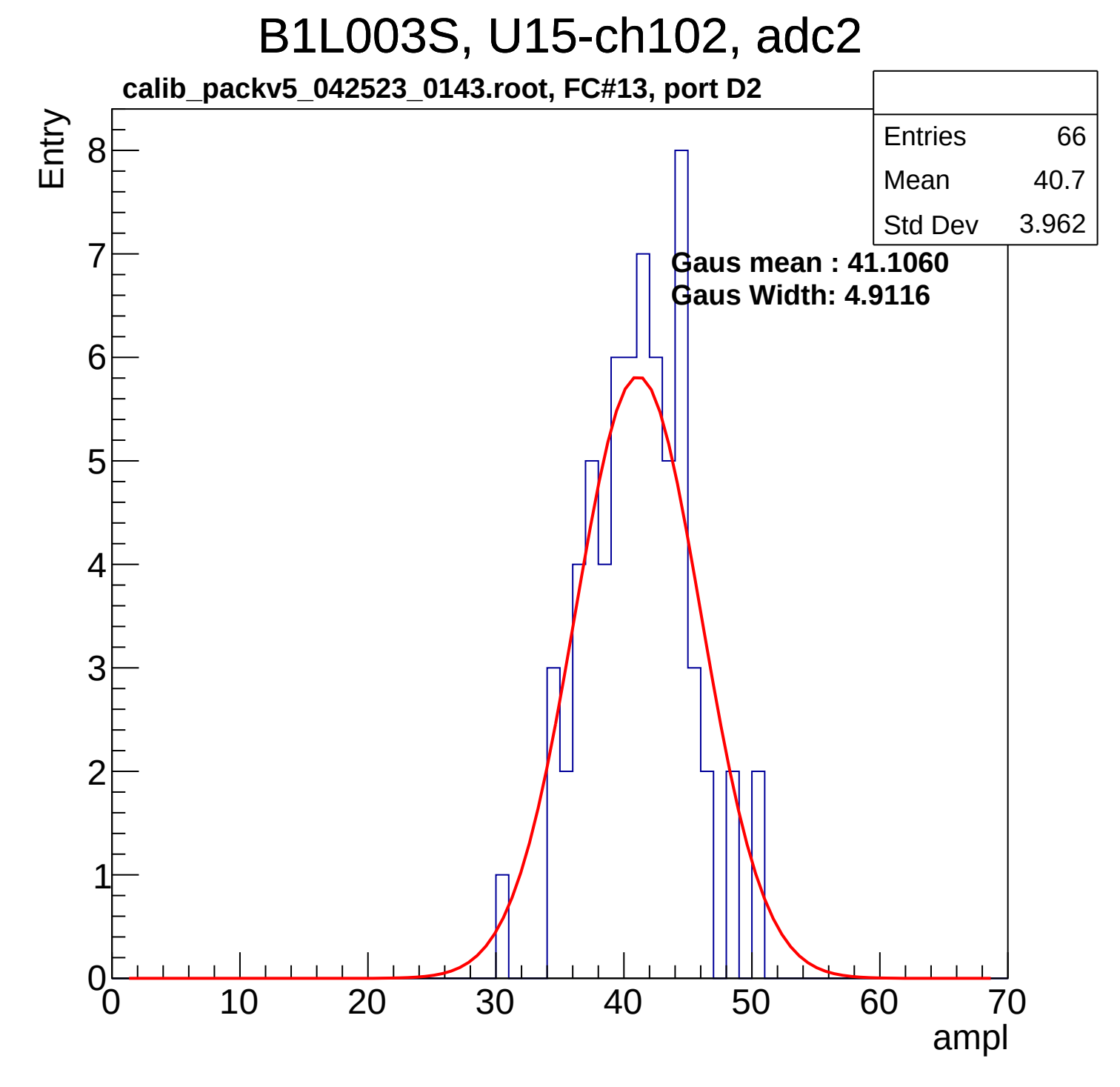
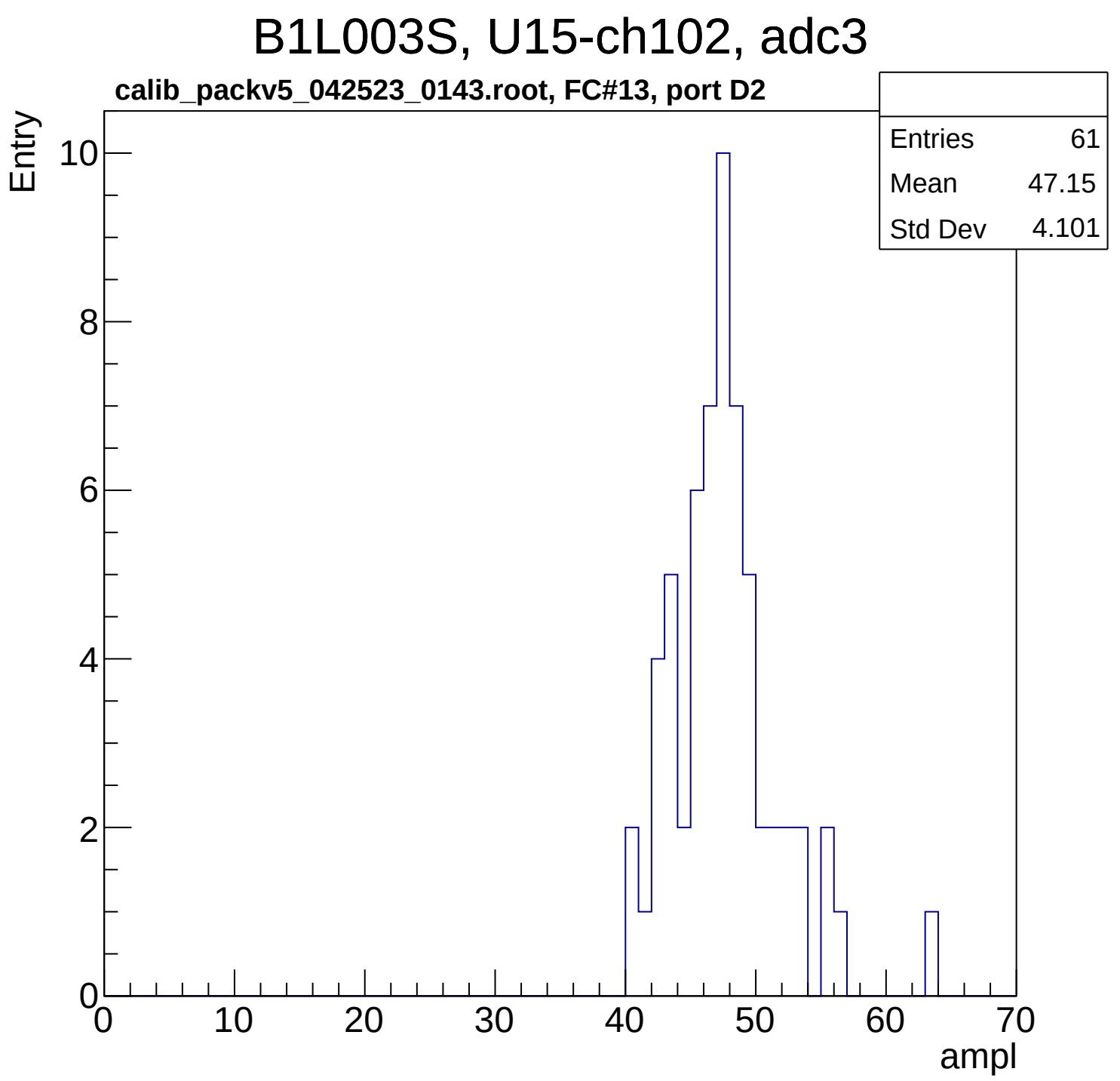

B1L003S, U15-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 60.88
Std Dev 1.691
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

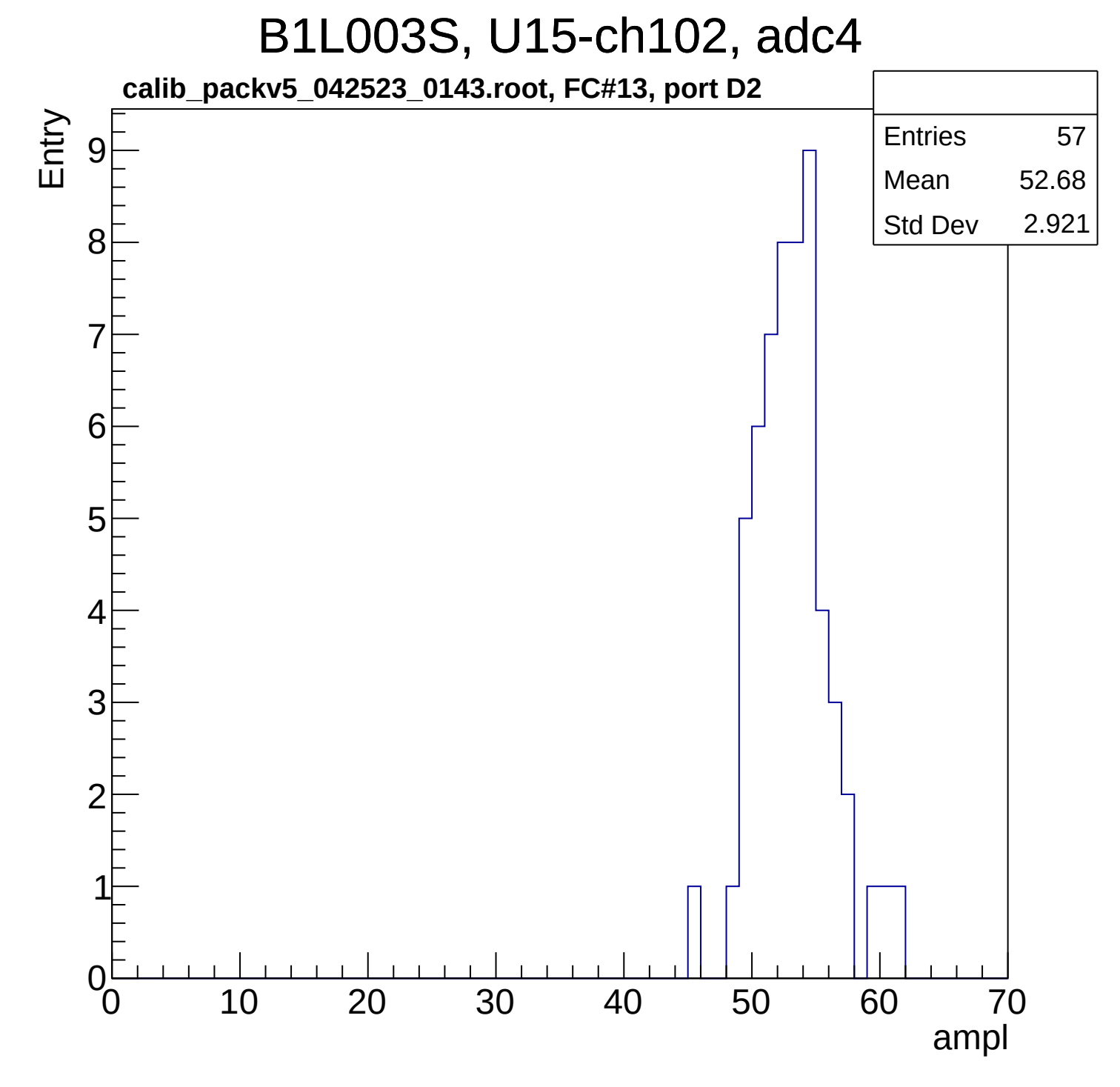

B1L003S, U15-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

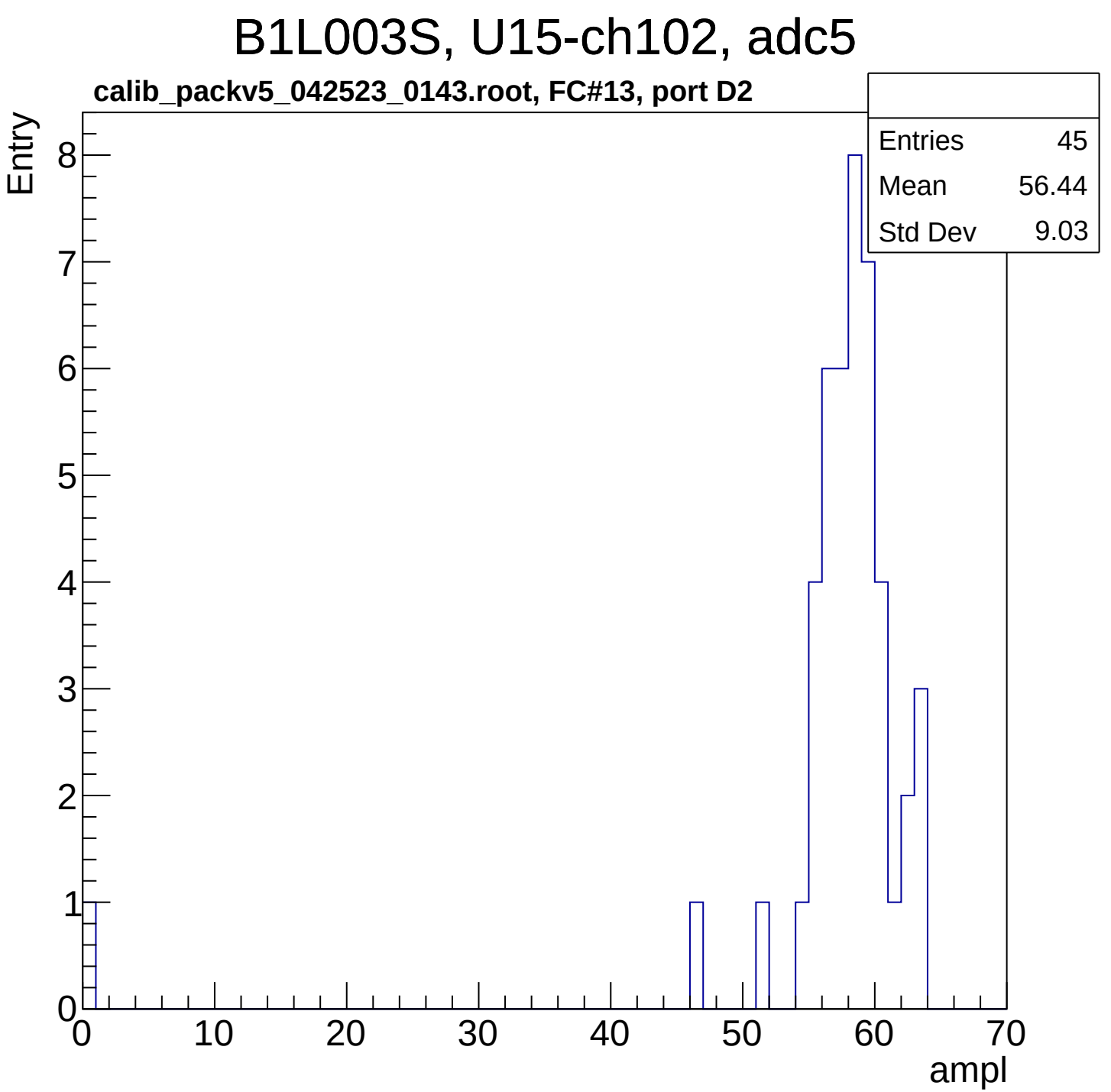

B1L003S, U15-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 27.13
Std Dev 4.693
Gaus mean : 28.1188
Gaus Width: 2.8185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

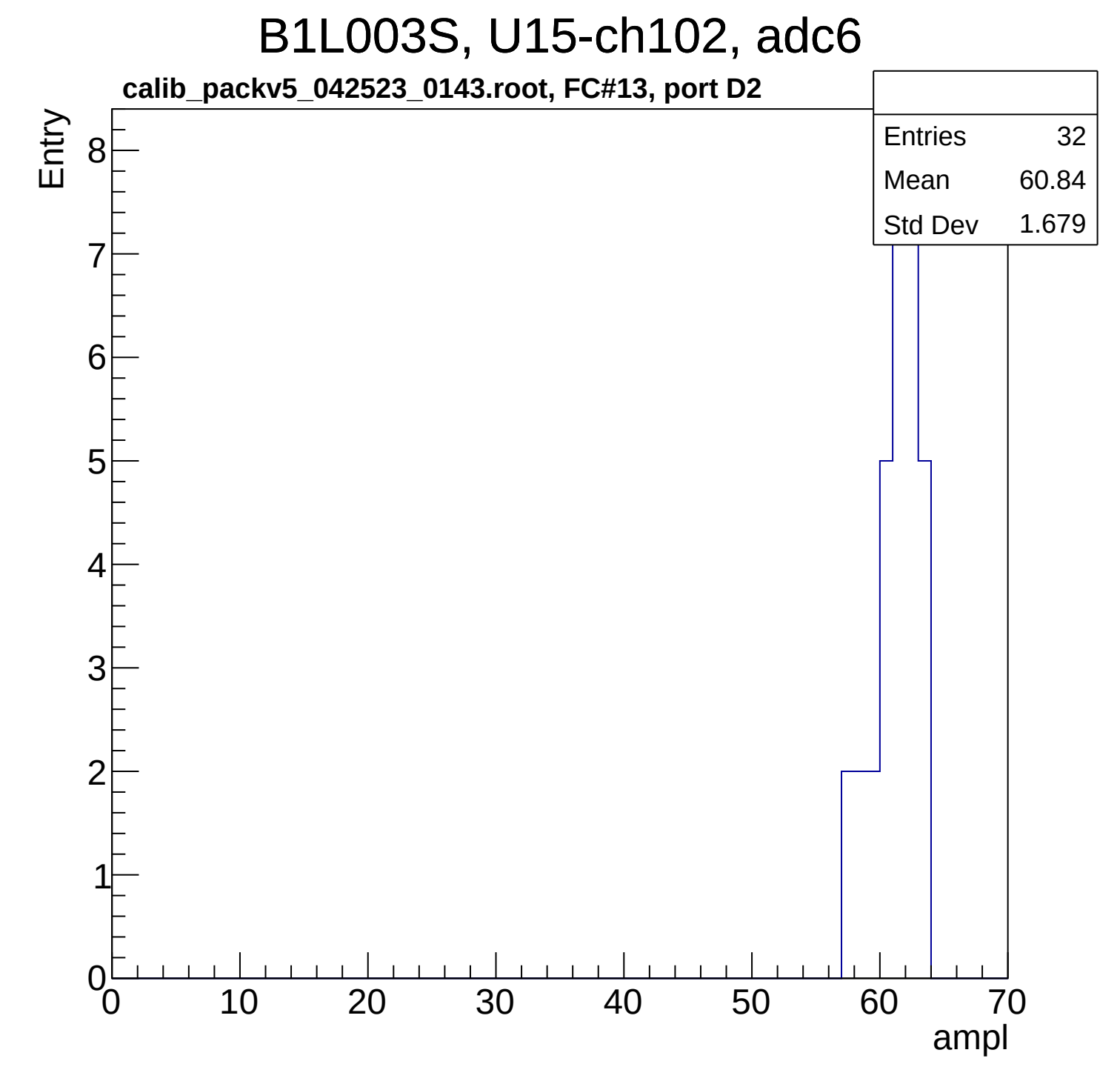

B1L003S, U15-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 34.46
Std Dev 3.187
Gaus mean : 35.0057
Gaus Width: 3.7543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

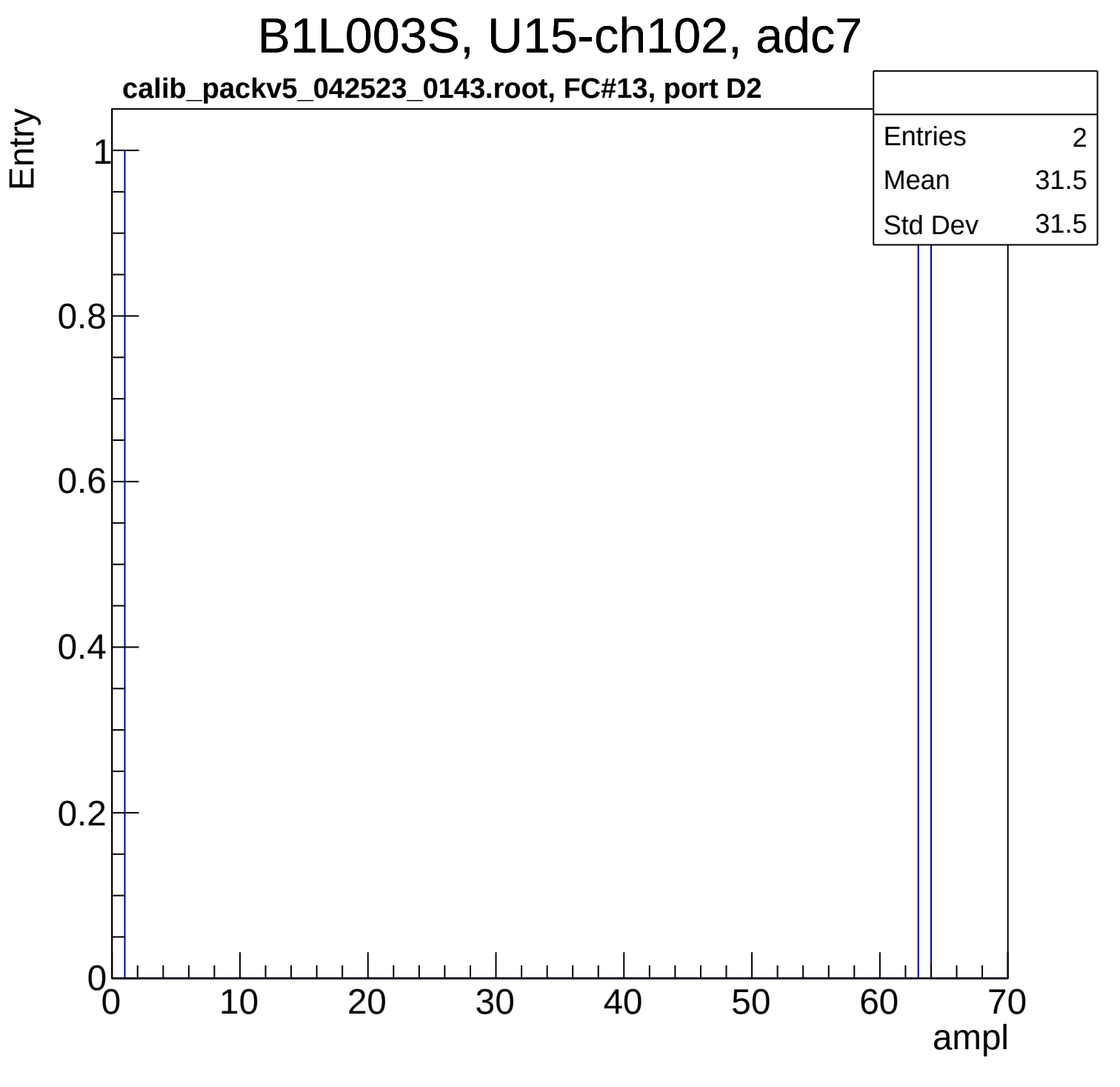

B1L003S, U15-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 40.7
Std Dev 3.962
Gaus mean : 41.1060
Gaus Width: 4.9116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

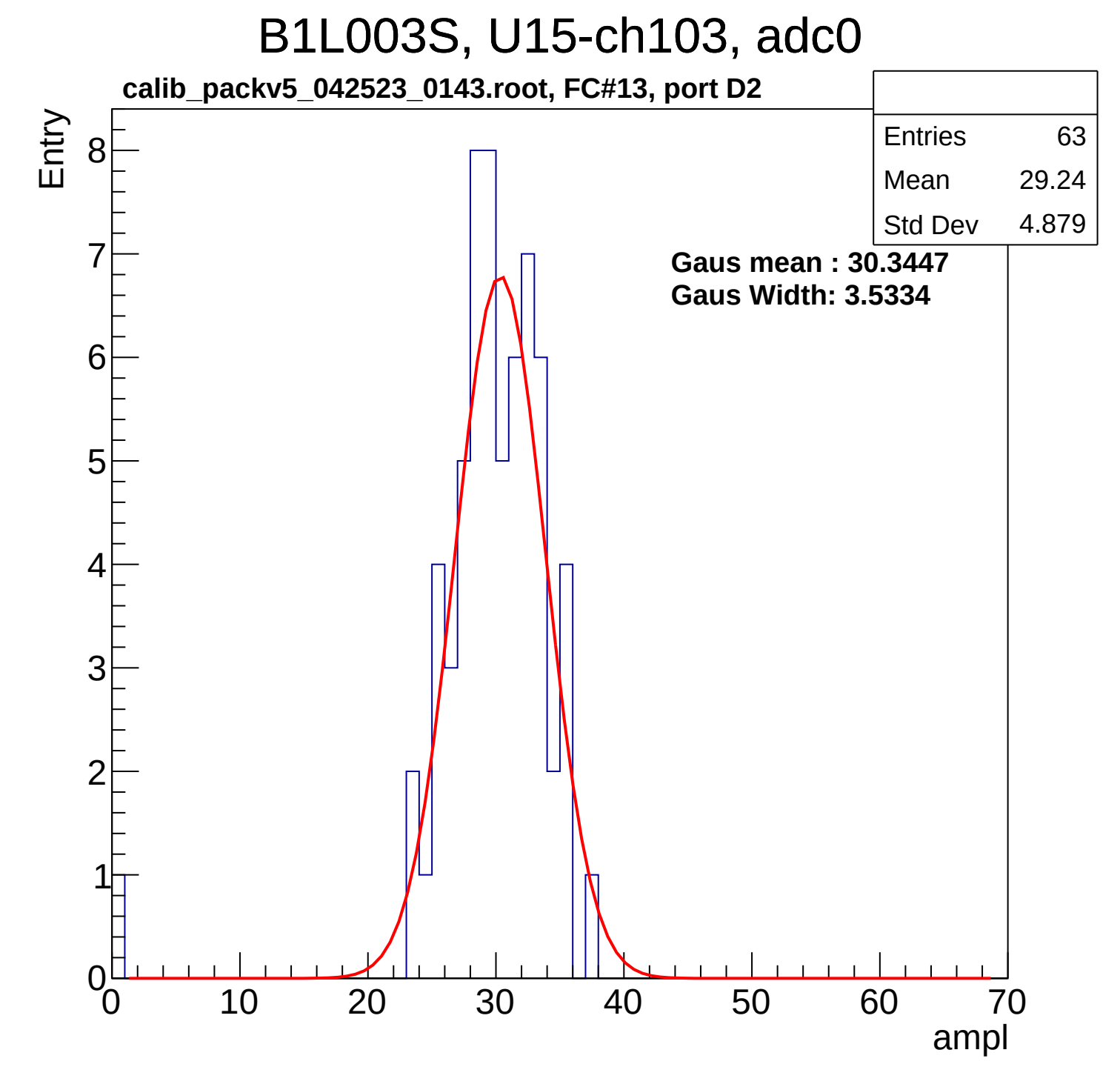

B1L003S, U15-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 47.15
Std Dev 4.101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

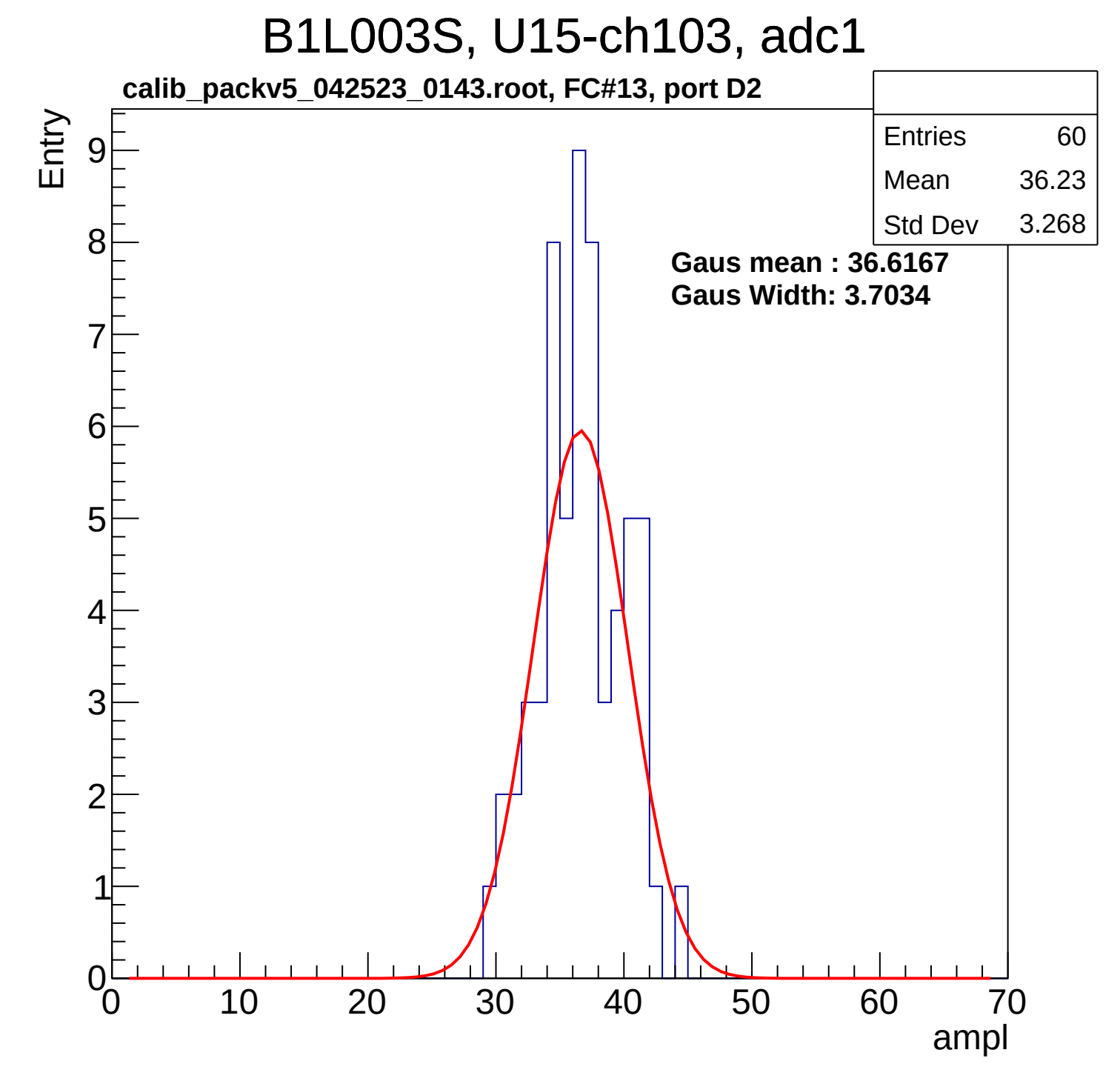

B1L003S, U15-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 52.68
Std Dev 2.921
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

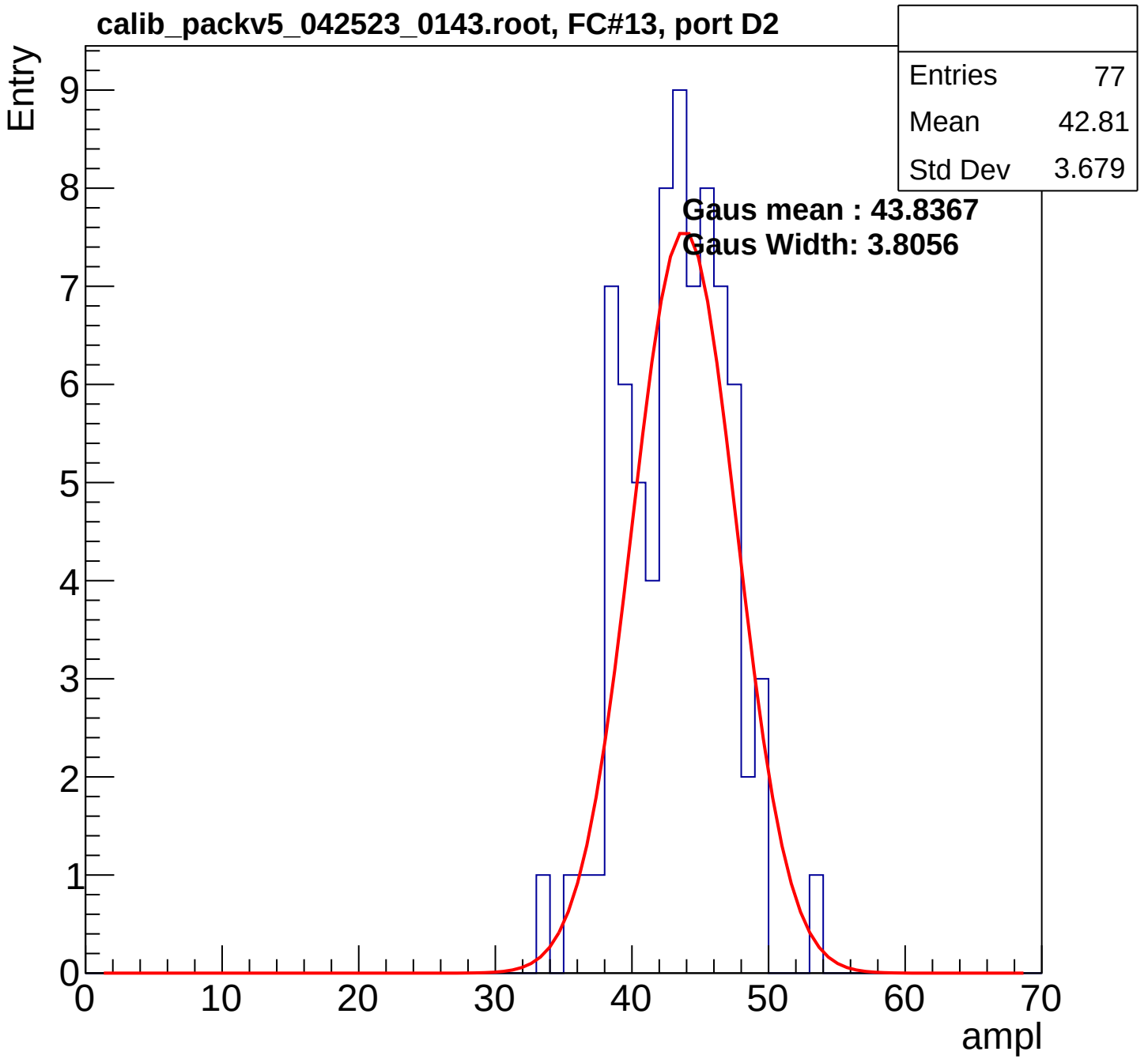

B1L003S, U15-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 56.44
Std Dev 9.03
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 60.84
Std Dev 1.679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.24
Std Dev 4.879
Gaus mean : 30.3447
Gaus Width: 3.5334
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.23
Std Dev 3.268
Gaus mean : 36.6167
Gaus Width: 3.7034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 42.81
Std Dev 3.679
Gaus mean : 43.8367
Gaus Width: 3.8056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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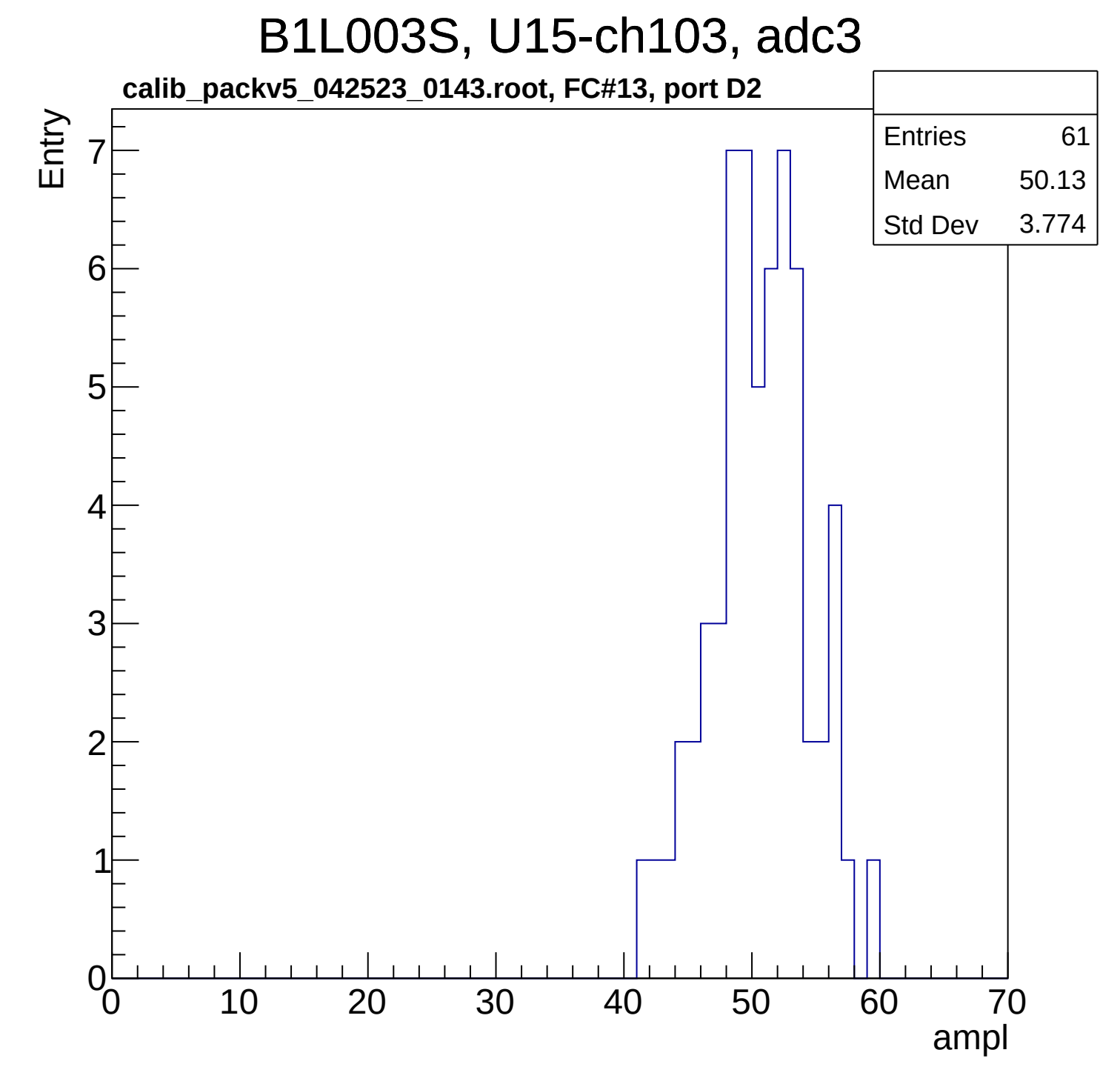
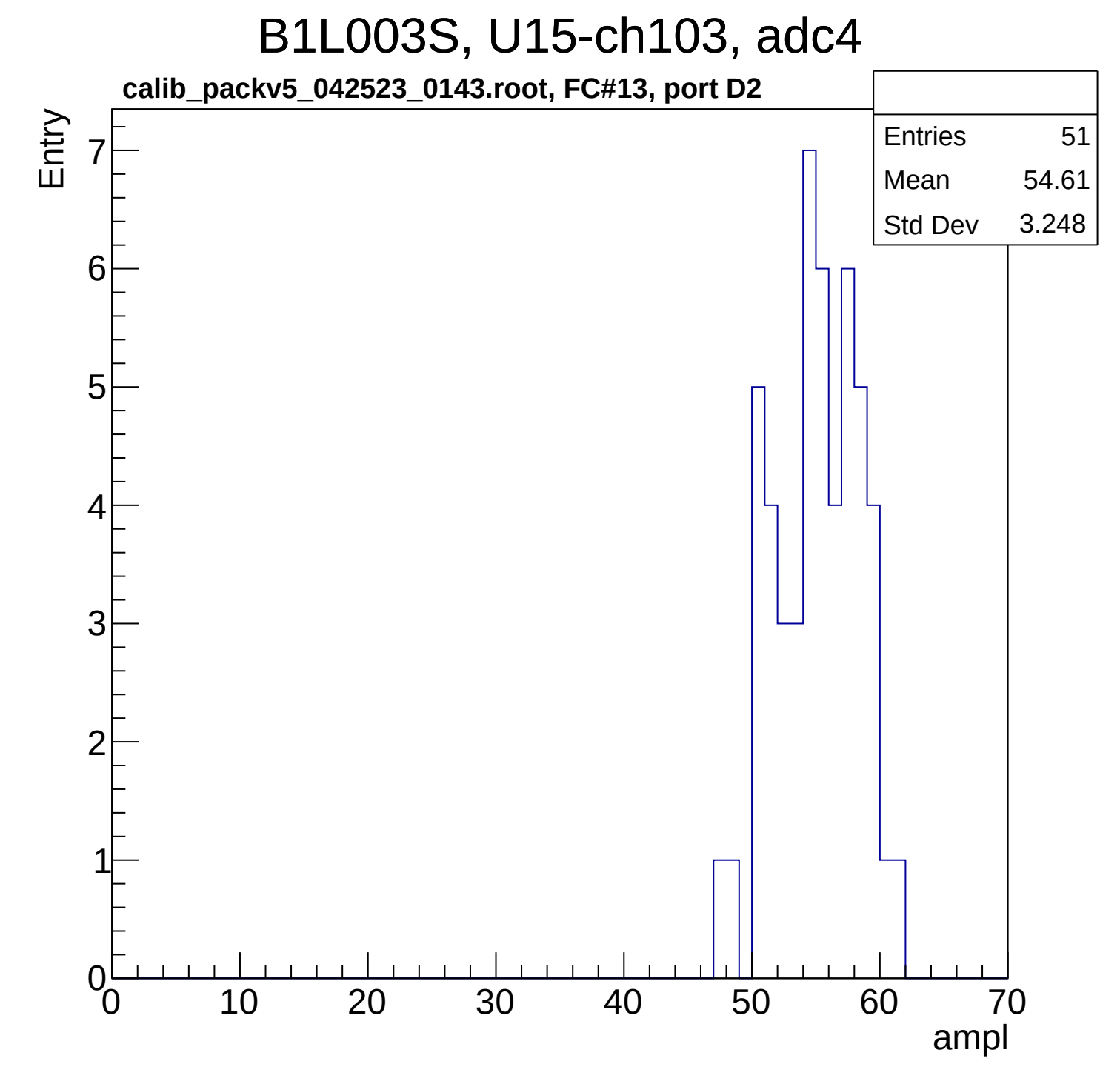
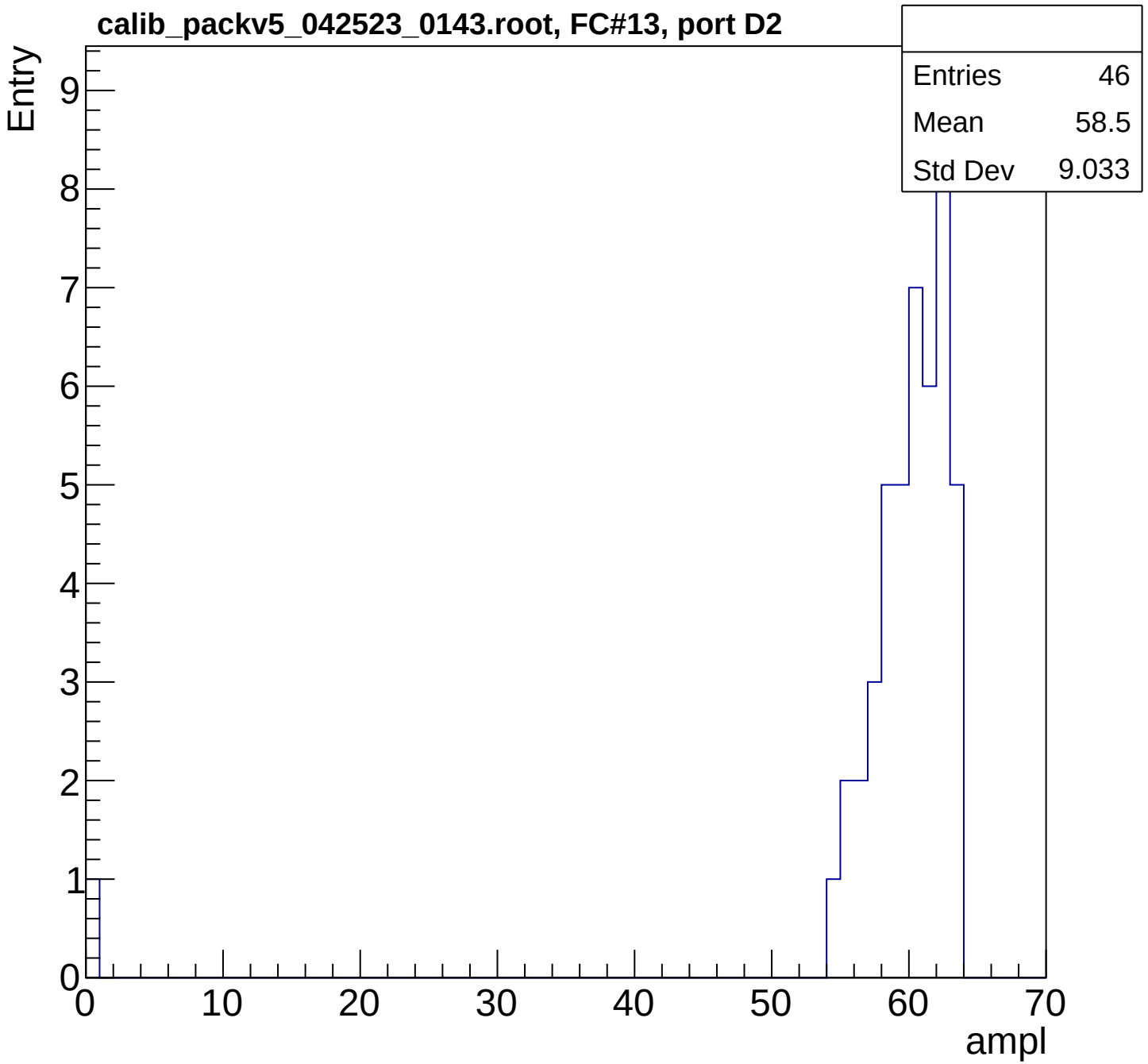
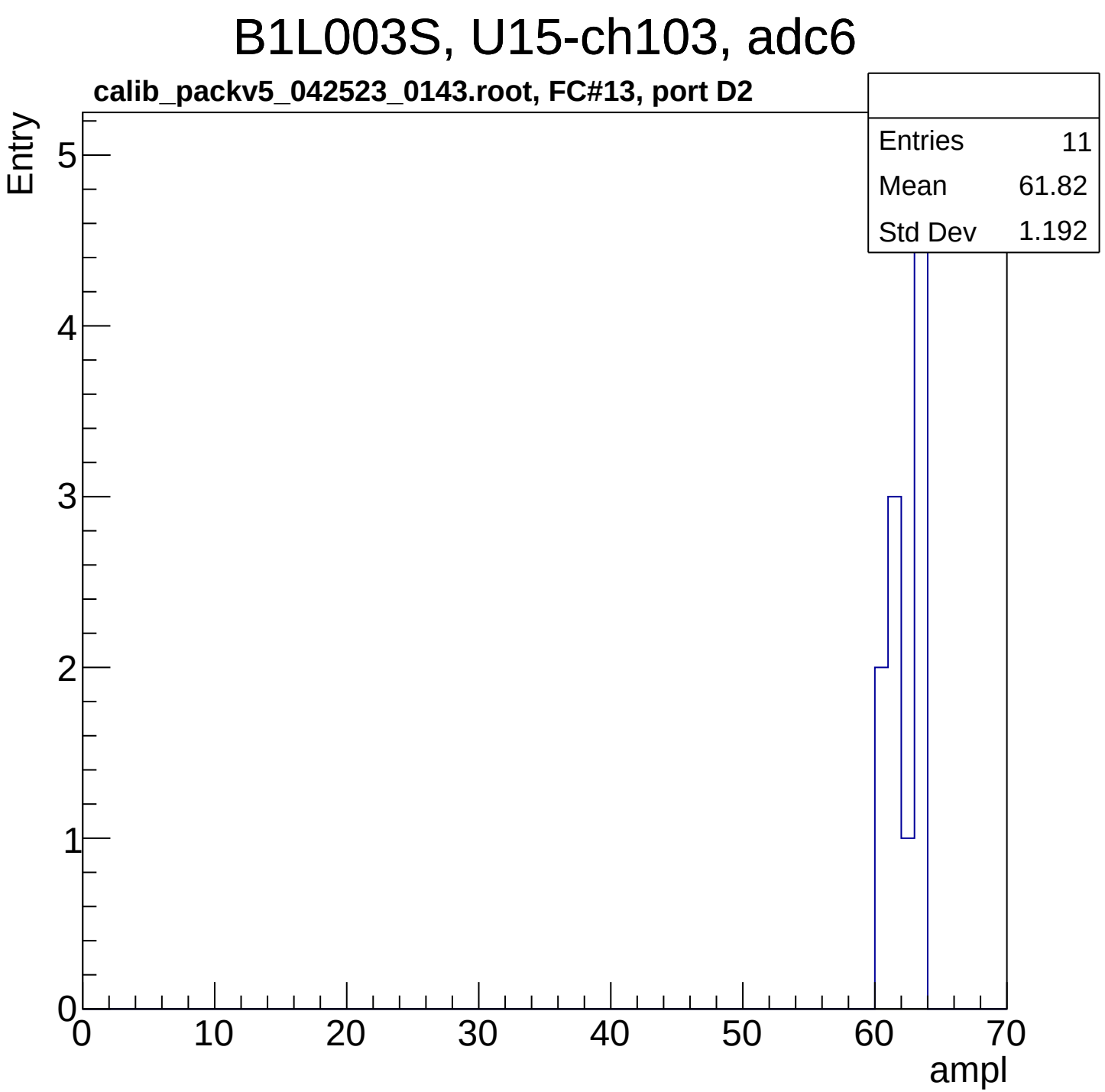
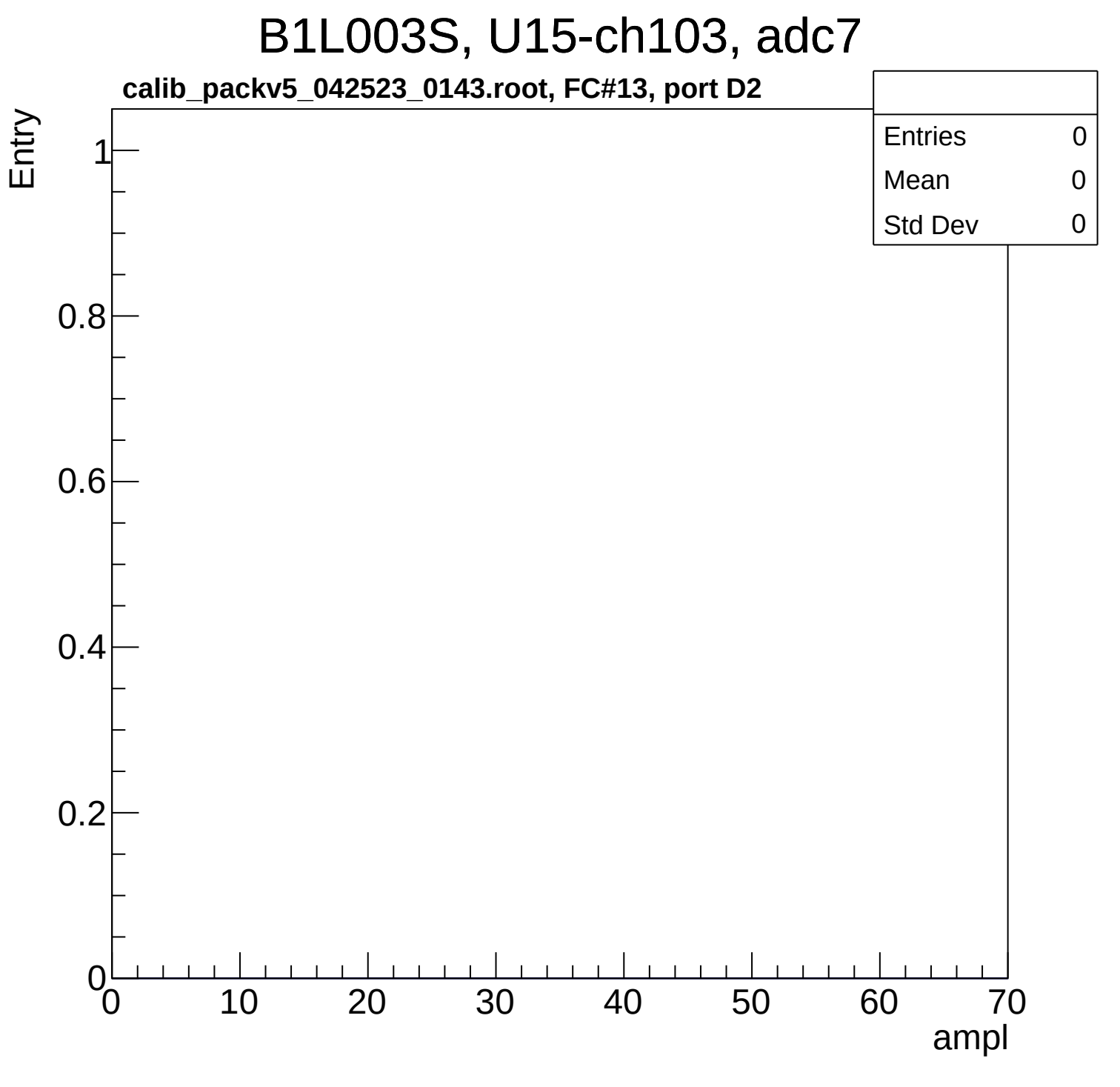
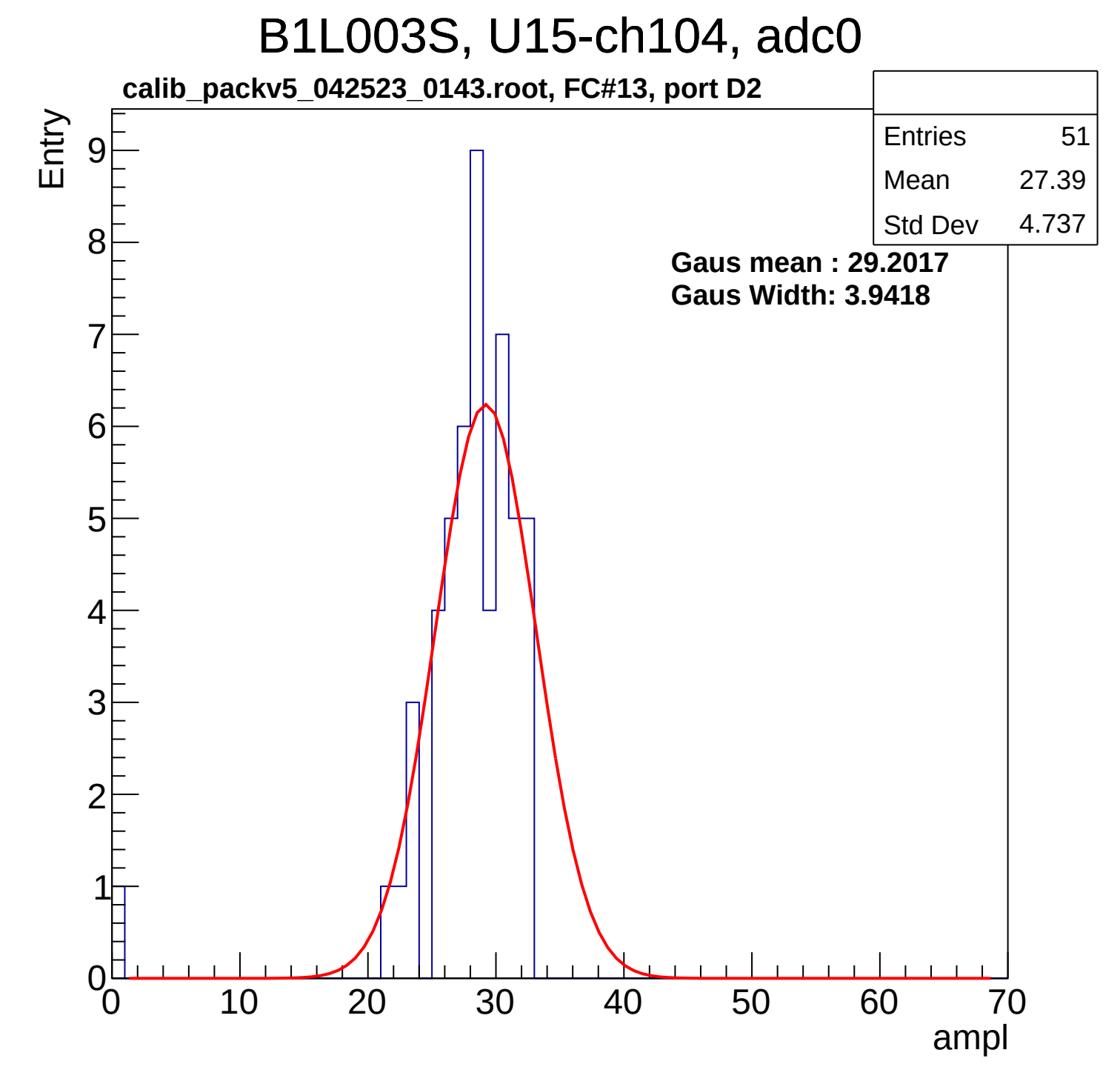

B1L003S, U15-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.13
Std Dev 3.774
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 54.61
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.5
Std Dev 9.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.82
Std Dev 1.192
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 27.39
Std Dev 4.737
Gaus mean : 29.2017
Gaus Width: 3.9418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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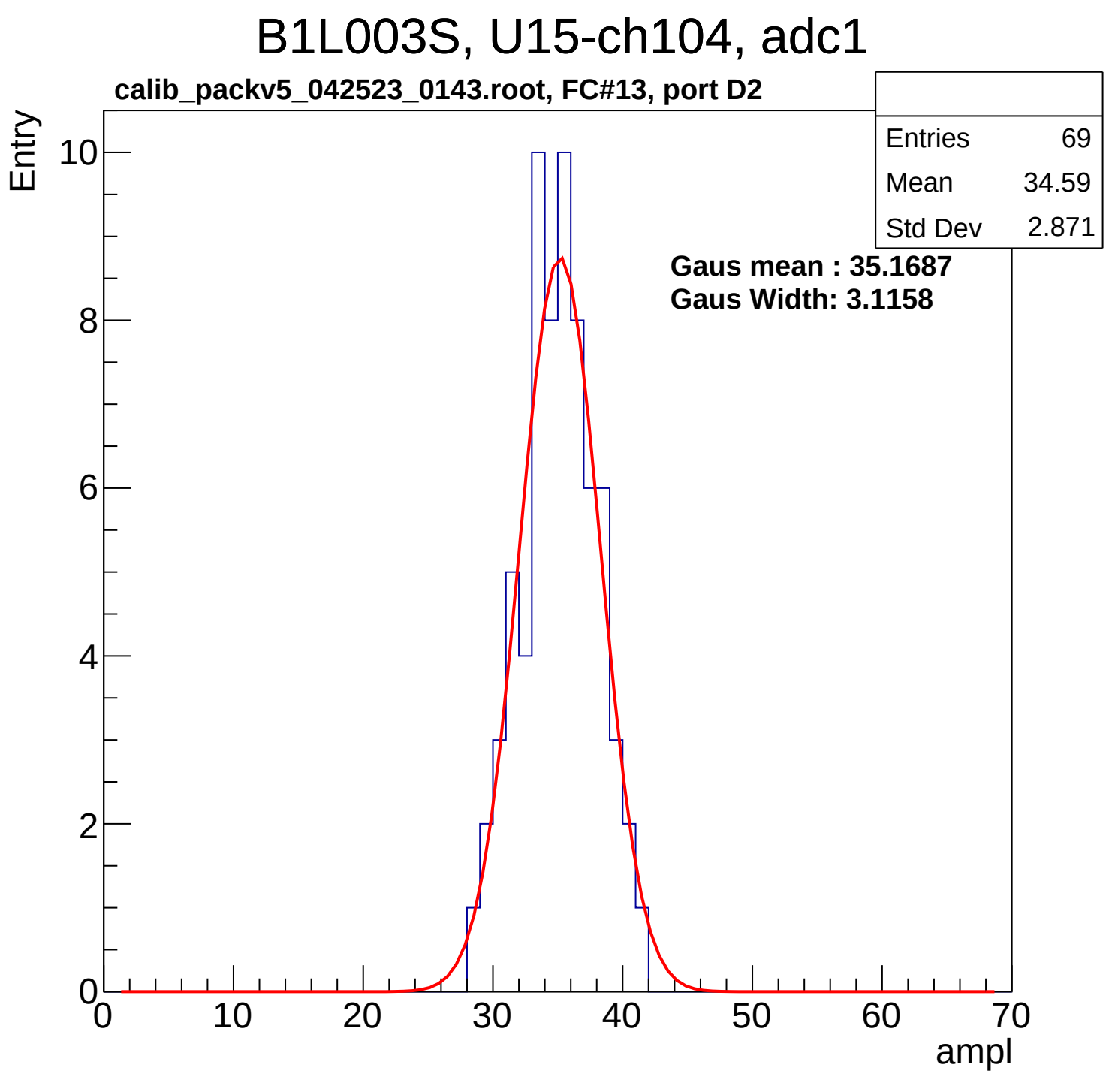
B1L003S, U15-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 34.59
Std Dev 2.871
Gaus mean : 35.1687
Gaus Width: 3.1158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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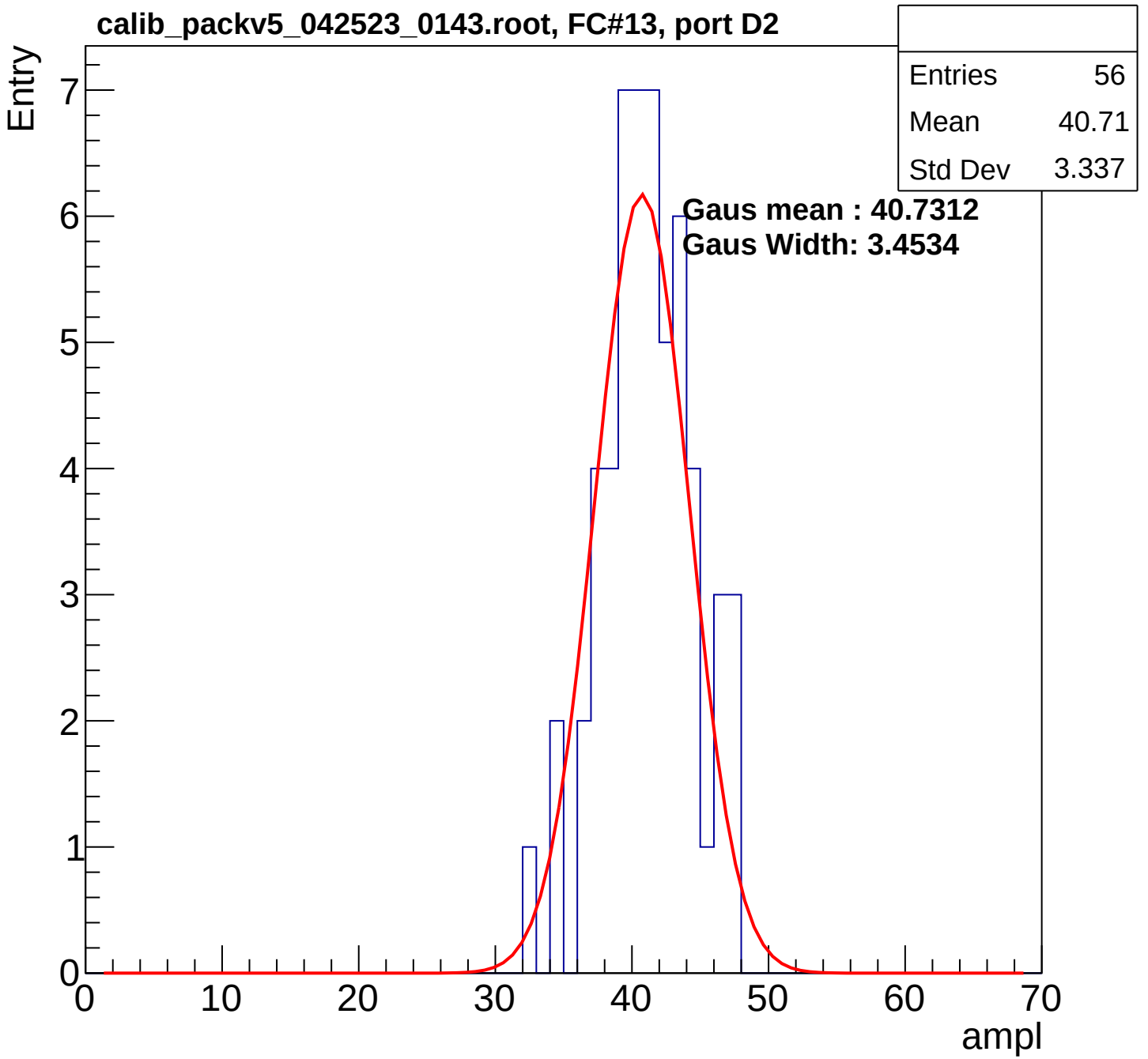
B1L003S, U15-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 40.71
Std Dev 3.337
Gaus mean : 40.7312
Gaus Width: 3.4534
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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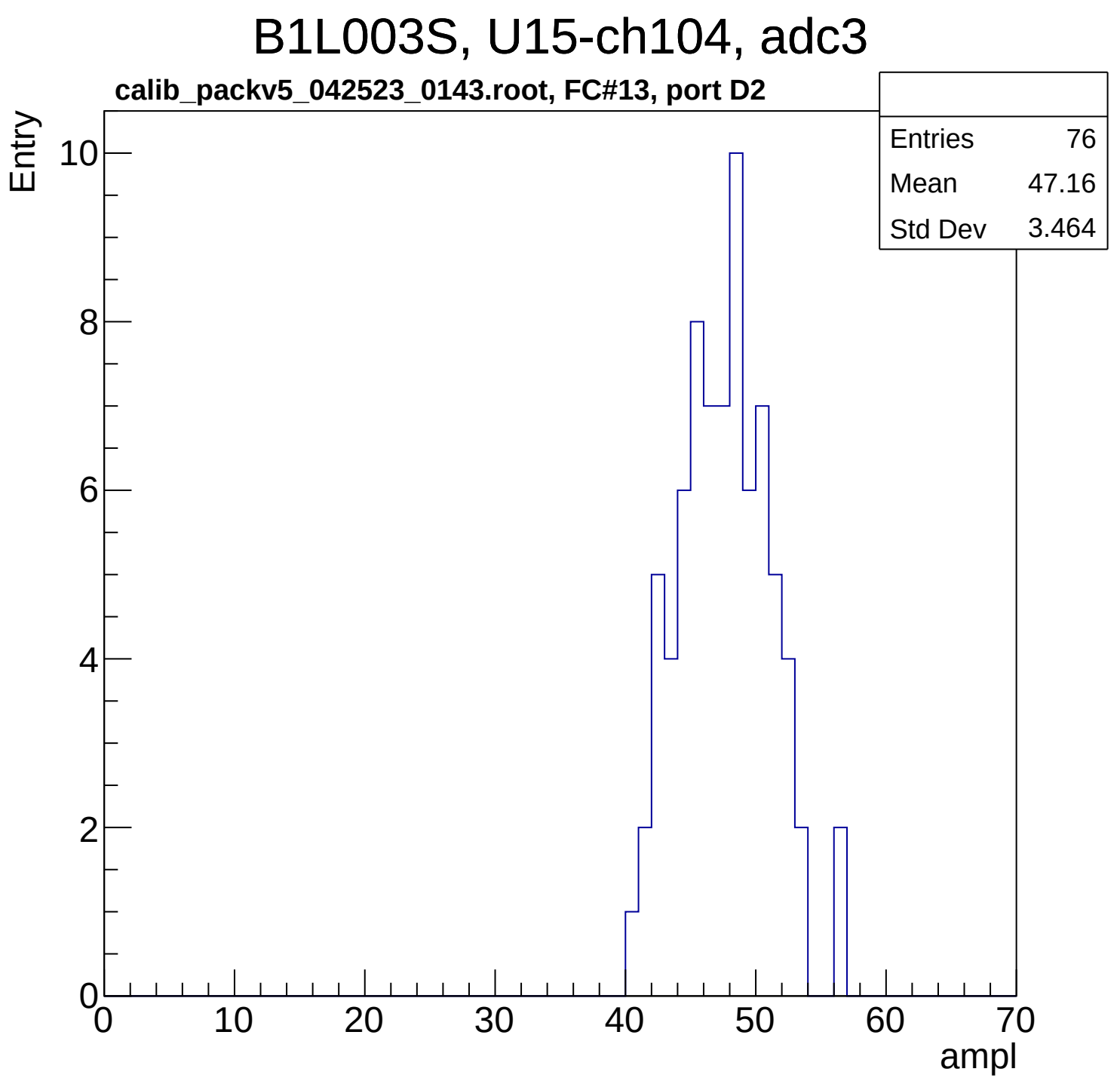
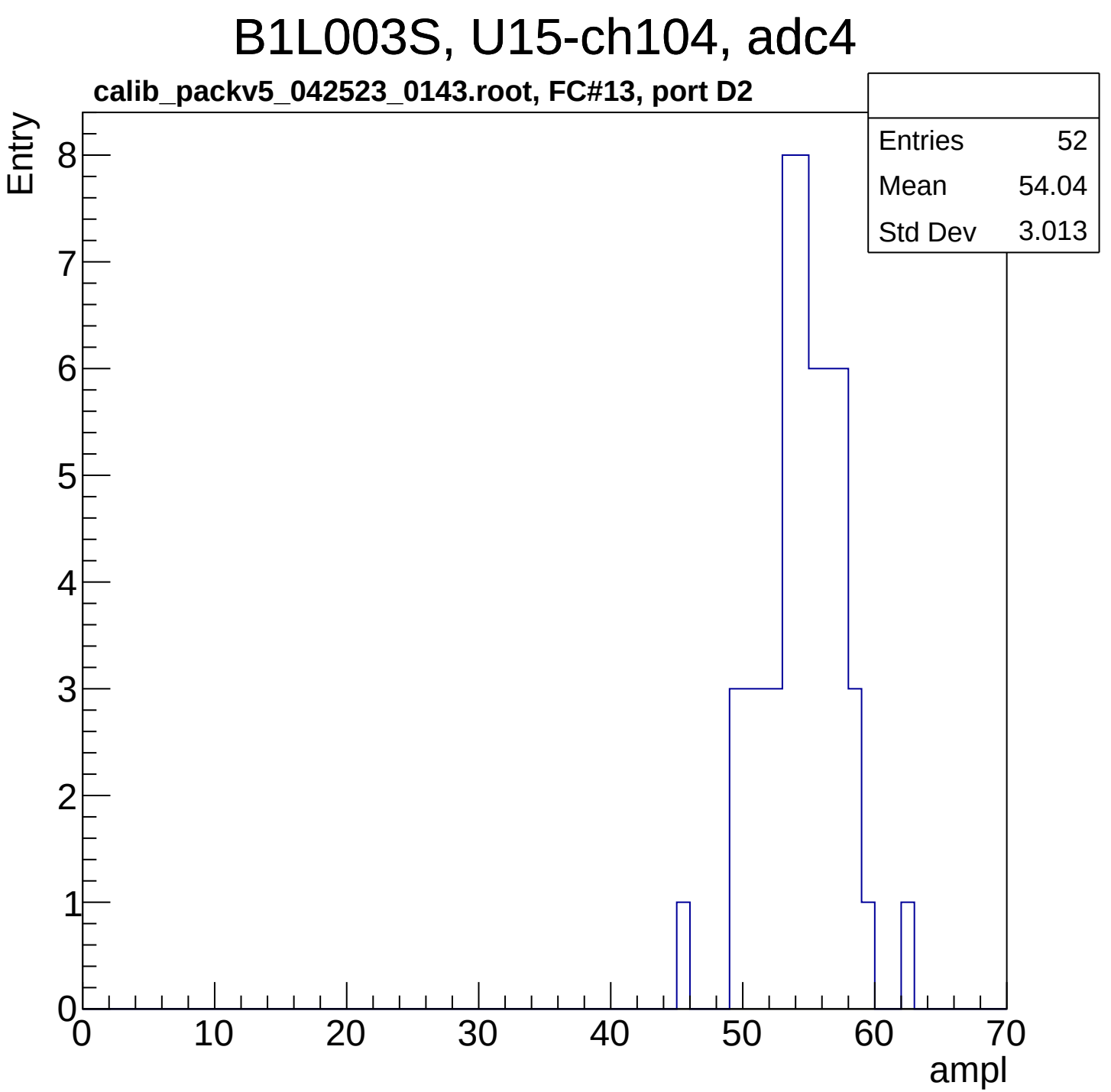

B1L003S, U15-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 47.16
Std Dev 3.464
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 54.04
Std Dev 3.013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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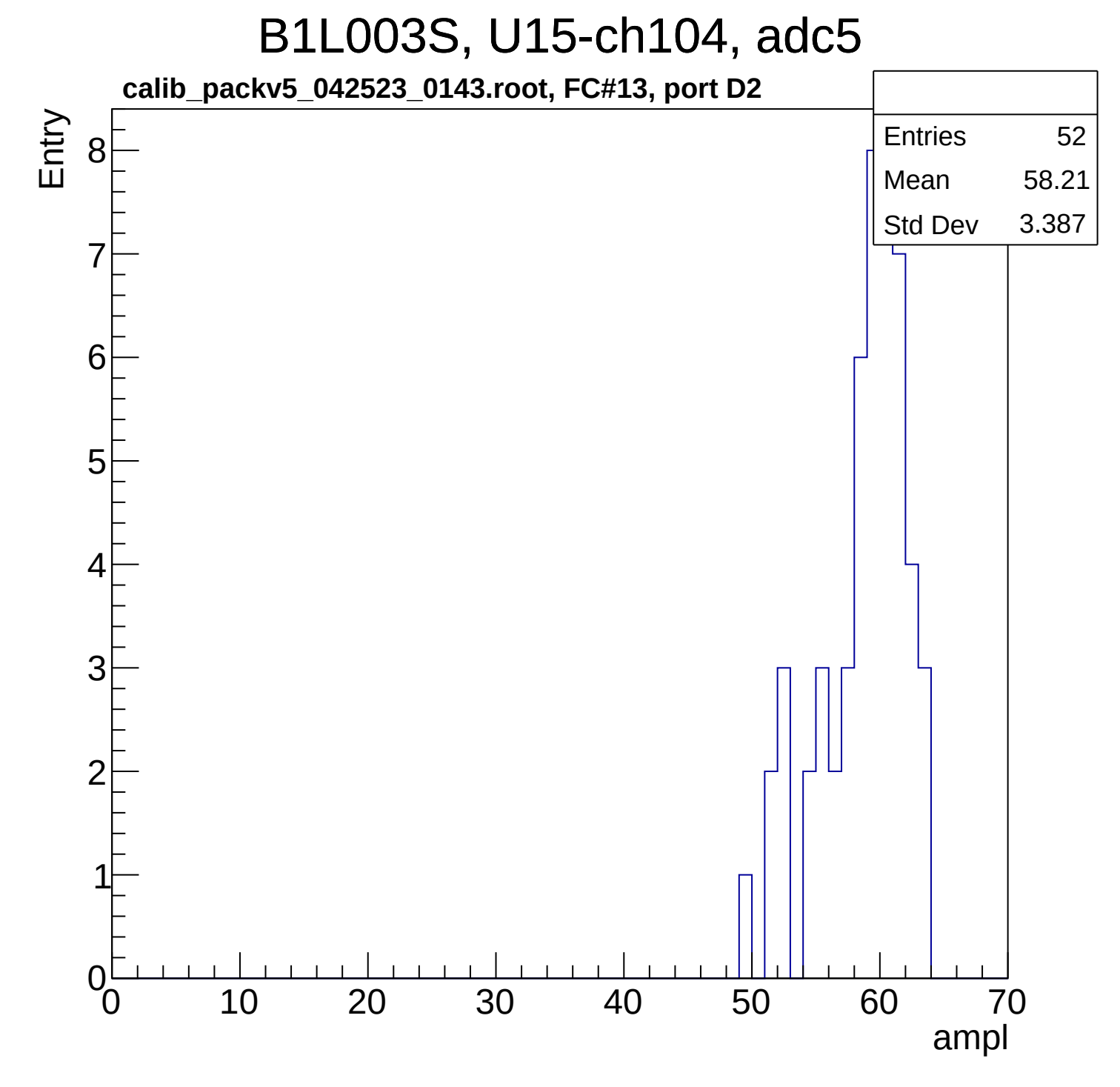
40
50
60
70


B1L003S, U15-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.21
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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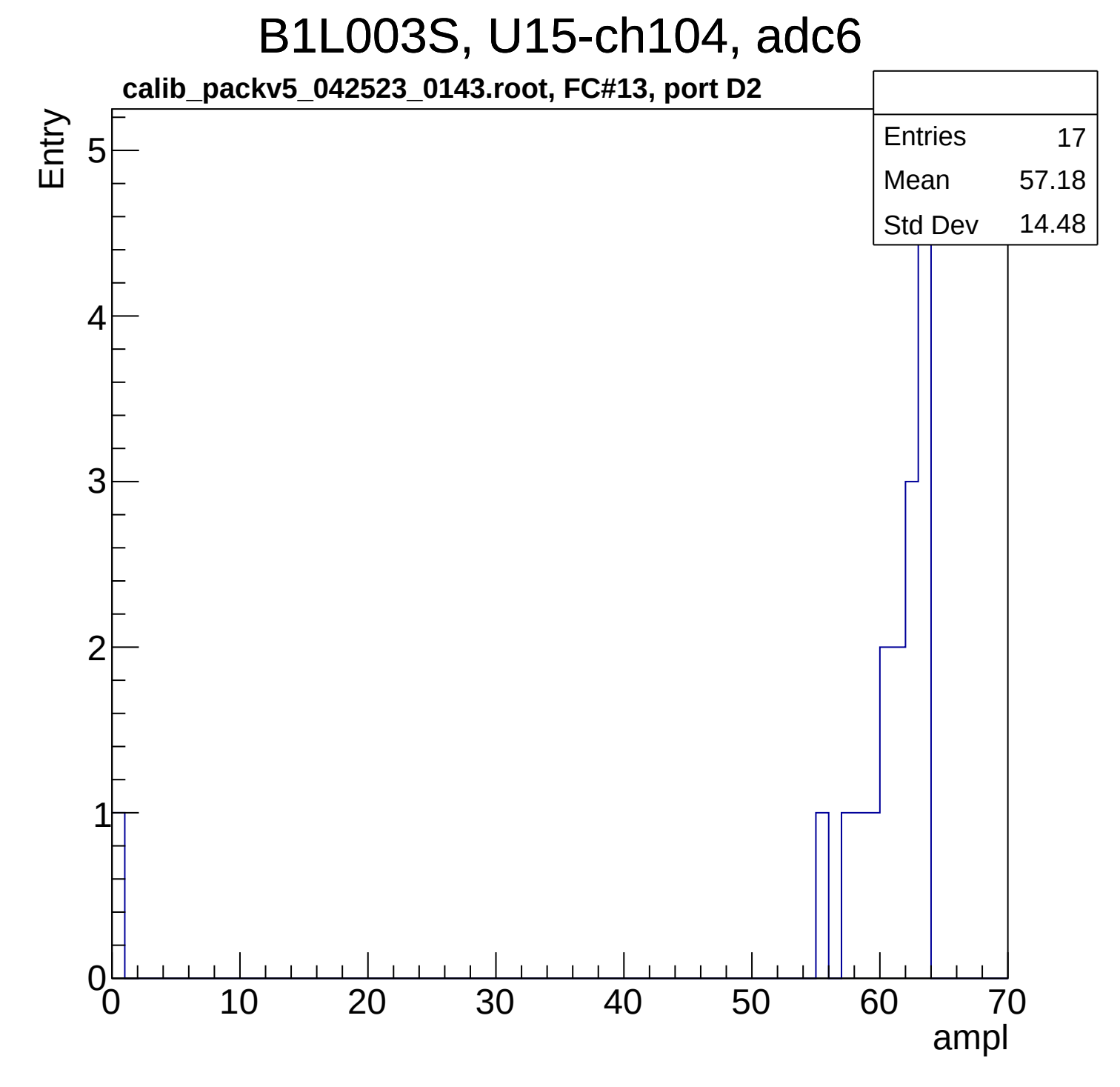
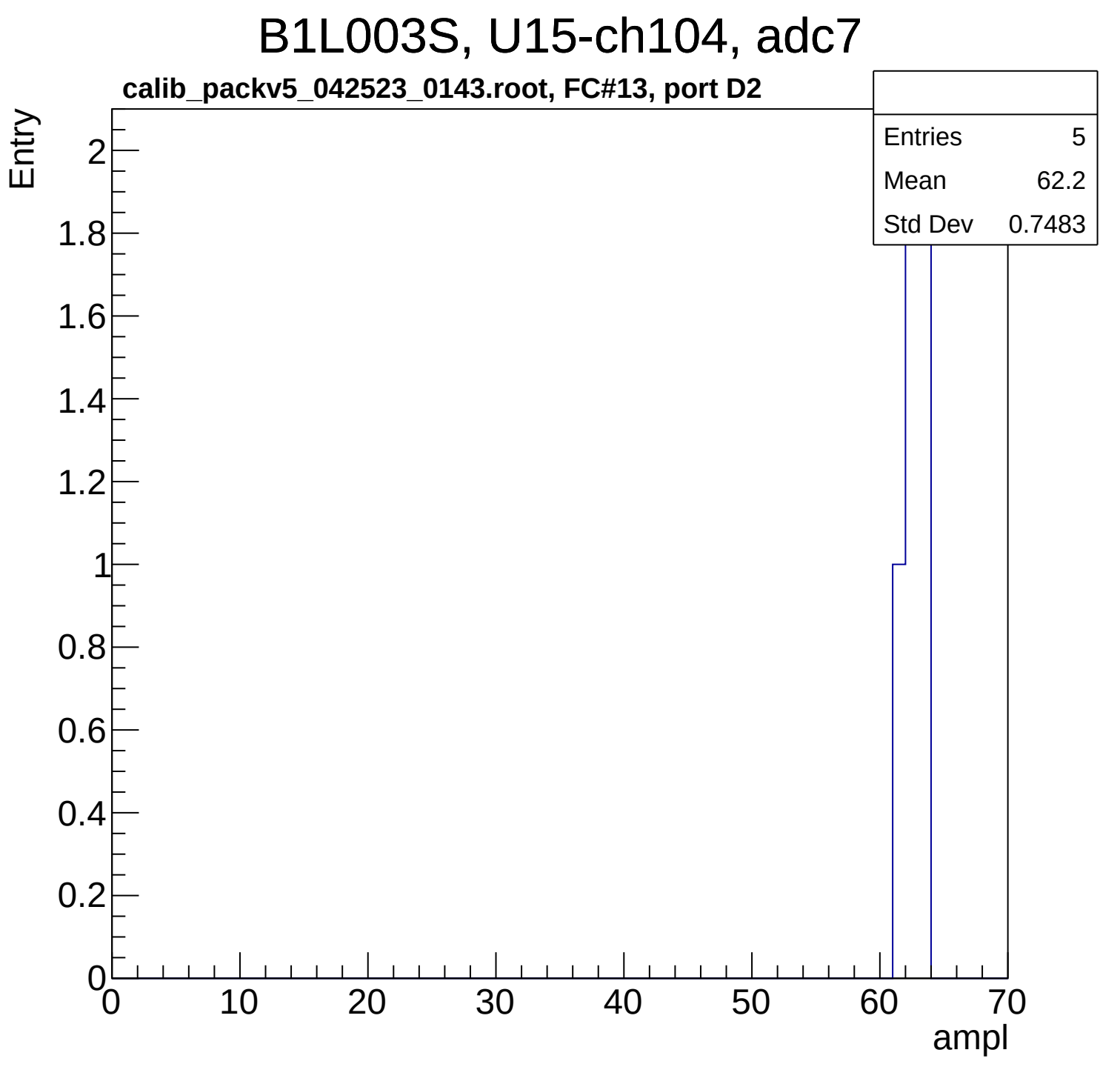

B1L003S, U15-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.18
Std Dev 14.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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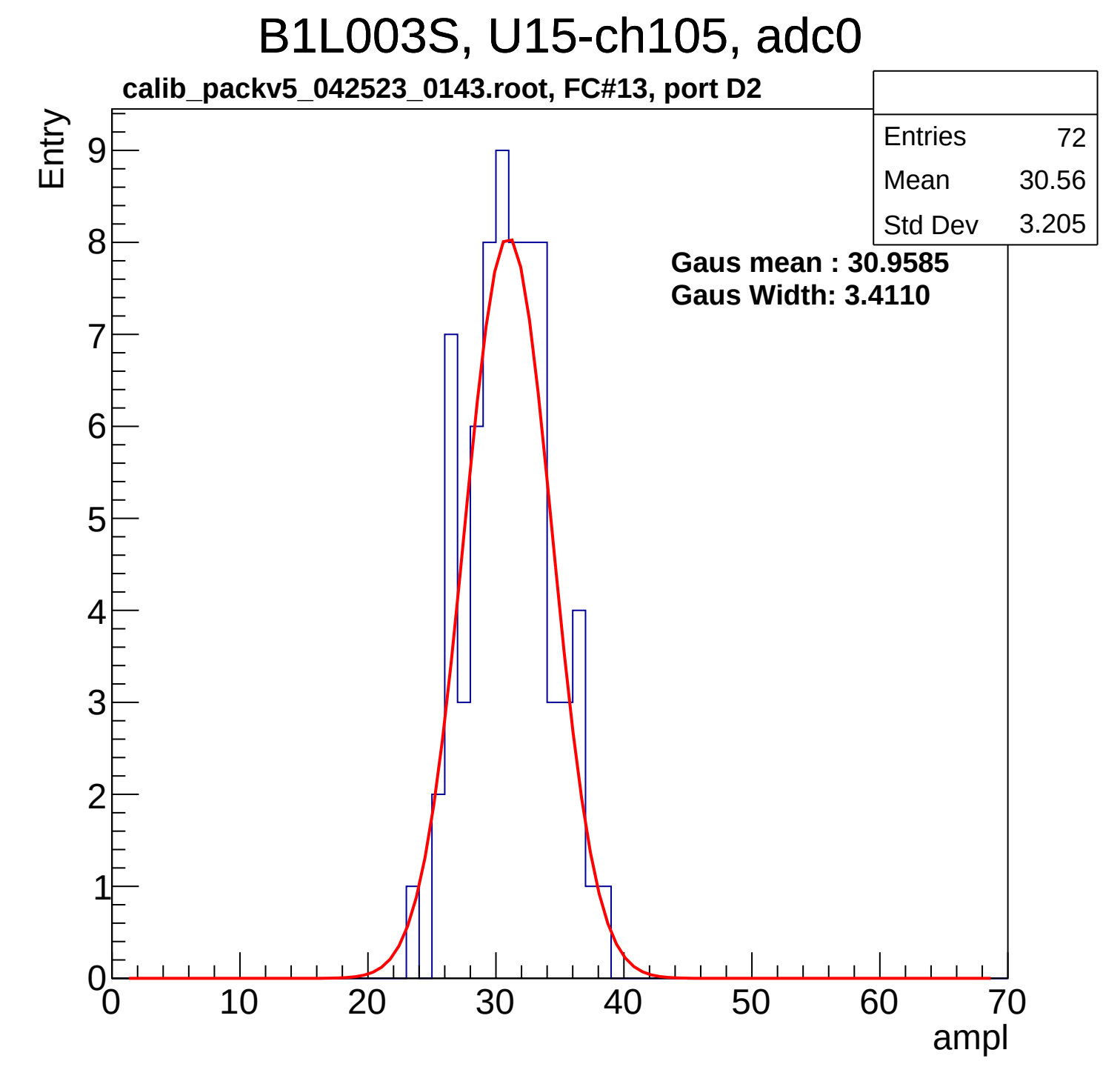
B1L003S, U15-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 30.56
Std Dev 3.205
Gaus mean : 30.9585
Gaus Width: 3.4110
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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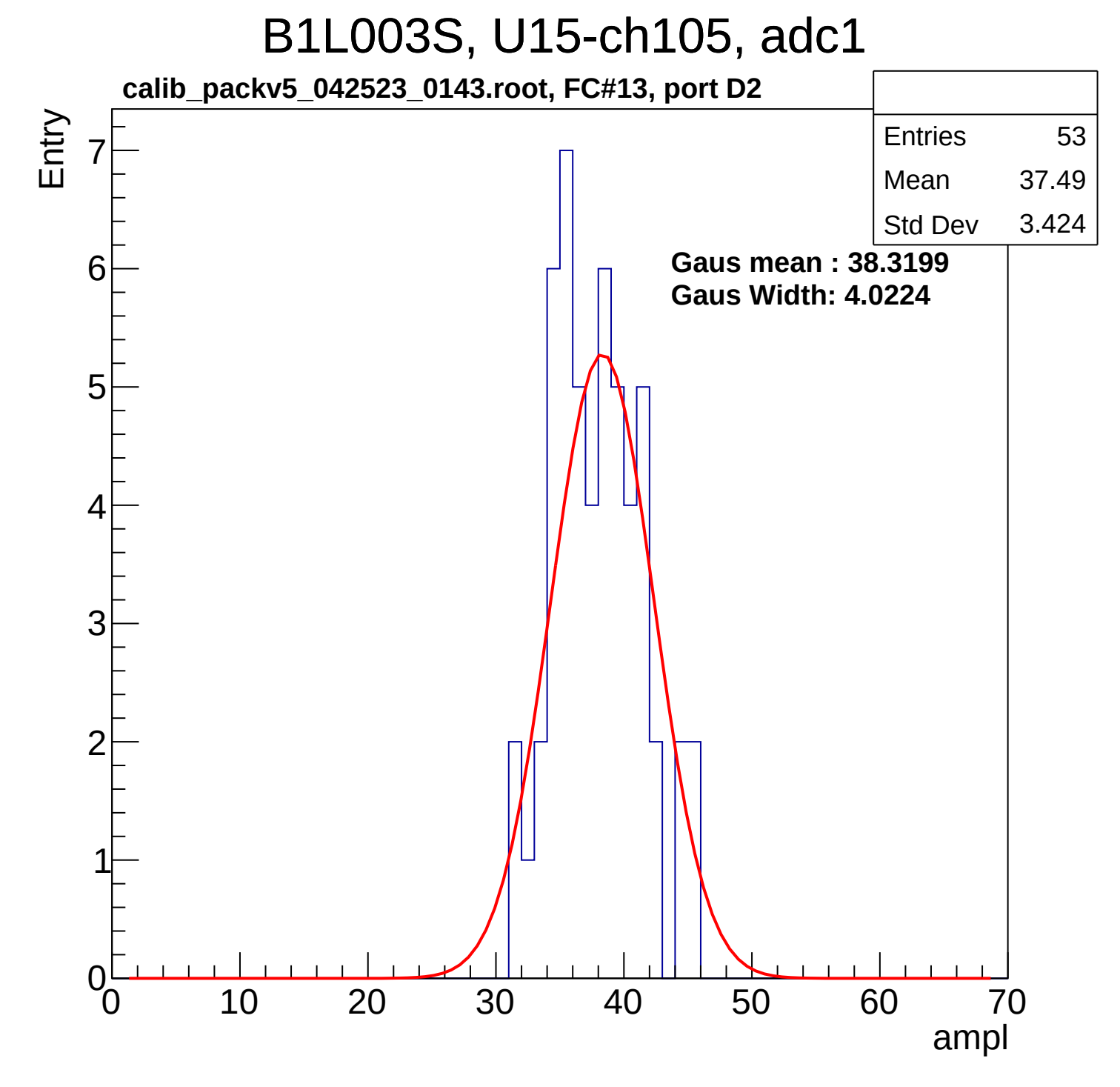
B1L003S, U15-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 37.49
Std Dev 3.424
Gaus mean : 38.3199
Gaus Width: 4.0224
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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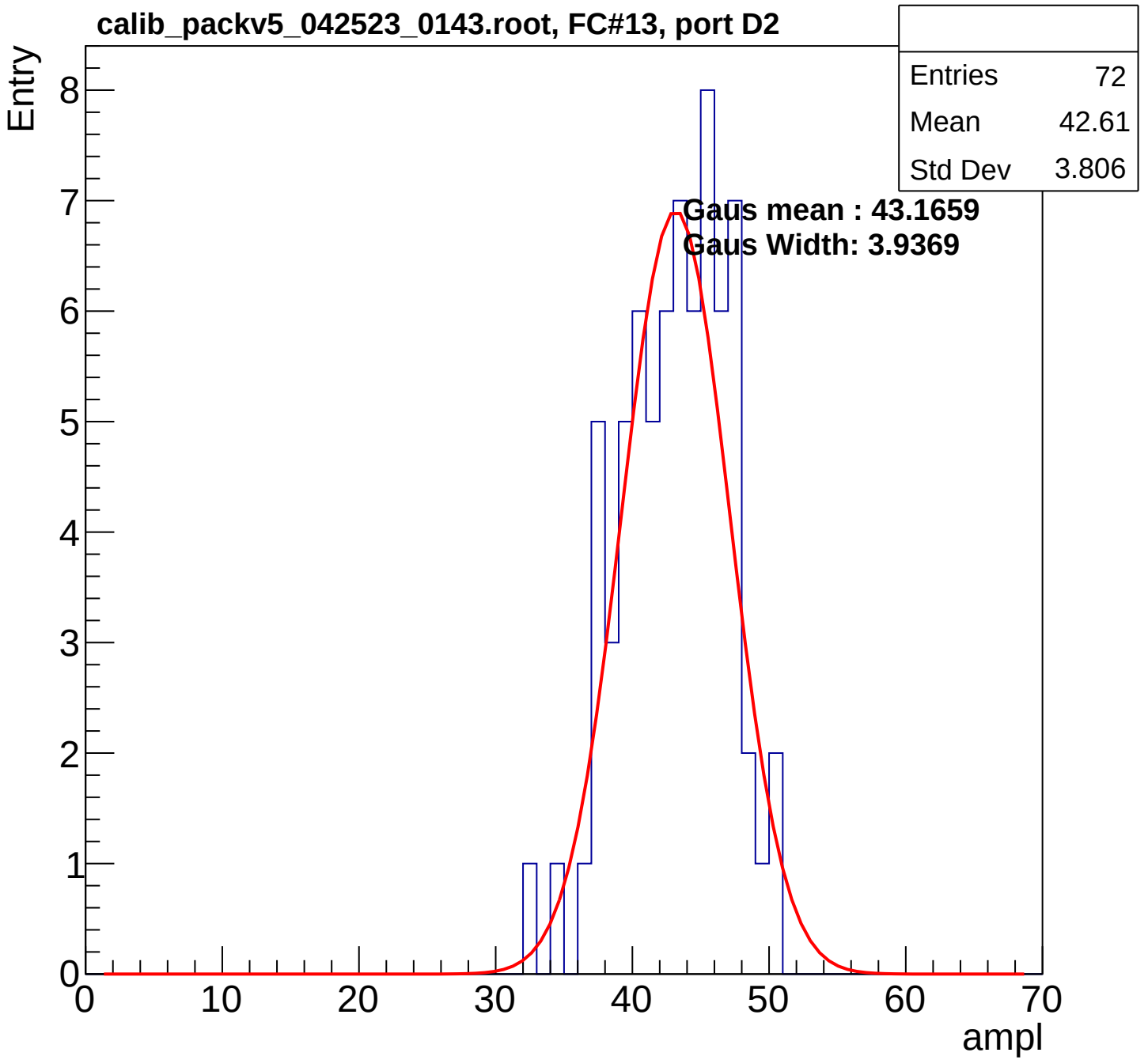
B1L003S, U15-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.61
Std Dev 3.806
Gaus mean : 43.1659
Gaus Width: 3.9369
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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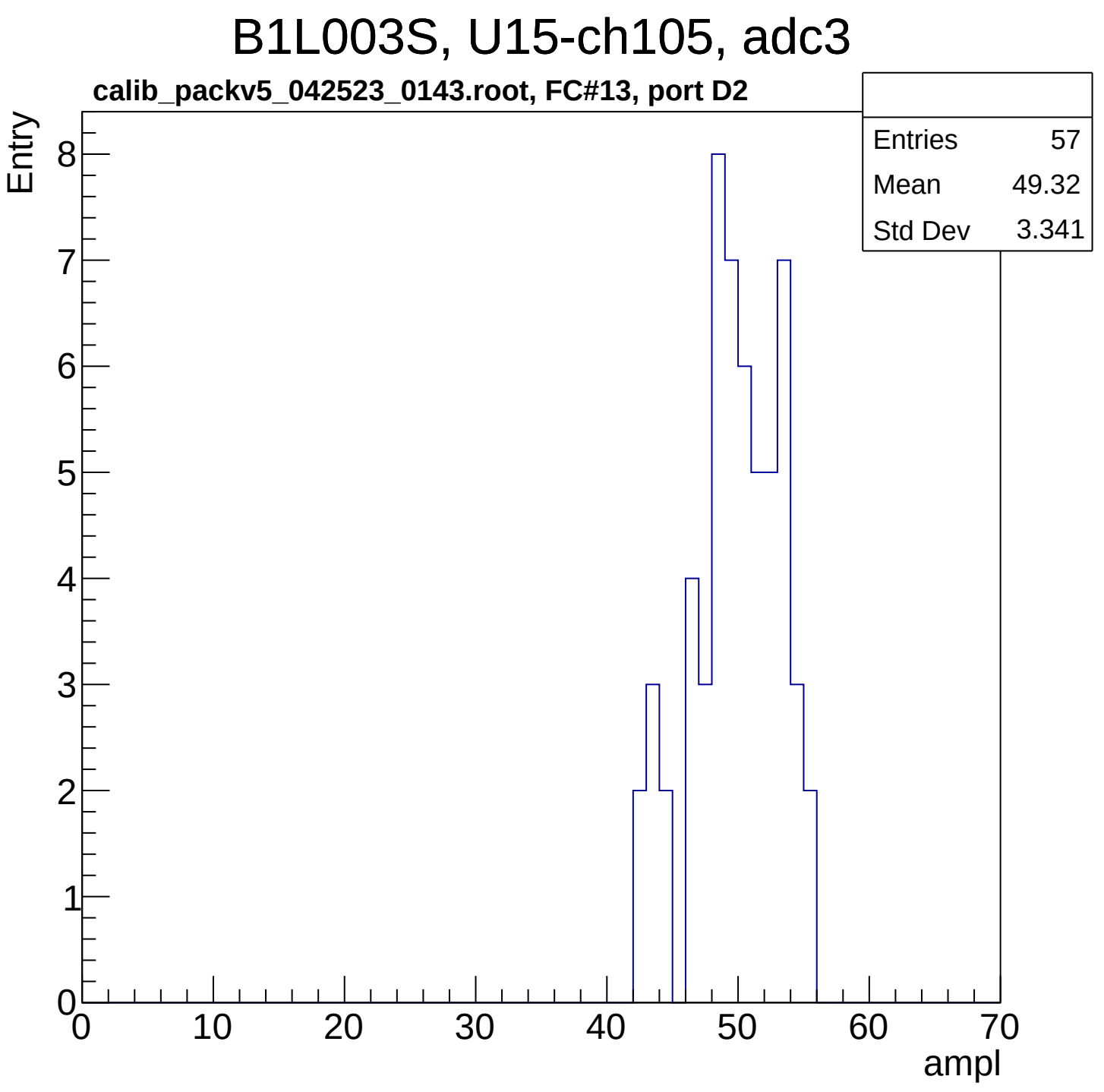
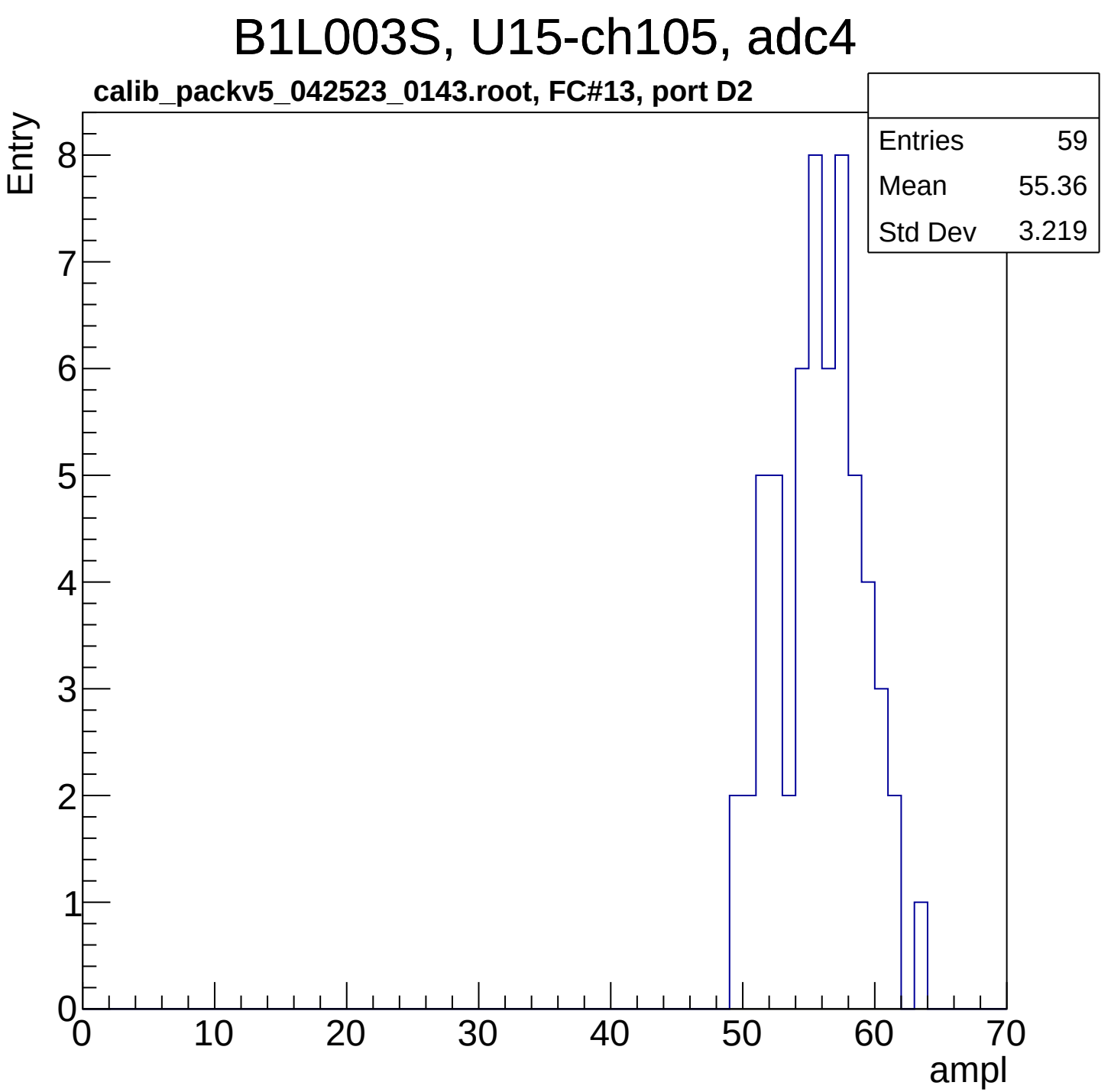

B1L003S, U15-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.32
Std Dev 3.341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.36
Std Dev 3.219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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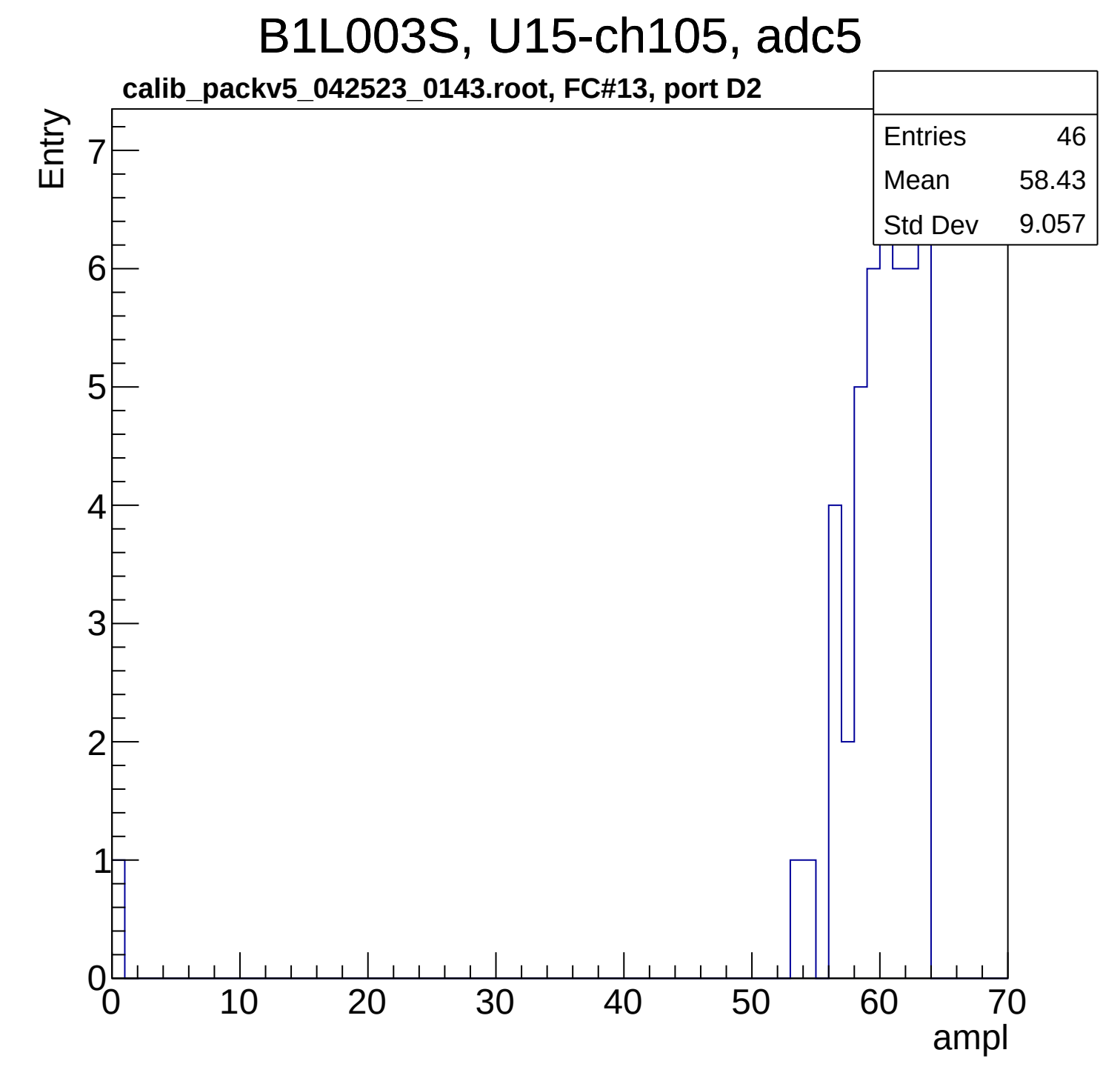
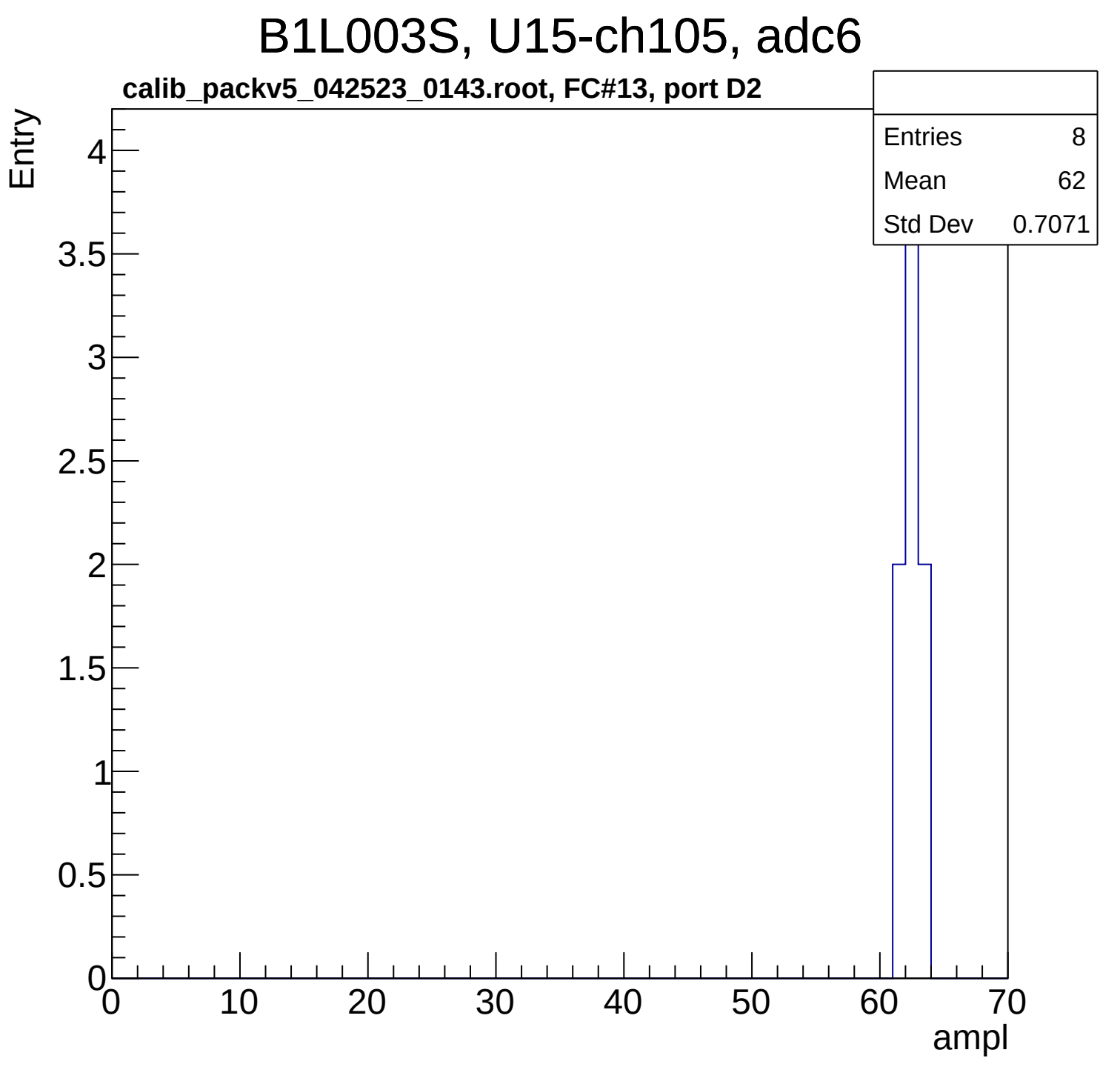
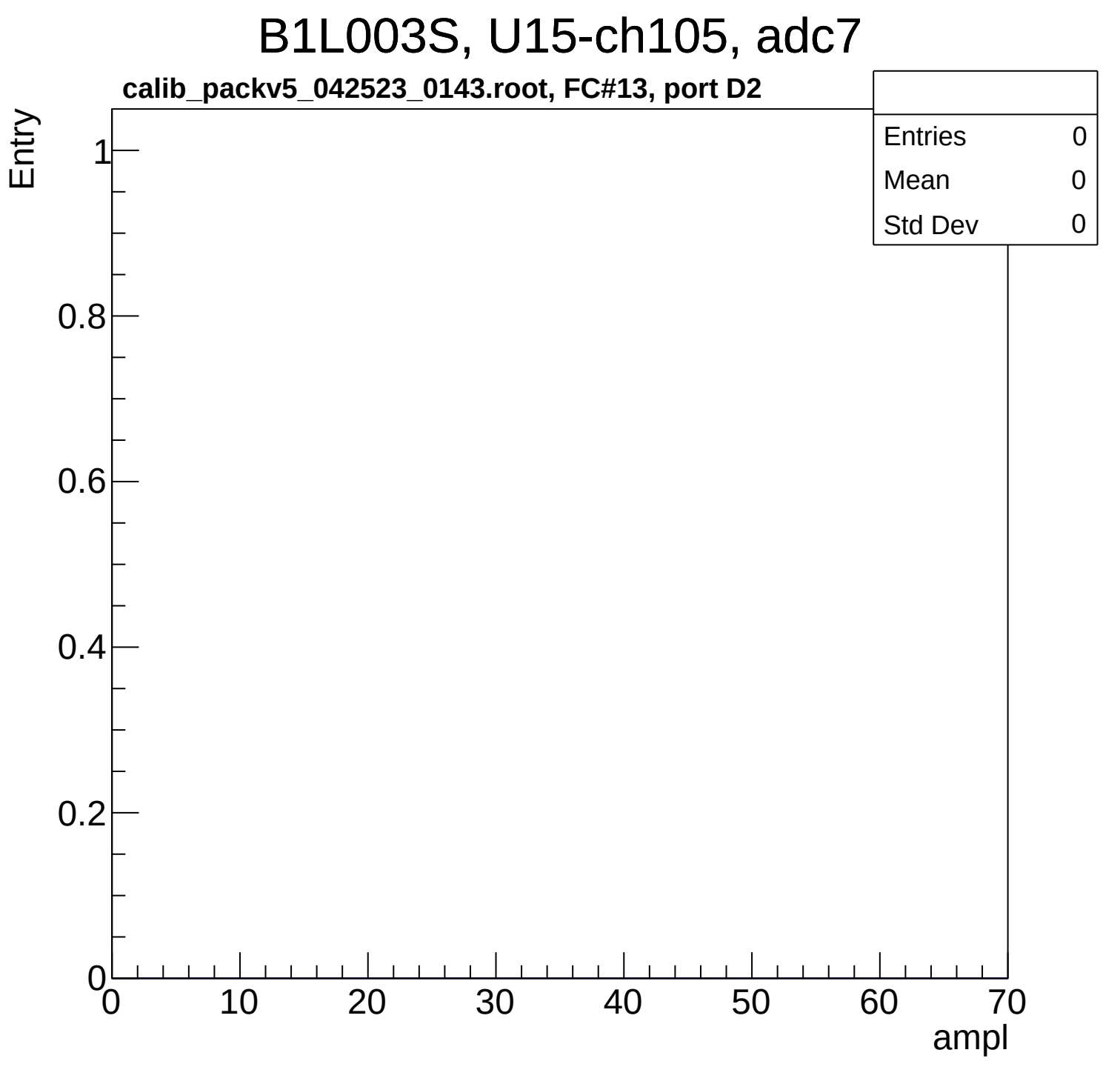
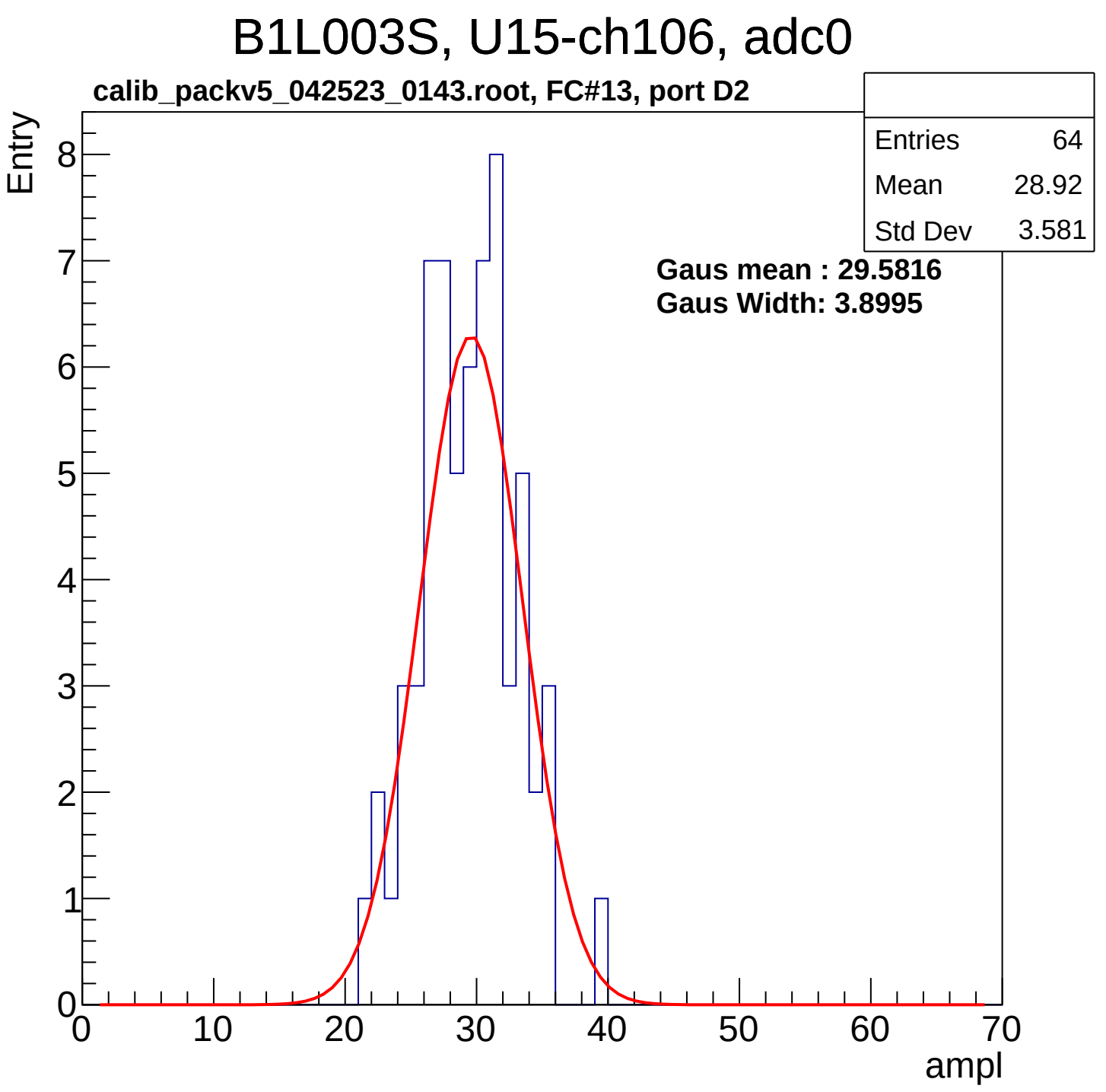

B1L003S, U15-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.43
Std Dev 9.057
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.92
Std Dev 3.581
Gaus mean : 29.5816
Gaus Width: 3.8995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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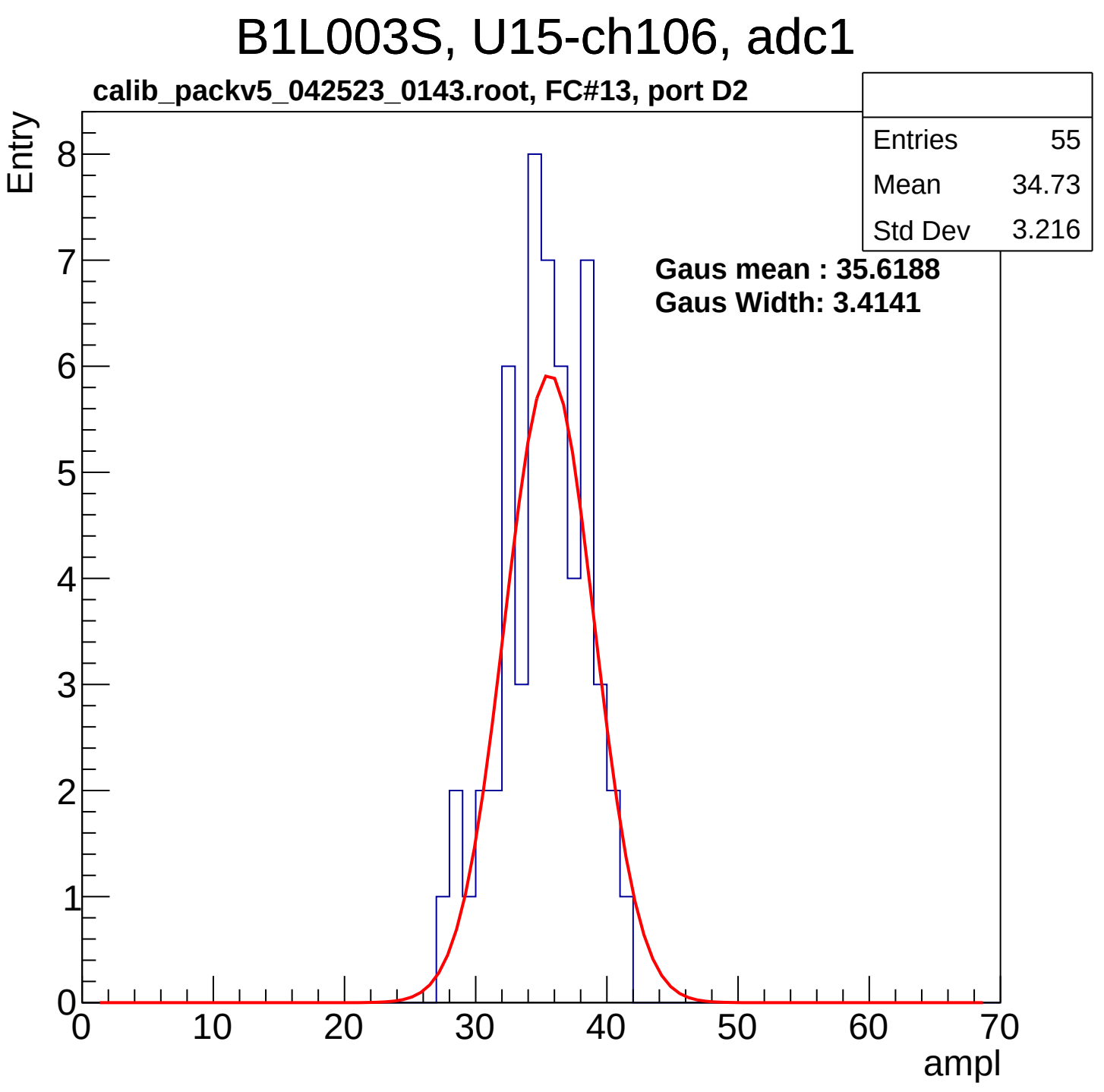
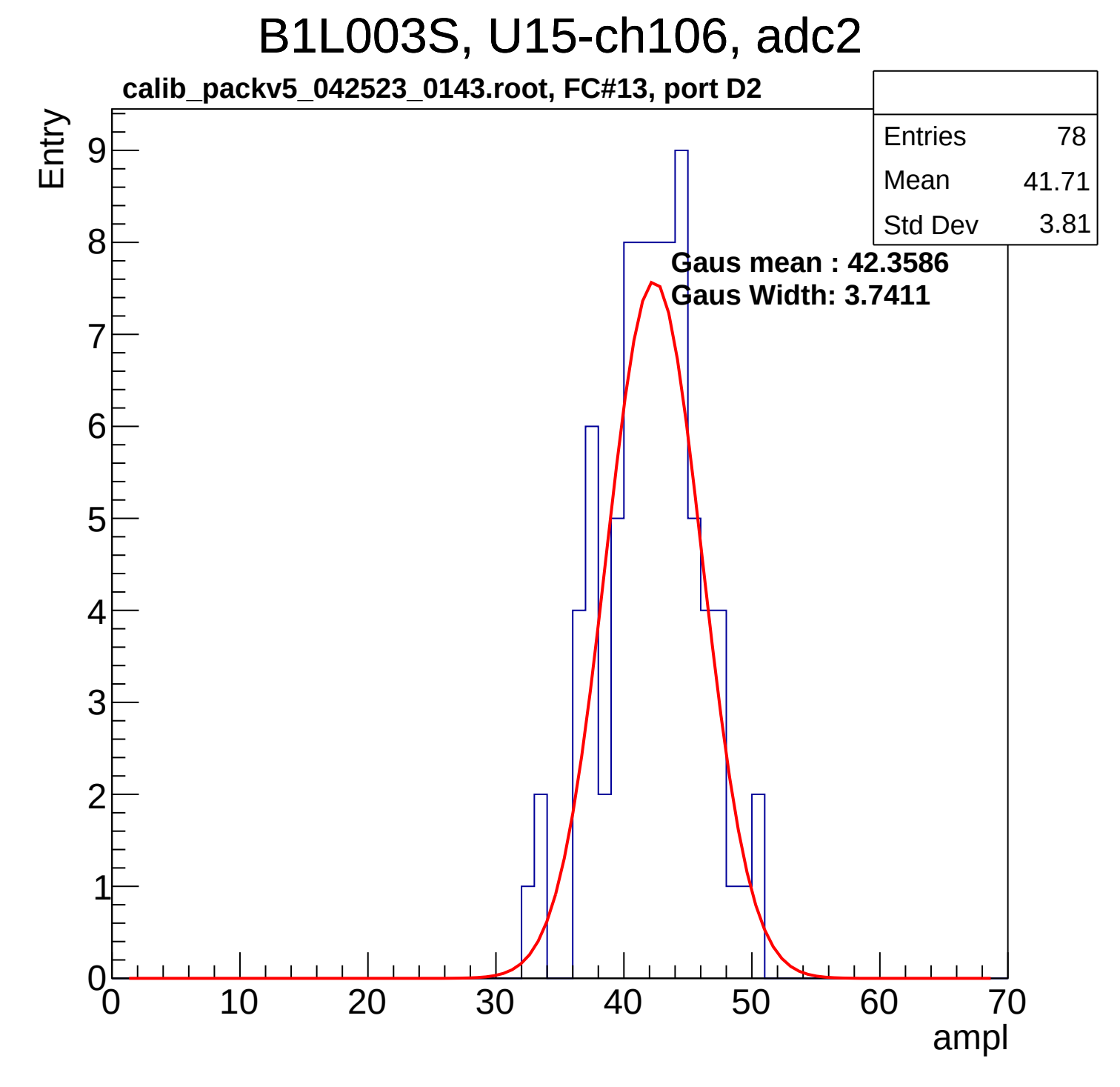

B1L003S, U15-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.73
Std Dev 3.216
Gaus mean : 35.6188
Gaus Width: 3.4141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 41.71
Std Dev 3.81
Gaus mean : 42.3586
Gaus Width: 3.7411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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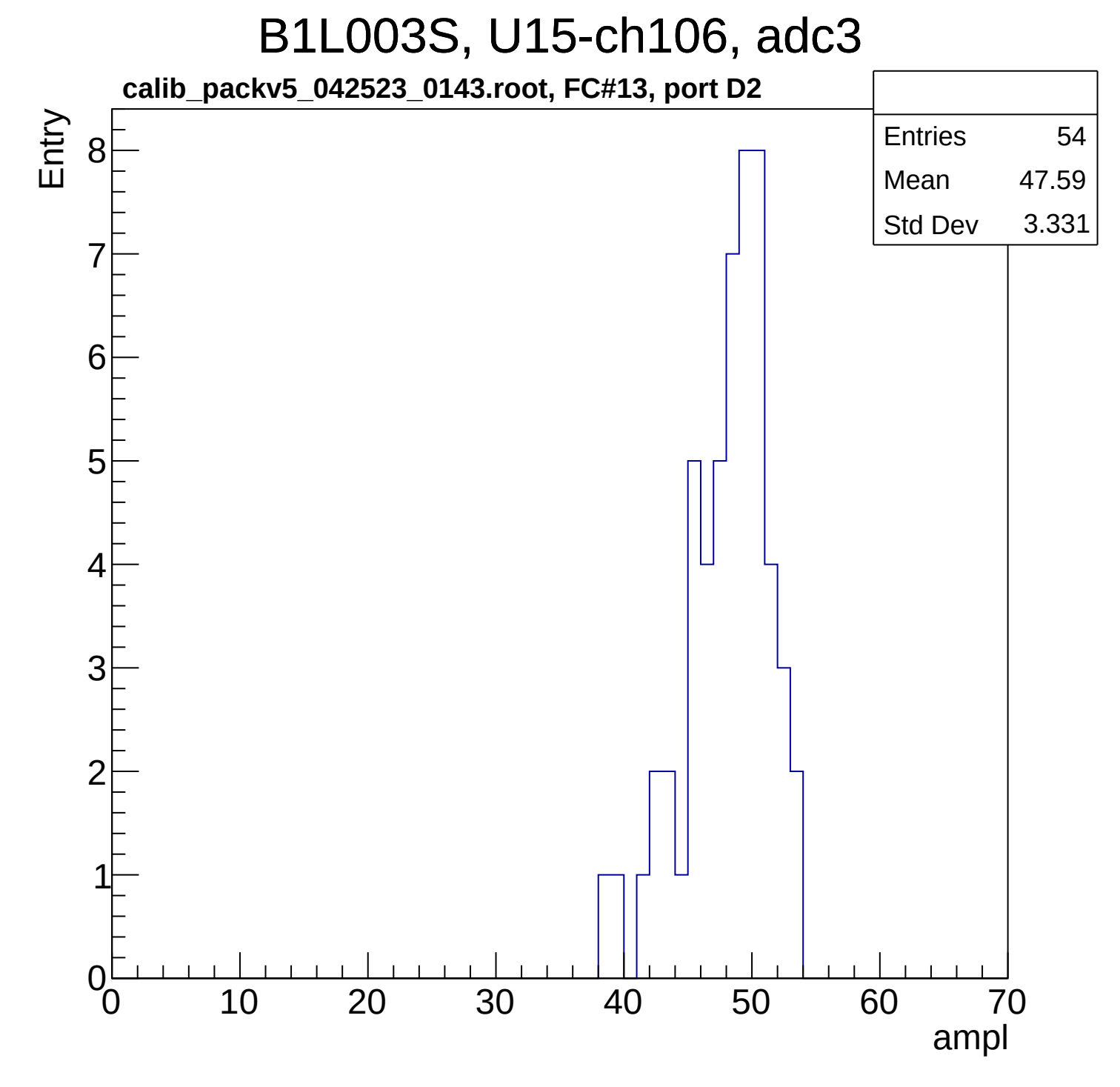
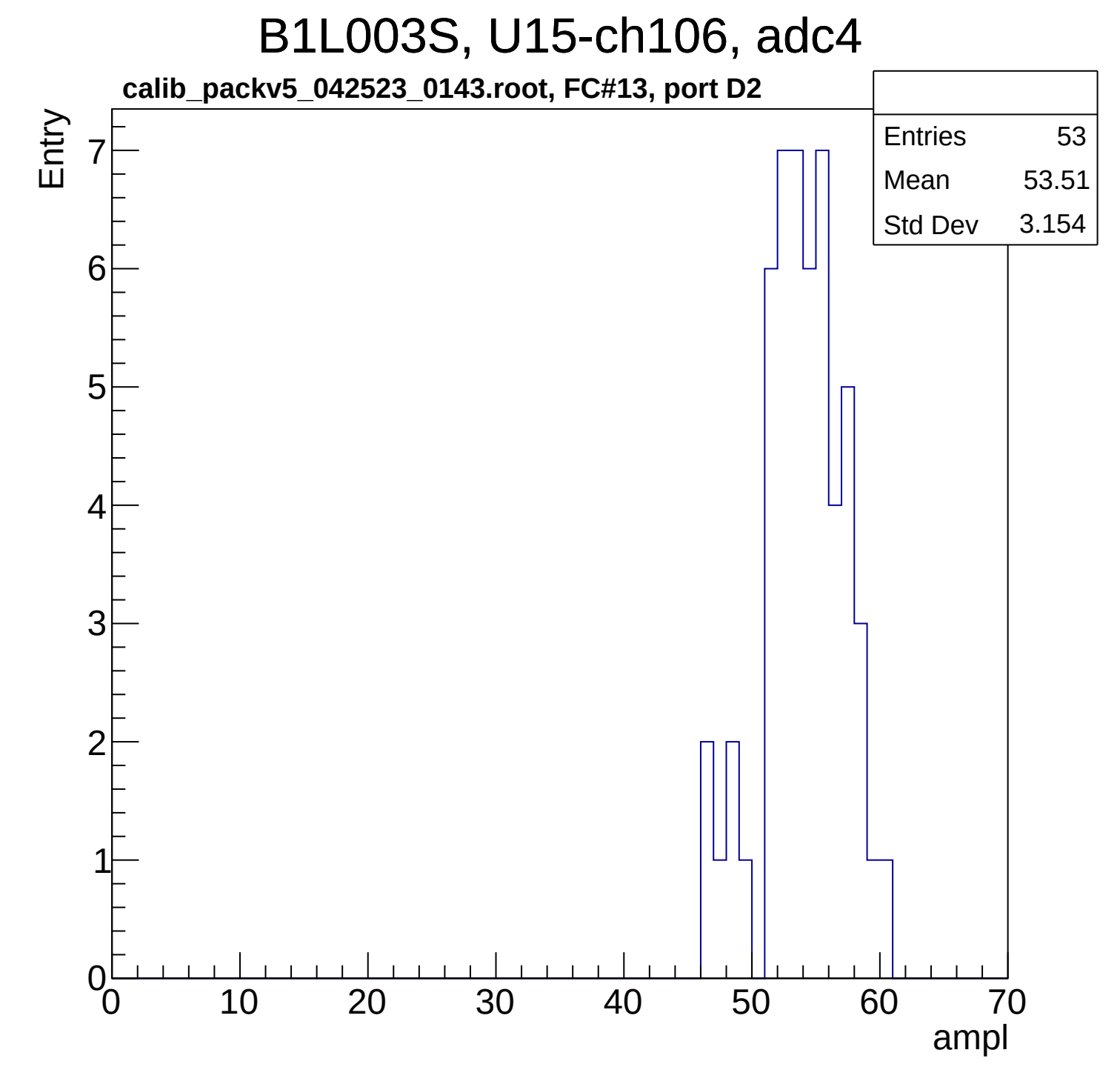
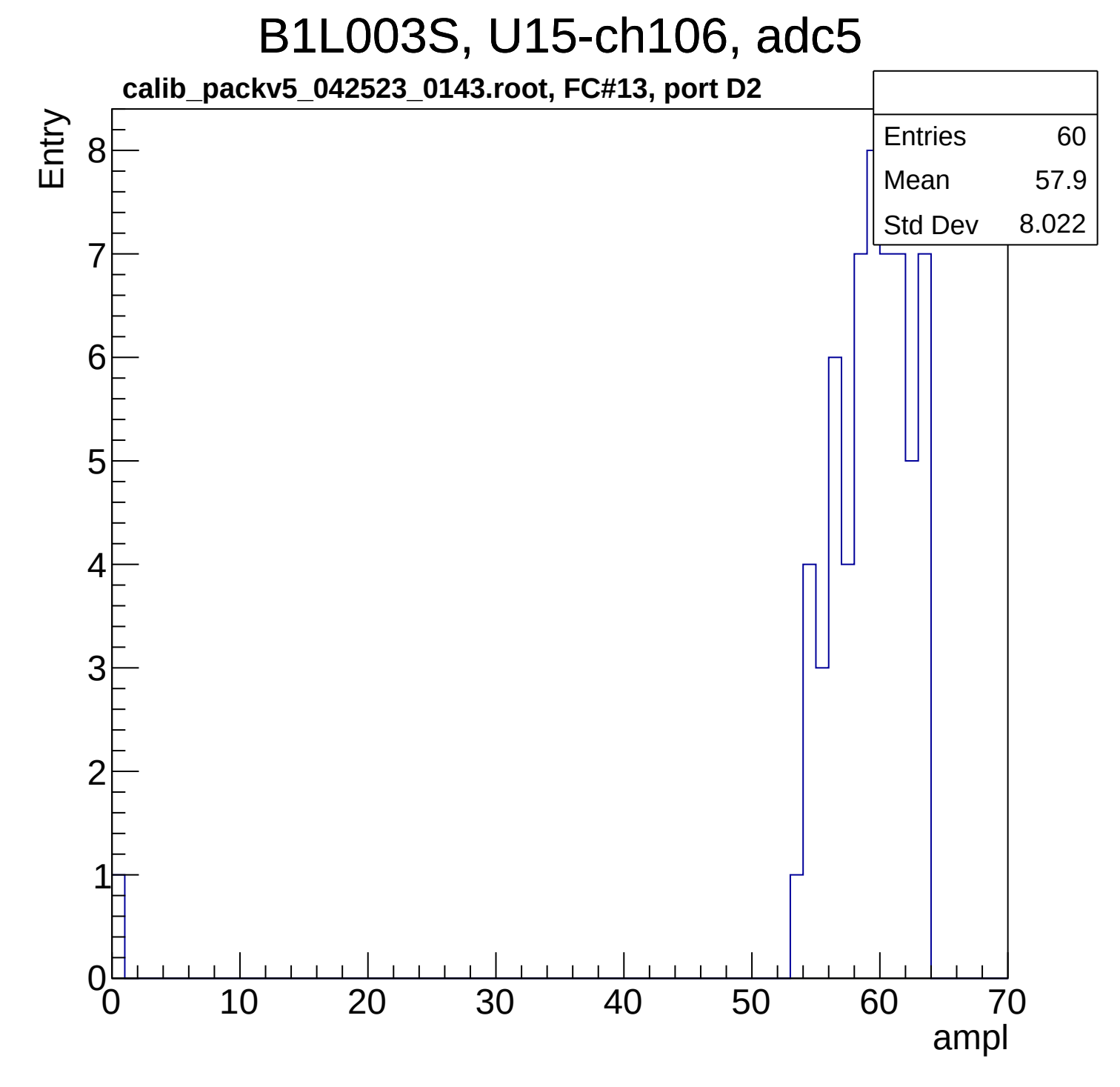

B1L003S, U15-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 47.59
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 53.51
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.9
Std Dev 8.022
Entry
ampl
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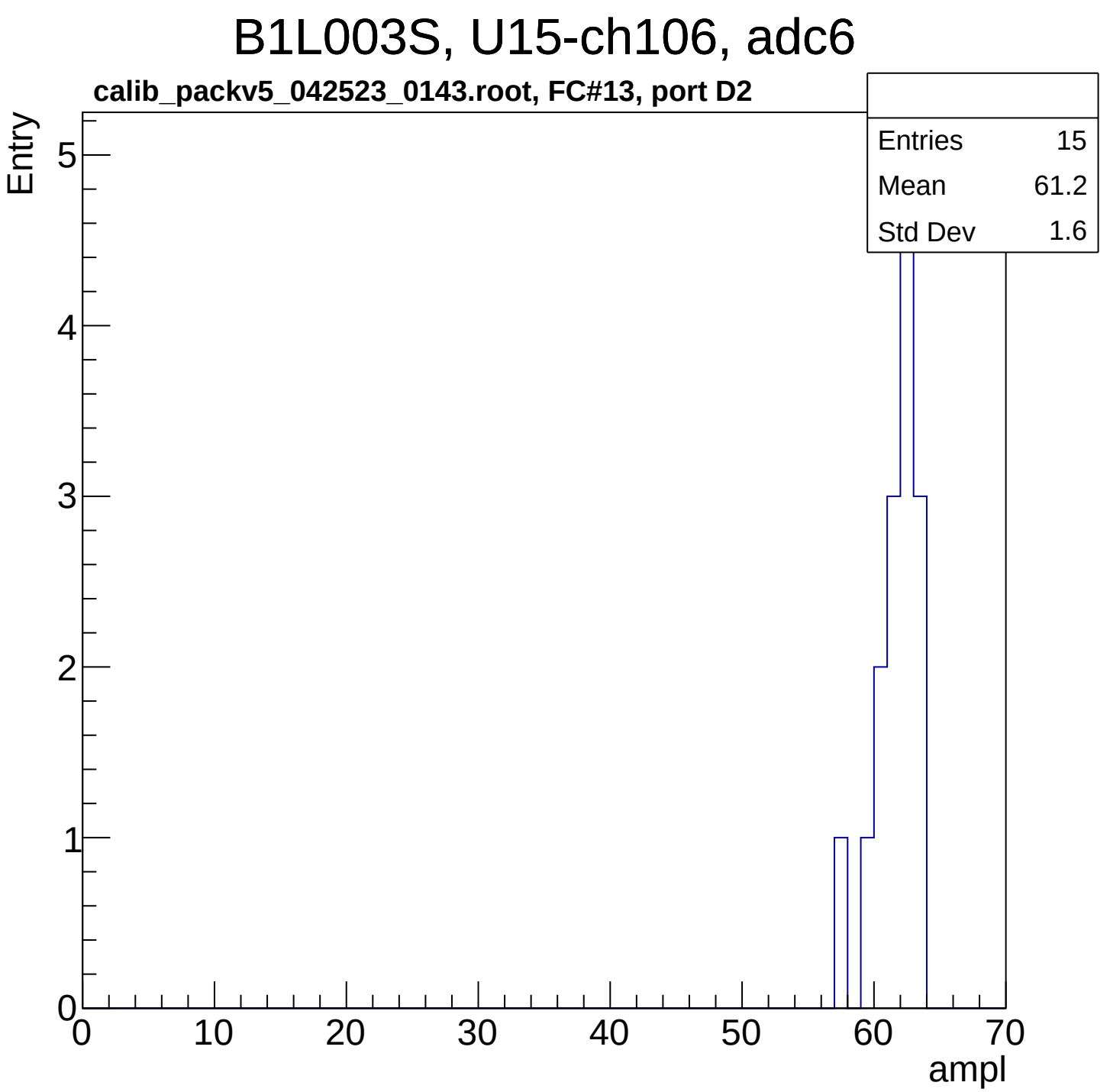
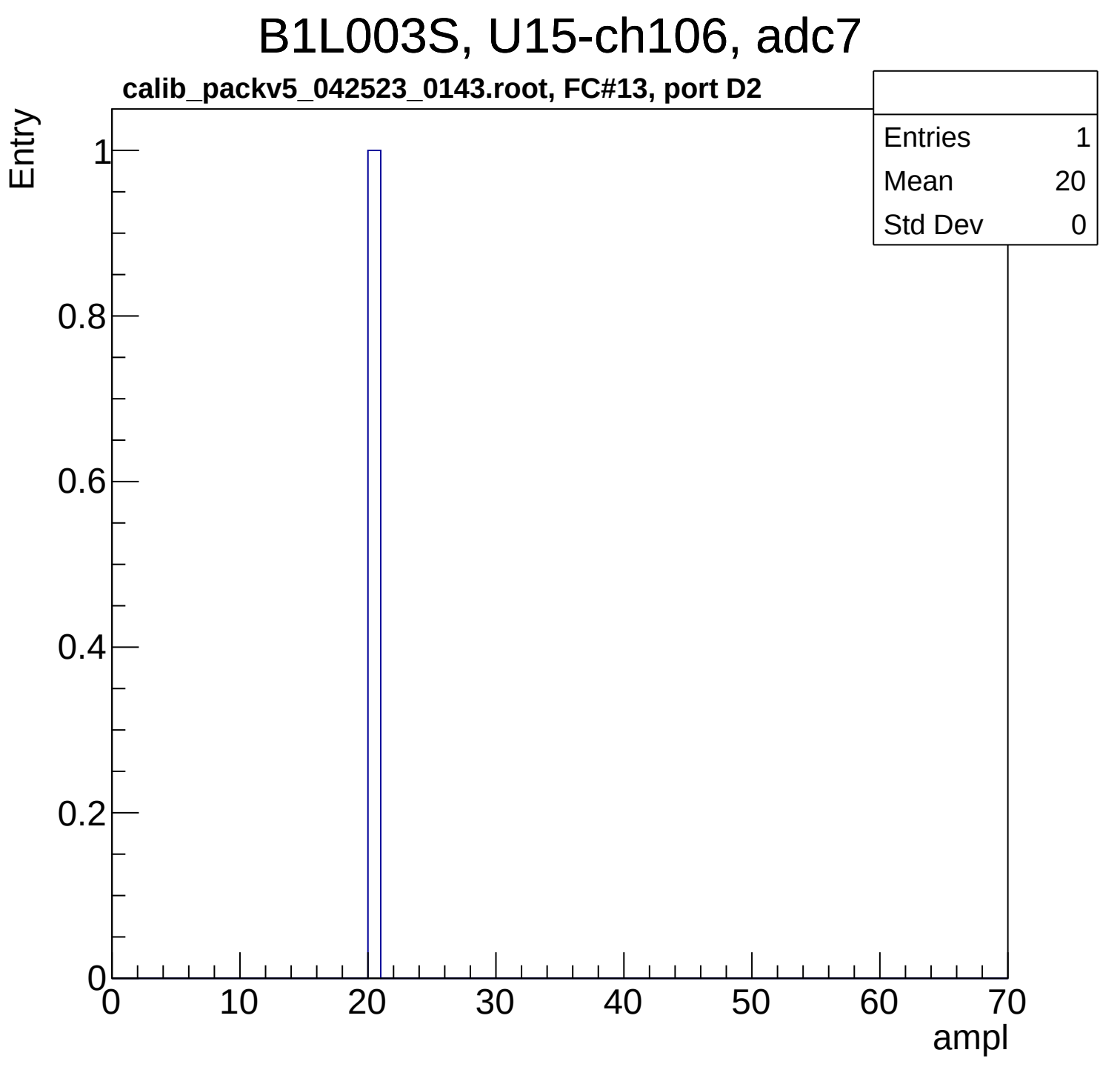
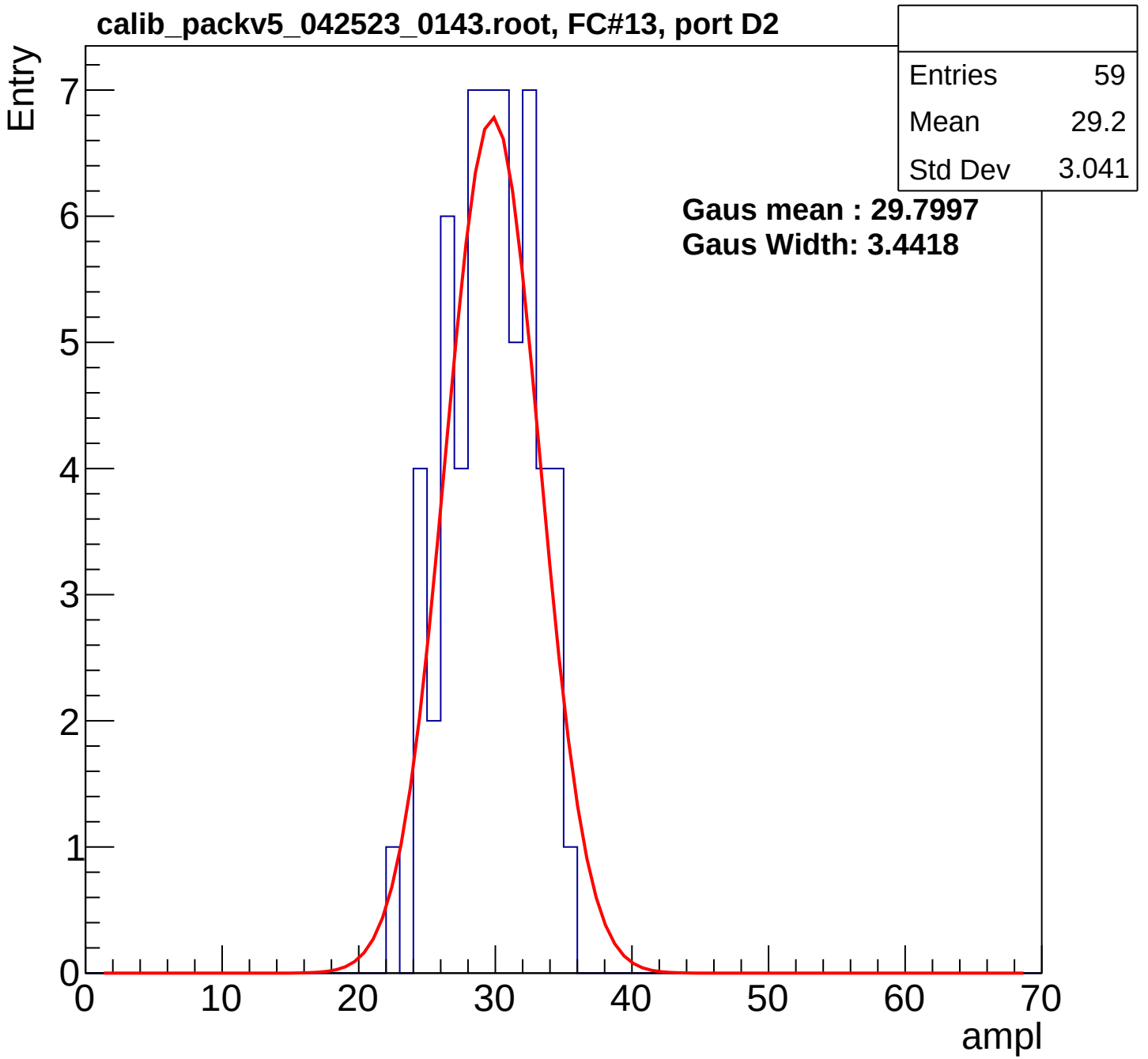
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.2
Std Dev 1.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.2
Std Dev 3.041
Gaus mean : 29.7997
Gaus Width: 3.4418
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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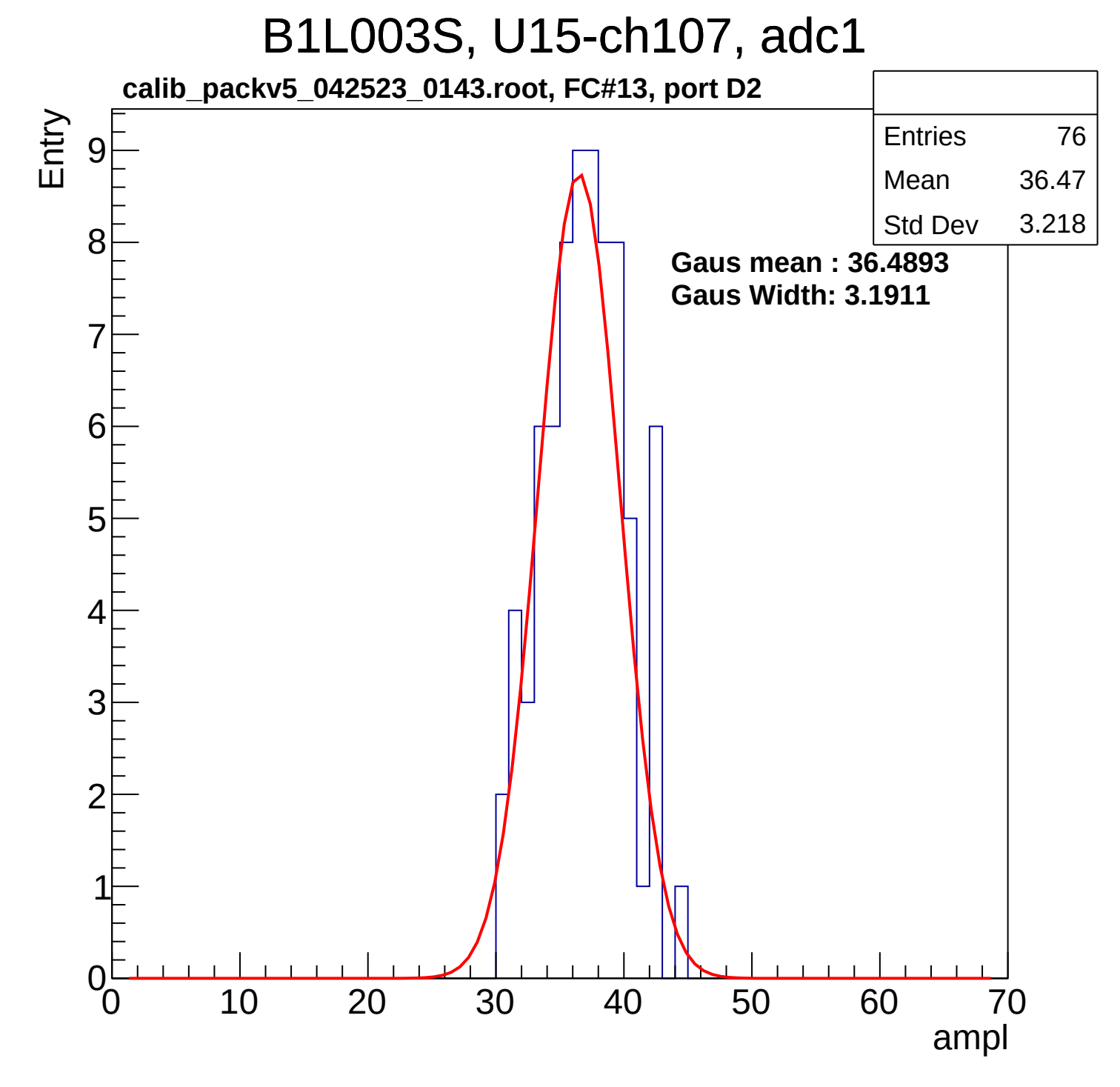
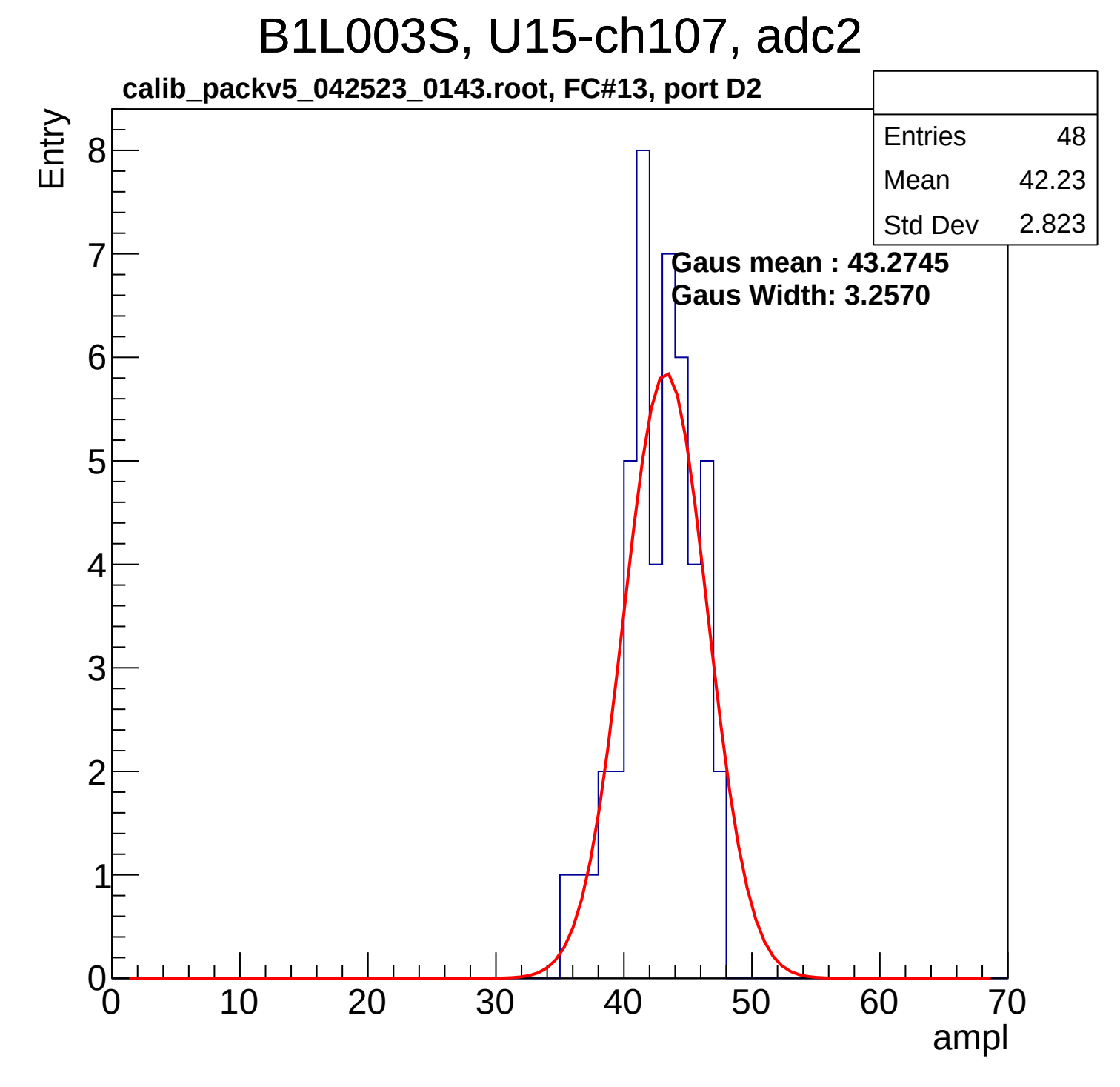
40
50
60
70


B1L003S, U15-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.47
Std Dev 3.218
Gaus mean : 36.4893
Gaus Width: 3.1911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 42.23
Std Dev 2.823
Gaus mean : 43.2745
Gaus Width: 3.2570
Entry
ampl
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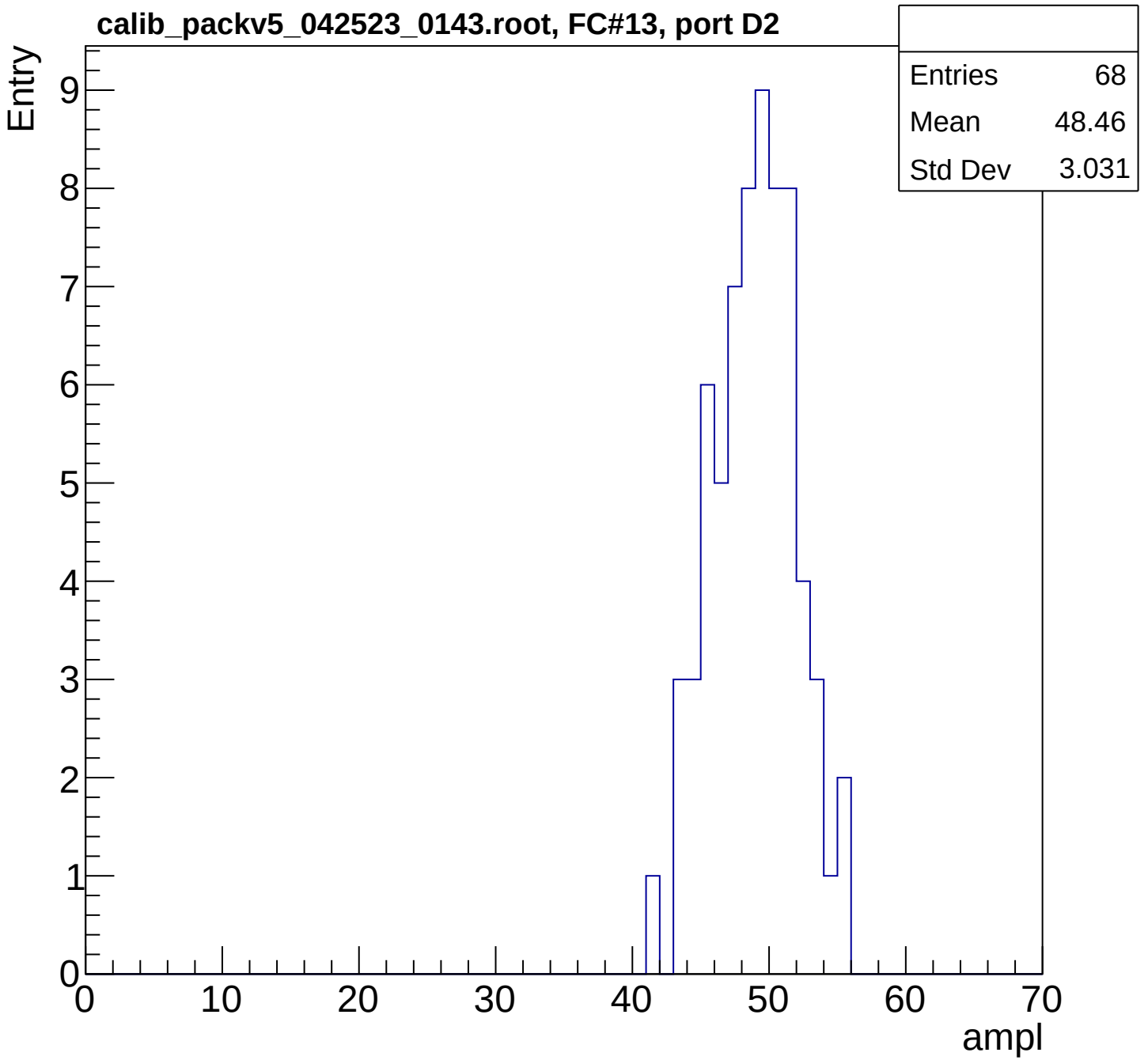
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.46
Std Dev 3.031
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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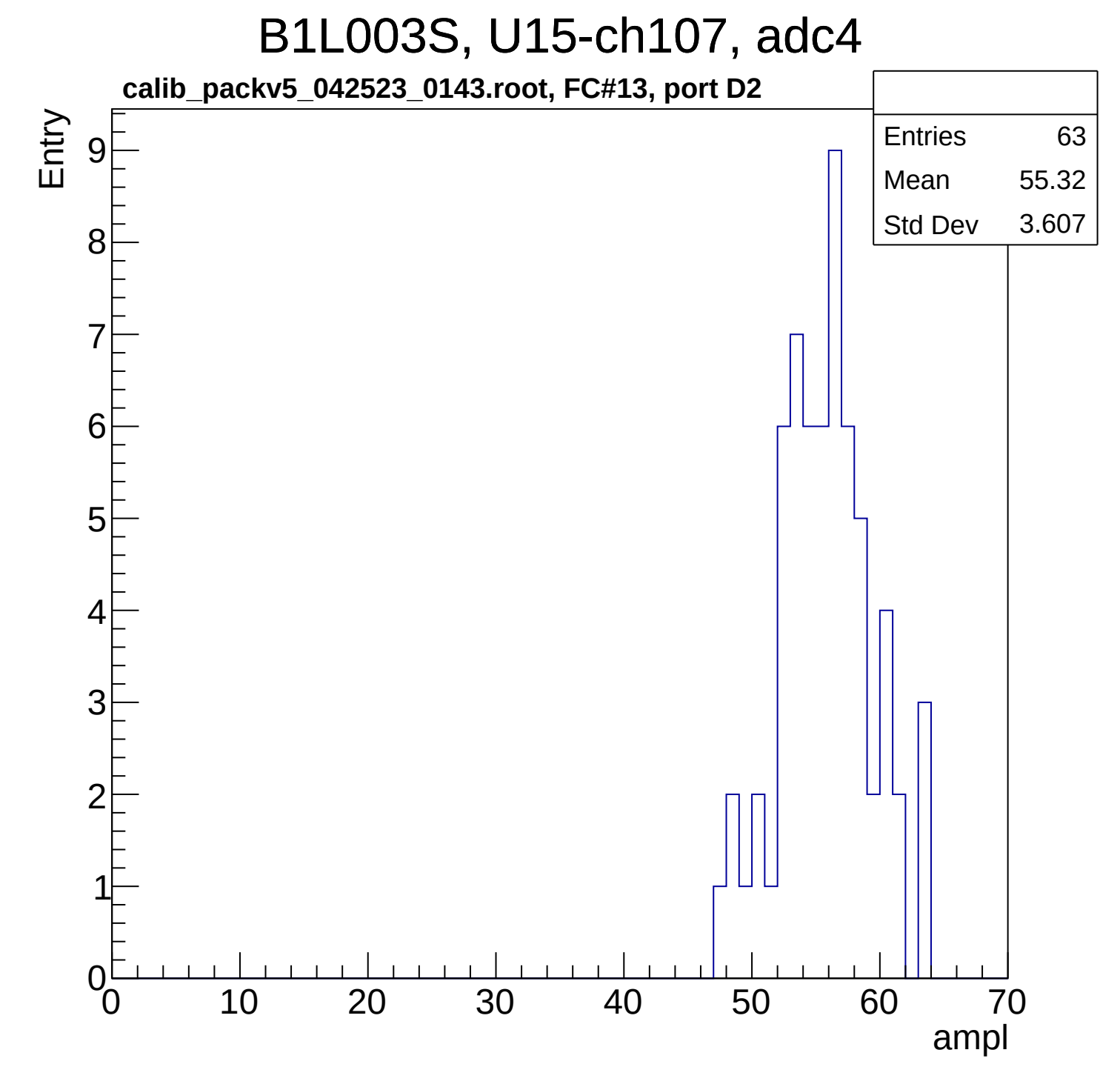
B1L003S, U15-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.32
Std Dev 3.607
Entry
ampl
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6
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60
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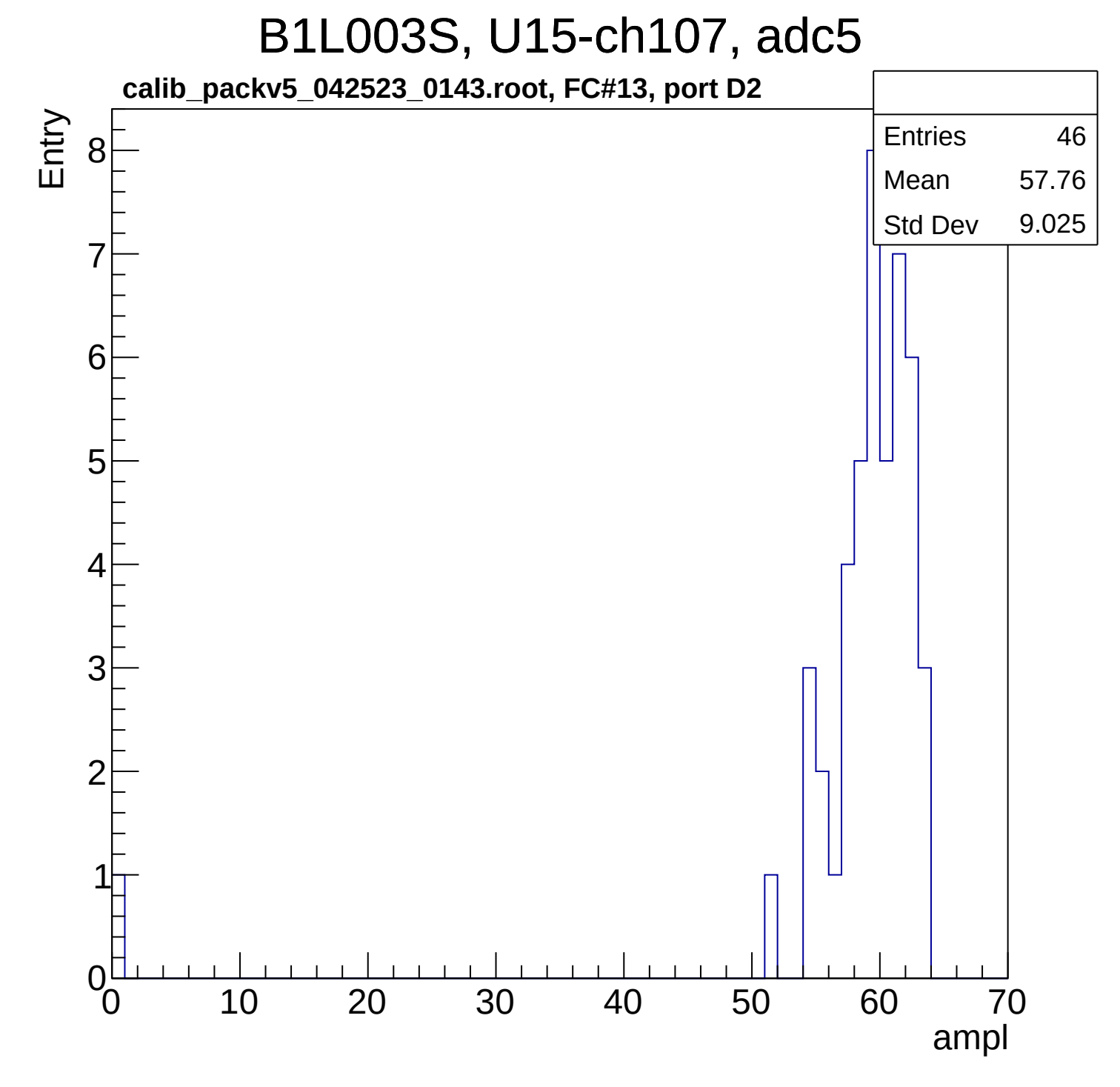
B1L003S, U15-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 57.76
Std Dev 9.025
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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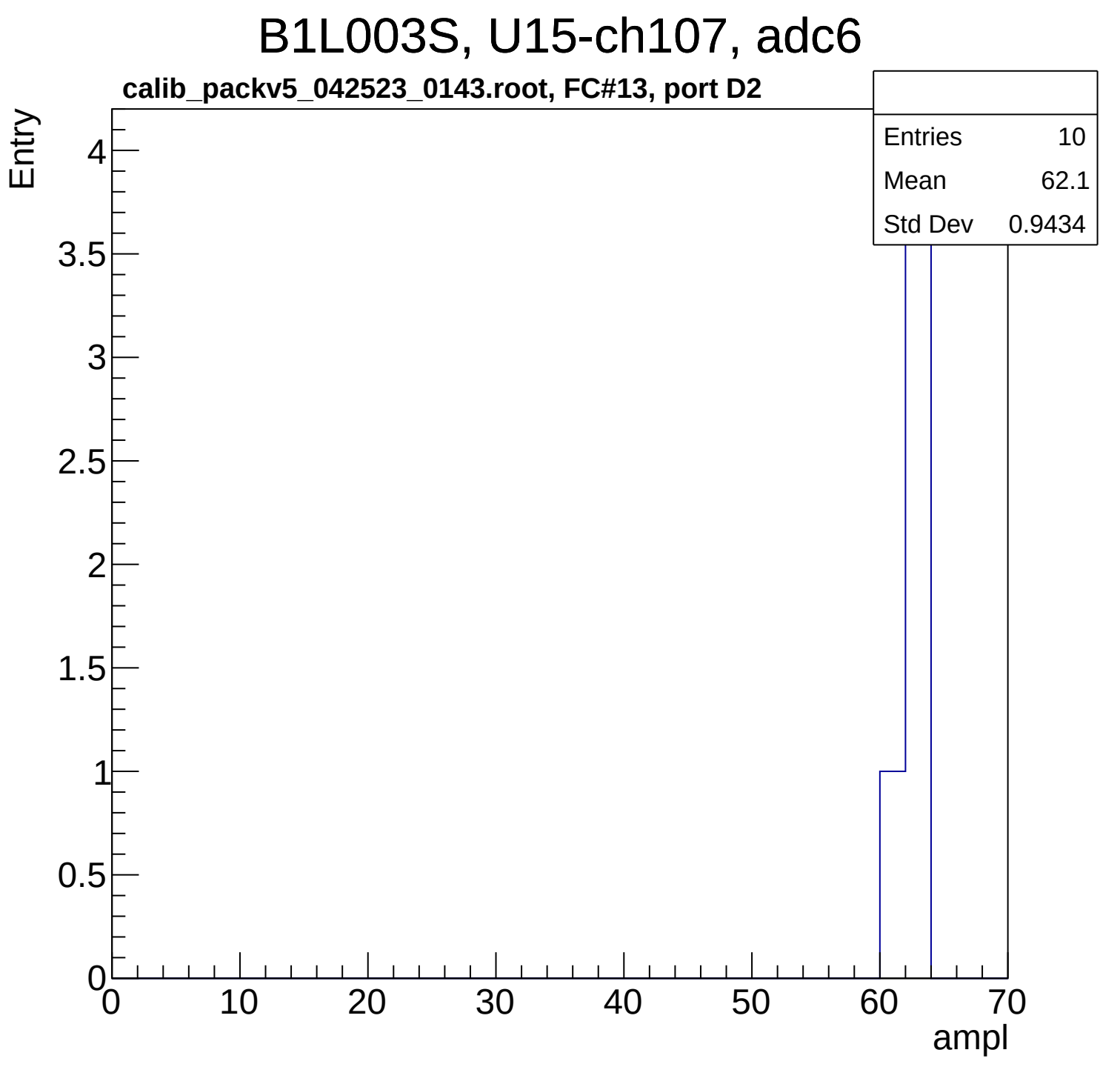
B1L003S, U15-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
ampl
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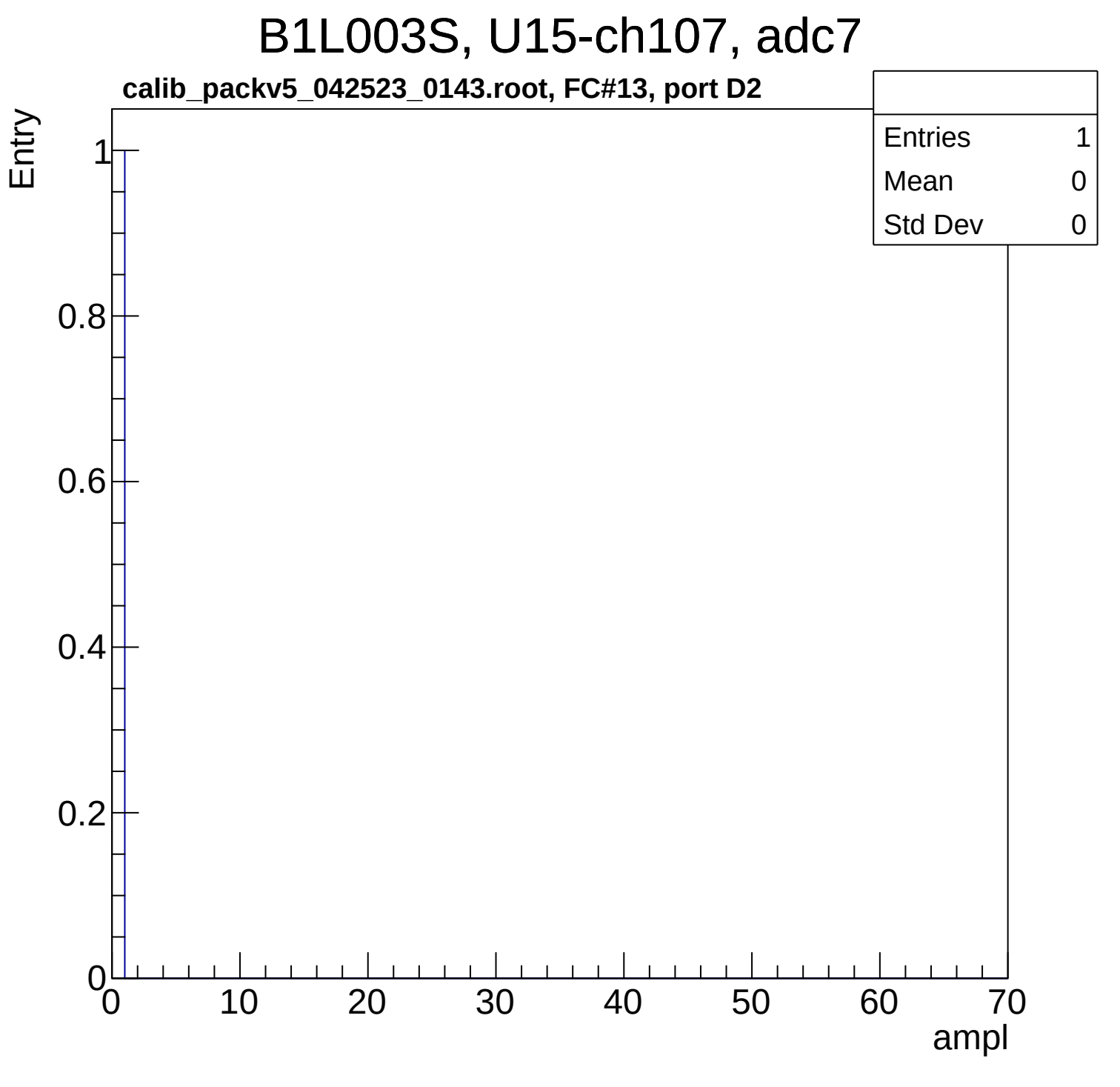
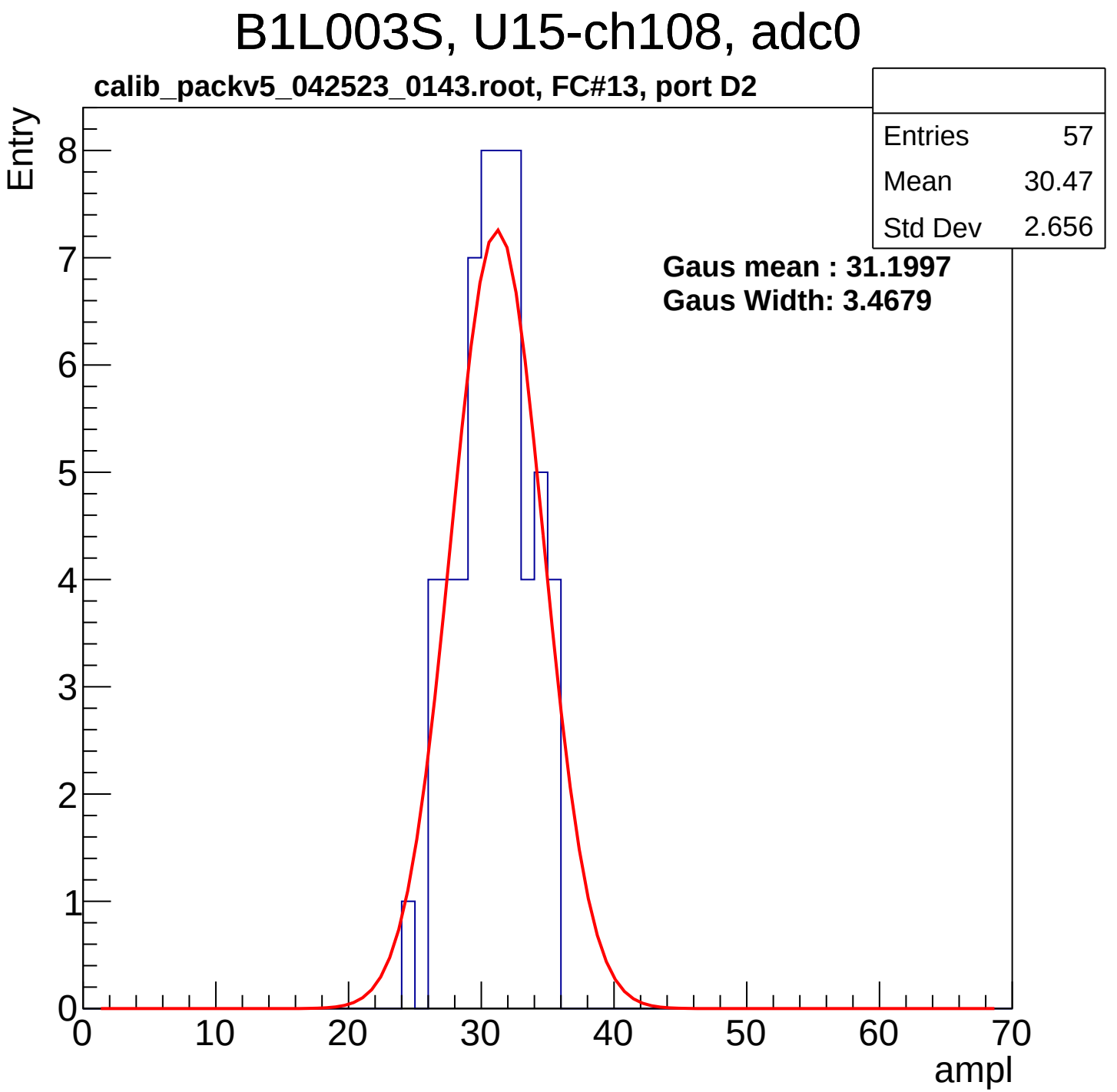
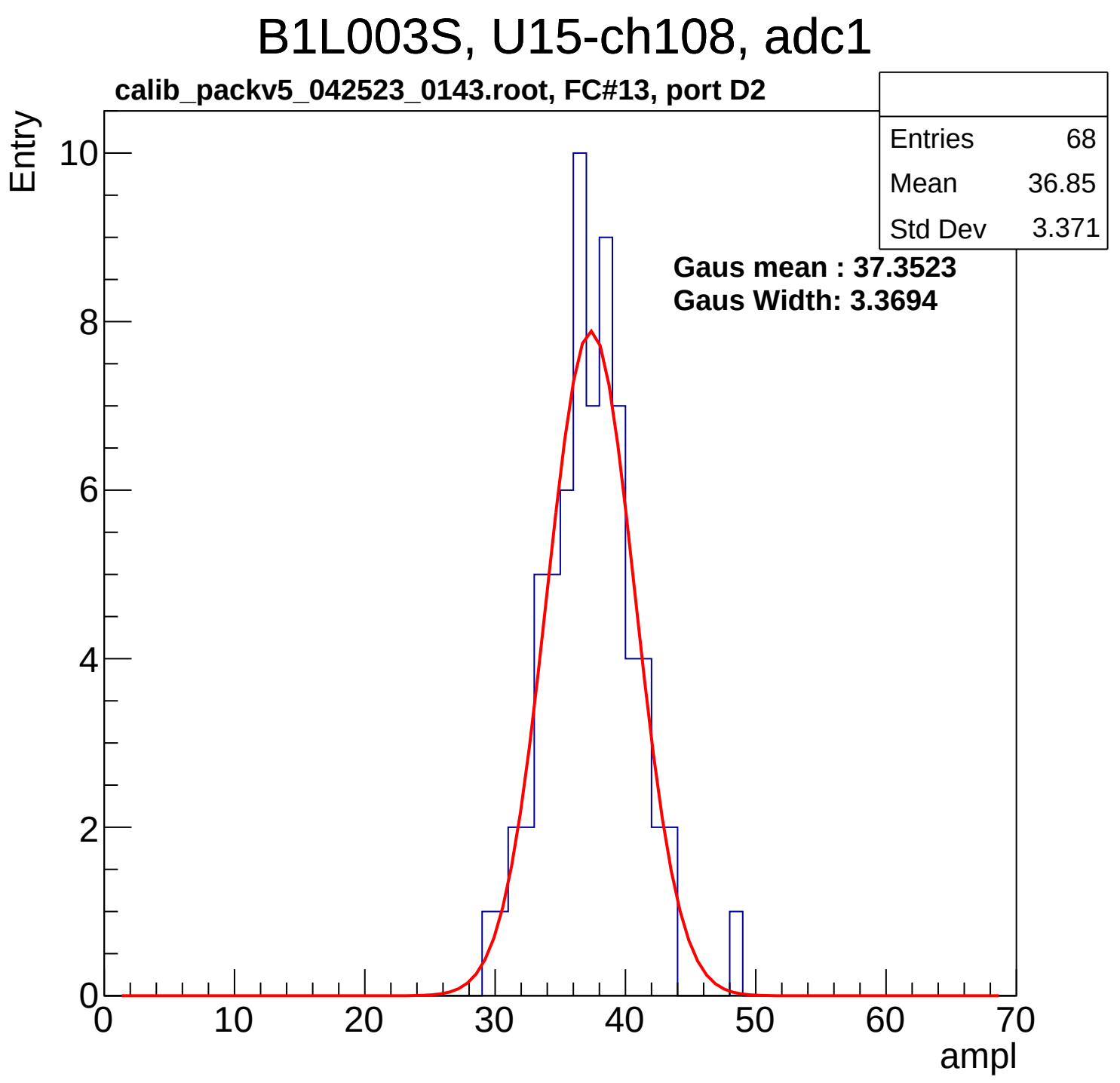
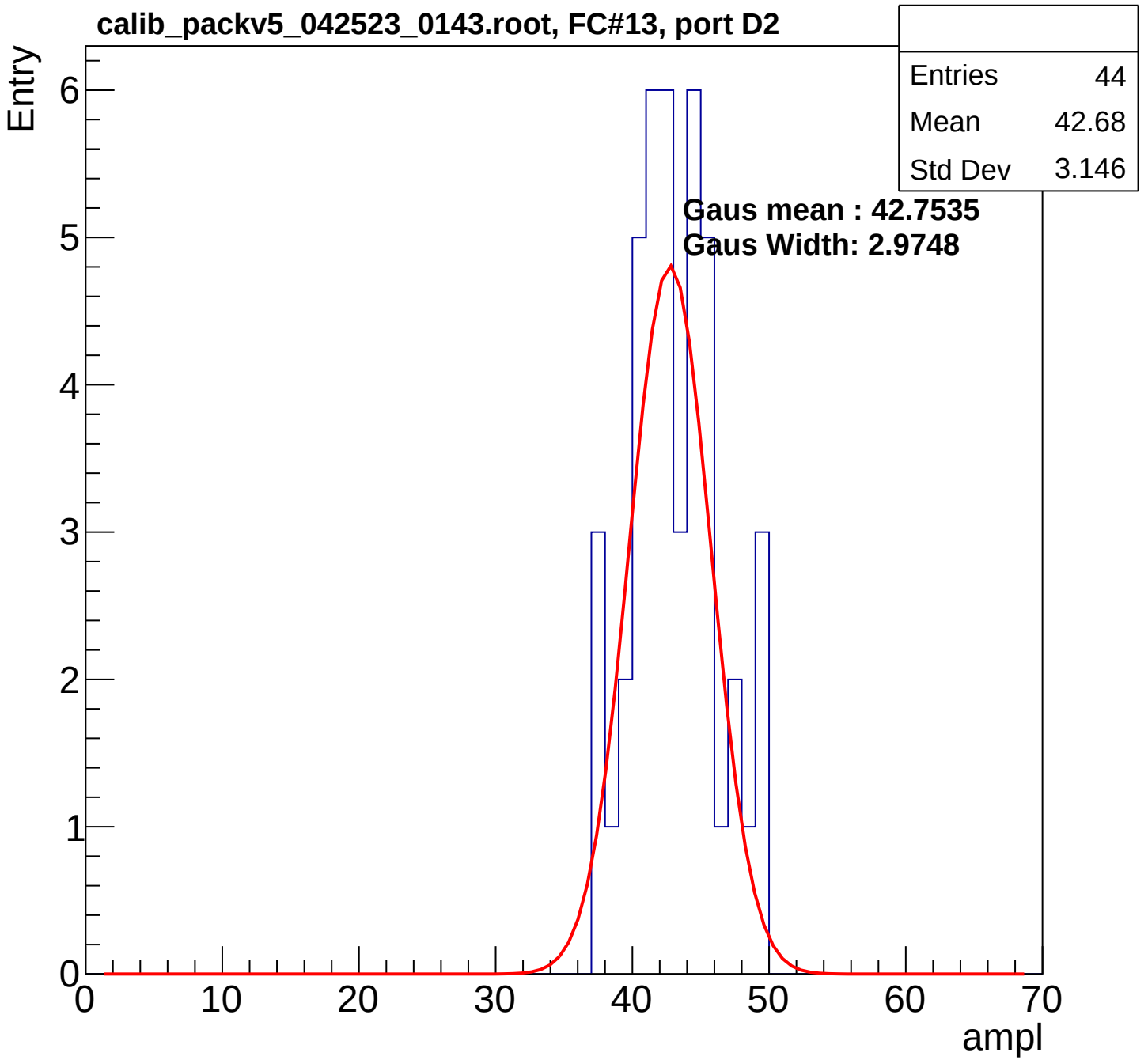
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 30.47
Std Dev 2.656
Gaus mean : 31.1997
Gaus Width: 3.4679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.85
Std Dev 3.371
Gaus mean : 37.3523
Gaus Width: 3.3694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 42.68
Std Dev 3.146
Gaus mean : 42.7535
Gaus Width: 2.9748
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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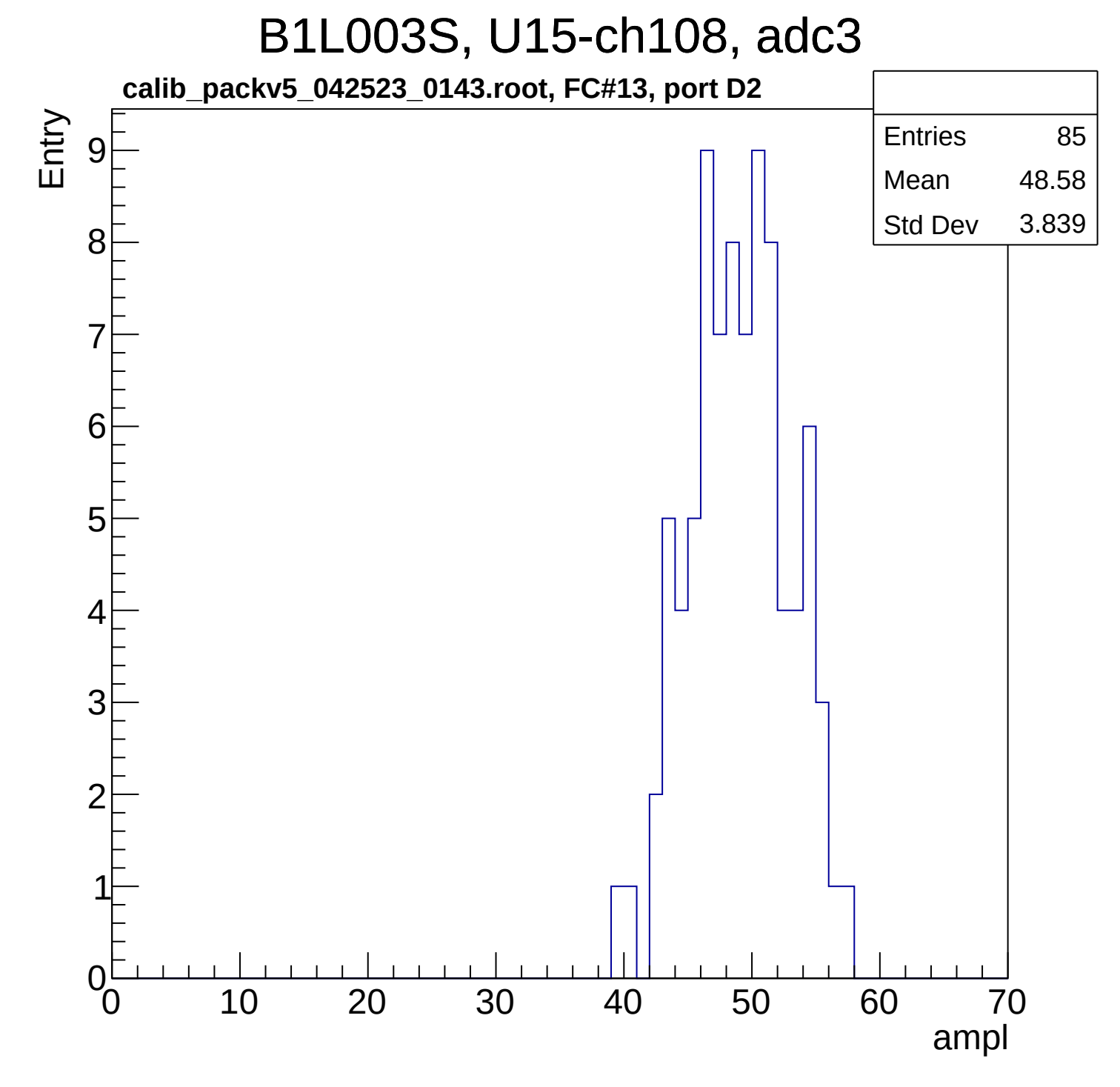
B1L003S, U15-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 85
Mean 48.58
Std Dev 3.839
Entry
ampl
0
1
2
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5
6
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0
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20
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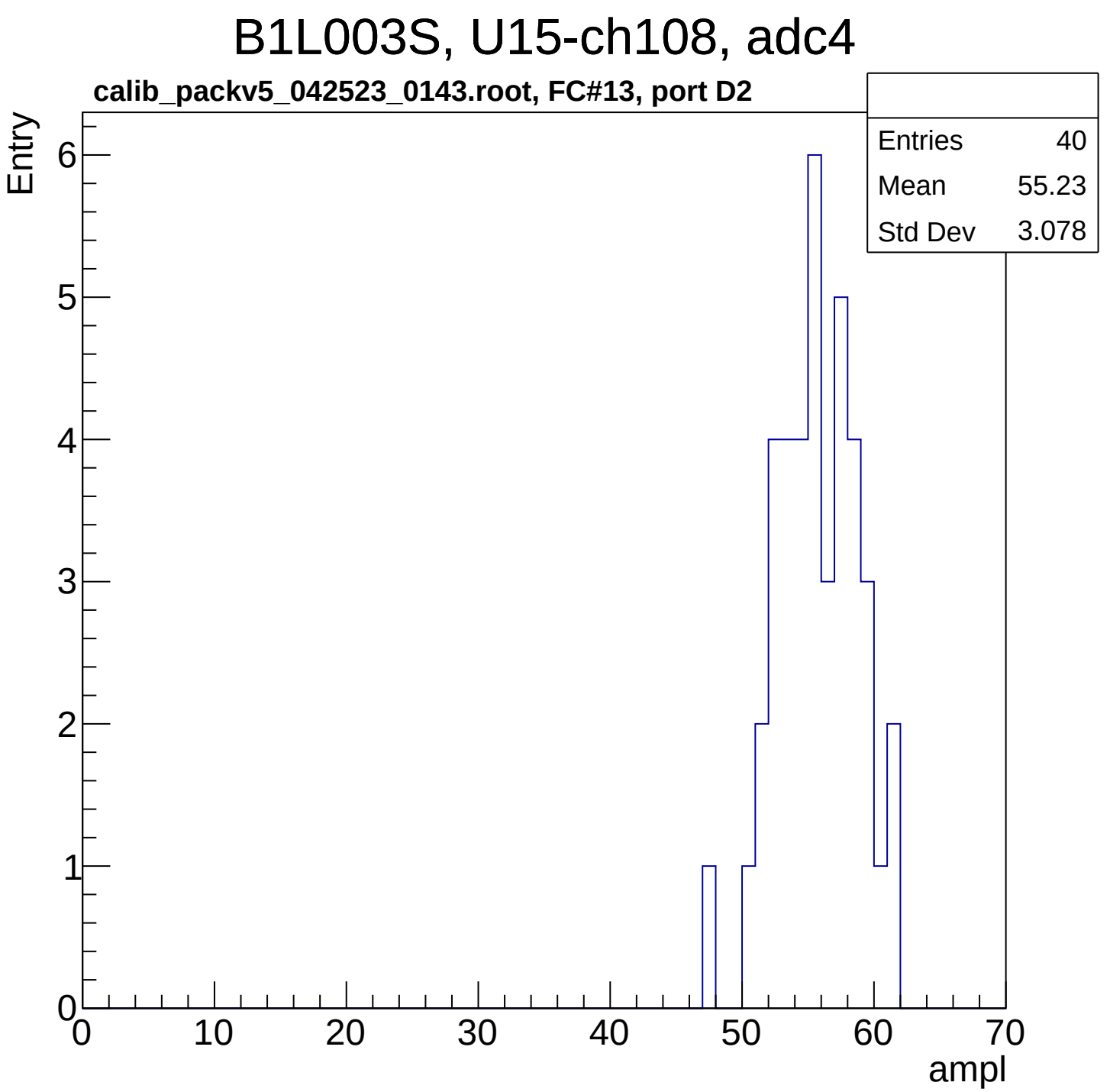
60
70


B1L003S, U15-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 55.23
Std Dev 3.078
Entry
0
1
2
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5
6
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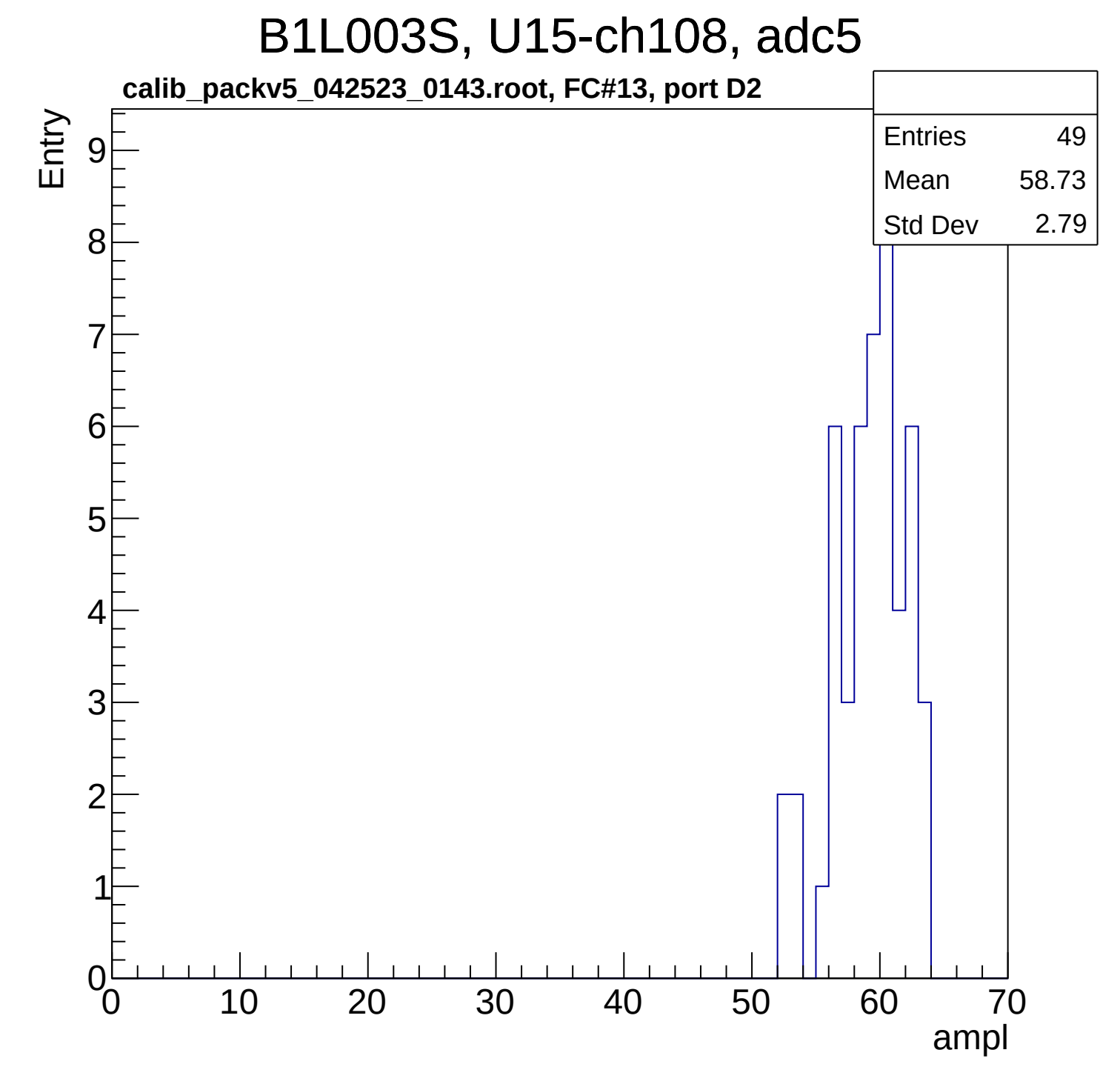
60
70
ampl


B1L003S, U15-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.73
Std Dev 2.79
Entry
ampl
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2
3
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6
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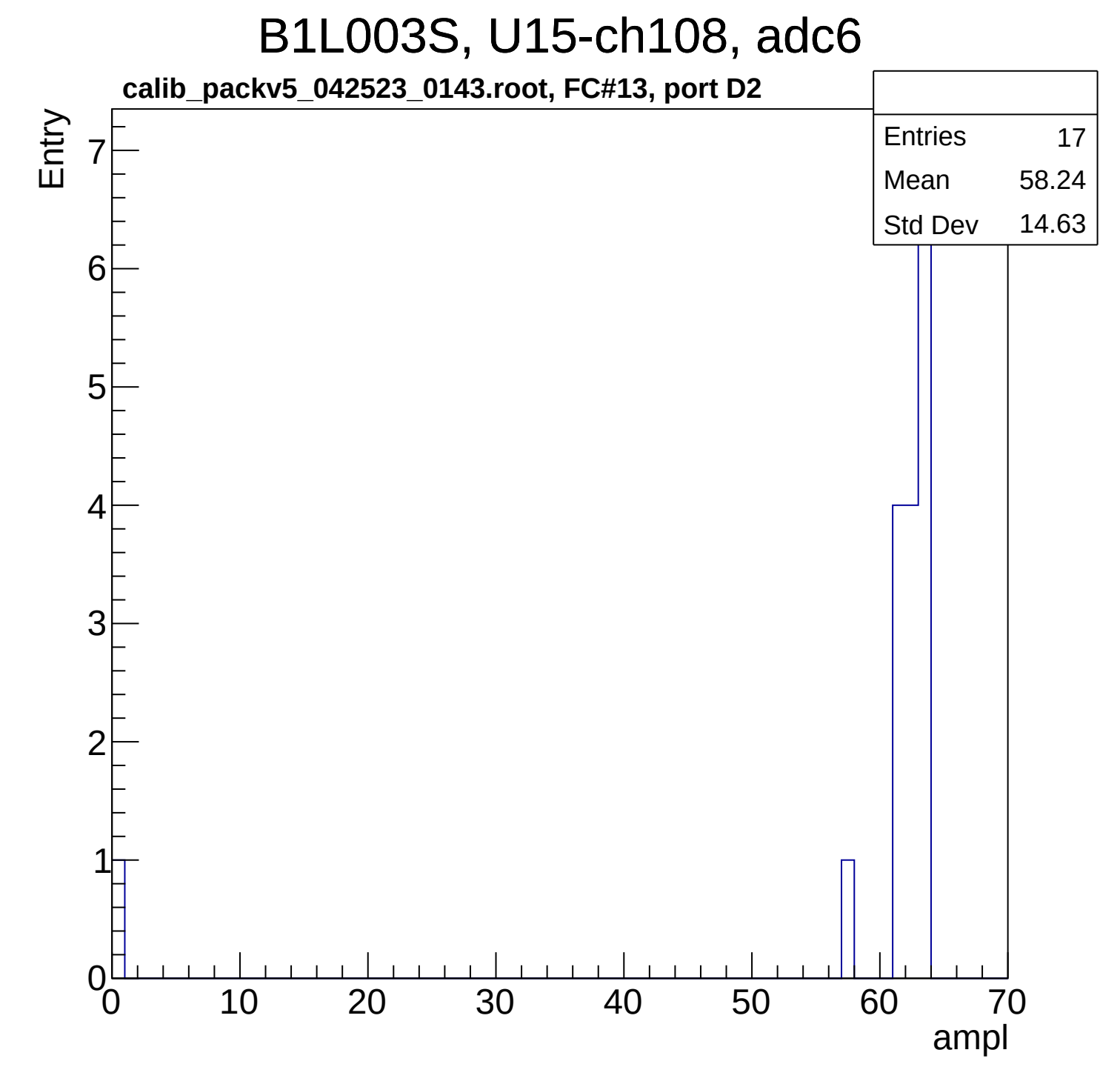
B1L003S, U15-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 58.24
Std Dev 14.63
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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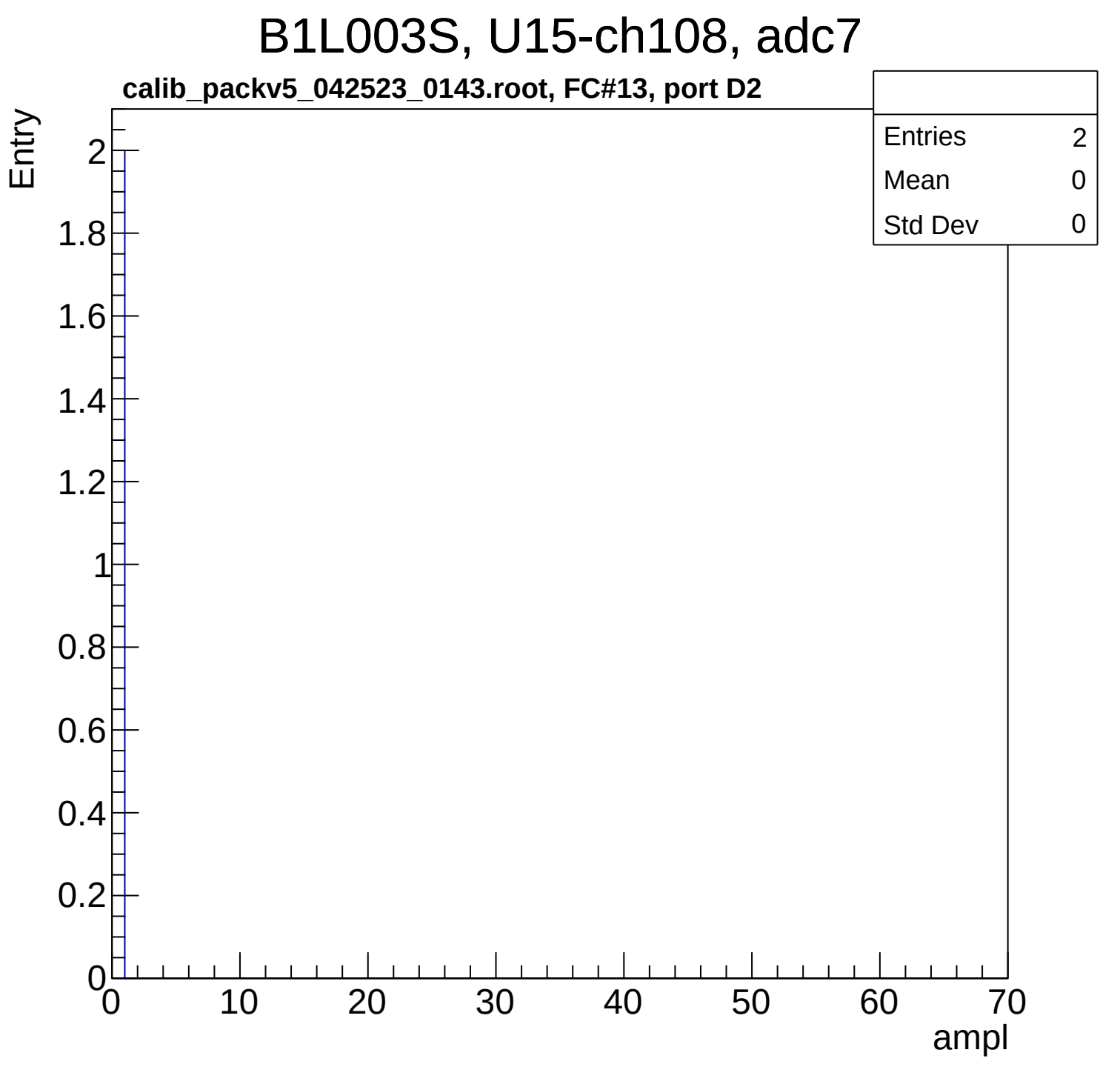
B1L003S, U15-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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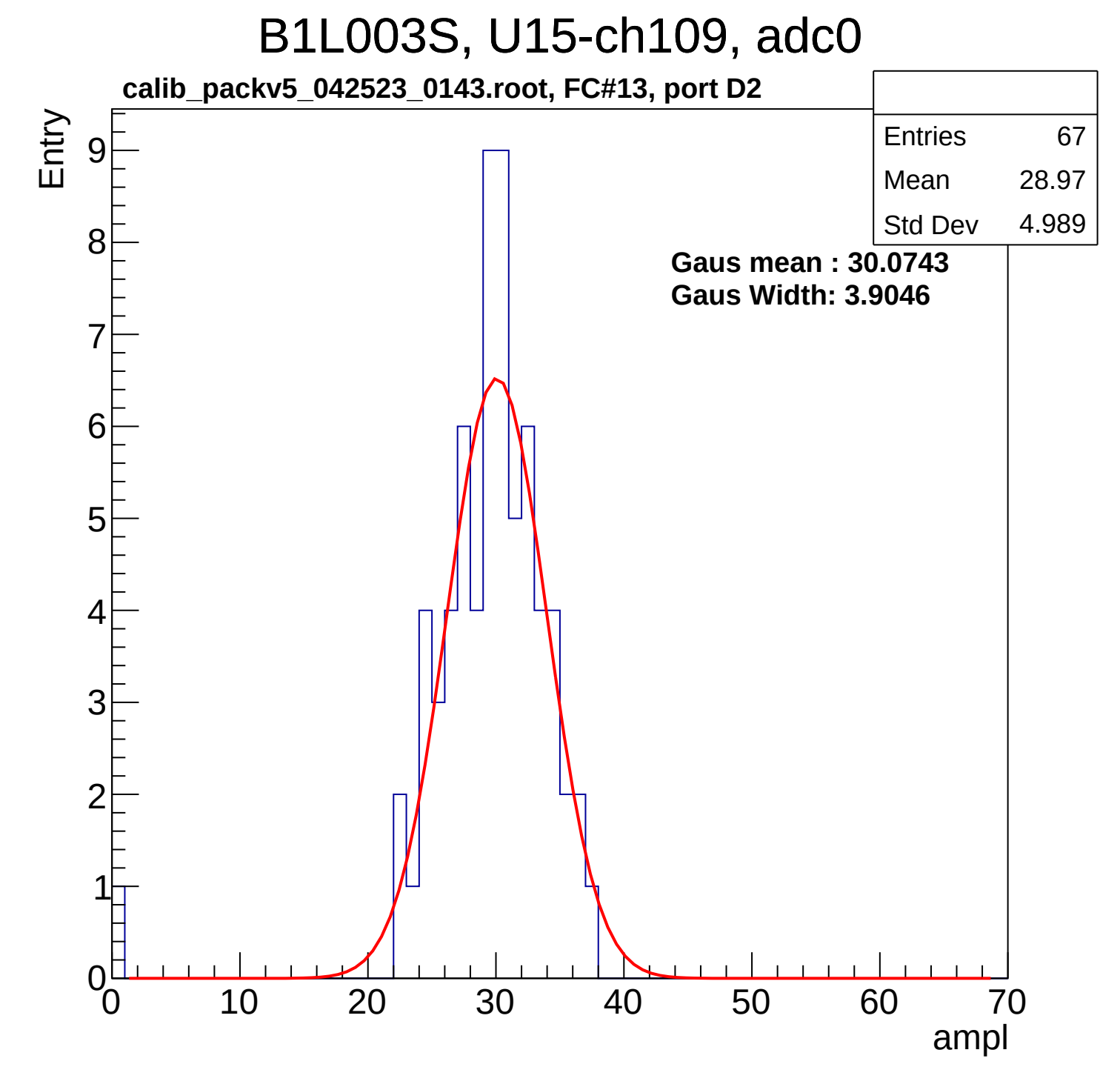
B1L003S, U15-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.97
Std Dev 4.989
Gaus mean : 30.0743
Gaus Width: 3.9046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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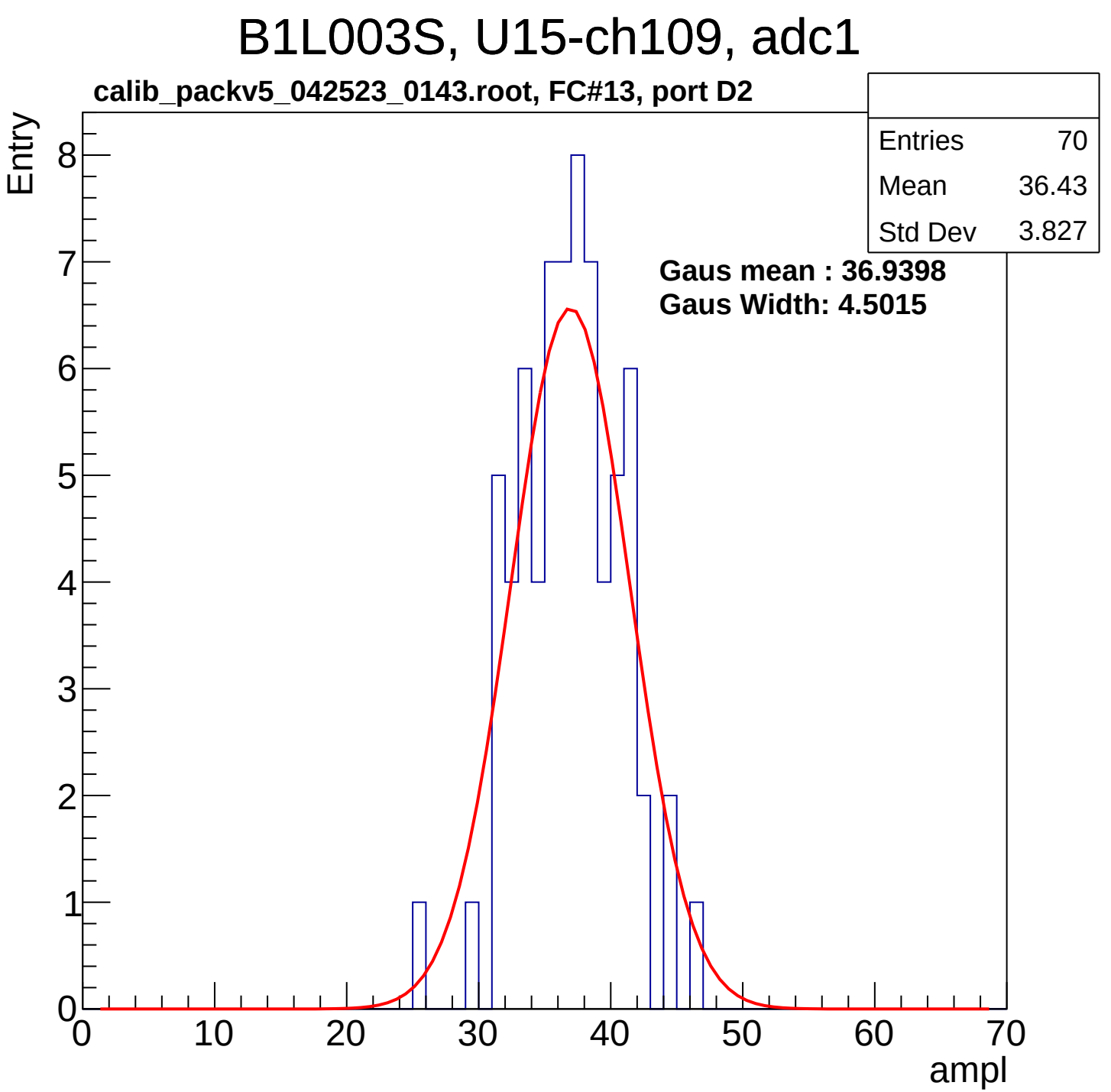
B1L003S, U15-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.43
Std Dev 3.827
Gaus mean : 36.9398
Gaus Width: 4.5015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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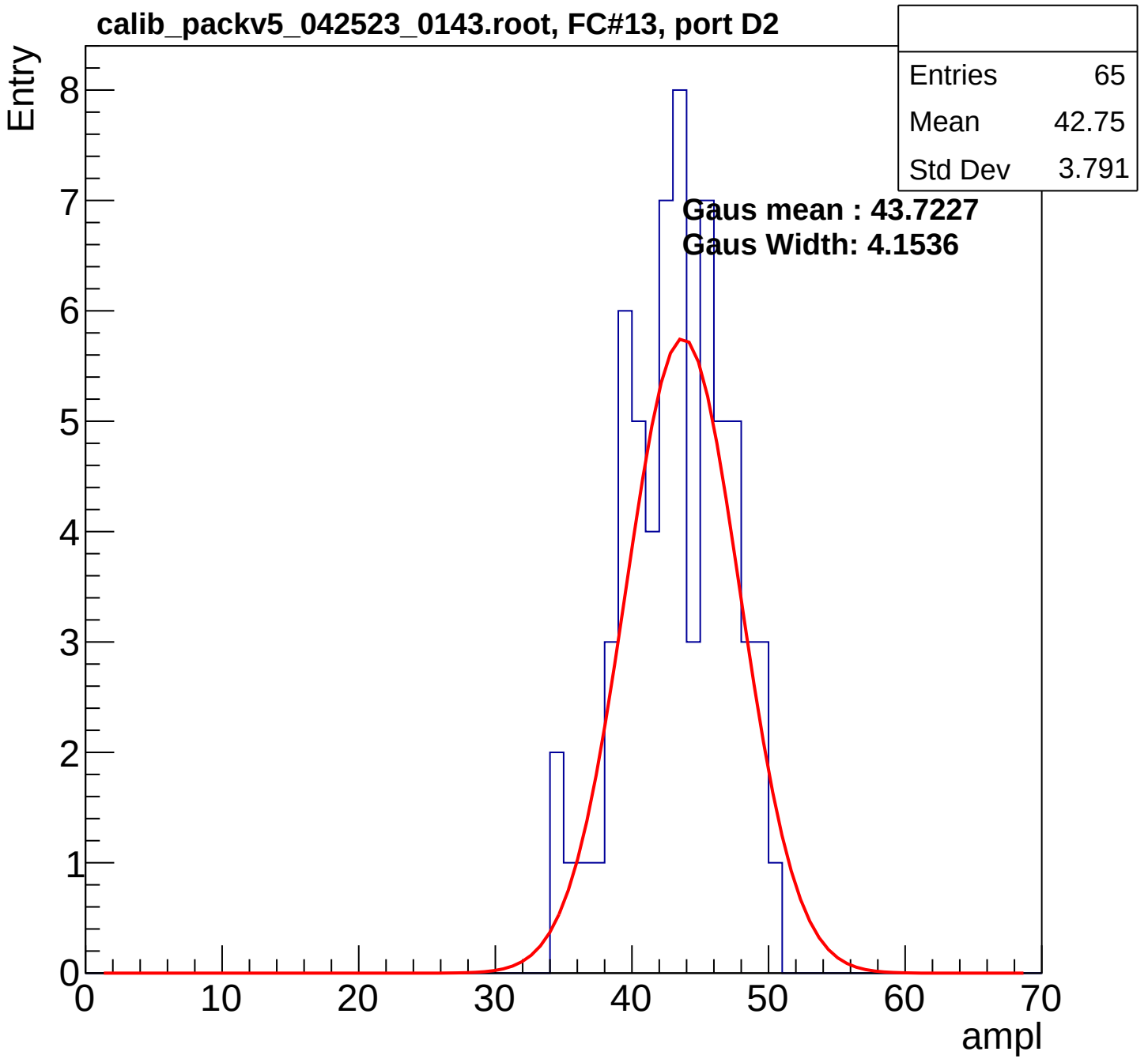
B1L003S, U15-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.75
Std Dev 3.791
Gaus mean : 43.7227
Gaus Width: 4.1536
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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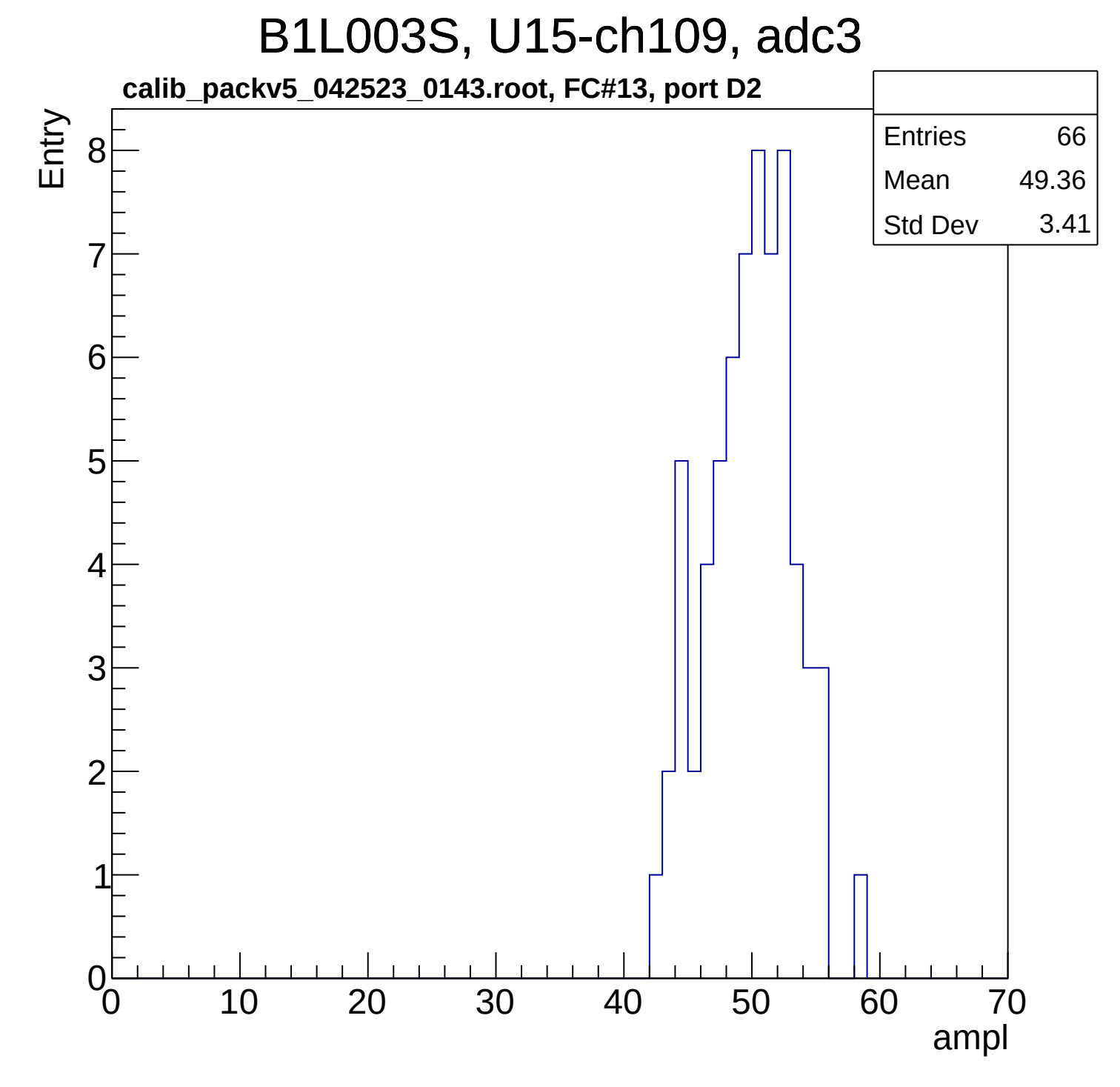
B1L003S, U15-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.36
Std Dev 3.41
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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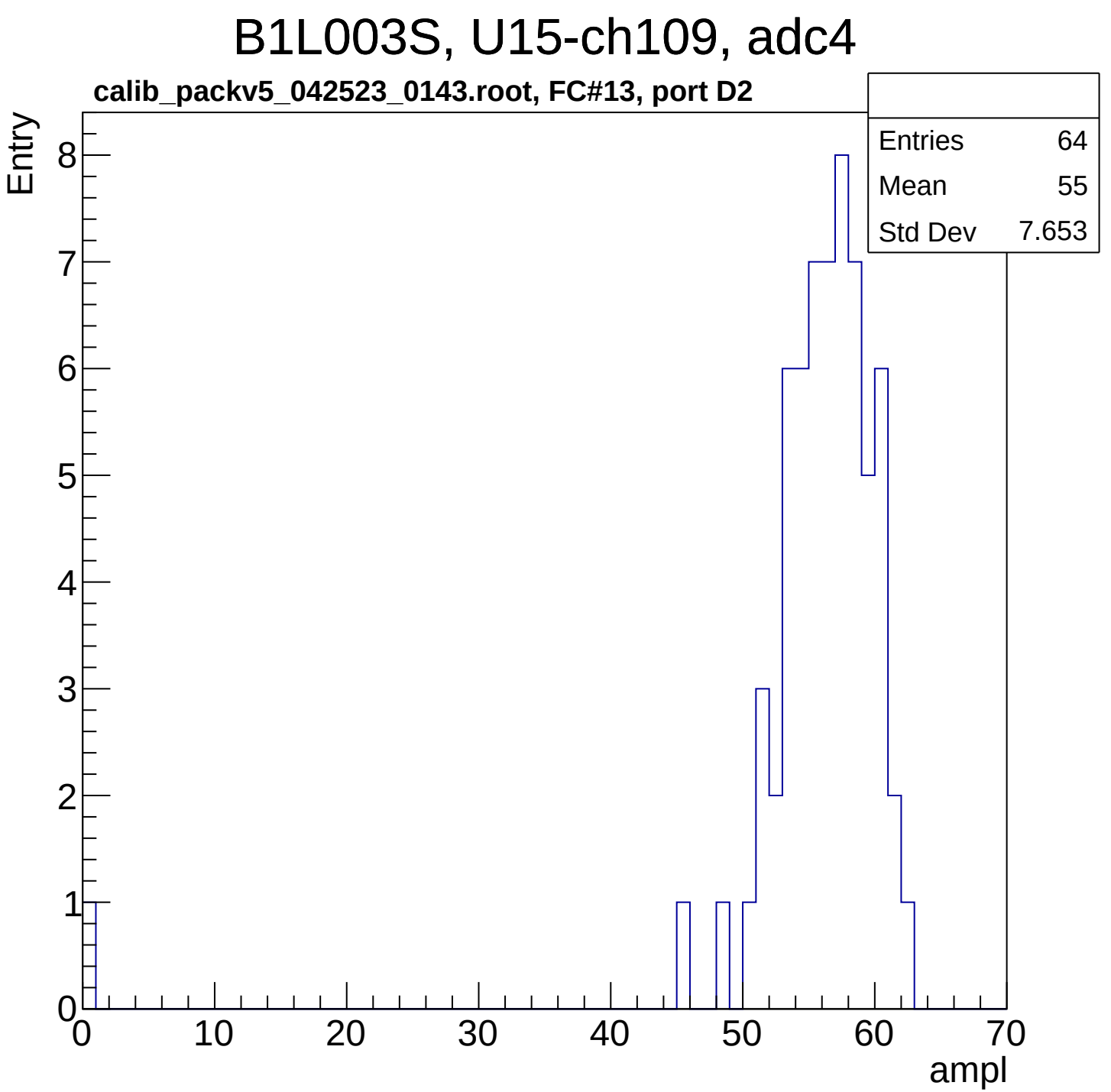
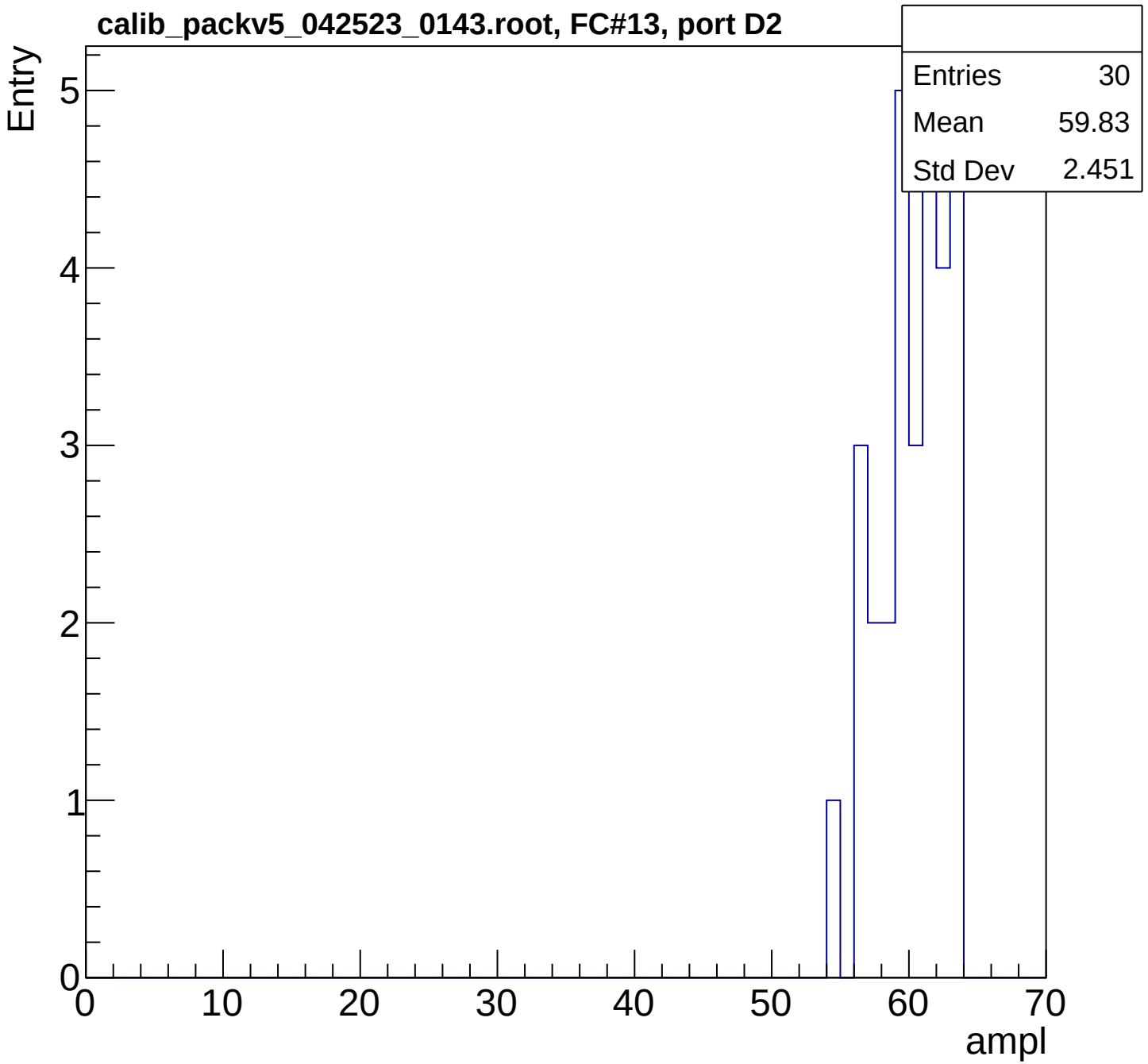
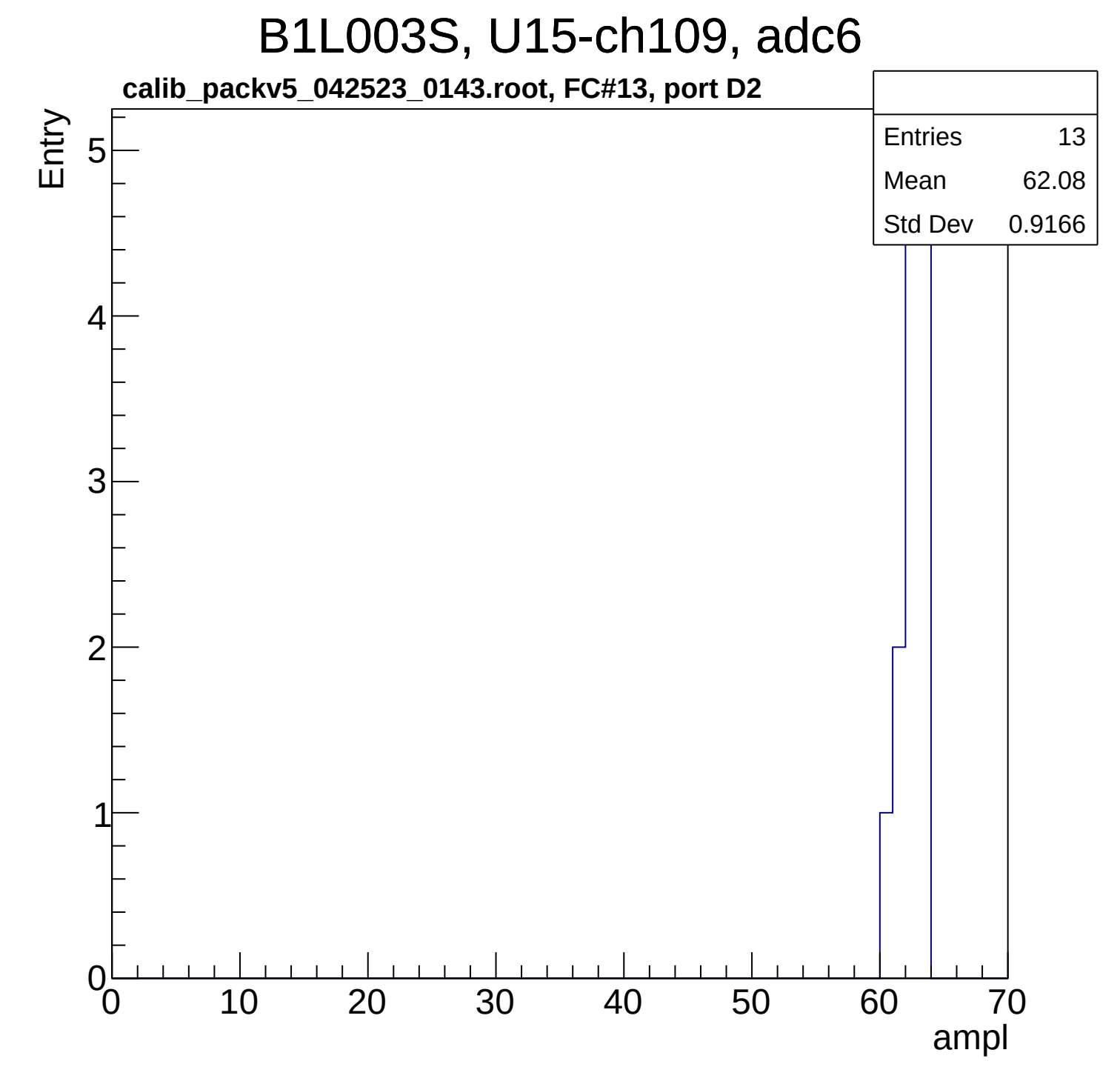
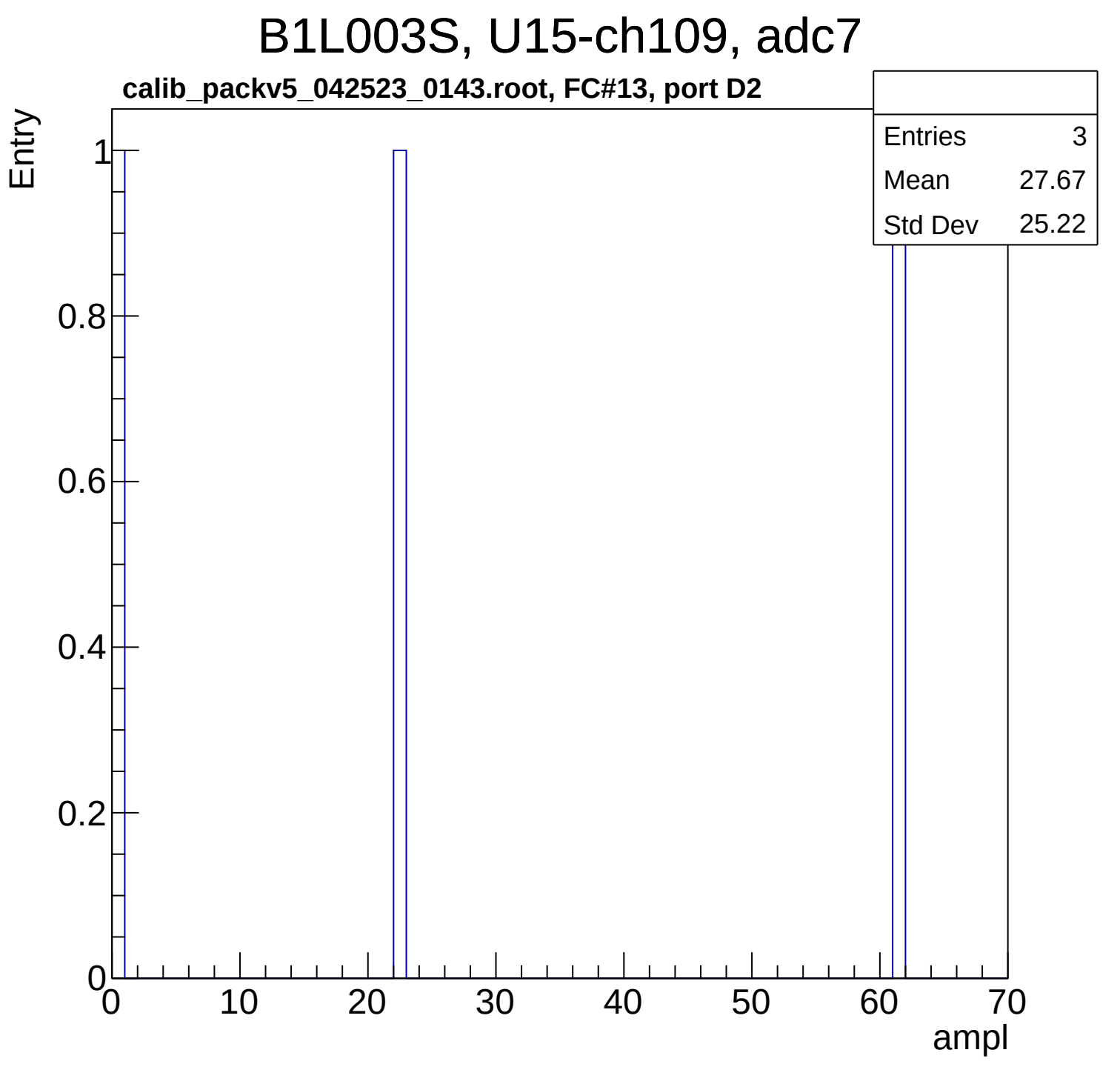

B1L003S, U15-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 55
Std Dev 7.653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 59.83
Std Dev 2.451
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 62.08
Std Dev 0.9166
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 27.67
Std Dev 25.22
Entry
ampl
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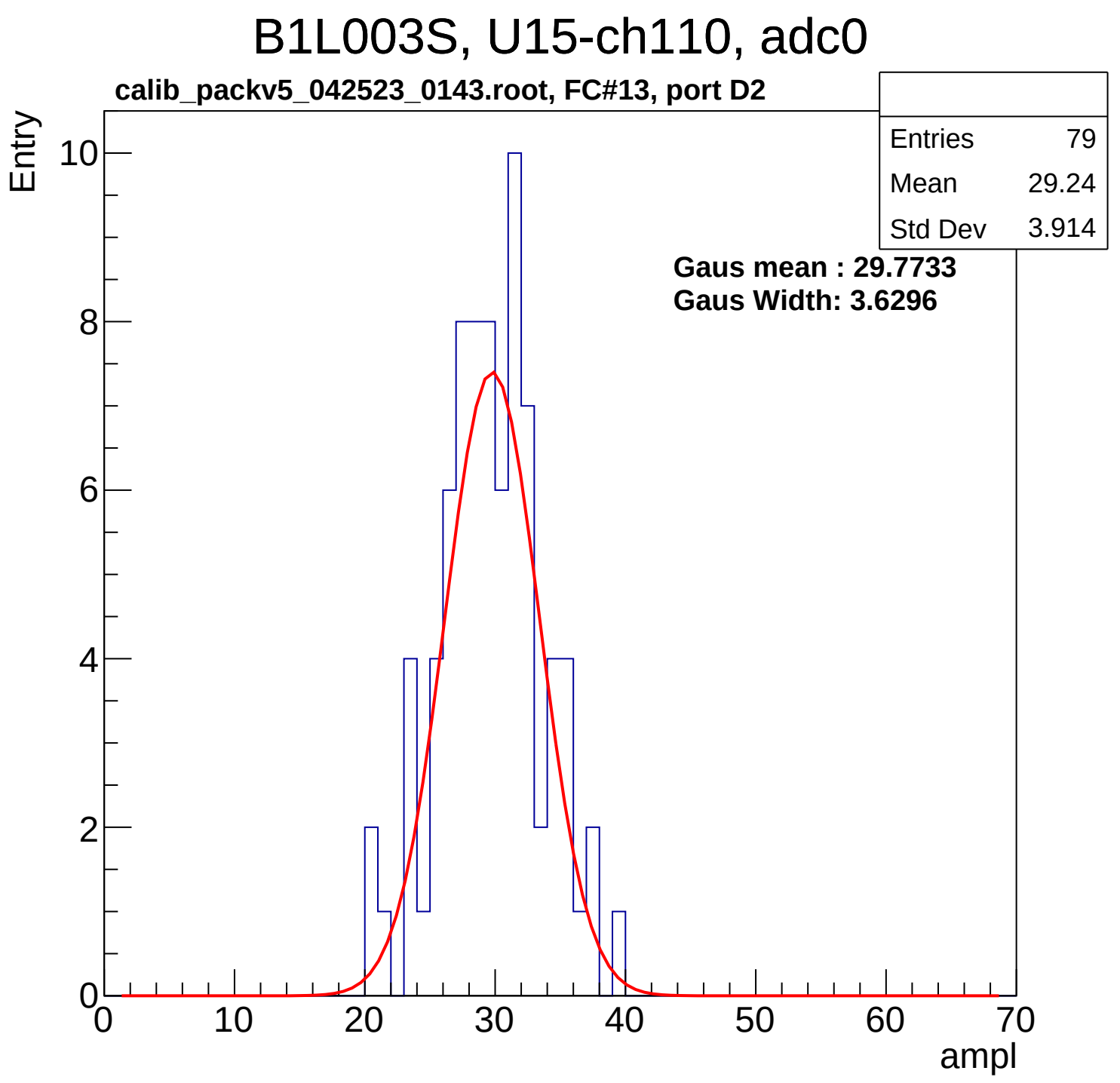
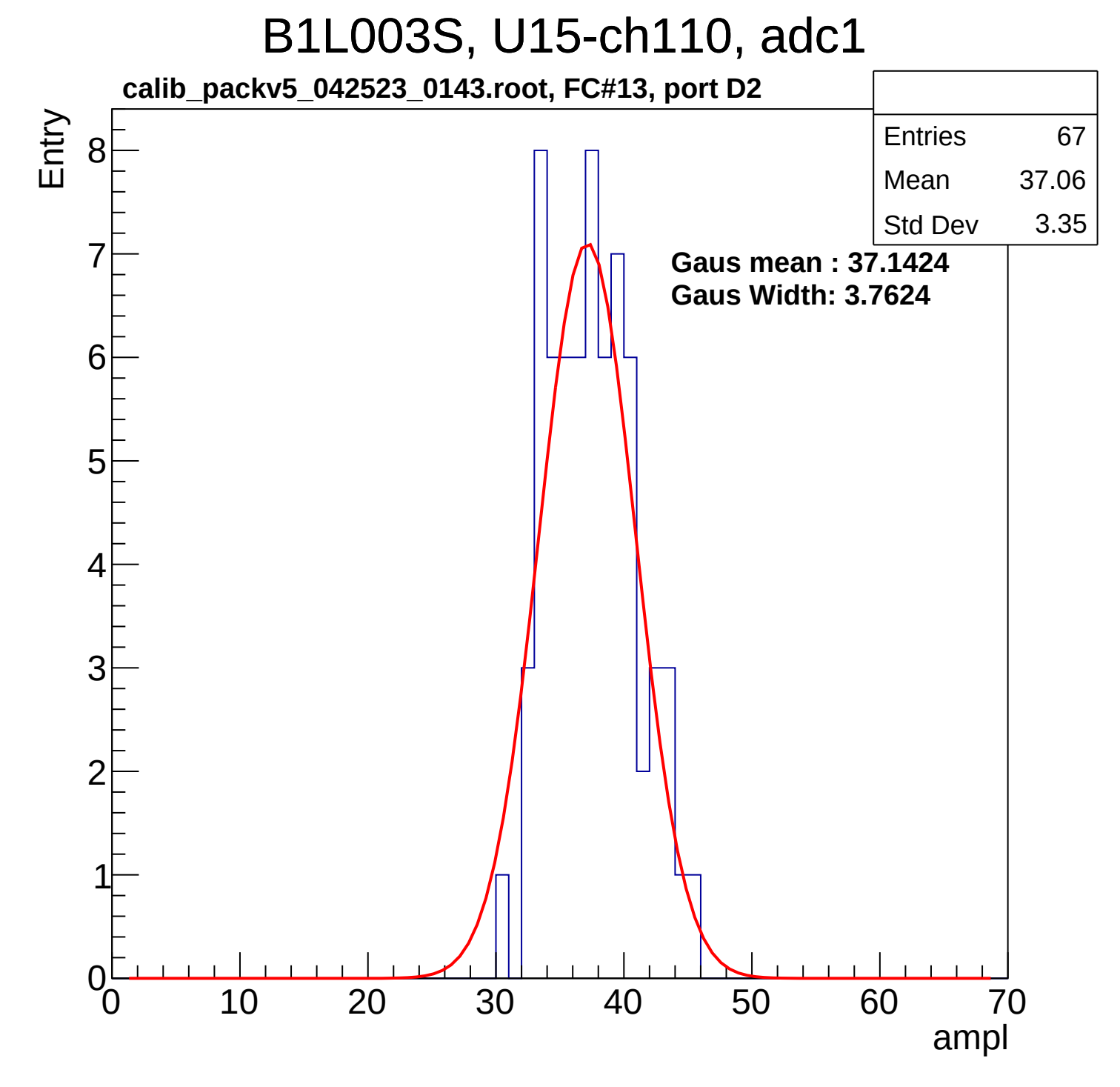
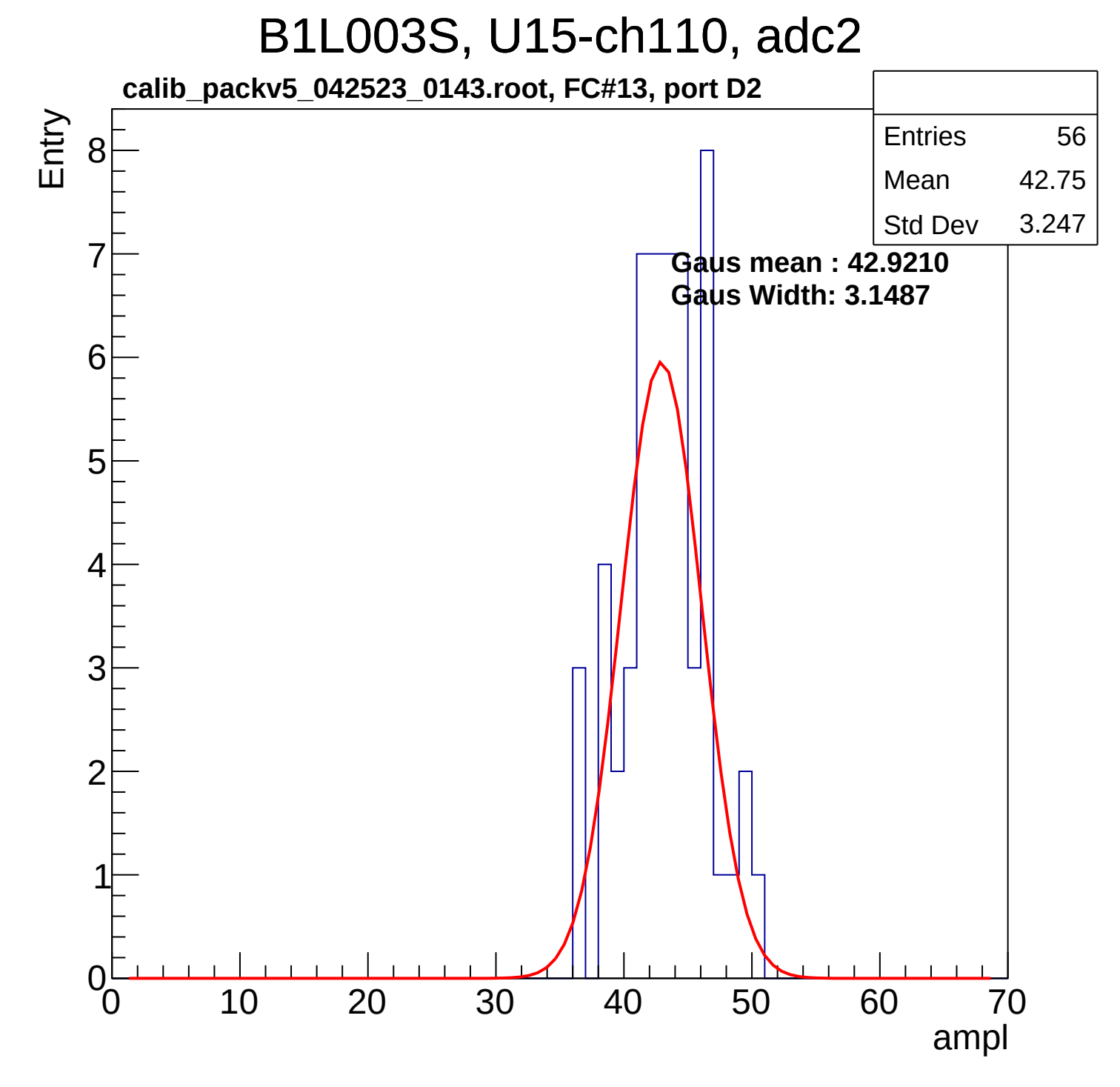
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L003S, U15-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 29.24
Std Dev 3.914
Gaus mean : 29.7733
Gaus Width: 3.6296
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 37.06
Std Dev 3.35
Gaus mean : 37.1424
Gaus Width: 3.7624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.75
Std Dev 3.247
Gaus mean : 42.9210
Gaus Width: 3.1487
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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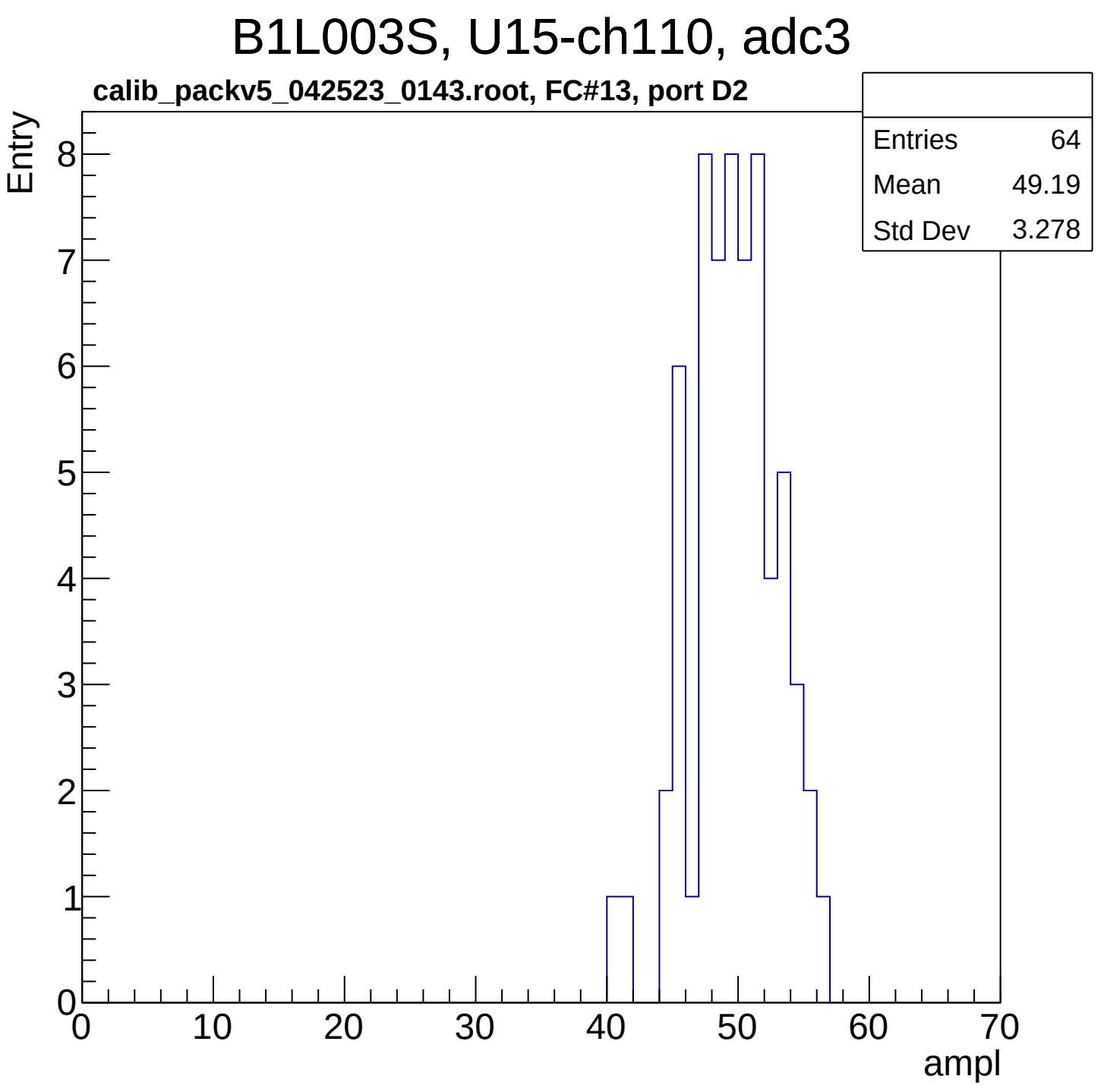
B1L003S, U15-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.19
Std Dev 3.278
Entry
ampl
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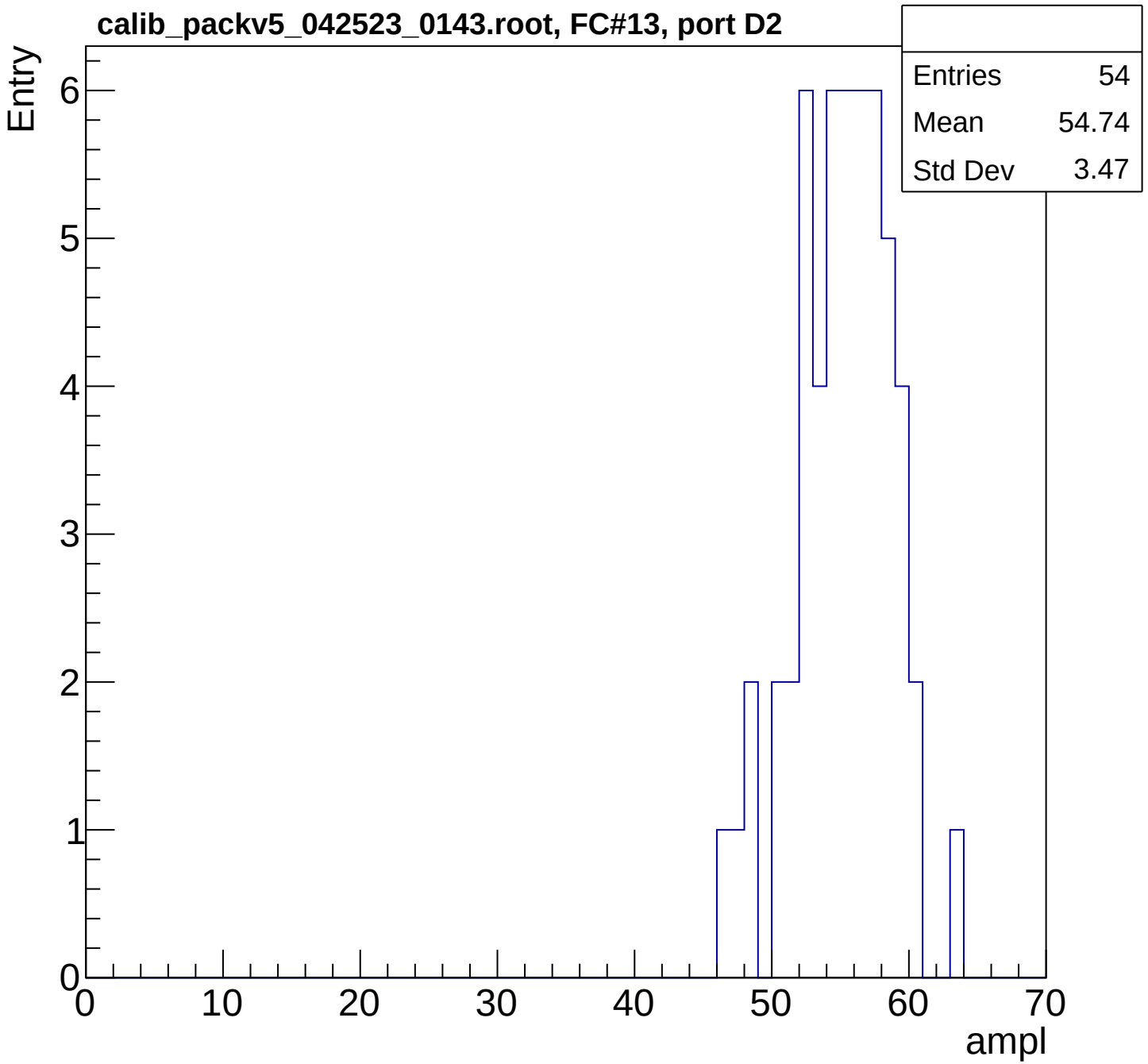
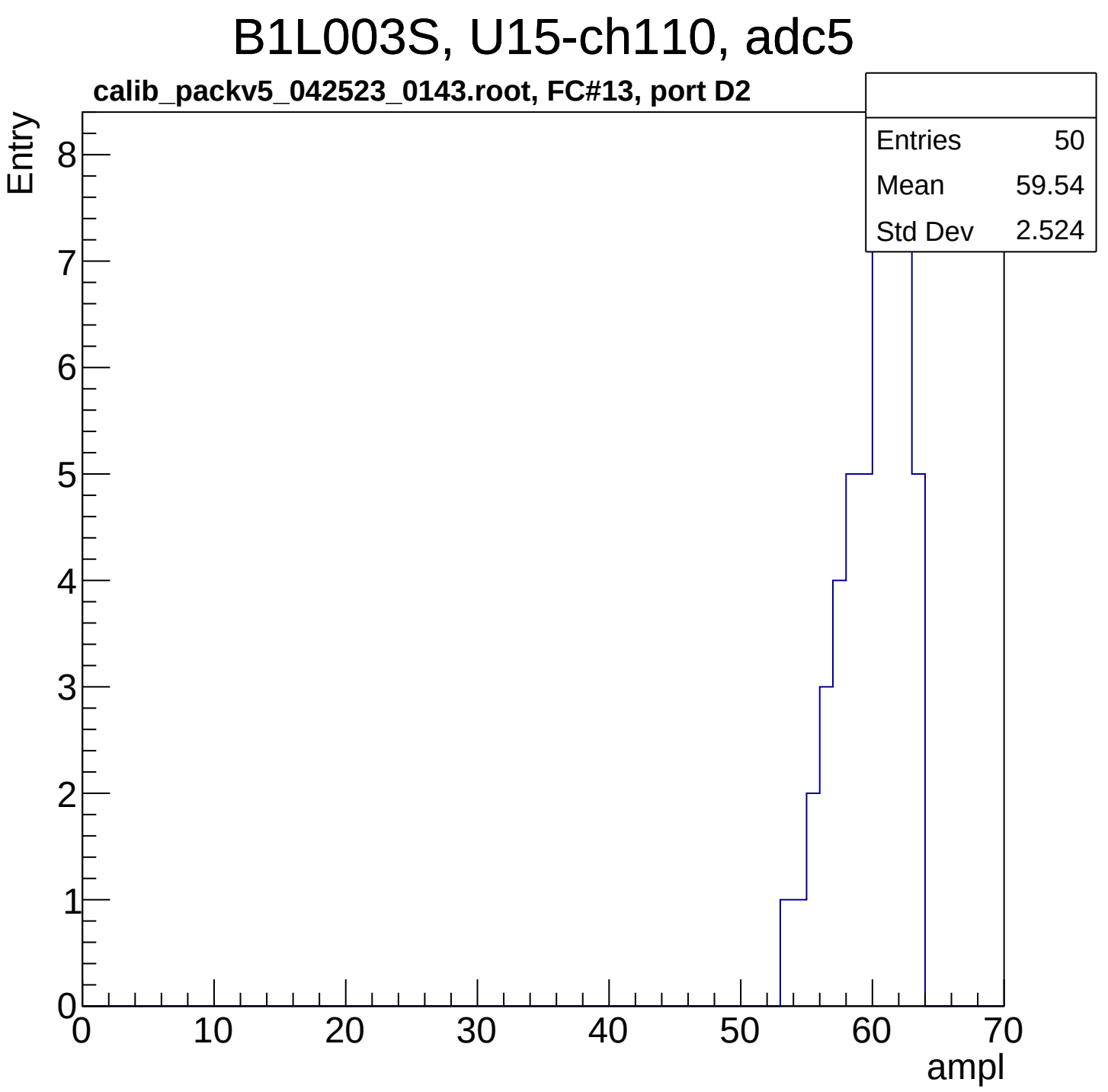
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 54.74
Std Dev 3.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.54
Std Dev 2.524
Entry
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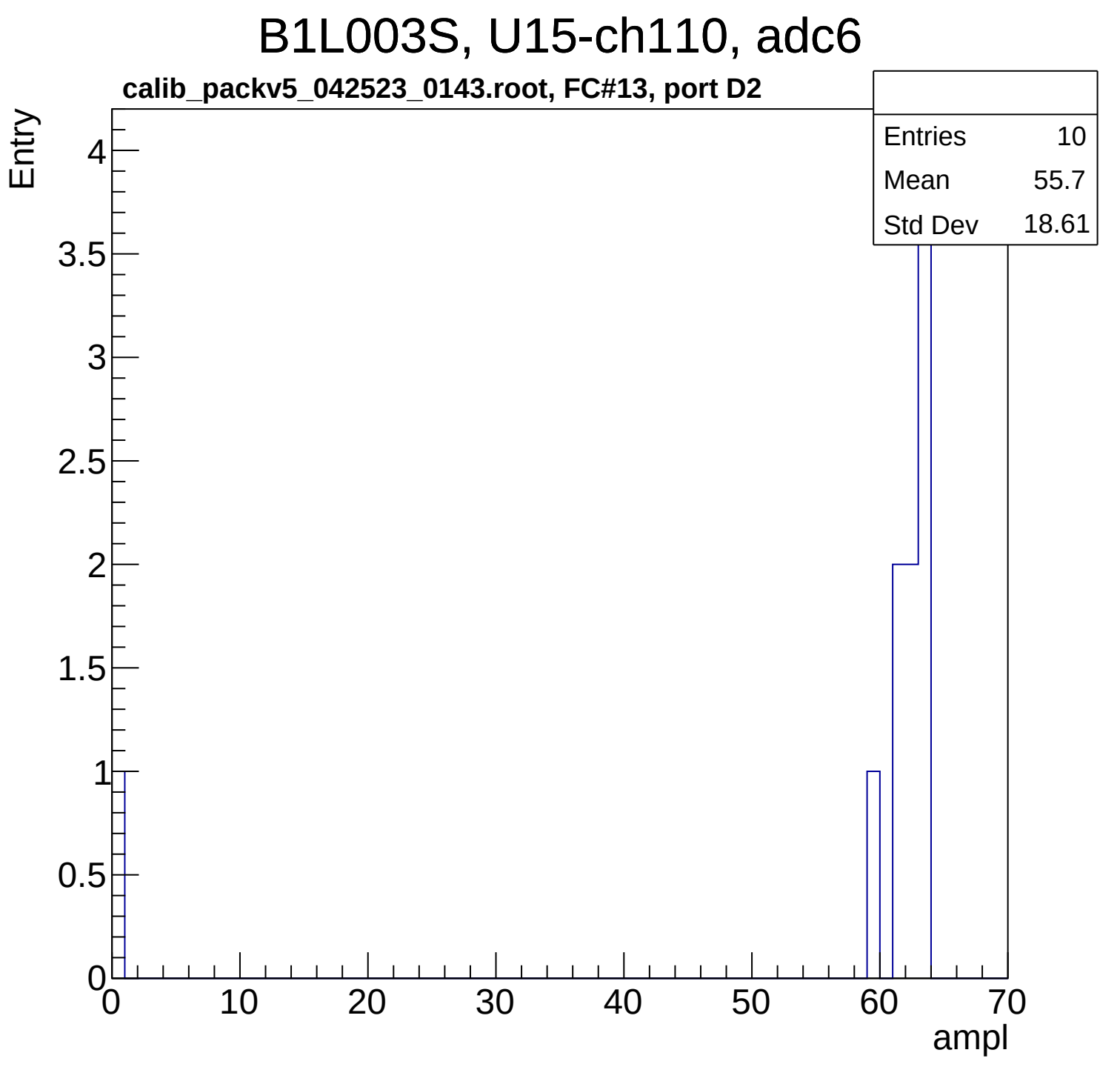
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.7
Std Dev 18.61
Entry
ampl
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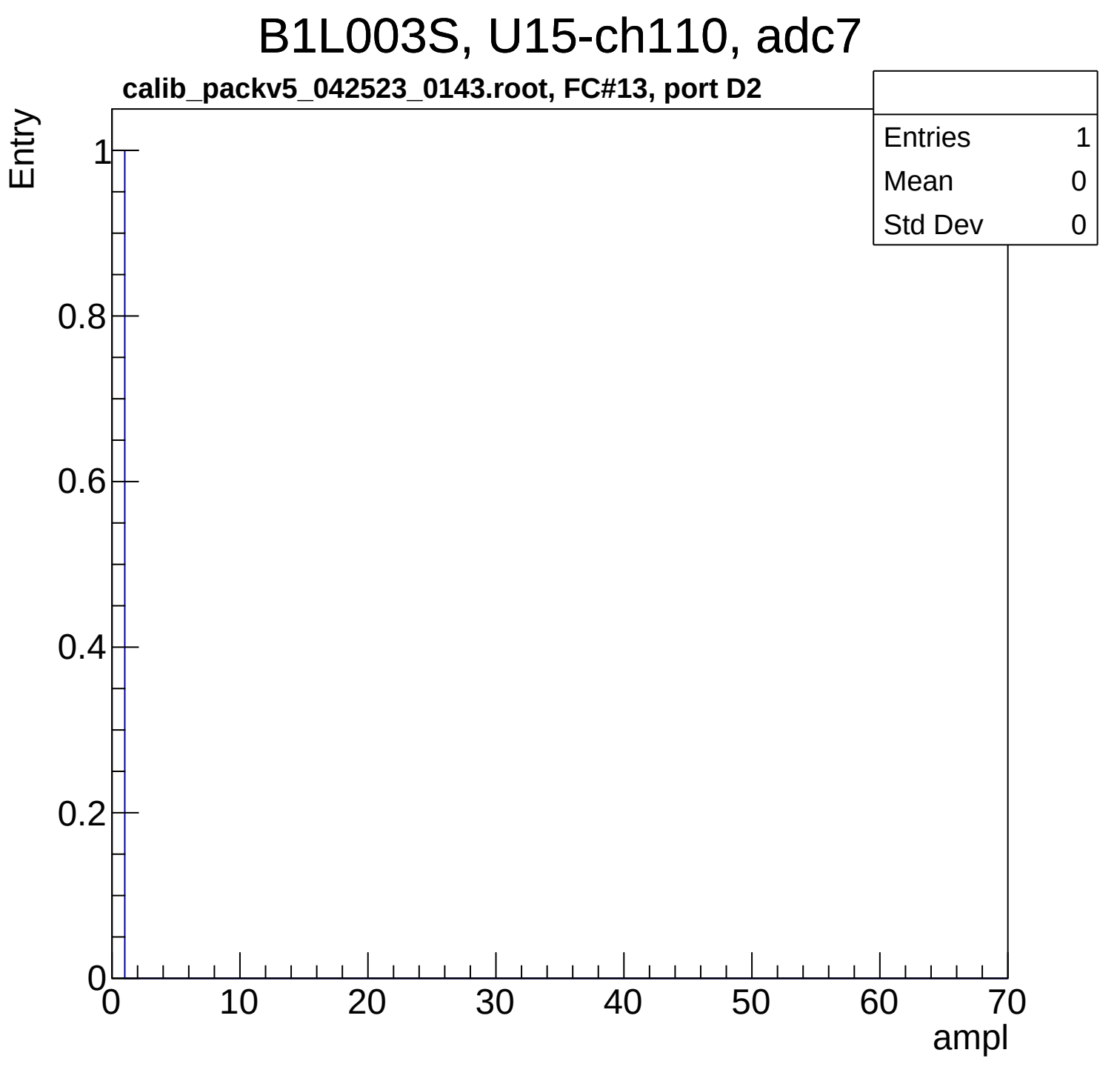
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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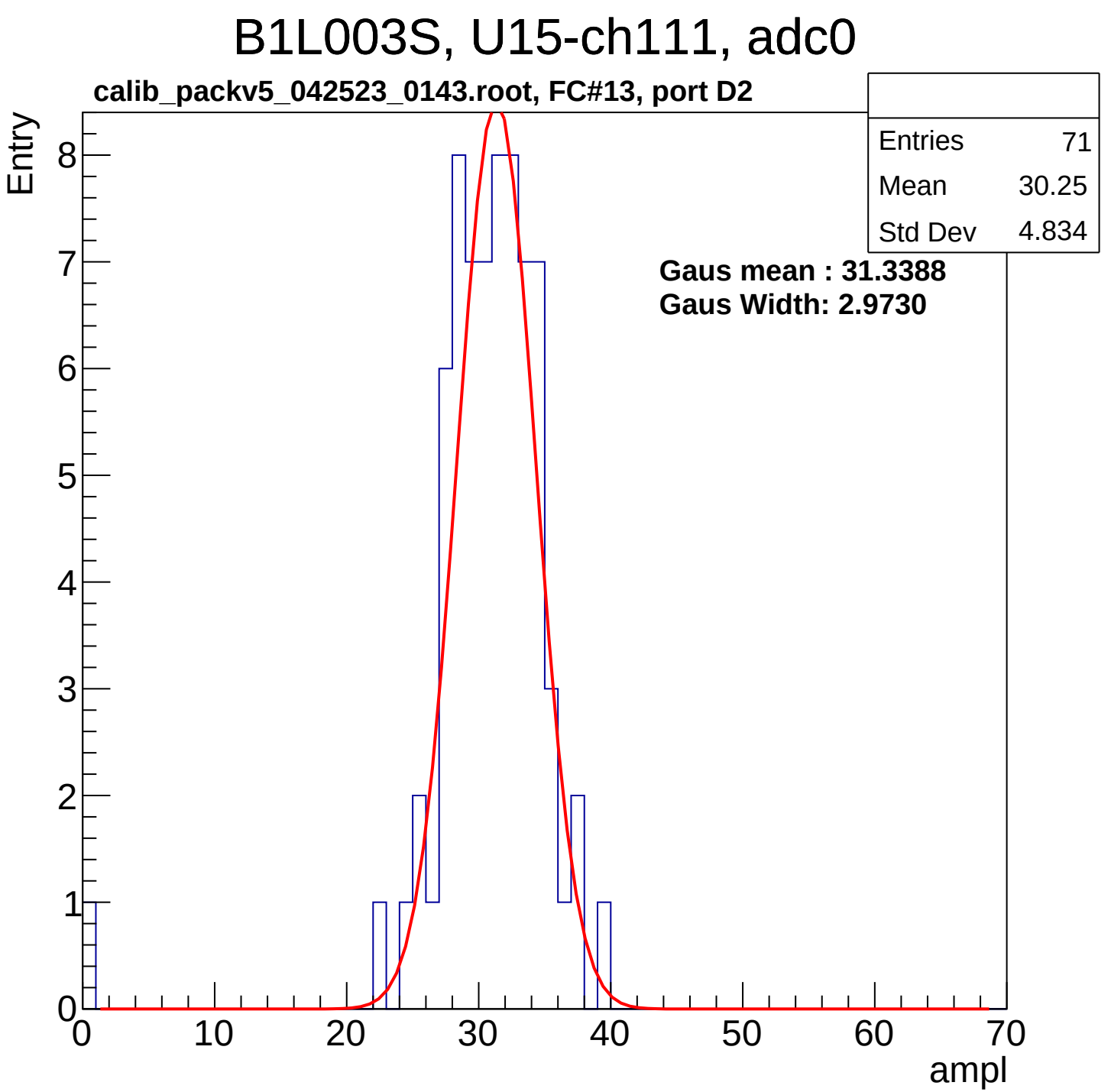
70
ampl


B1L003S, U15-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 30.25
Std Dev 4.834
Gaus mean : 31.3388
Gaus Width: 2.9730
Entry
ampl
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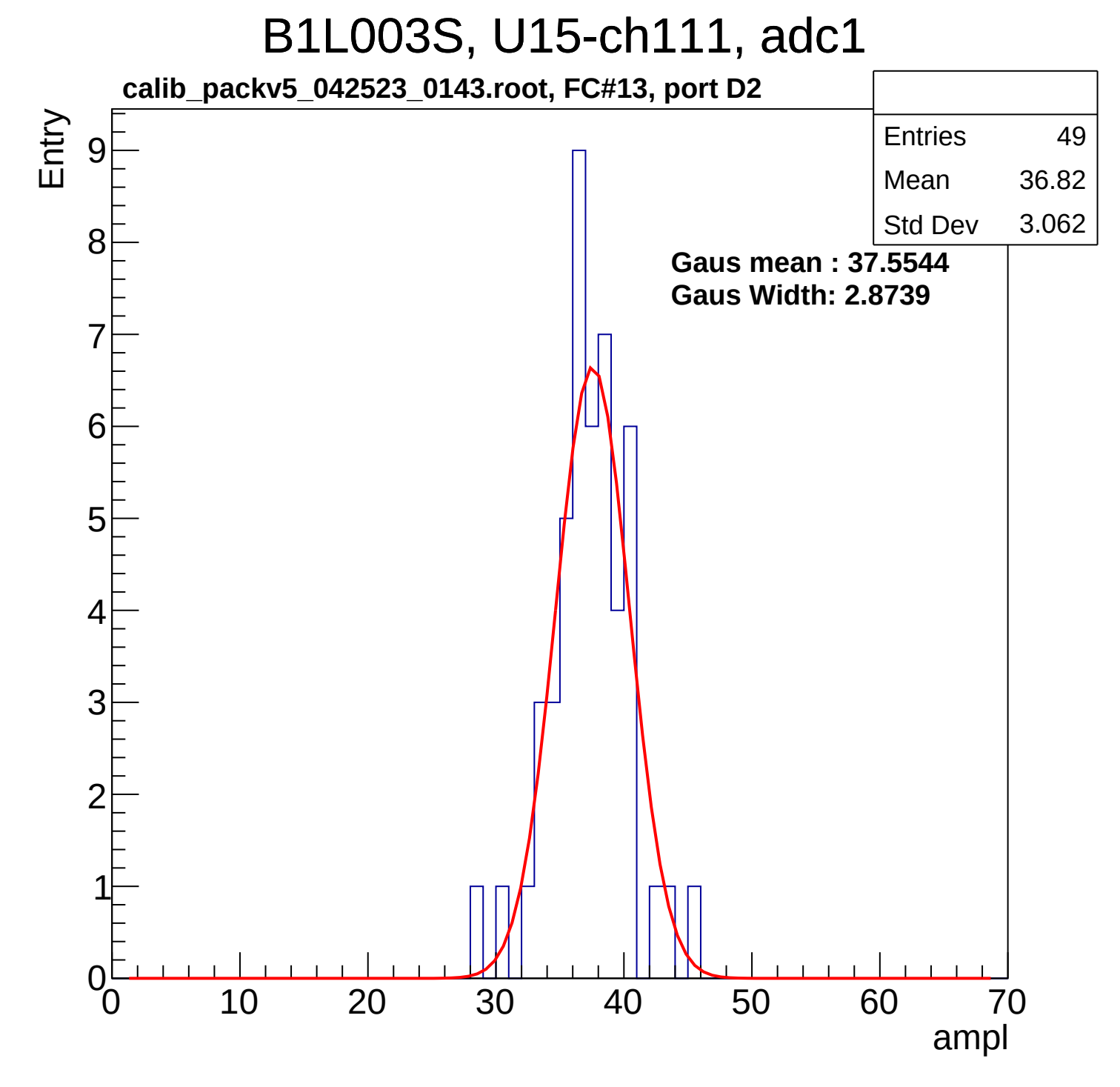
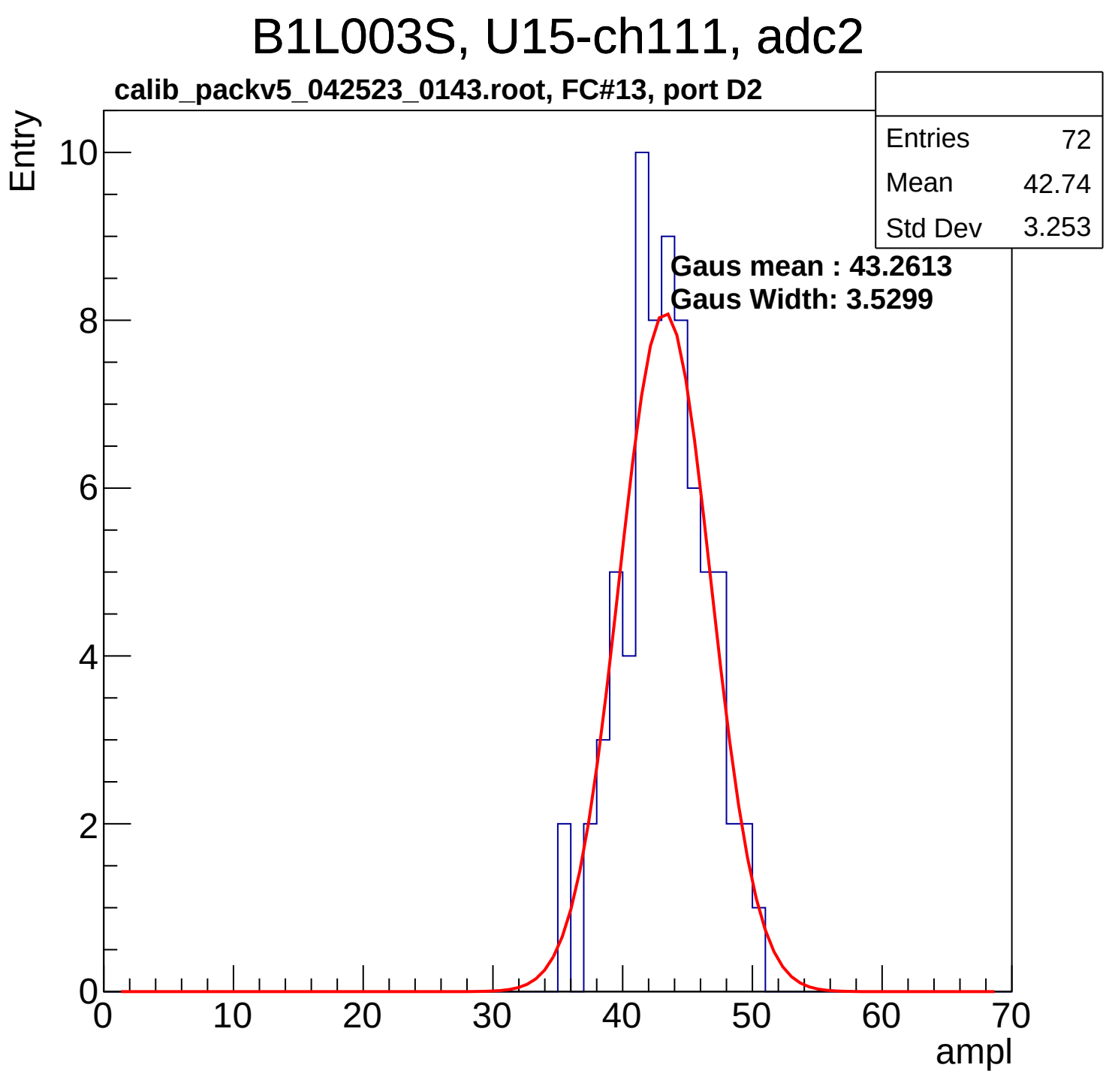
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 36.82
Std Dev 3.062
Gaus mean : 37.5544
Gaus Width: 2.8739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.74
Std Dev 3.253
Gaus mean : 43.2613
Gaus Width: 3.5299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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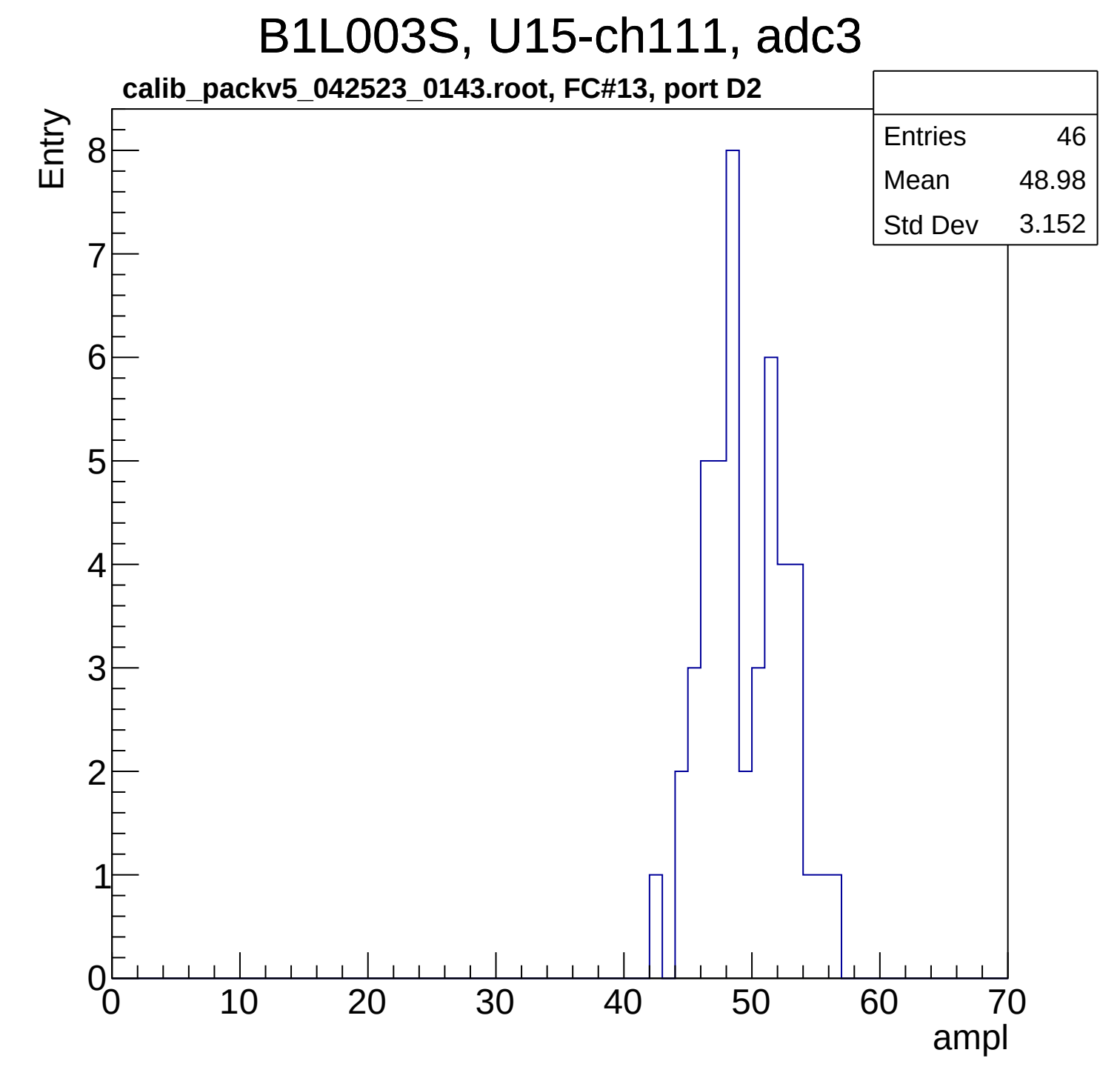
B1L003S, U15-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 48.98
Std Dev 3.152
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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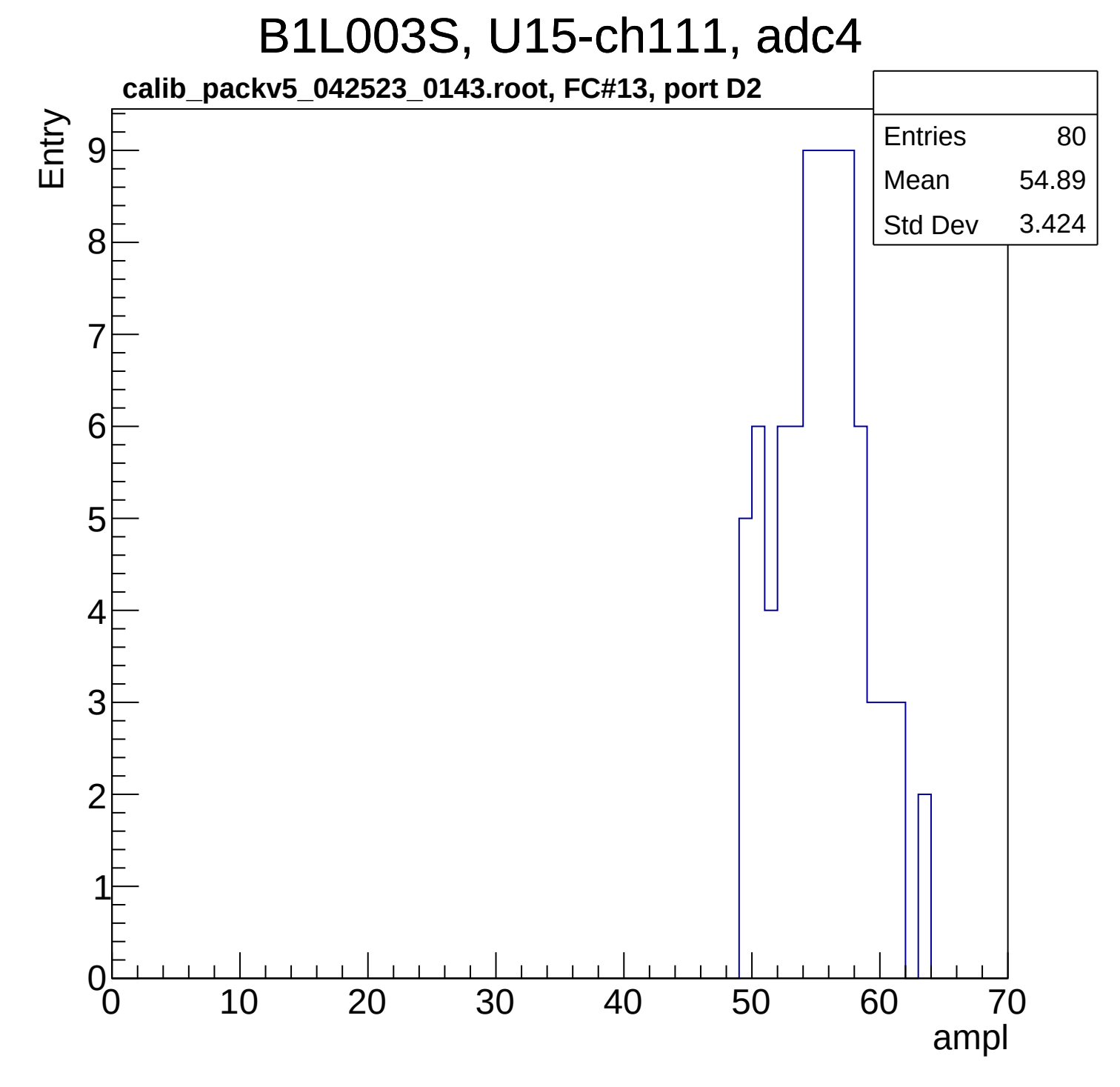
60
70


B1L003S, U15-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 54.89
Std Dev 3.424
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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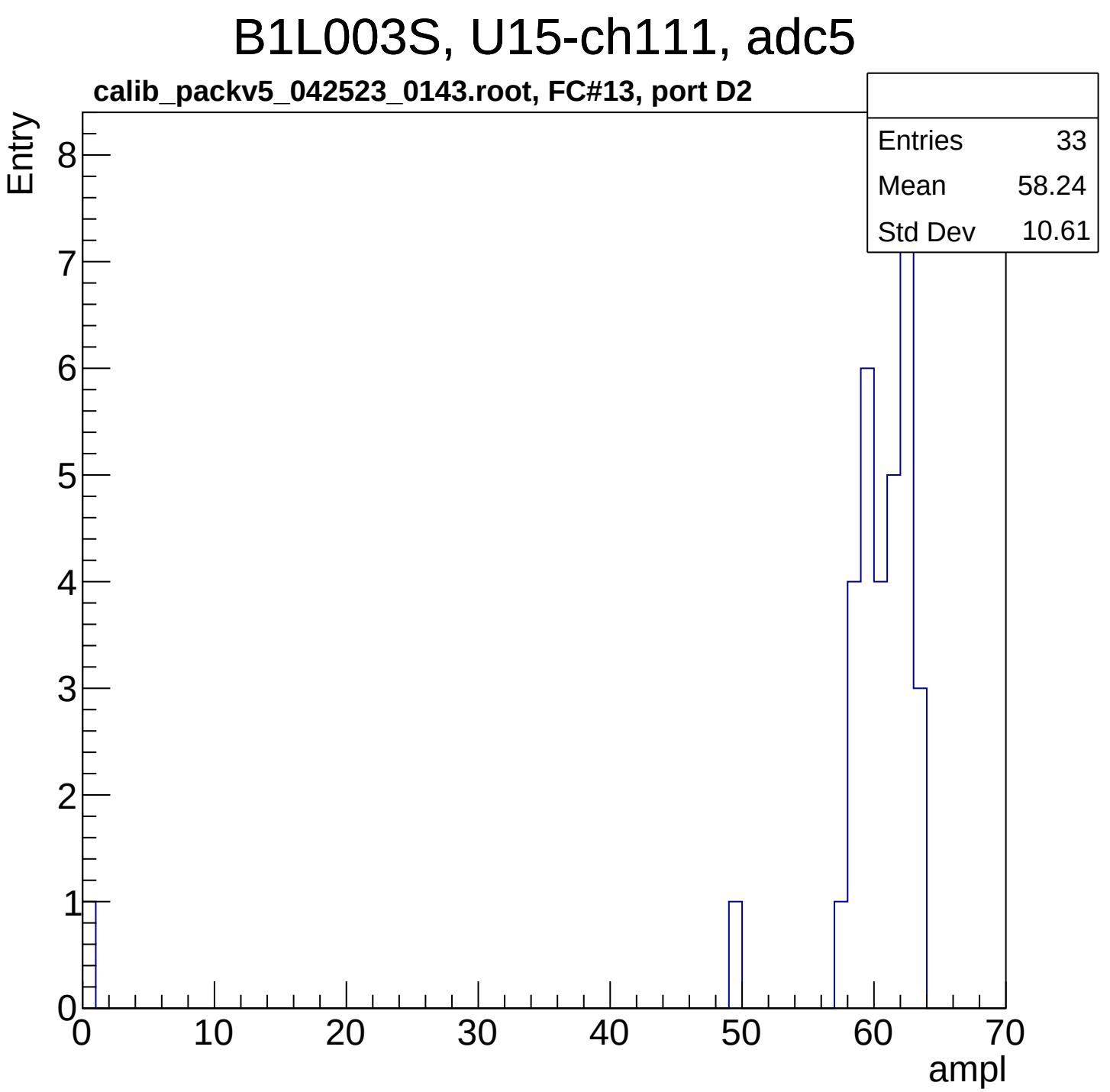
B1L003S, U15-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 58.24
Std Dev 10.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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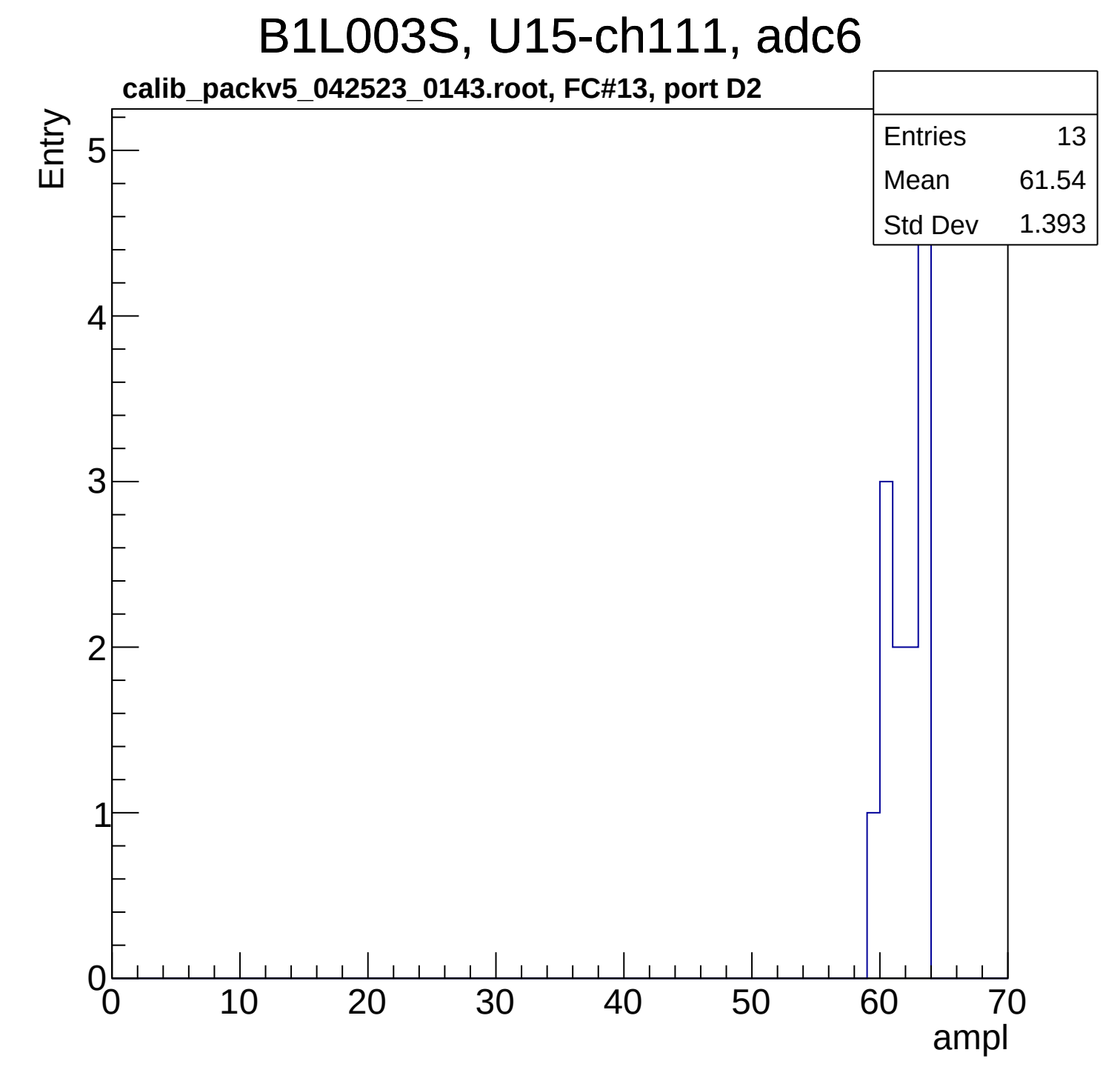
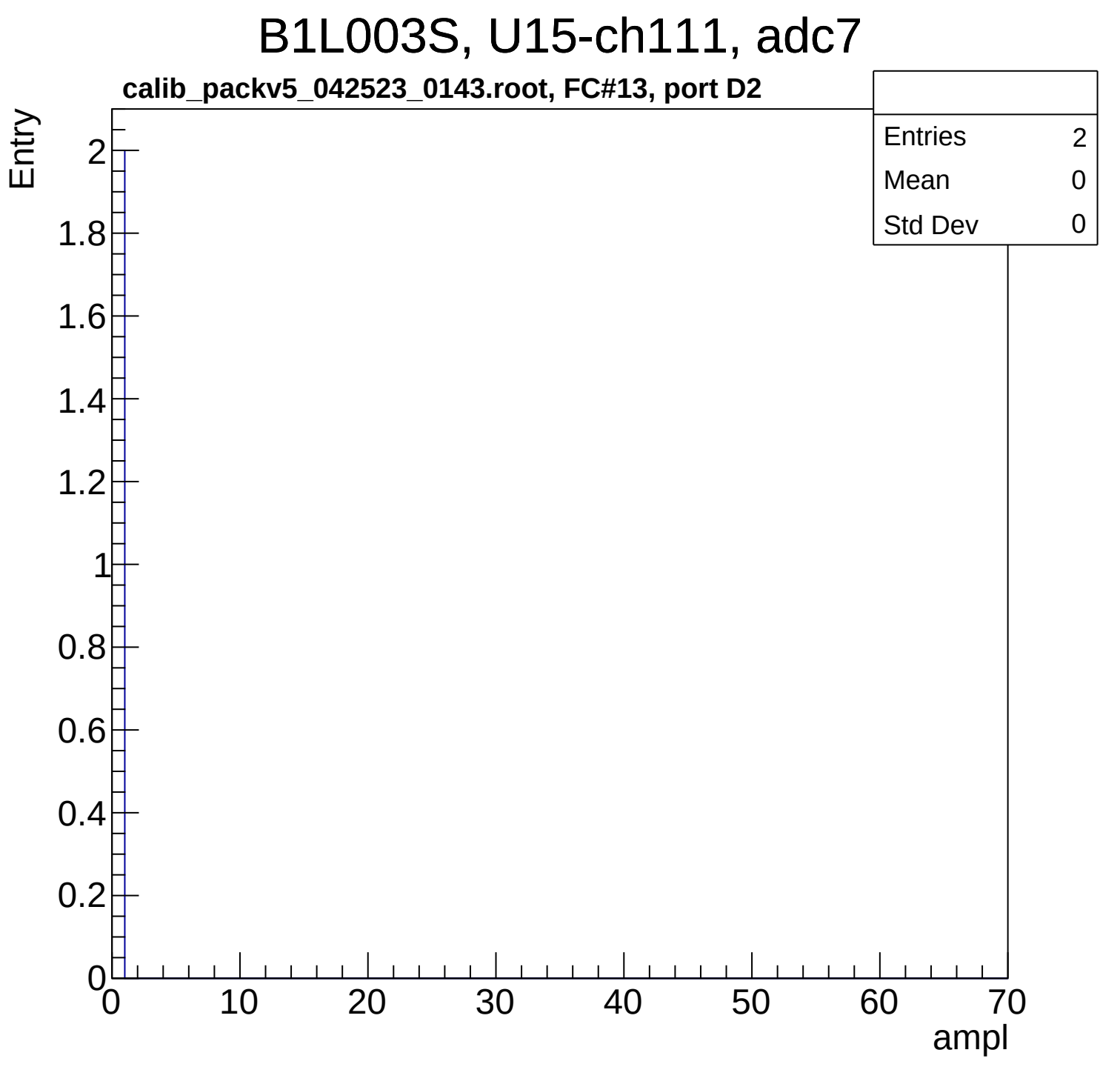

B1L003S, U15-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 61.54
Std Dev 1.393
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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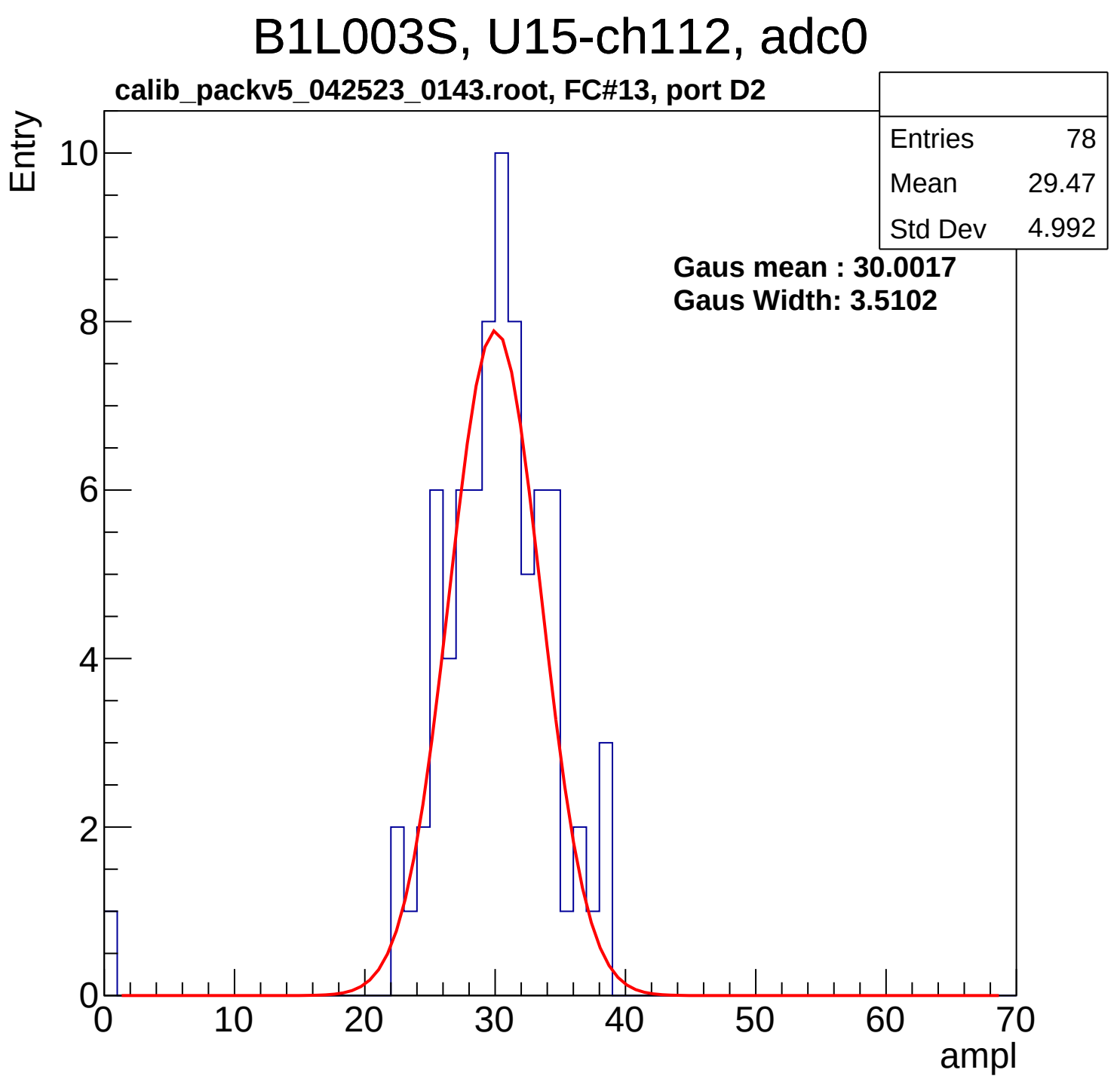
B1L003S, U15-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.47
Std Dev 4.992
Gaus mean : 30.0017
Gaus Width: 3.5102
Entry
ampl
0
2
4
6
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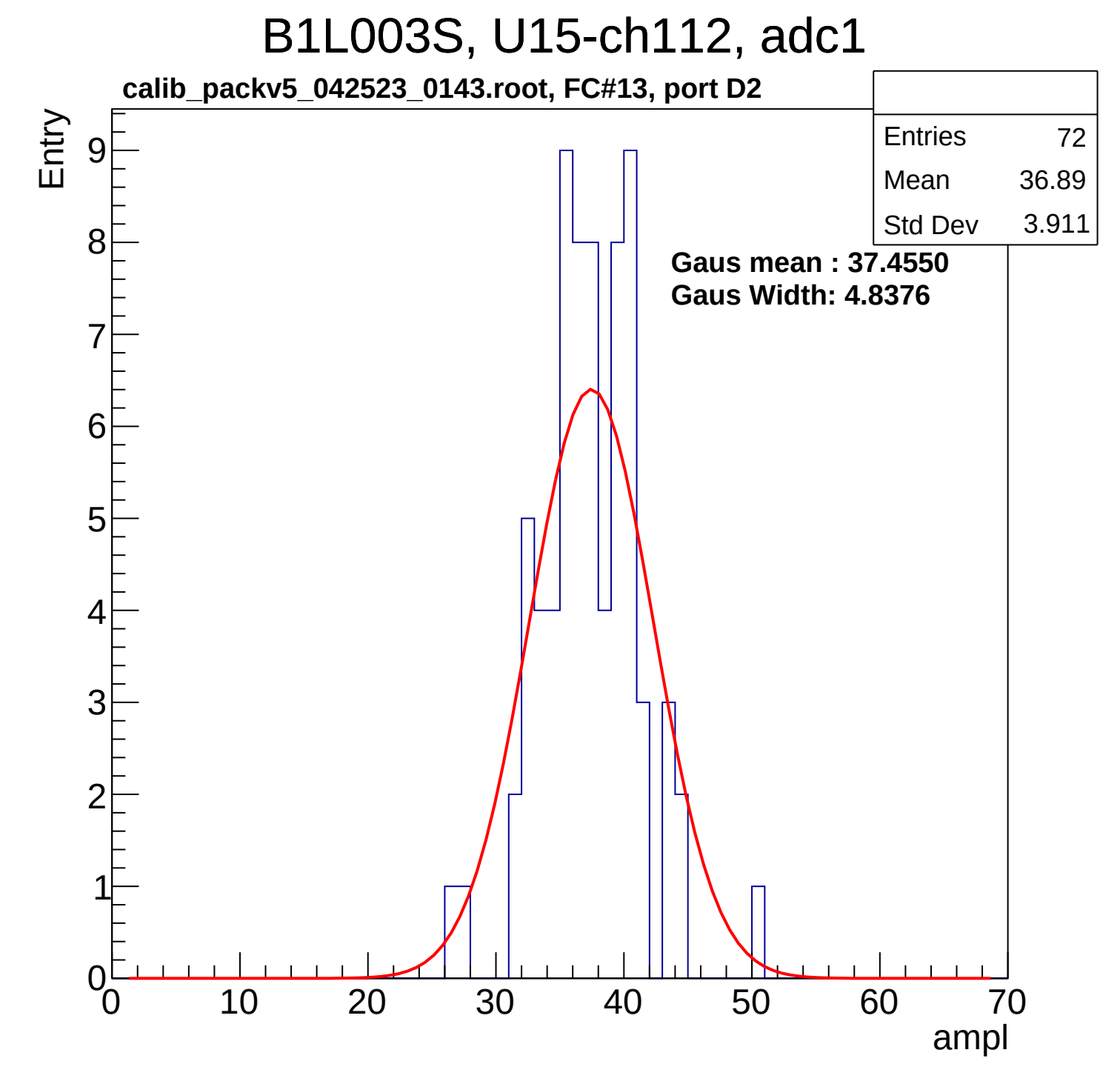
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.89
Std Dev 3.911
Gaus mean : 37.4550
Gaus Width: 4.8376
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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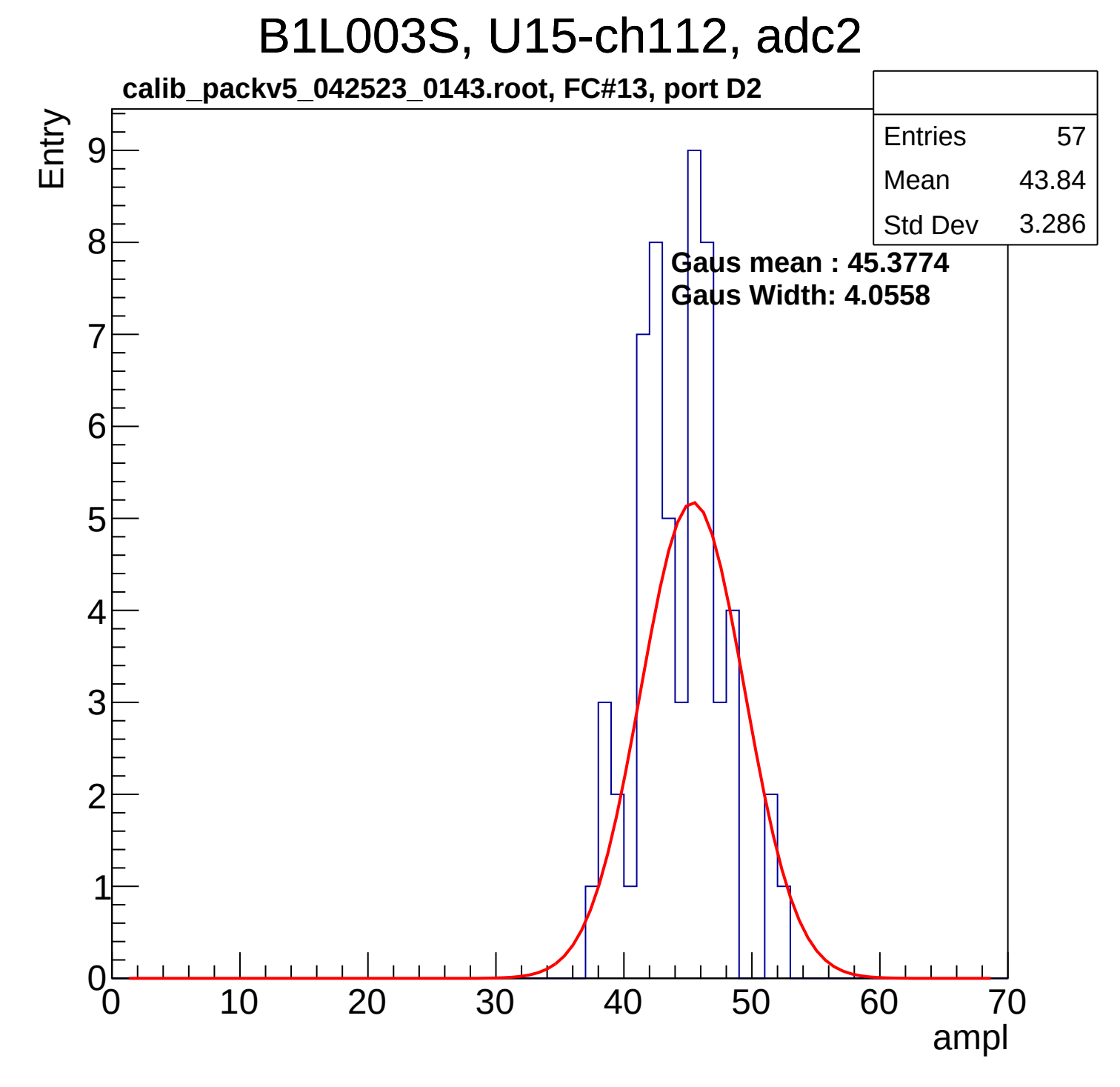
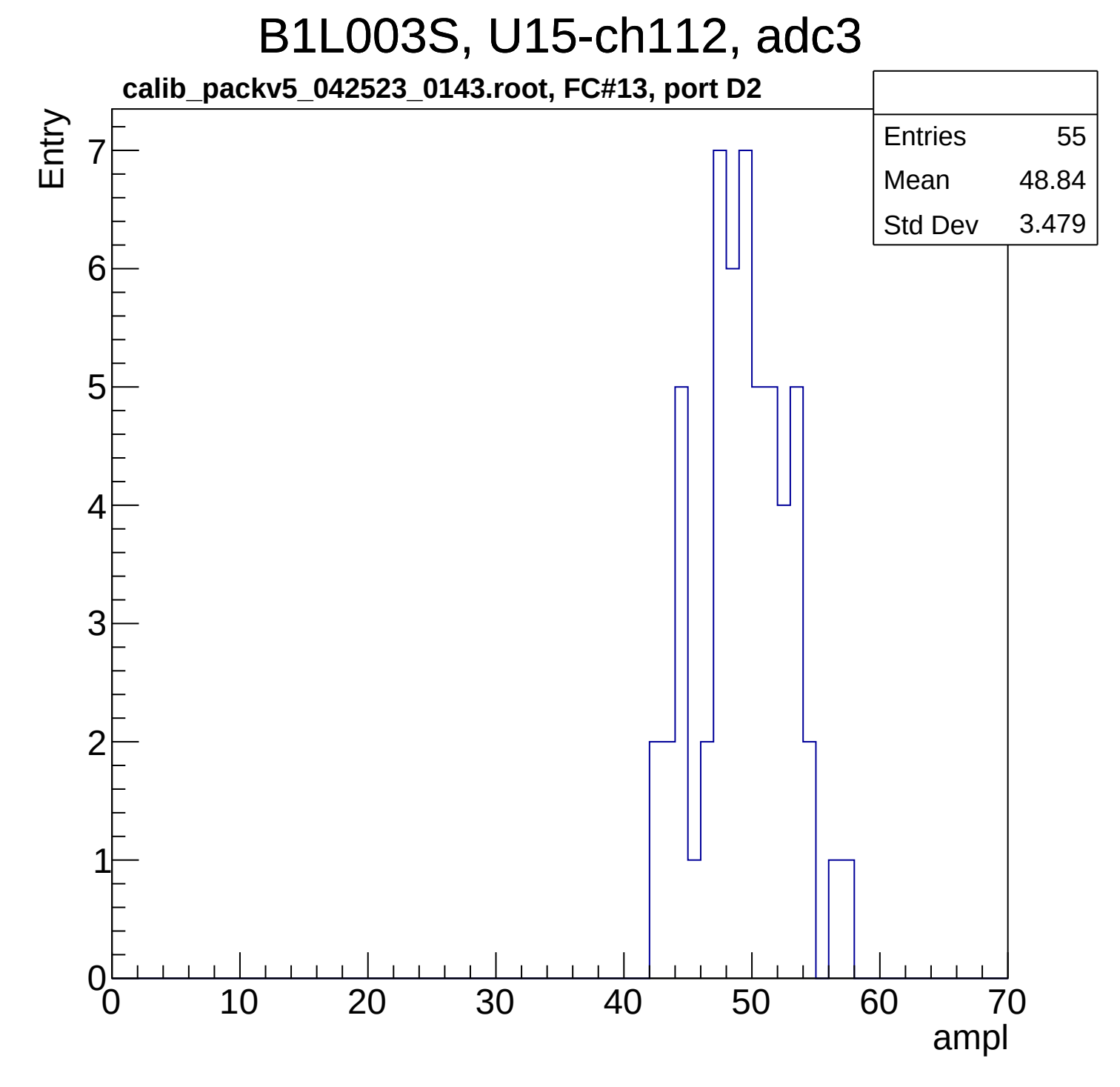

B1L003S, U15-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 43.84
Std Dev 3.286
Gaus mean : 45.3774
Gaus Width: 4.0558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.84
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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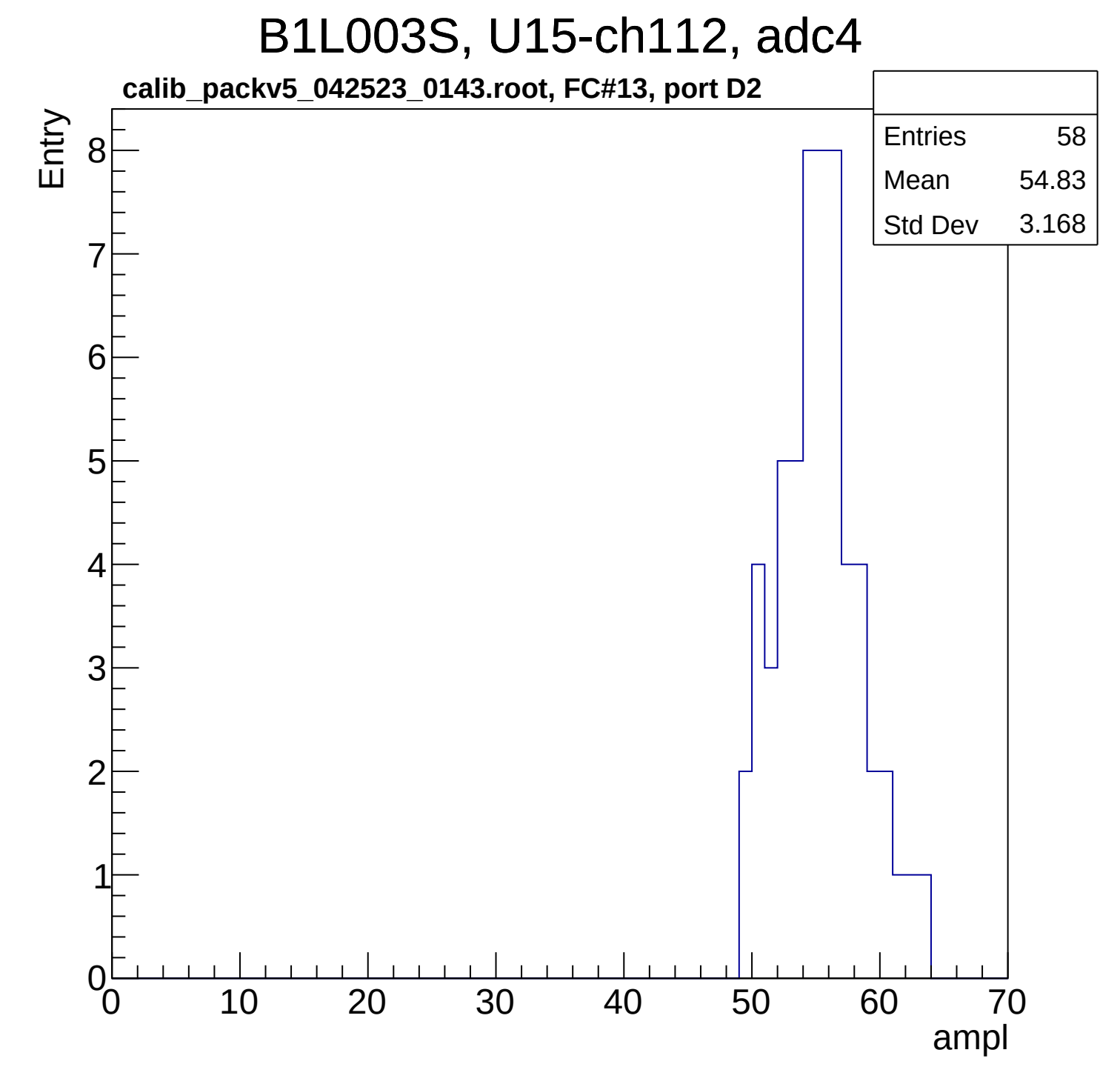
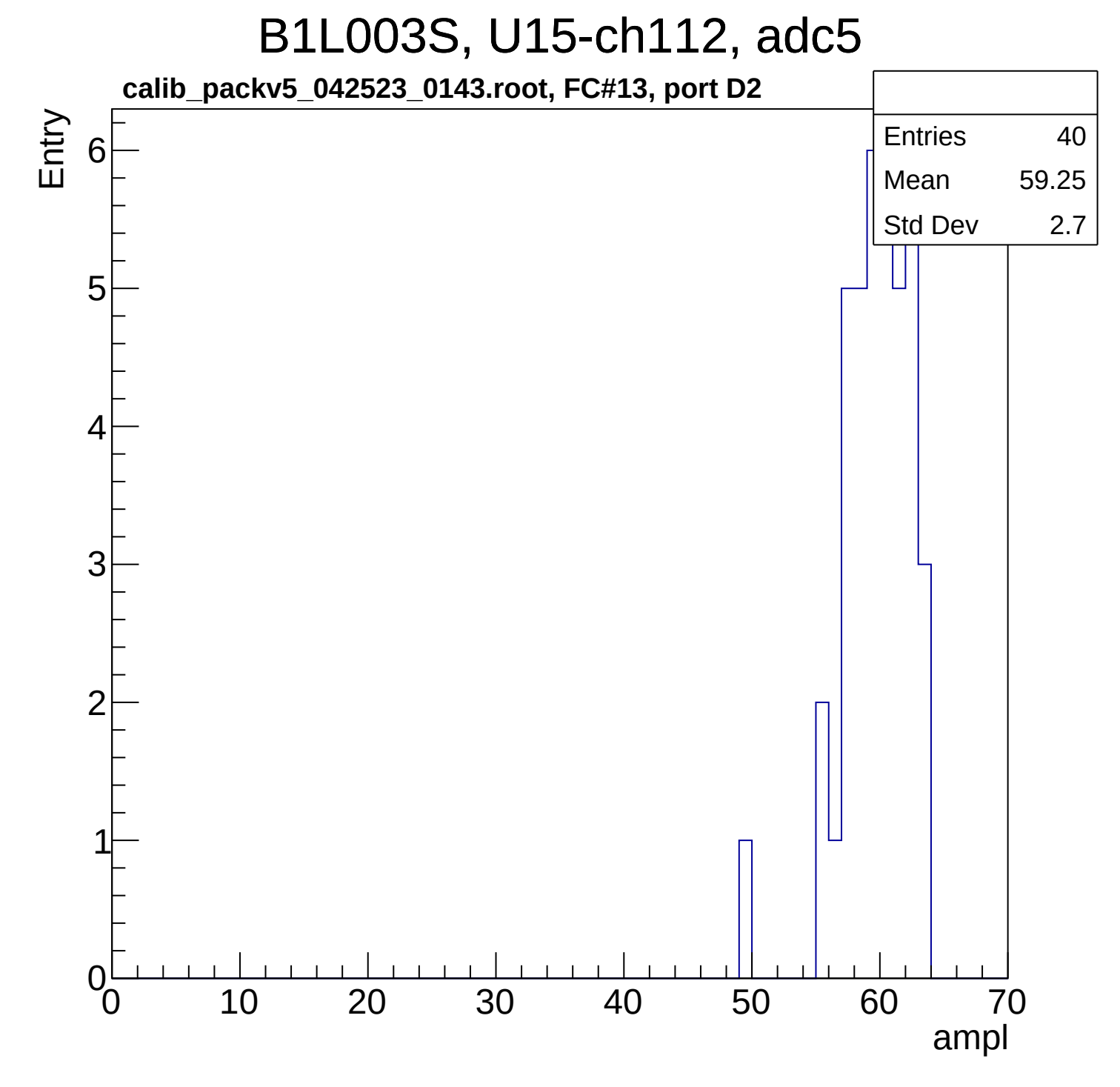
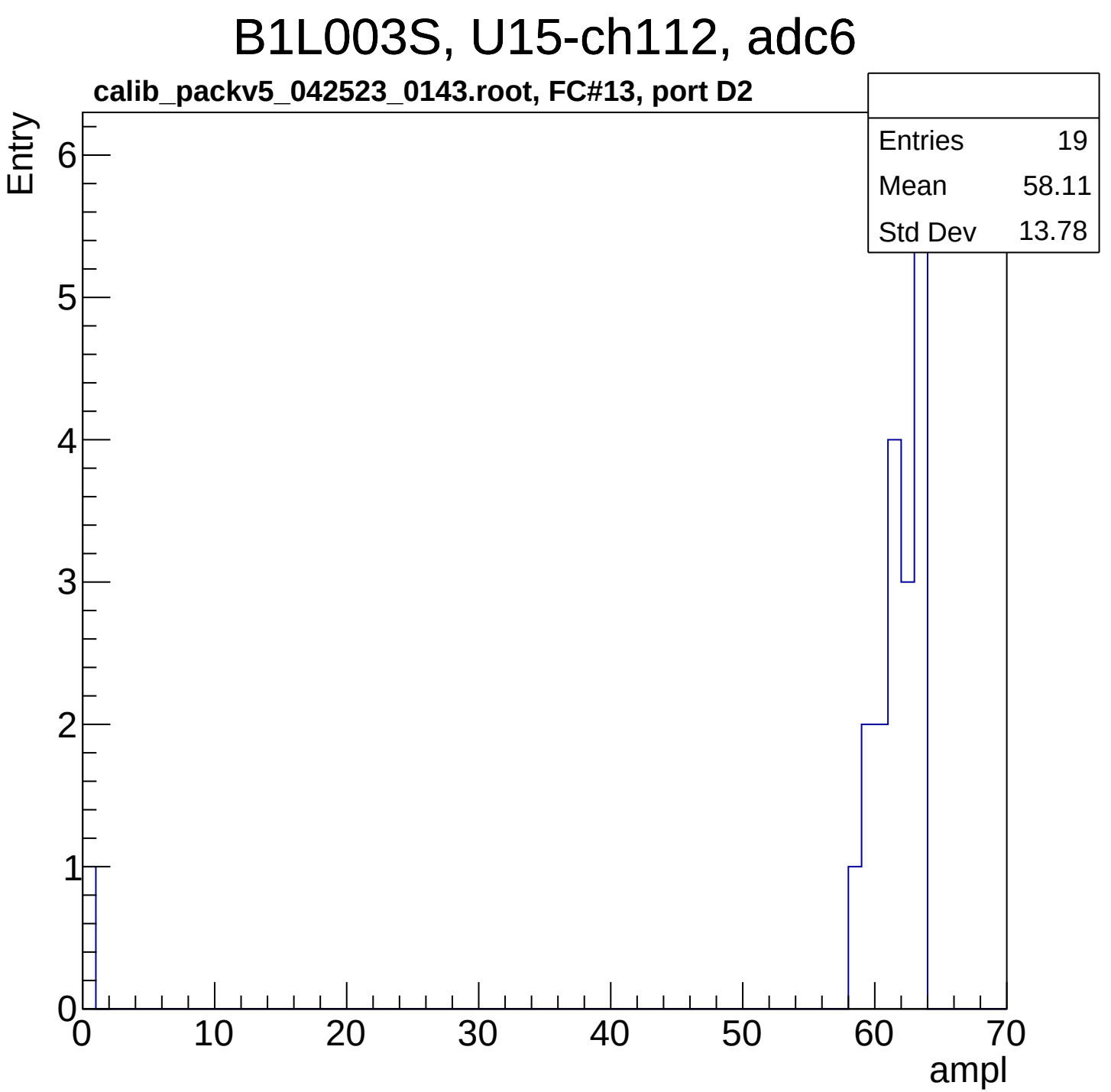
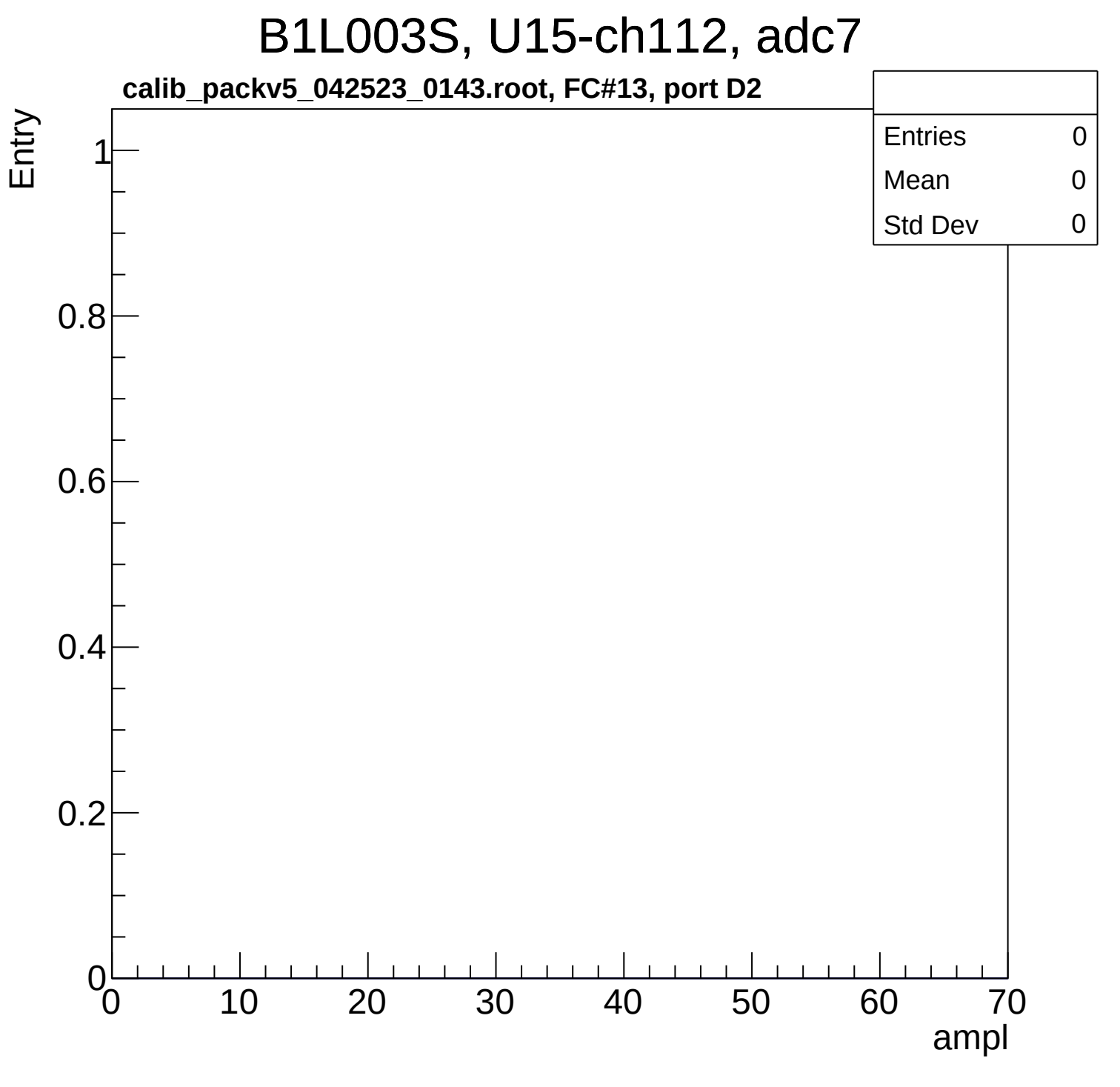
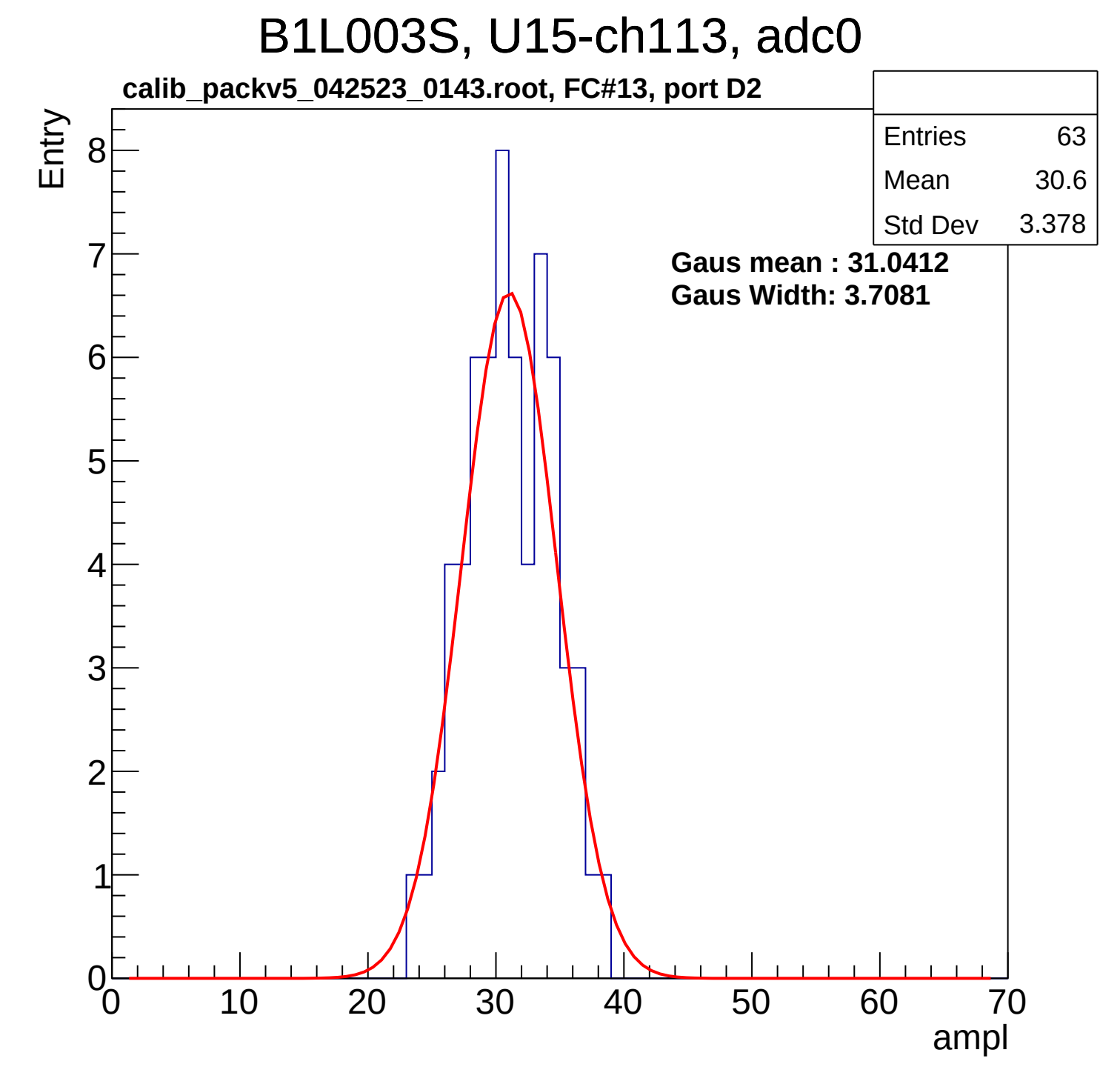
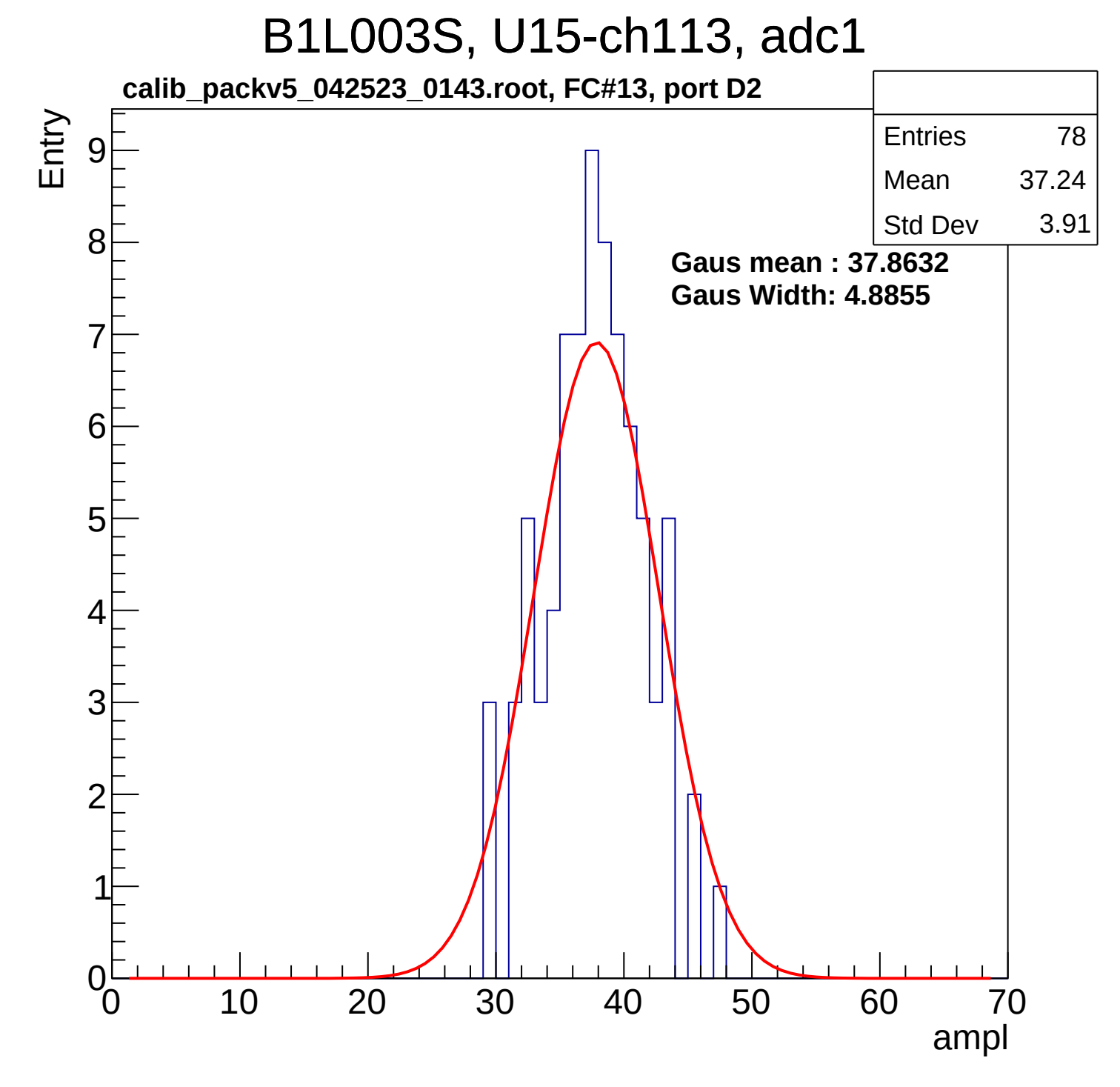

B1L003S, U15-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.83
Std Dev 3.168
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

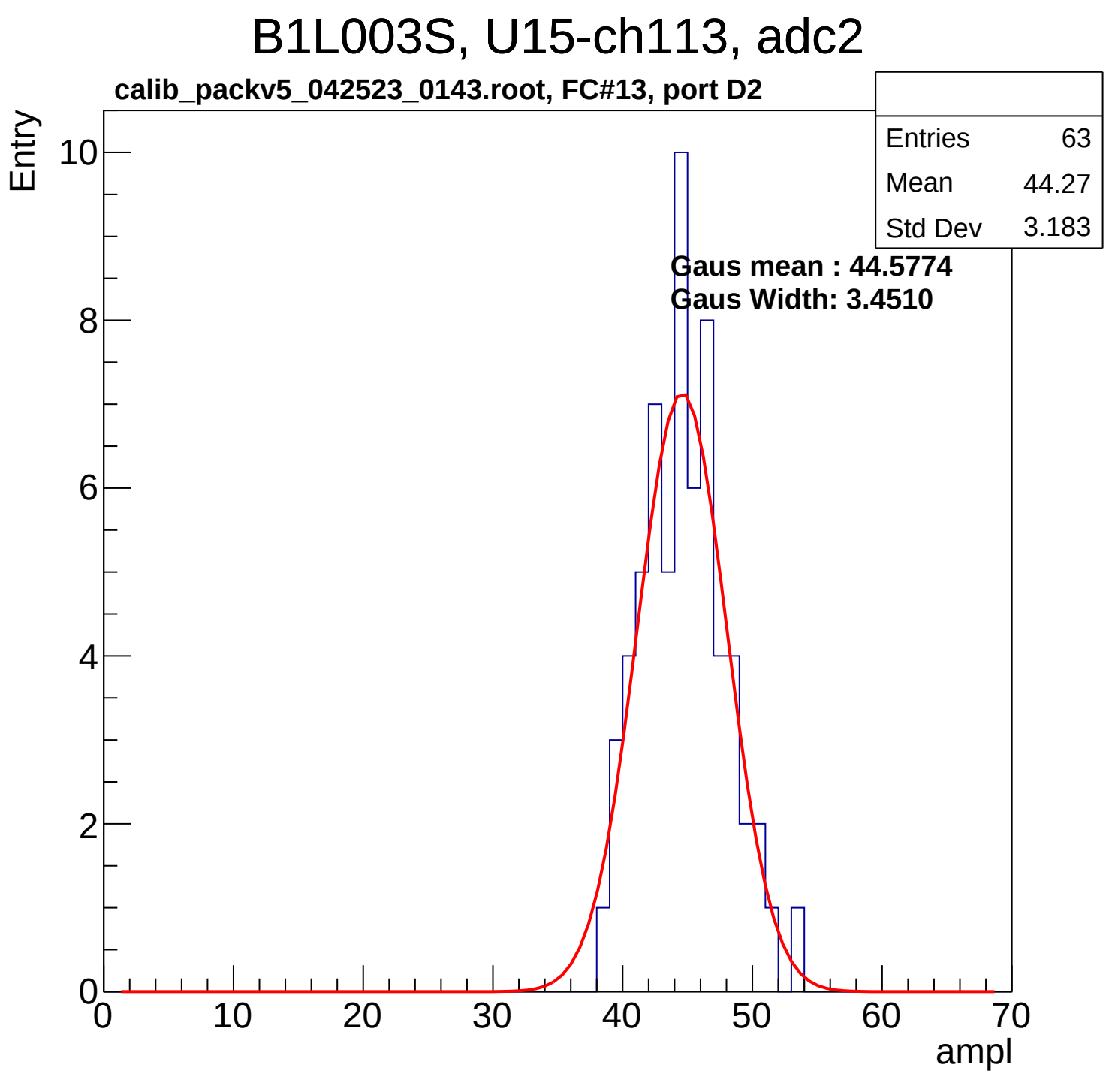

B1L003S, U15-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.25
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

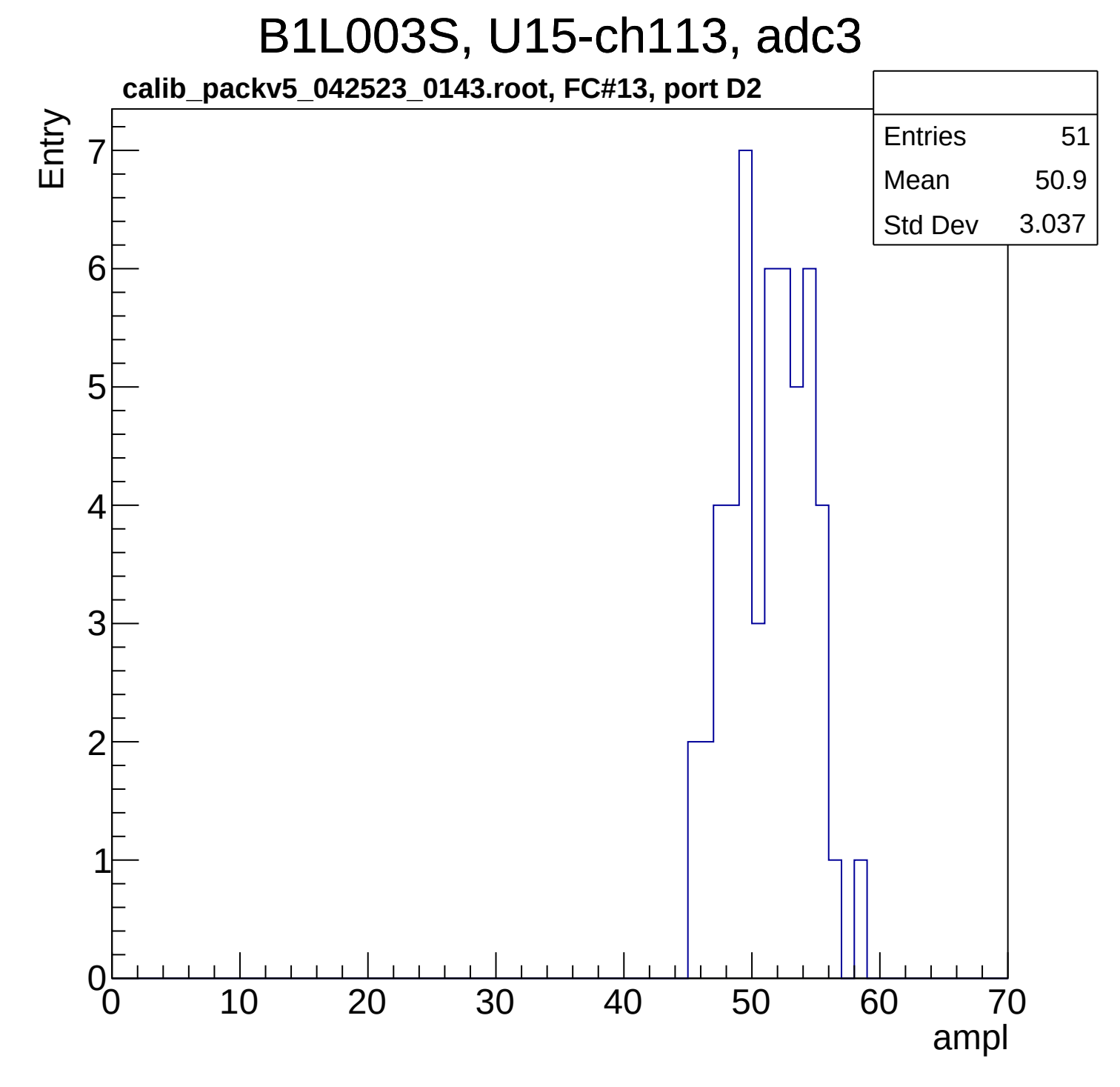

B1L003S, U15-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.11
Std Dev 13.78
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

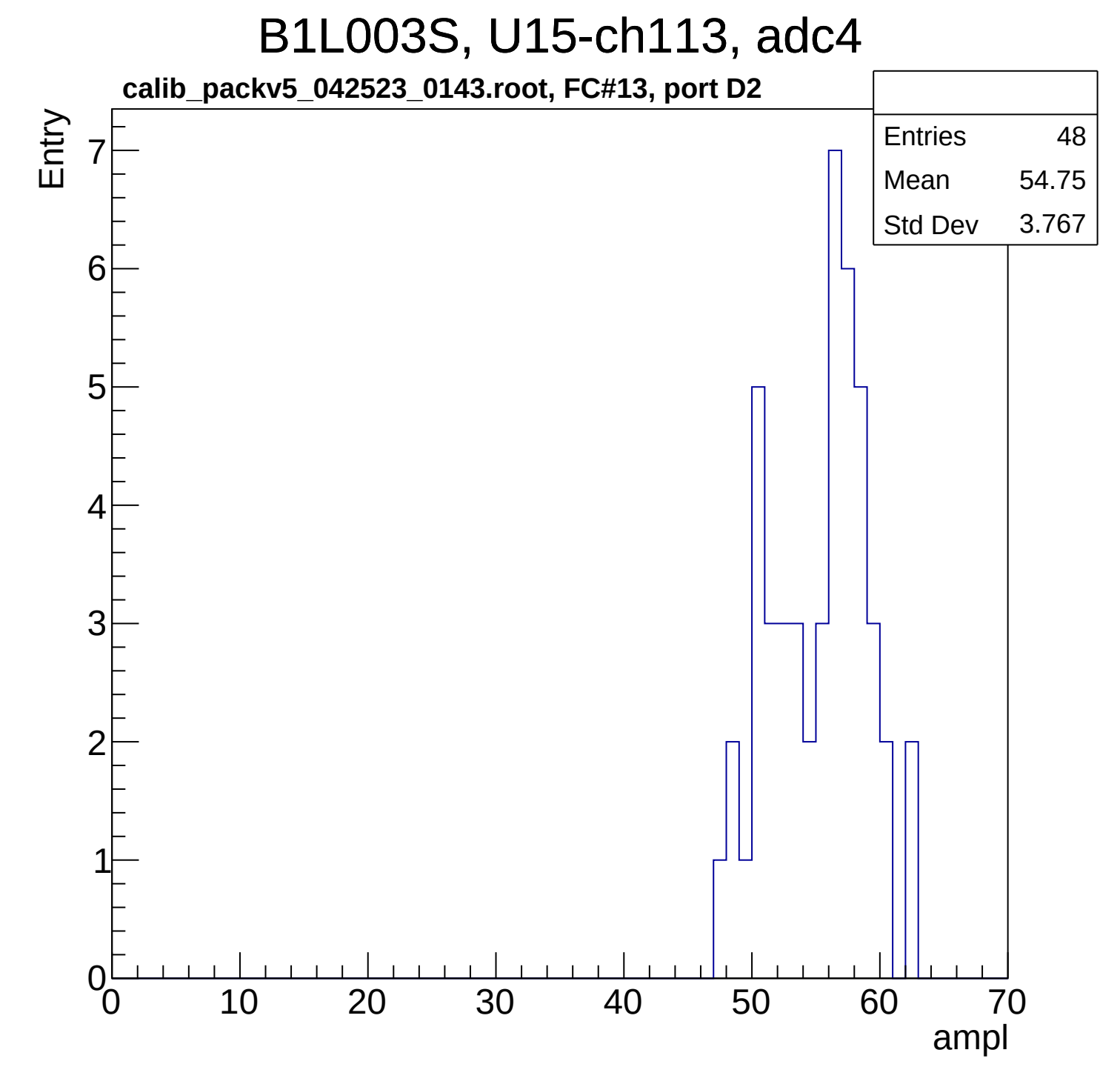

B1L003S, U15-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

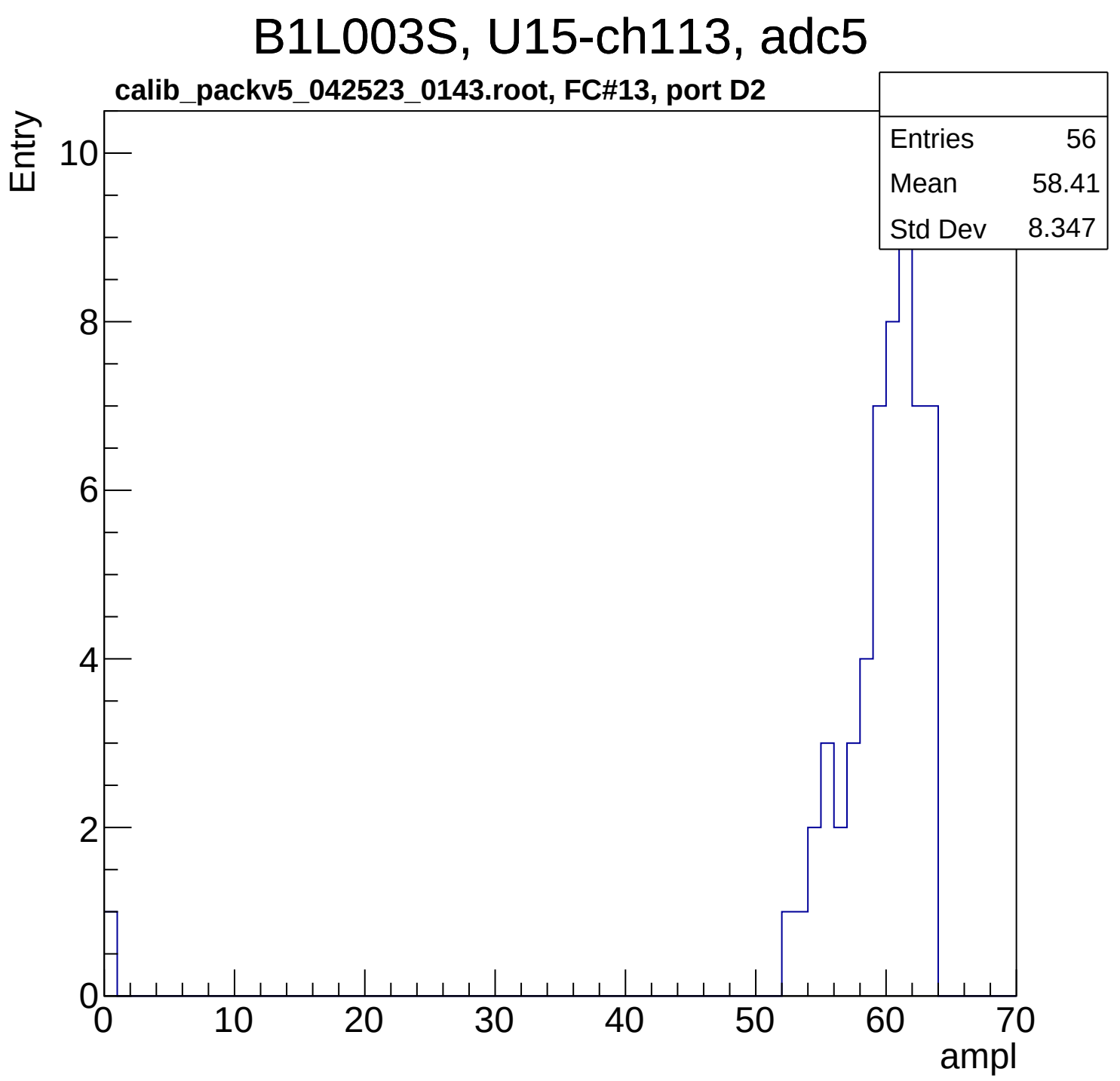

B1L003S, U15-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 30.6
Std Dev 3.378
Gaus mean : 31.0412
Gaus Width: 3.7081
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

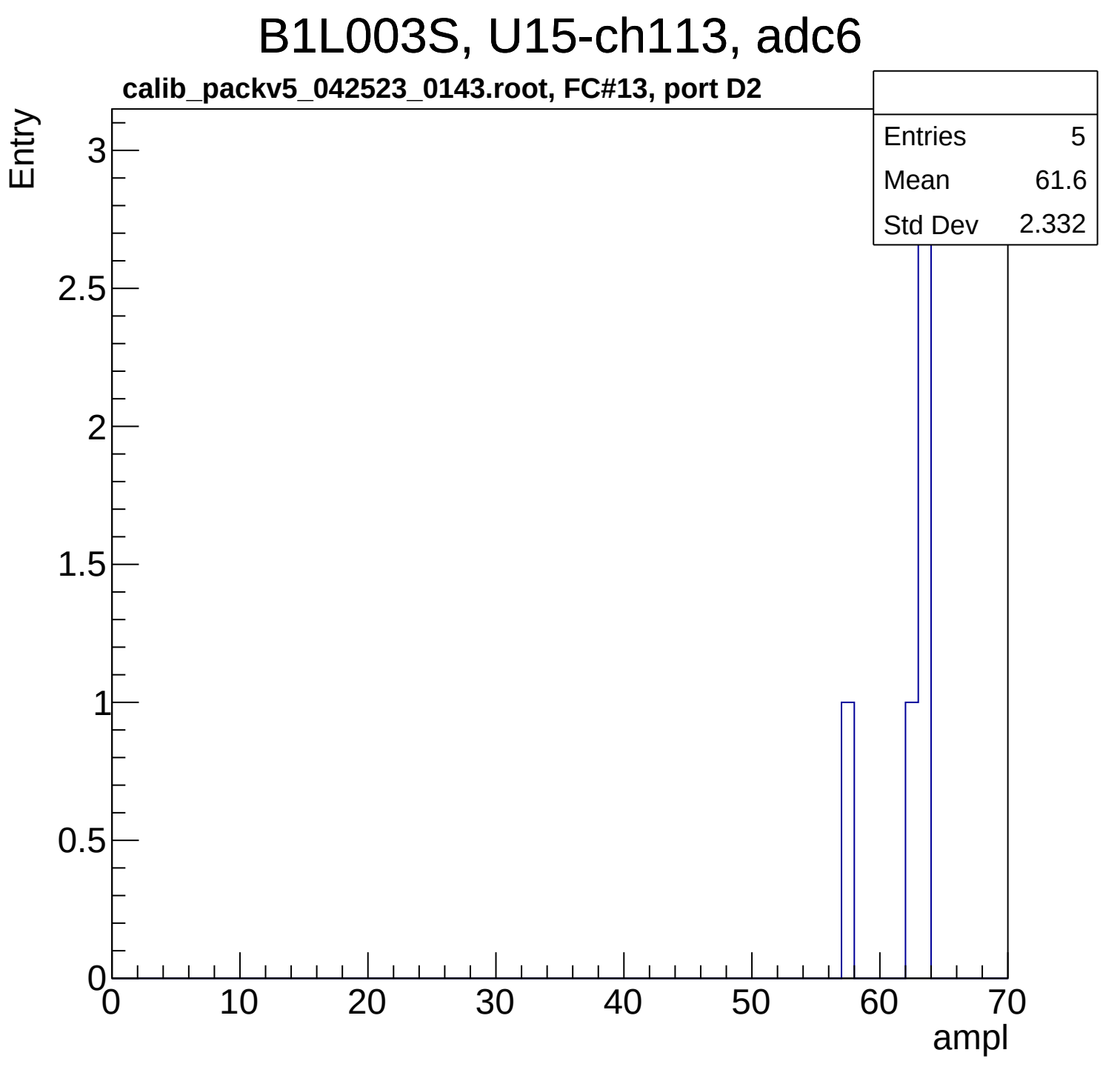

B1L003S, U15-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 37.24
Std Dev 3.91
Gaus mean : 37.8632
Gaus Width: 4.8855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 44.27
Std Dev 3.183
Gaus mean : 44.5774
Gaus Width: 3.4510
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U15-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 50.9
Std Dev 3.037
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 54.75
Std Dev 3.767
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.41
Std Dev 8.347
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 2.332
Entry
ampl
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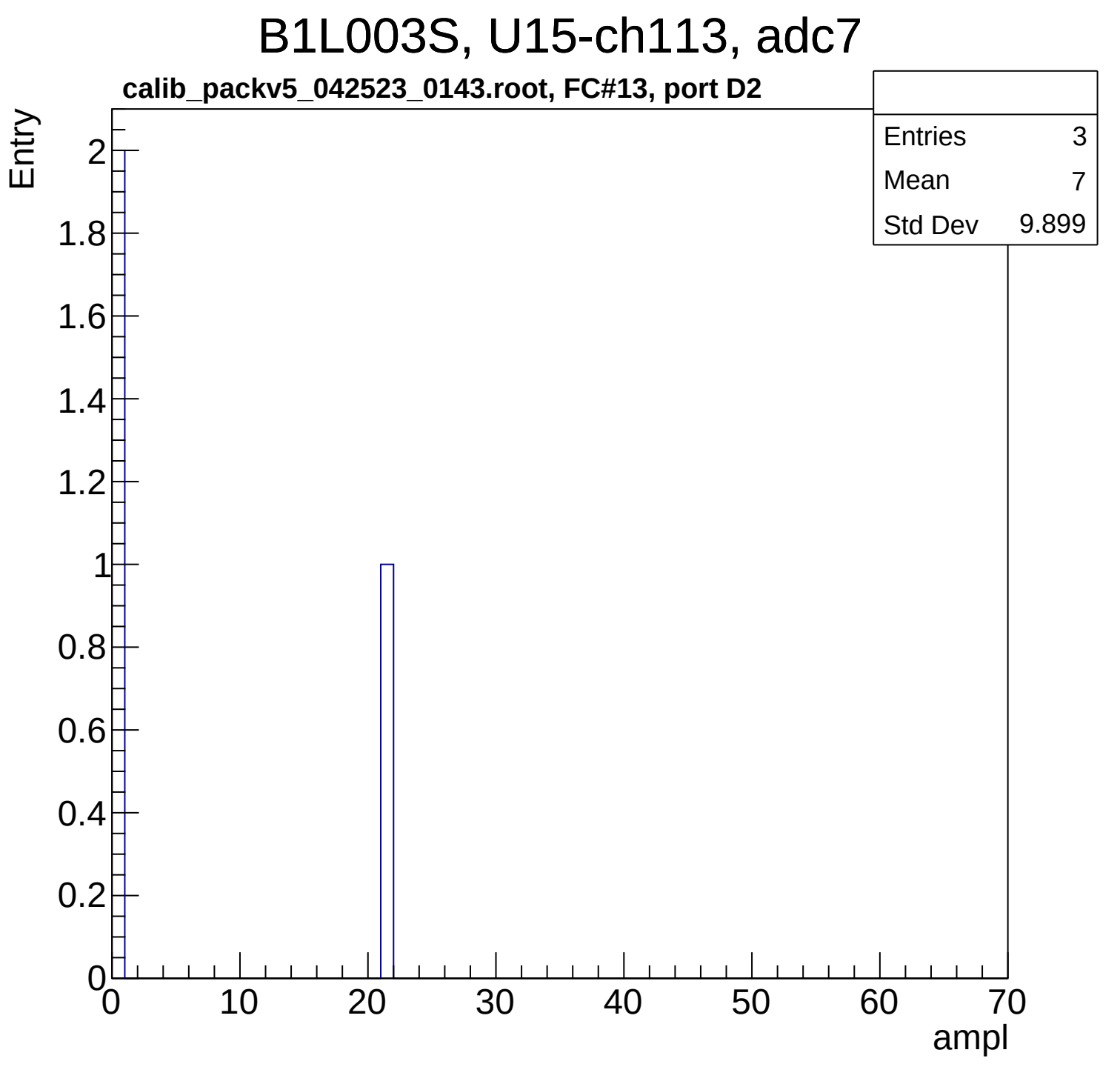
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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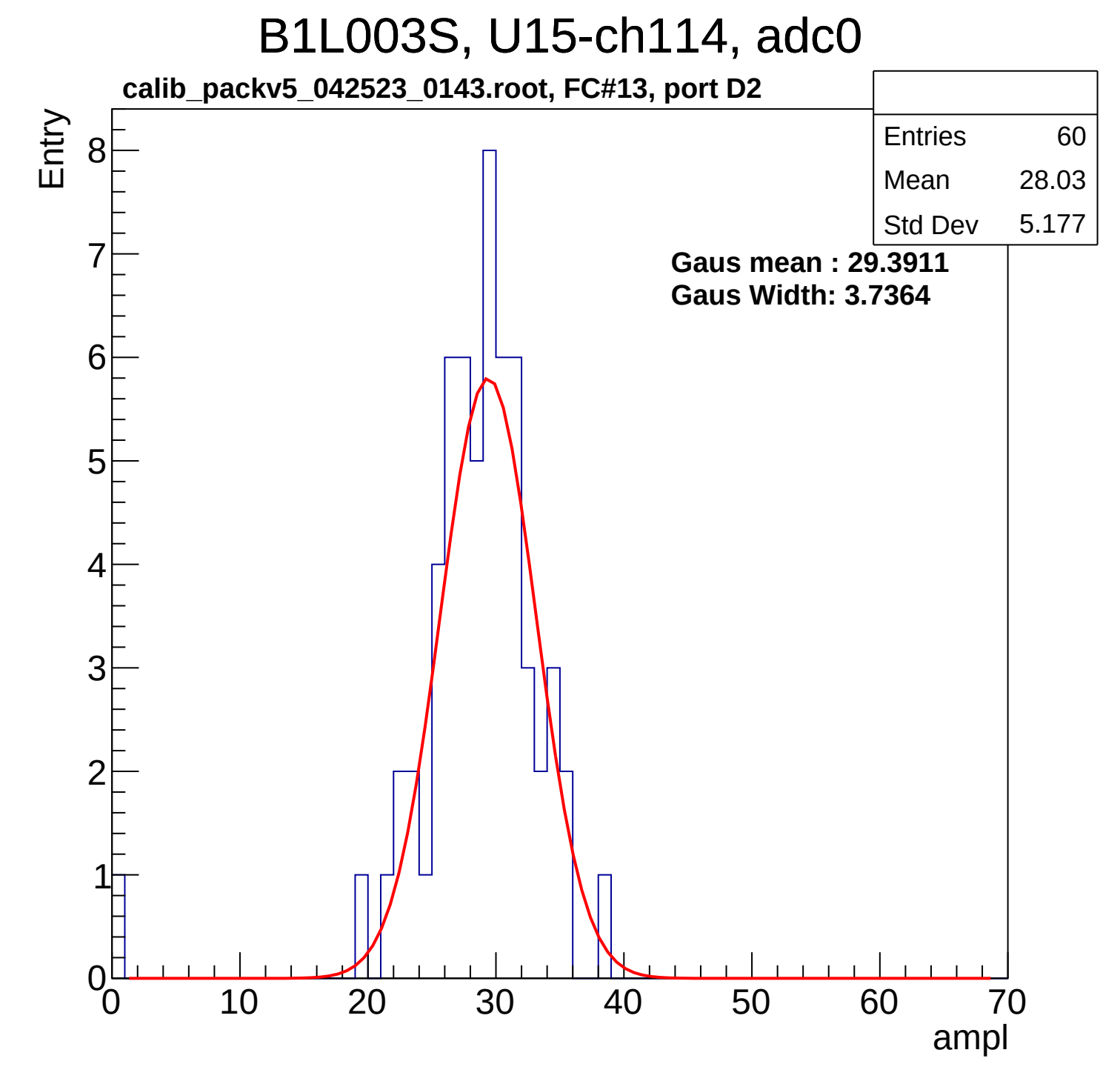
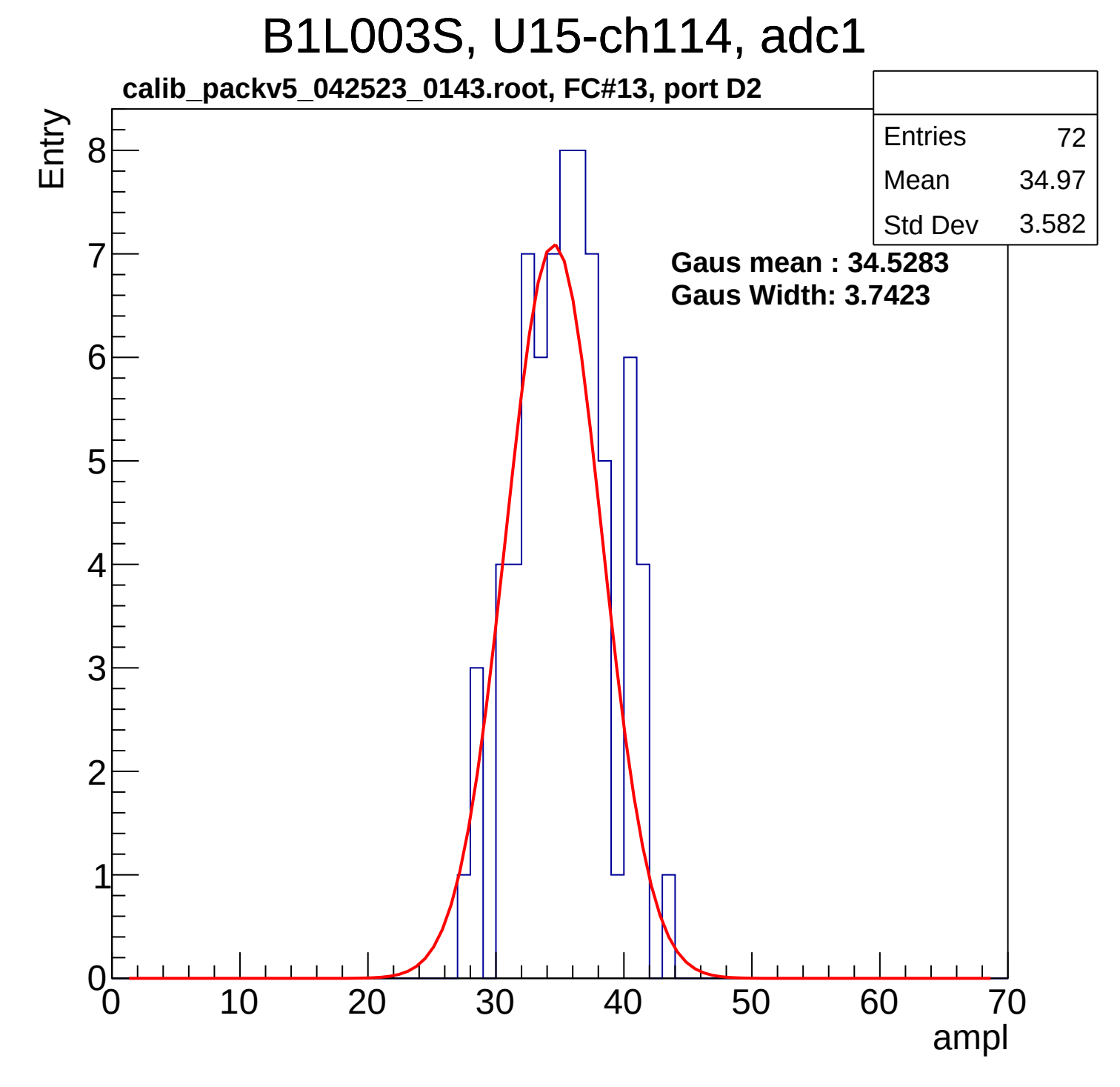
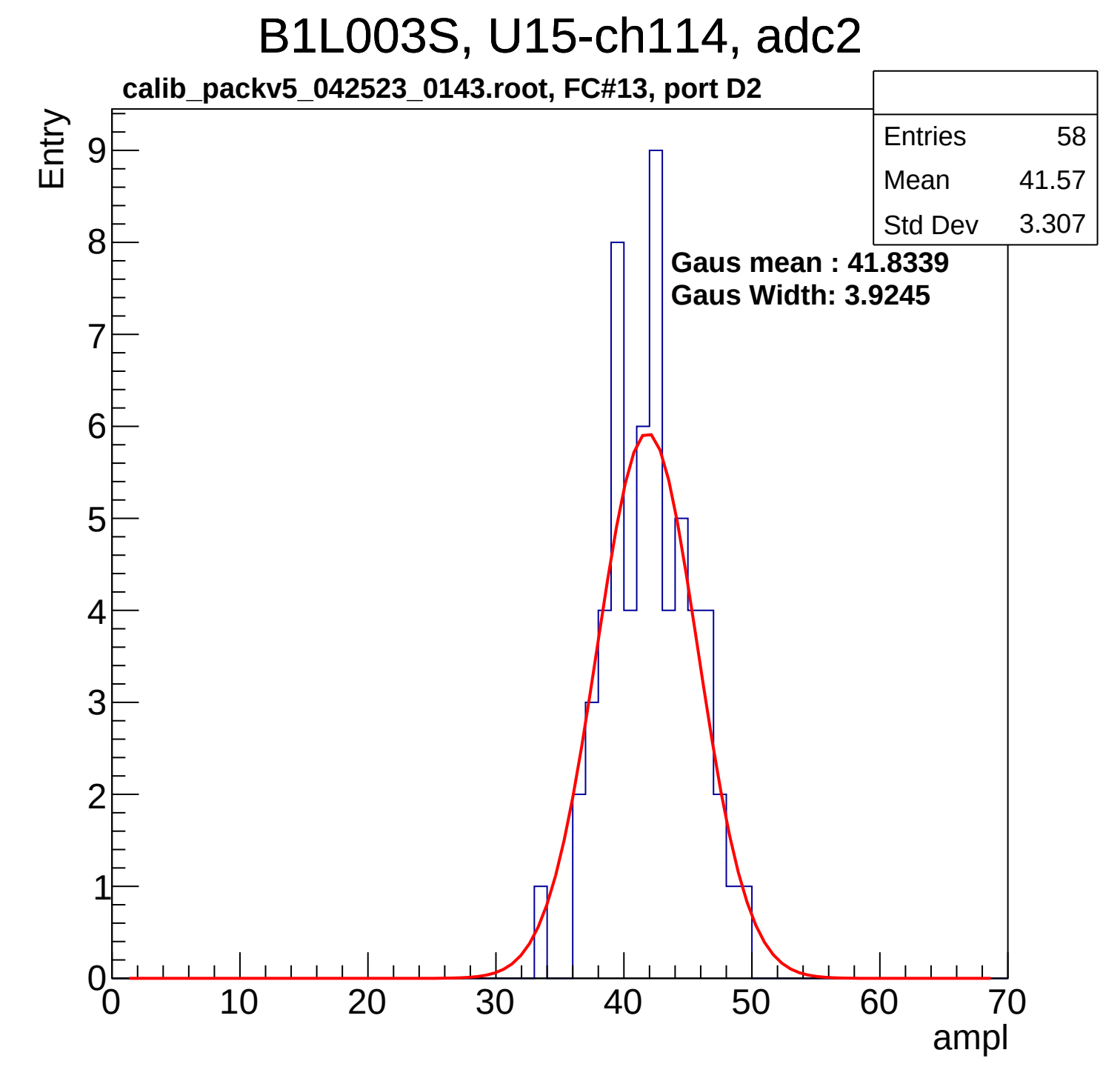
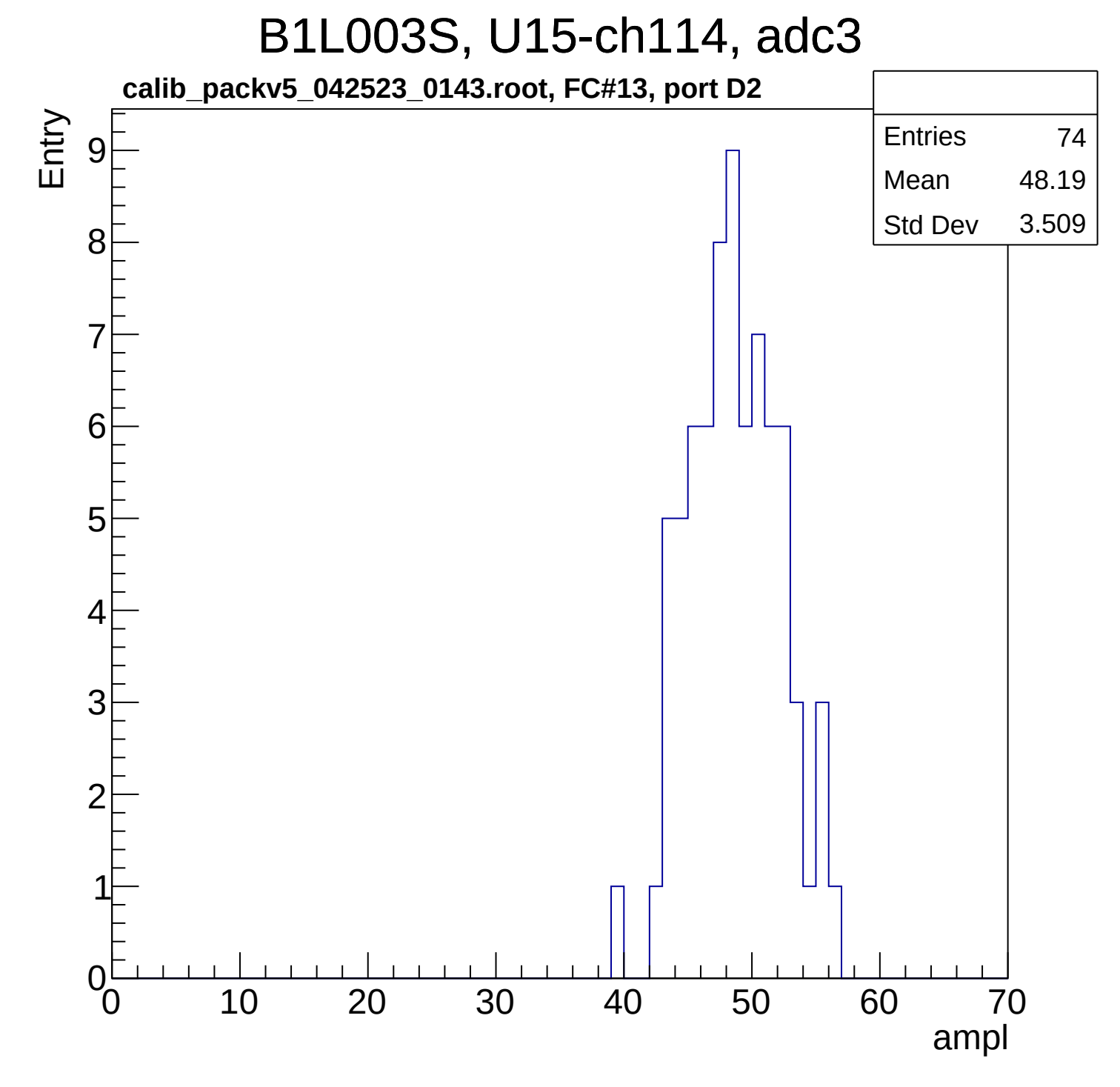

B1L003S, U15-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.03
Std Dev 5.177
Gaus mean : 29.3911
Gaus Width: 3.7364
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.97
Std Dev 3.582
Gaus mean : 34.5283
Gaus Width: 3.7423
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.57
Std Dev 3.307
Gaus mean : 41.8339
Gaus Width: 3.9245
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.19
Std Dev 3.509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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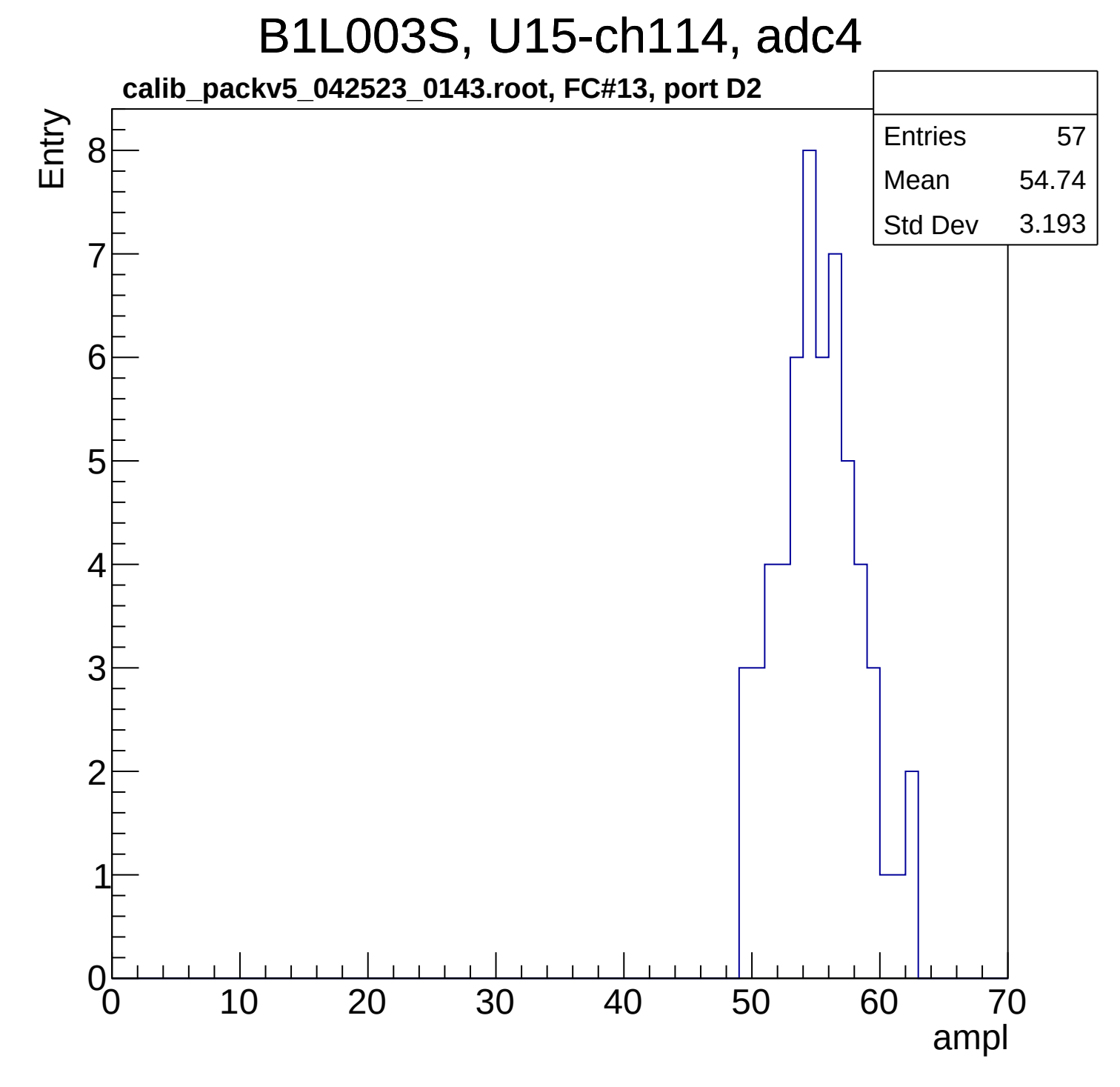
40
50
60
70


B1L003S, U15-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.74
Std Dev 3.193
Entry
ampl
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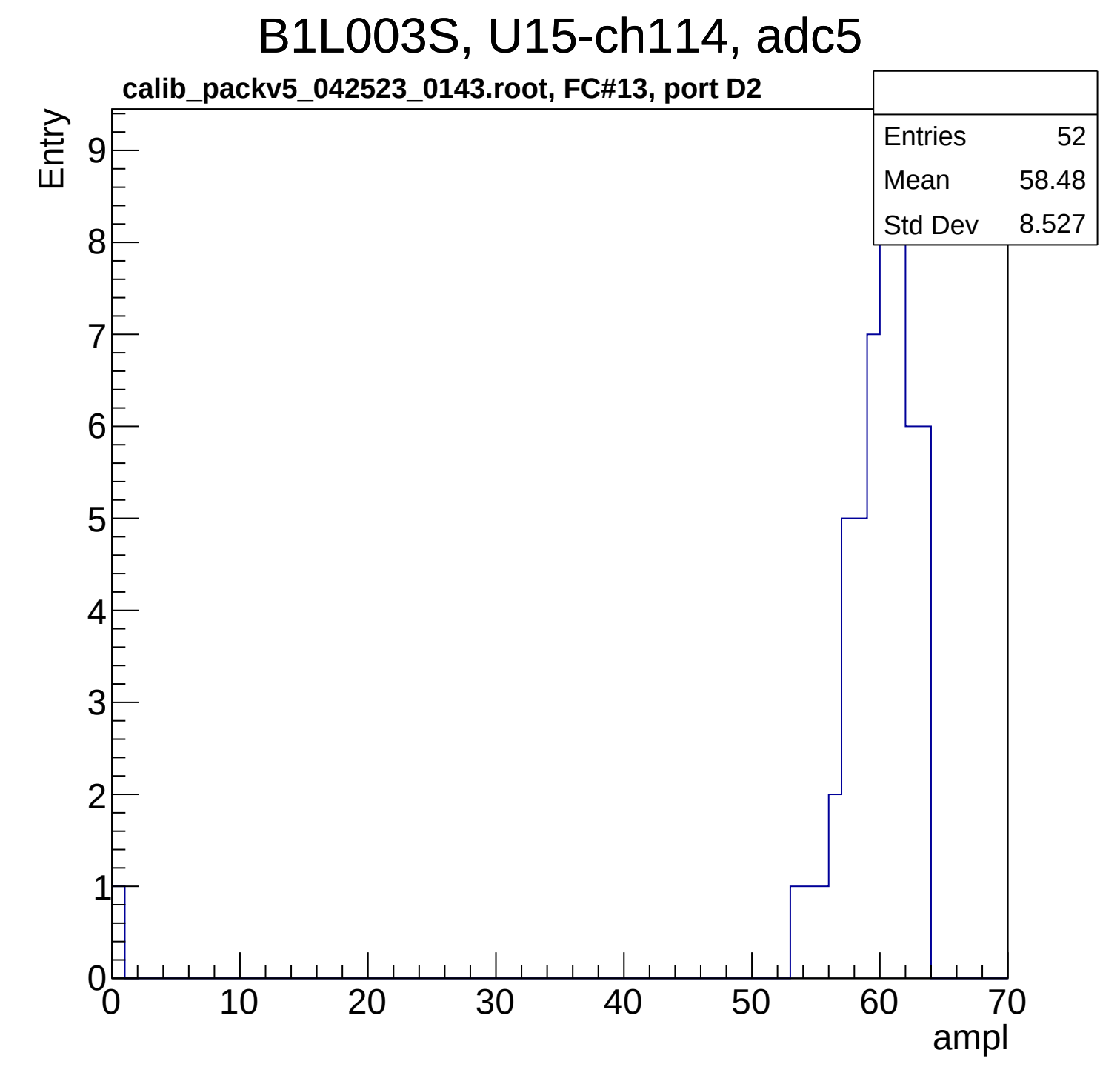
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.48
Std Dev 8.527
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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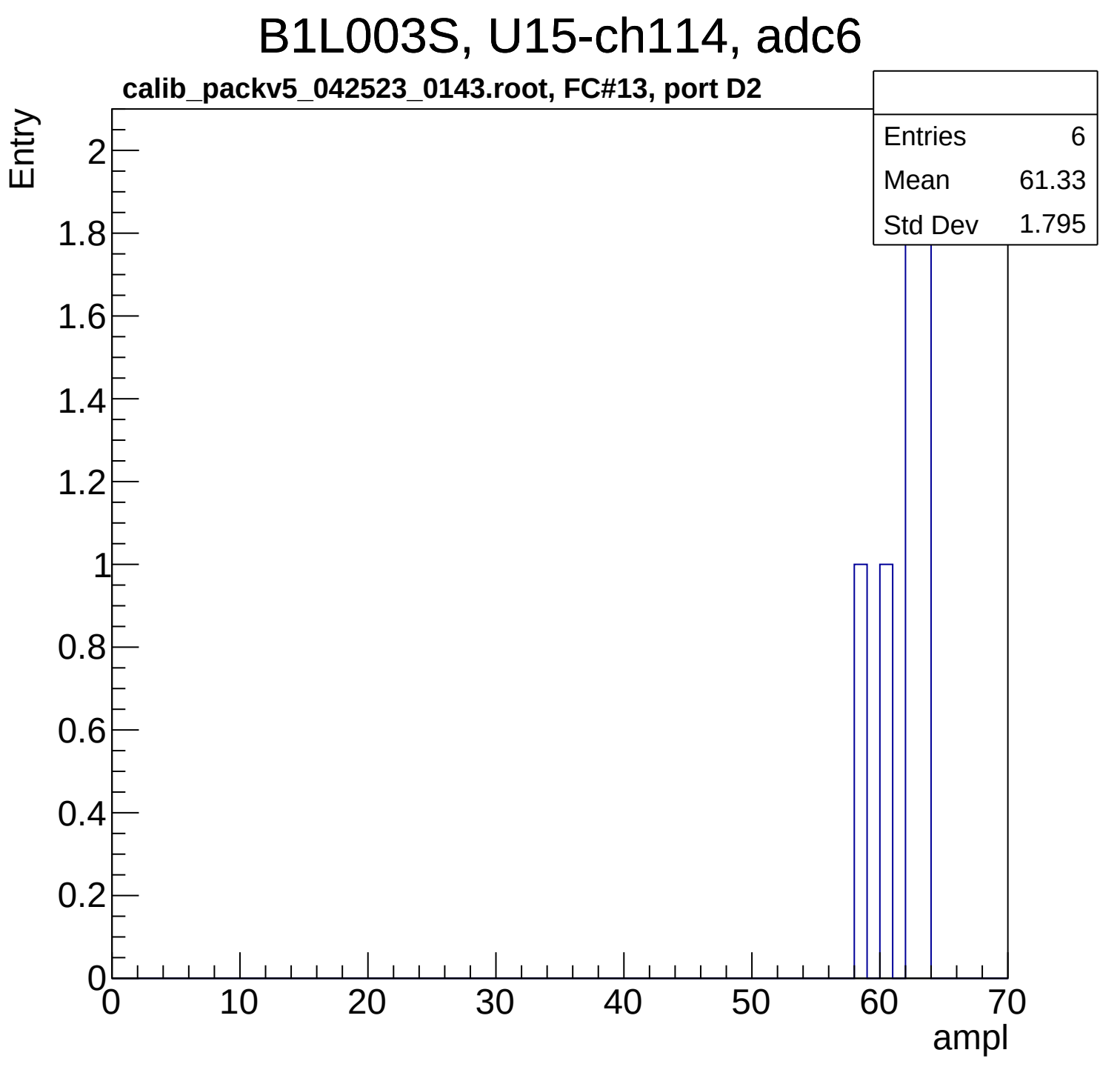
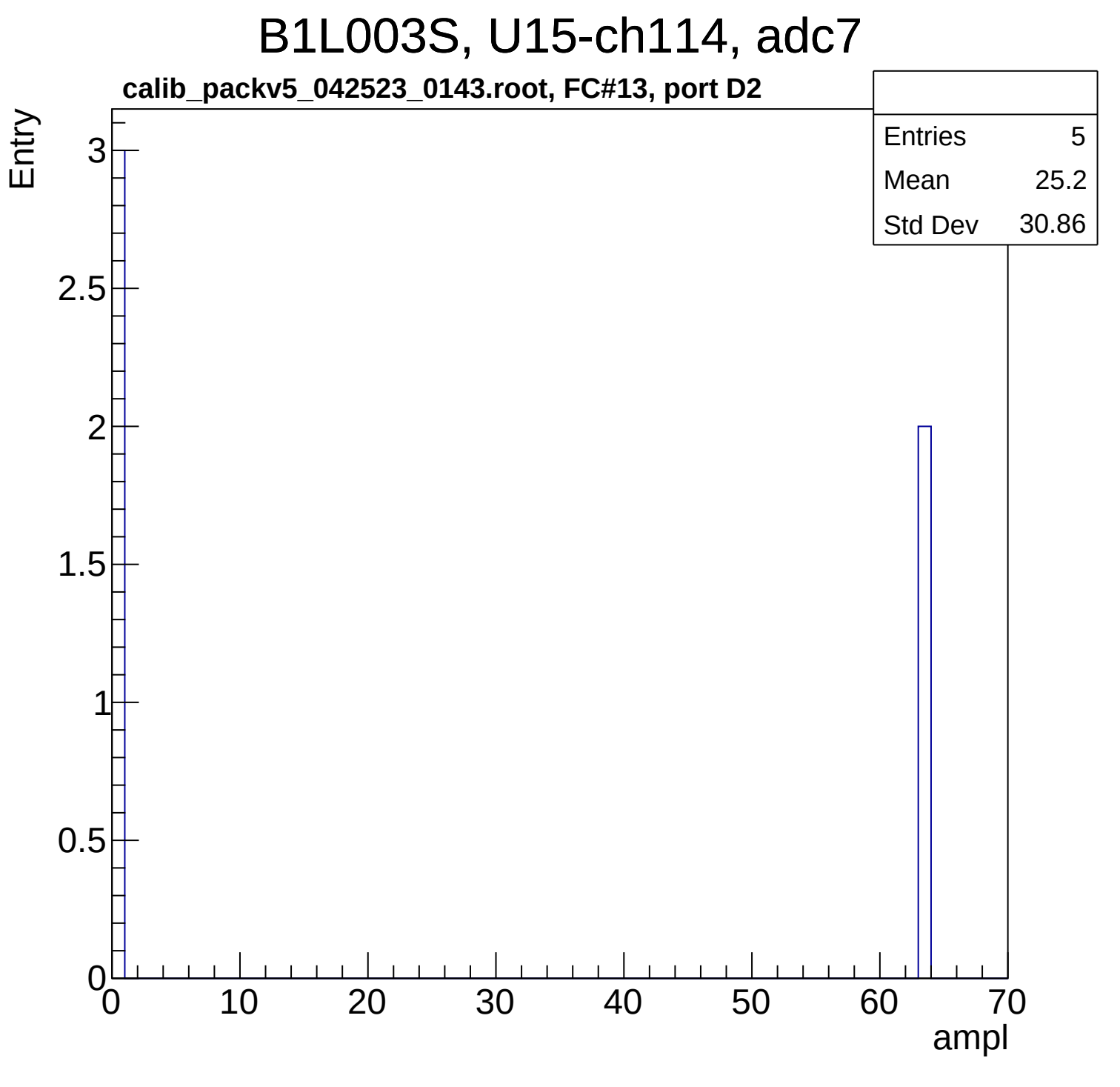
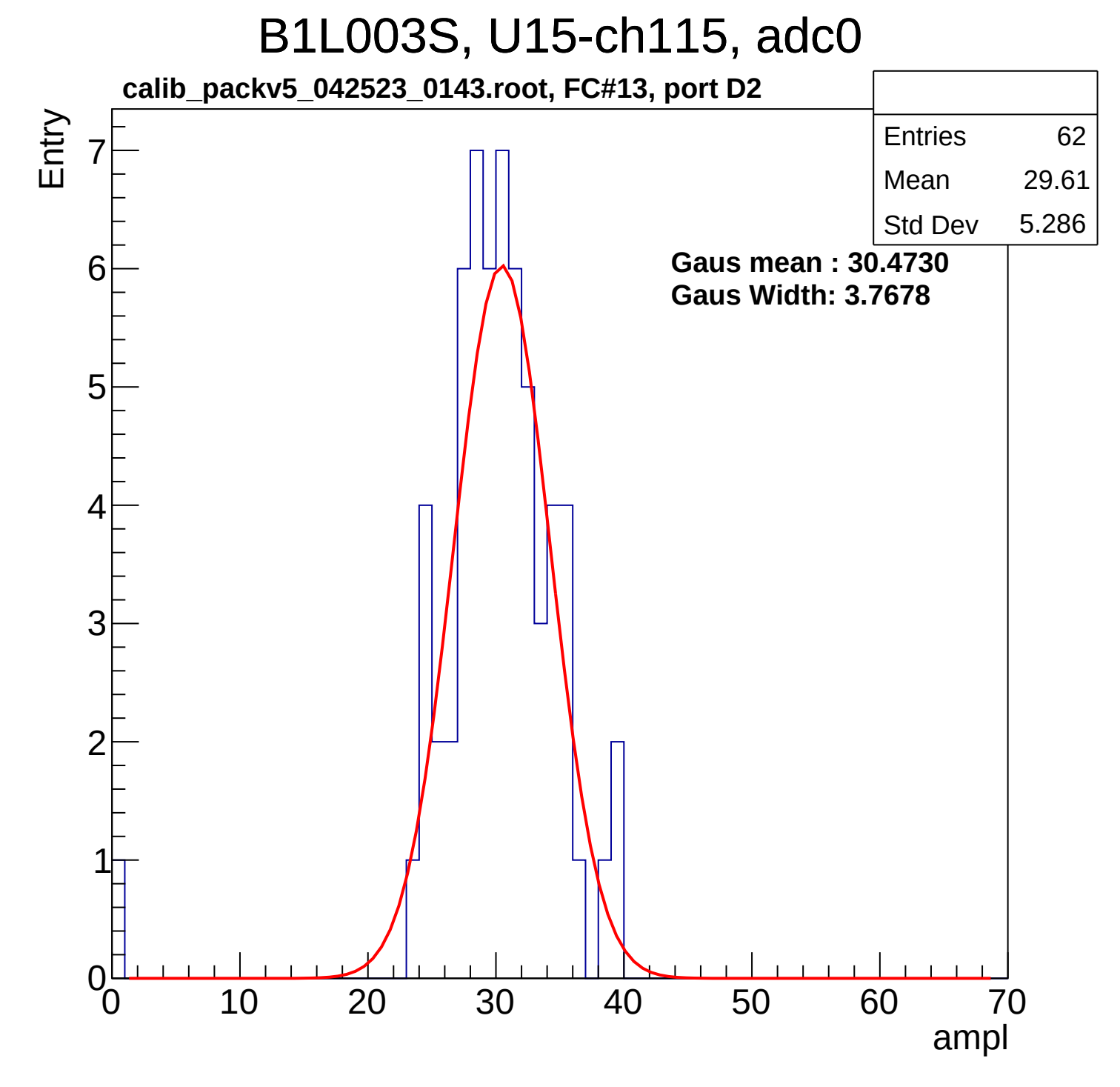
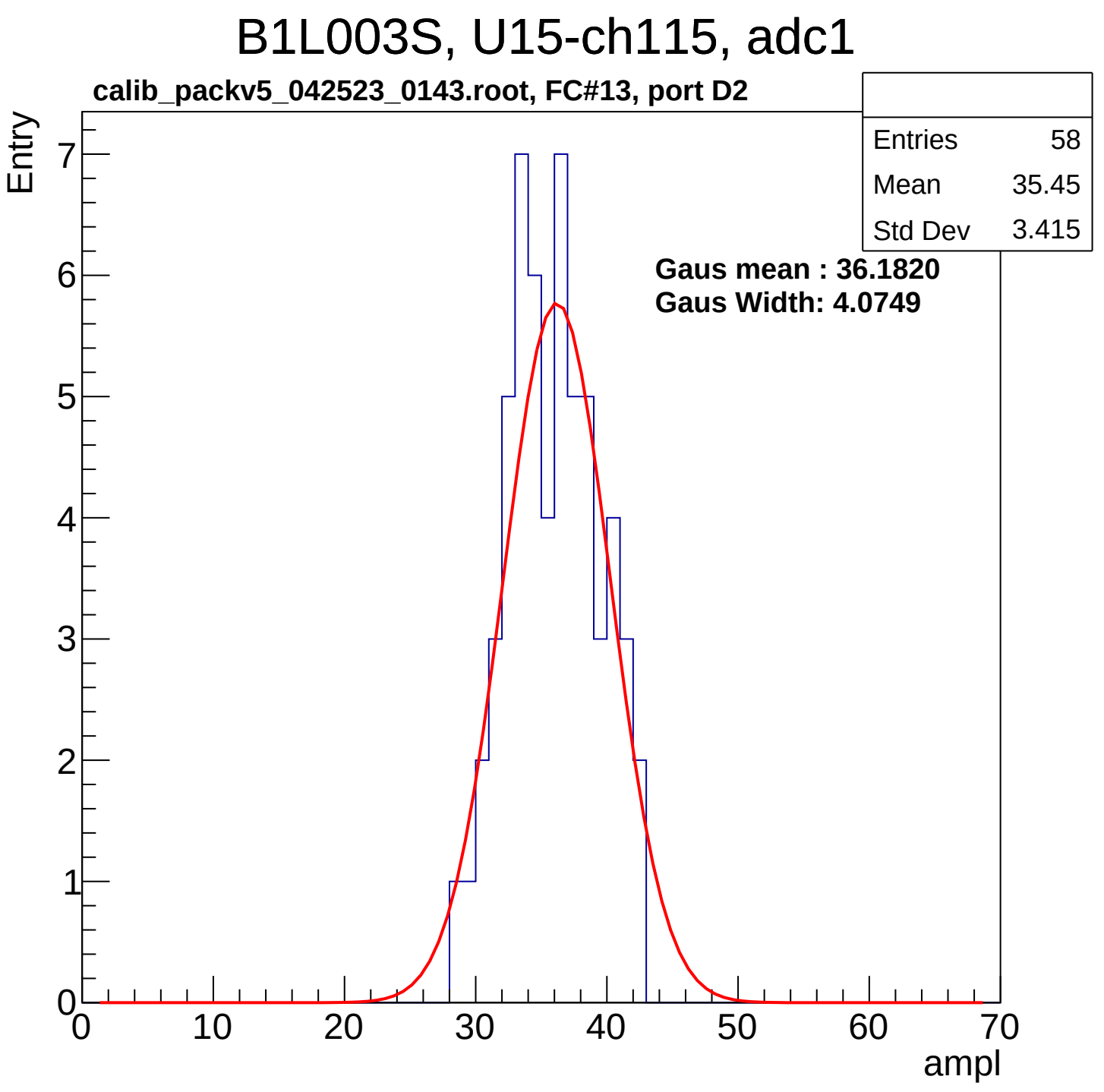
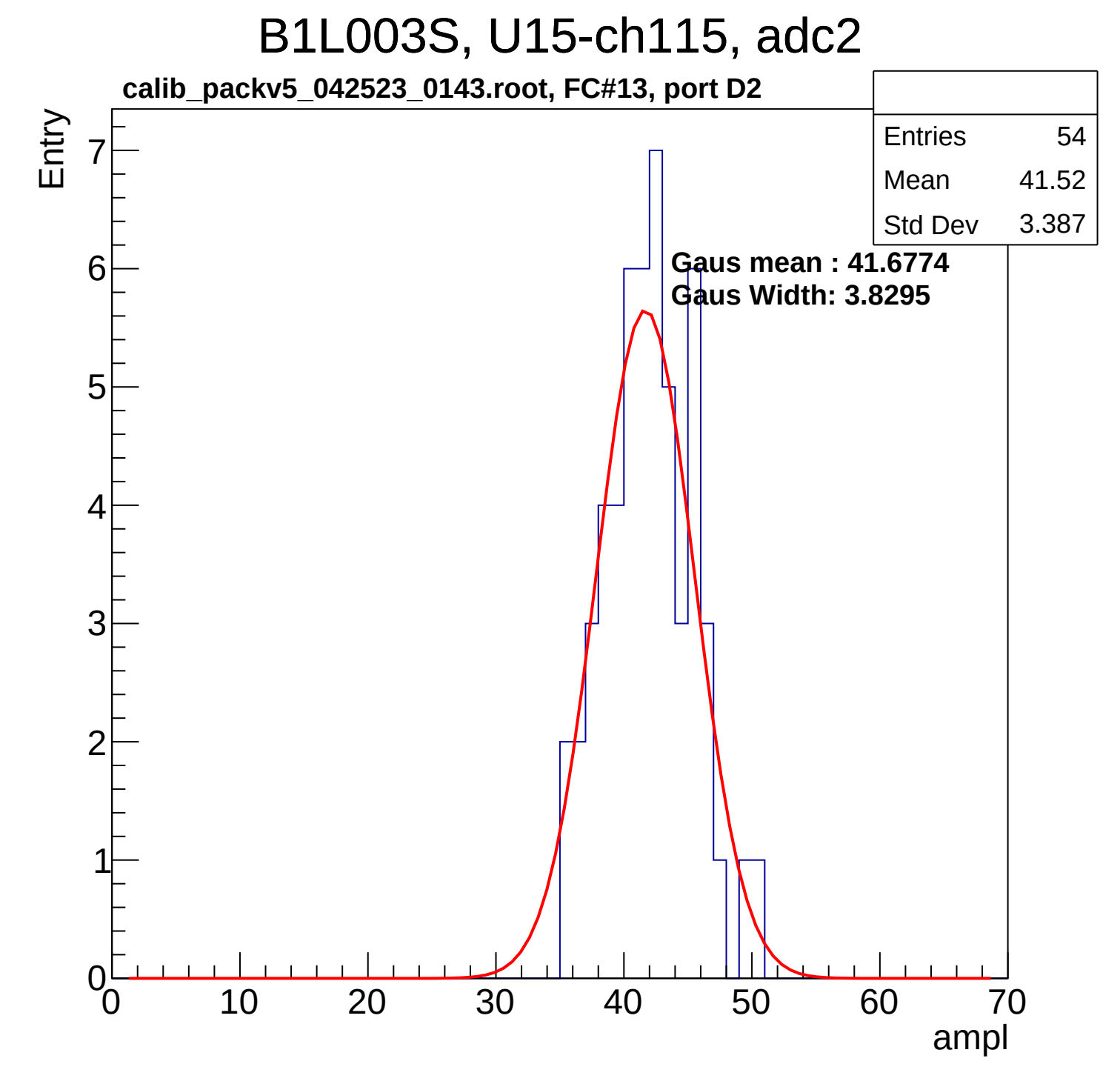

B1L003S, U15-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.33
Std Dev 1.795
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 25.2
Std Dev 30.86
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 29.61
Std Dev 5.286
Gaus mean : 30.4730
Gaus Width: 3.7678
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.45
Std Dev 3.415
Gaus mean : 36.1820
Gaus Width: 4.0749
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.52
Std Dev 3.387
Gaus mean : 41.6774
Gaus Width: 3.8295
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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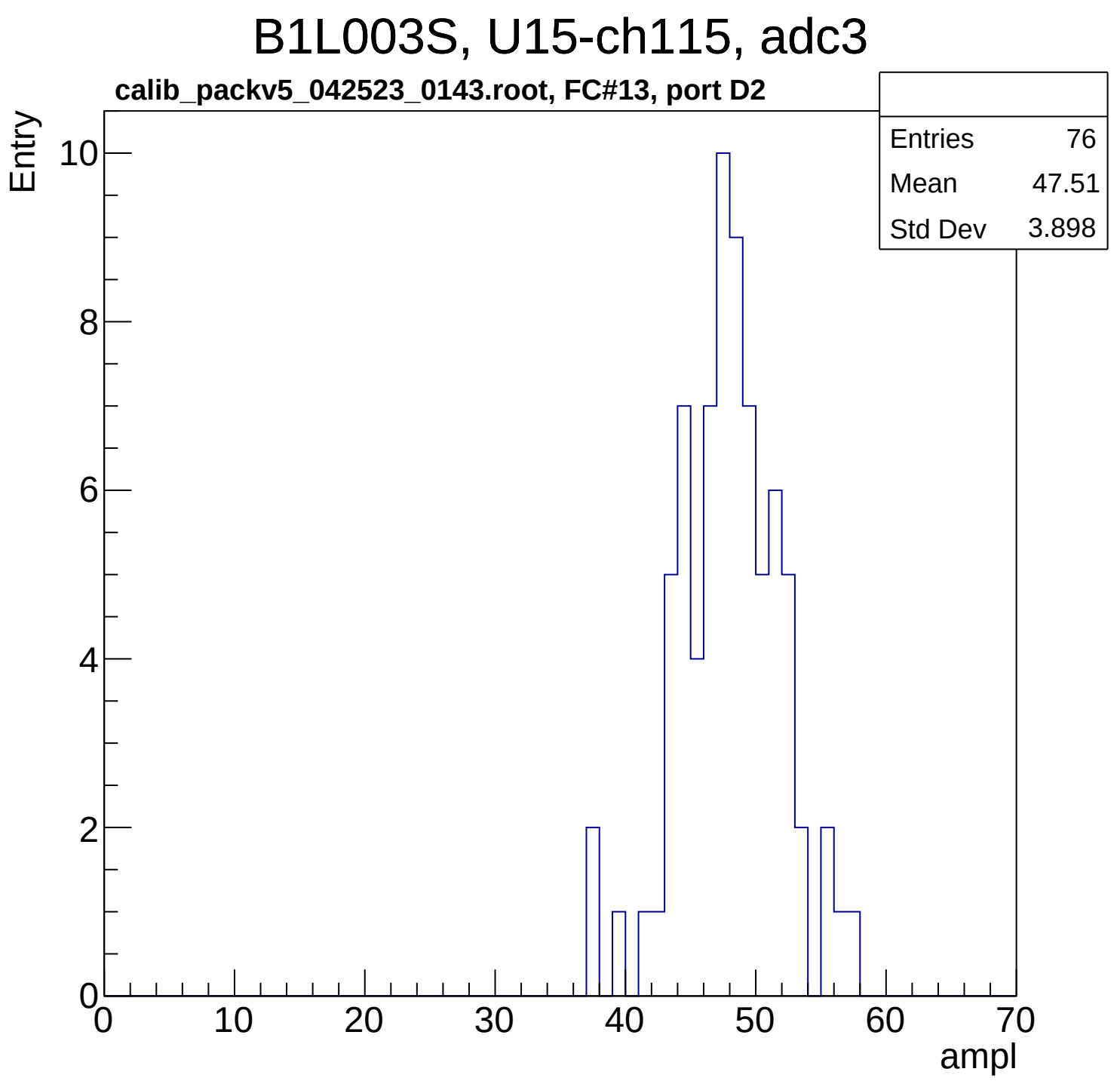
B1L003S, U15-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 47.51
Std Dev 3.898
Entry
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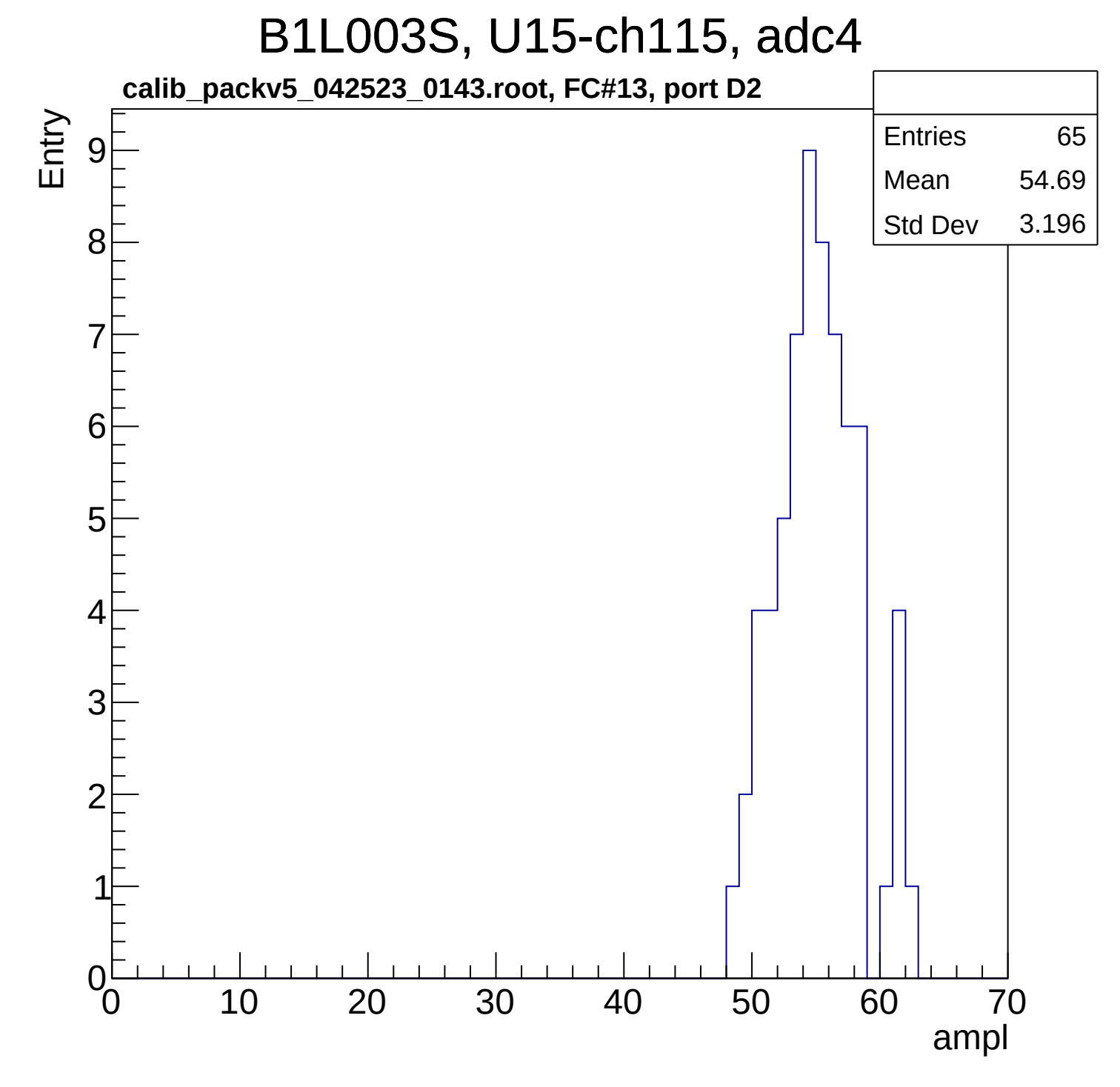
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.69
Std Dev 3.196
Entry
ampl
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4
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7
8
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0
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60
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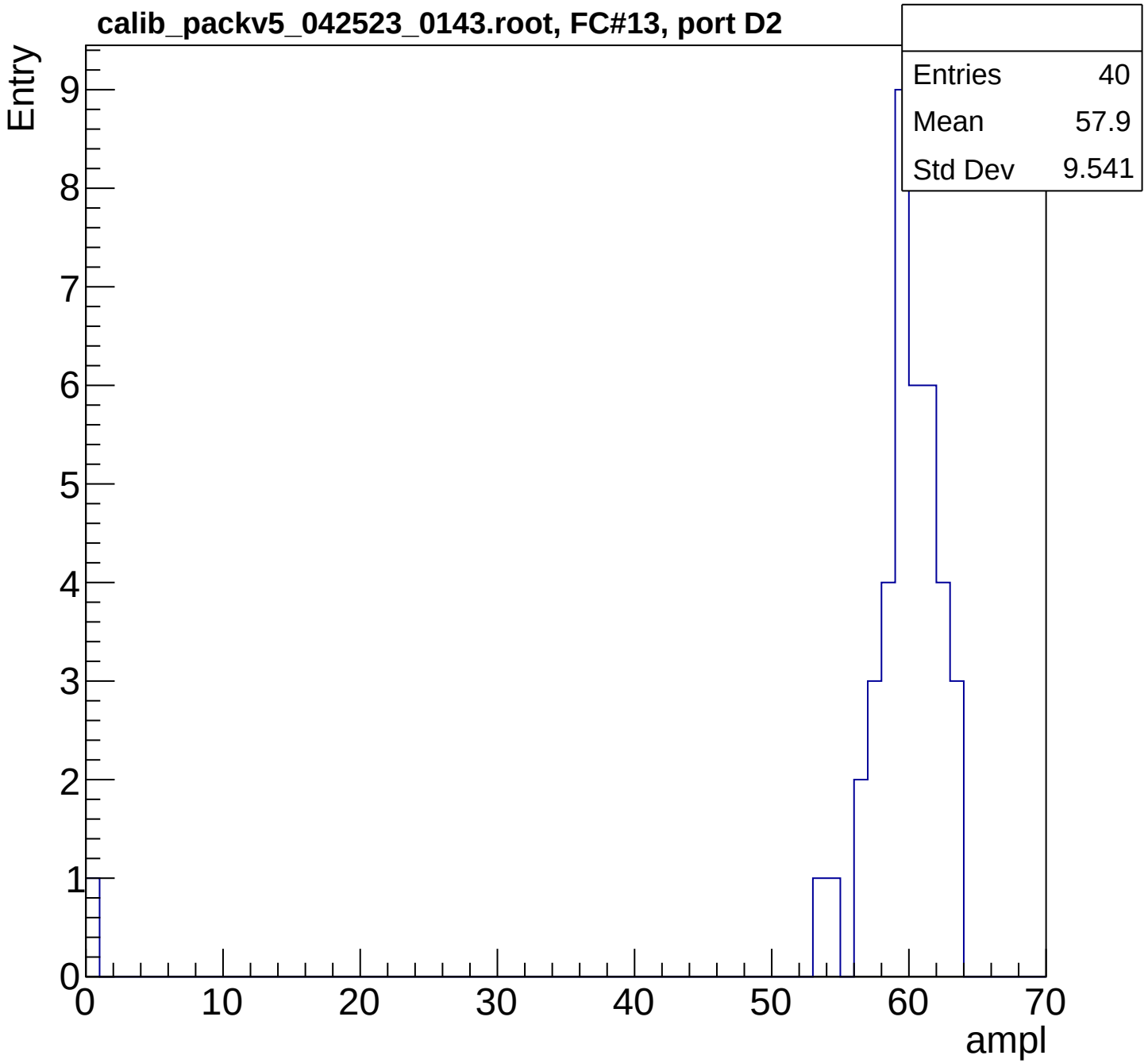
B1L003S, U15-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 57.9
Std Dev 9.541
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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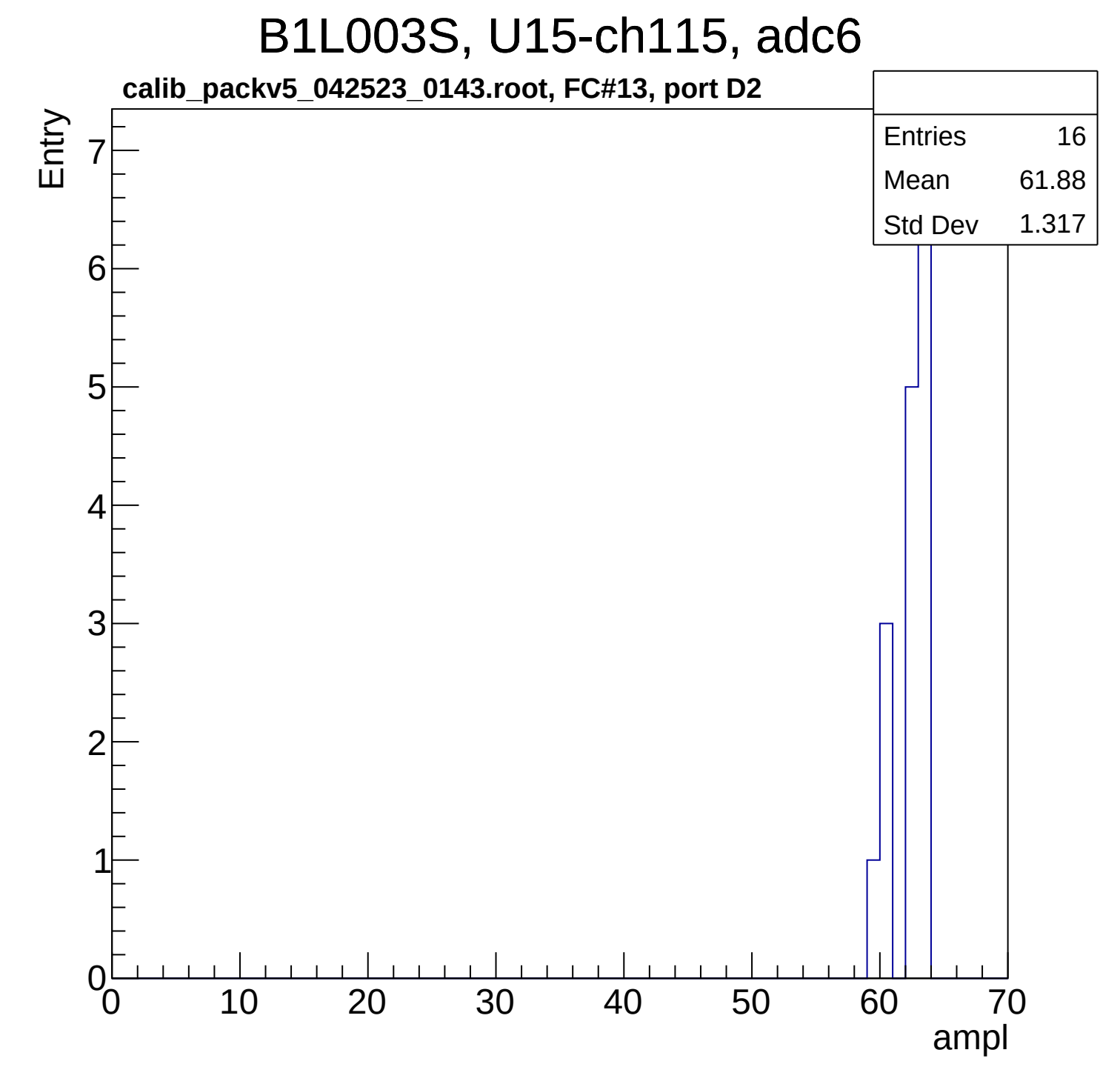
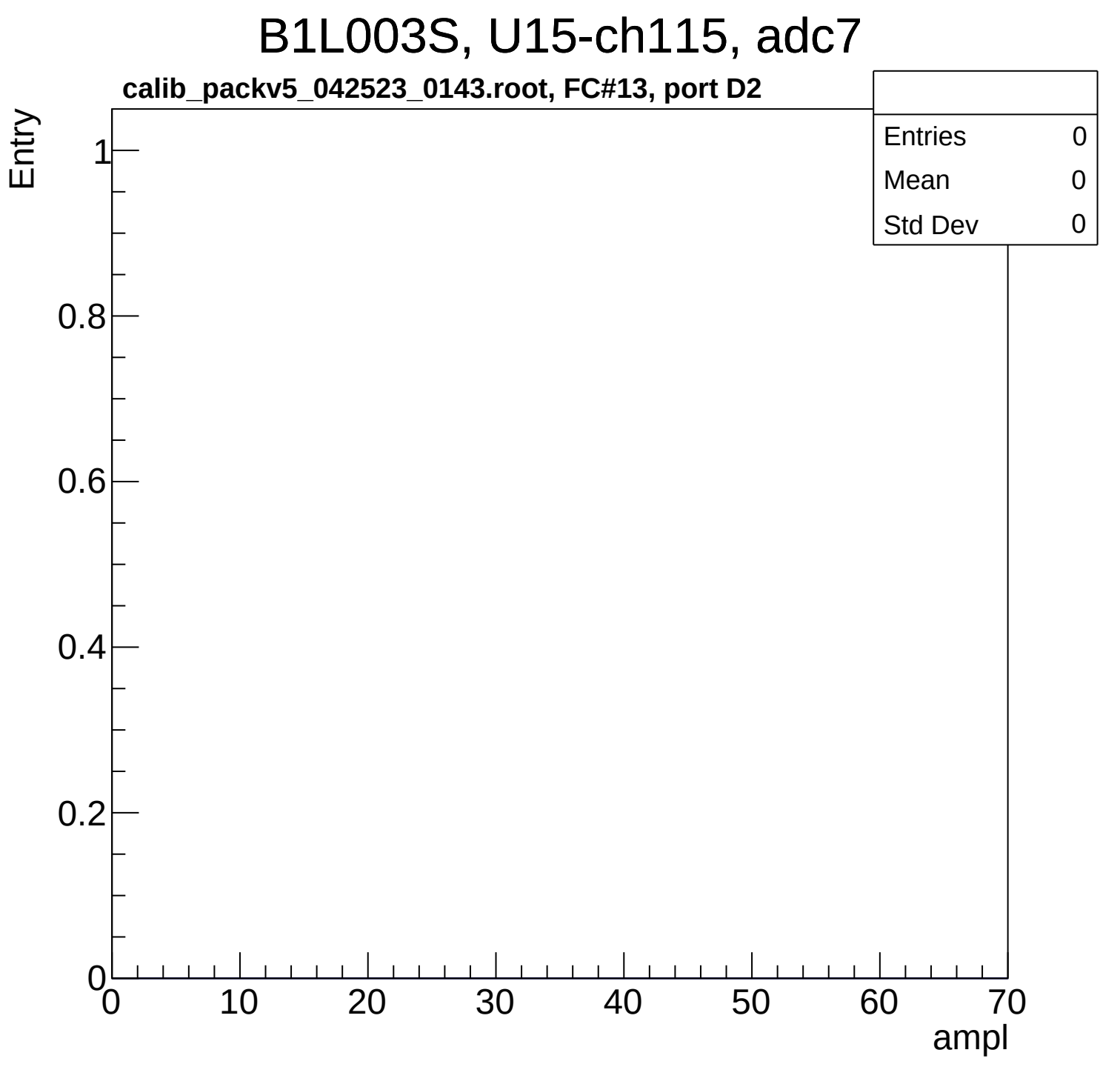

B1L003S, U15-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.88
Std Dev 1.317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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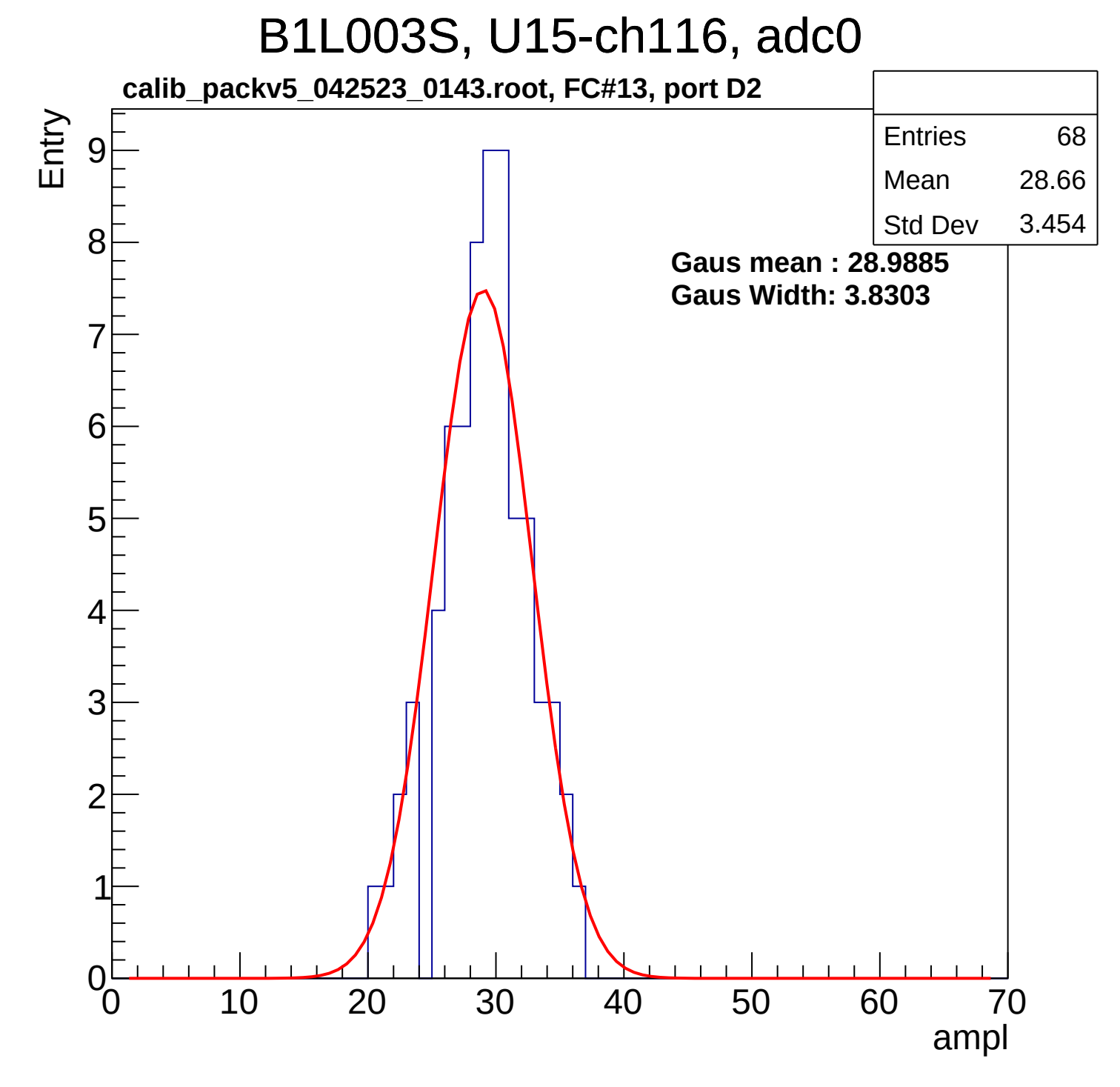
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.66
Std Dev 3.454
Gaus mean : 28.9885
Gaus Width: 3.8303
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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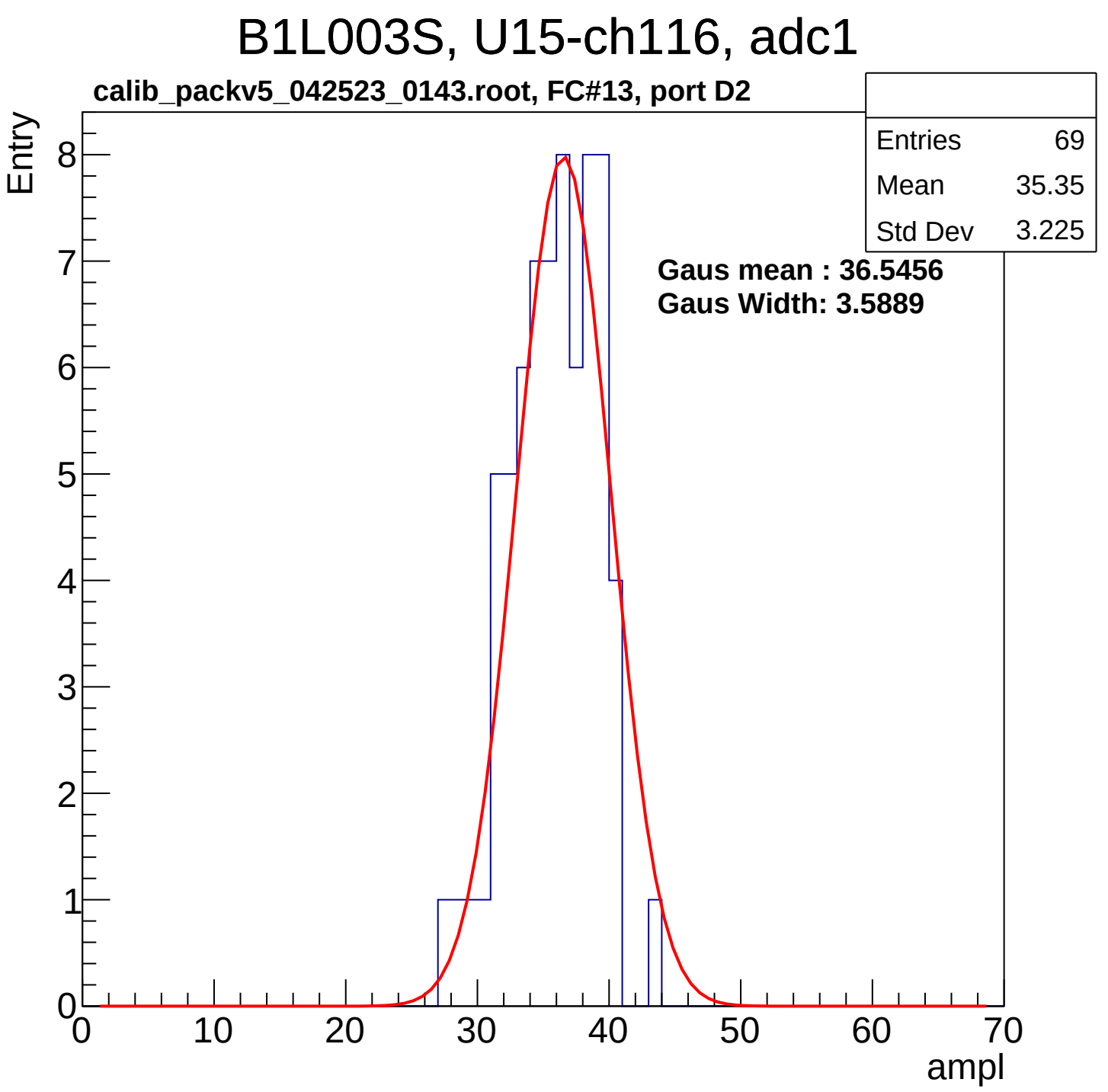
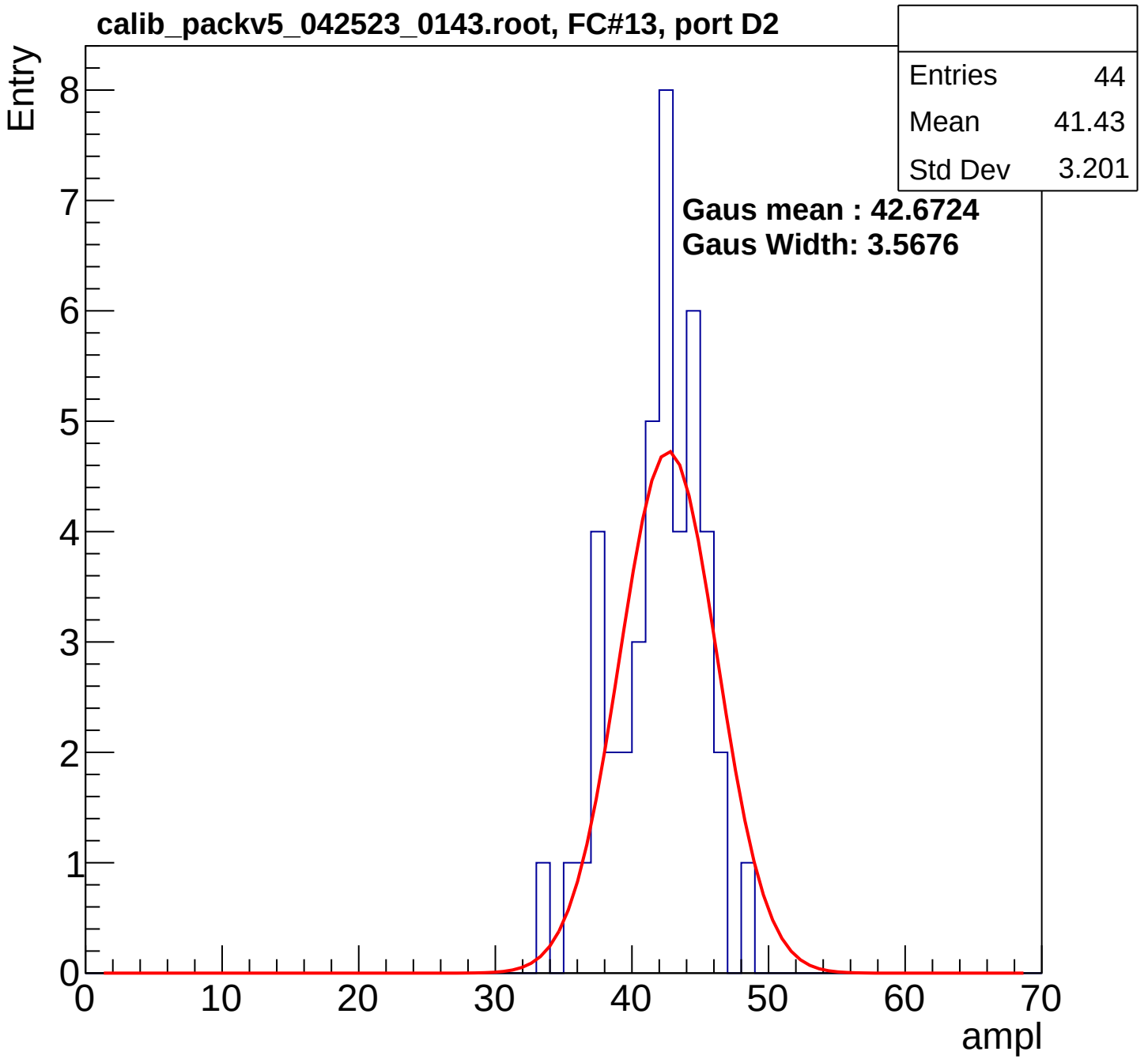
60
70


B1L003S, U15-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 35.35
Std Dev 3.225
Gaus mean : 36.5456
Gaus Width: 3.5889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 41.43
Std Dev 3.201
Gaus mean : 42.6724
Gaus Width: 3.5676
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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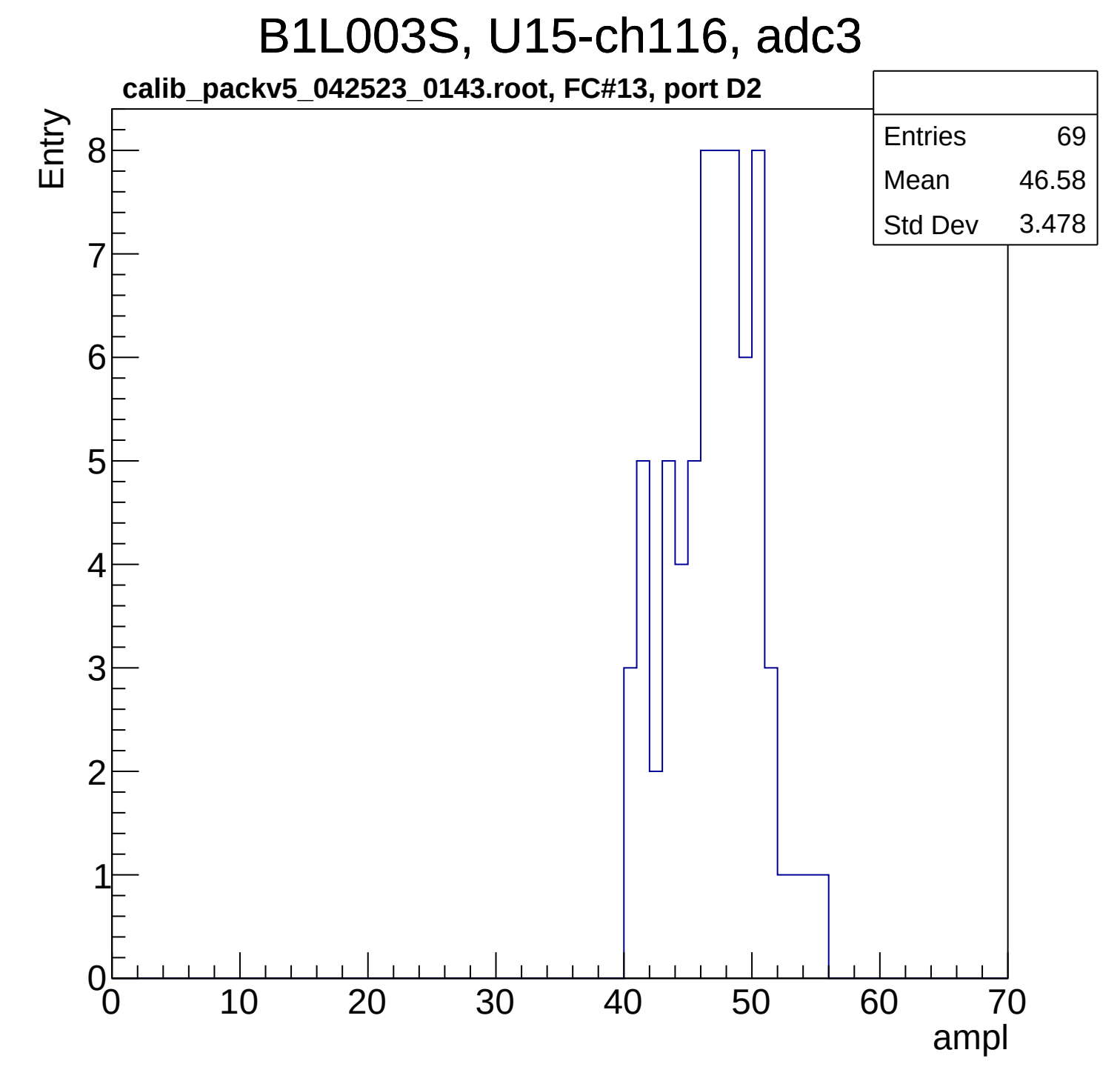
B1L003S, U15-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 46.58
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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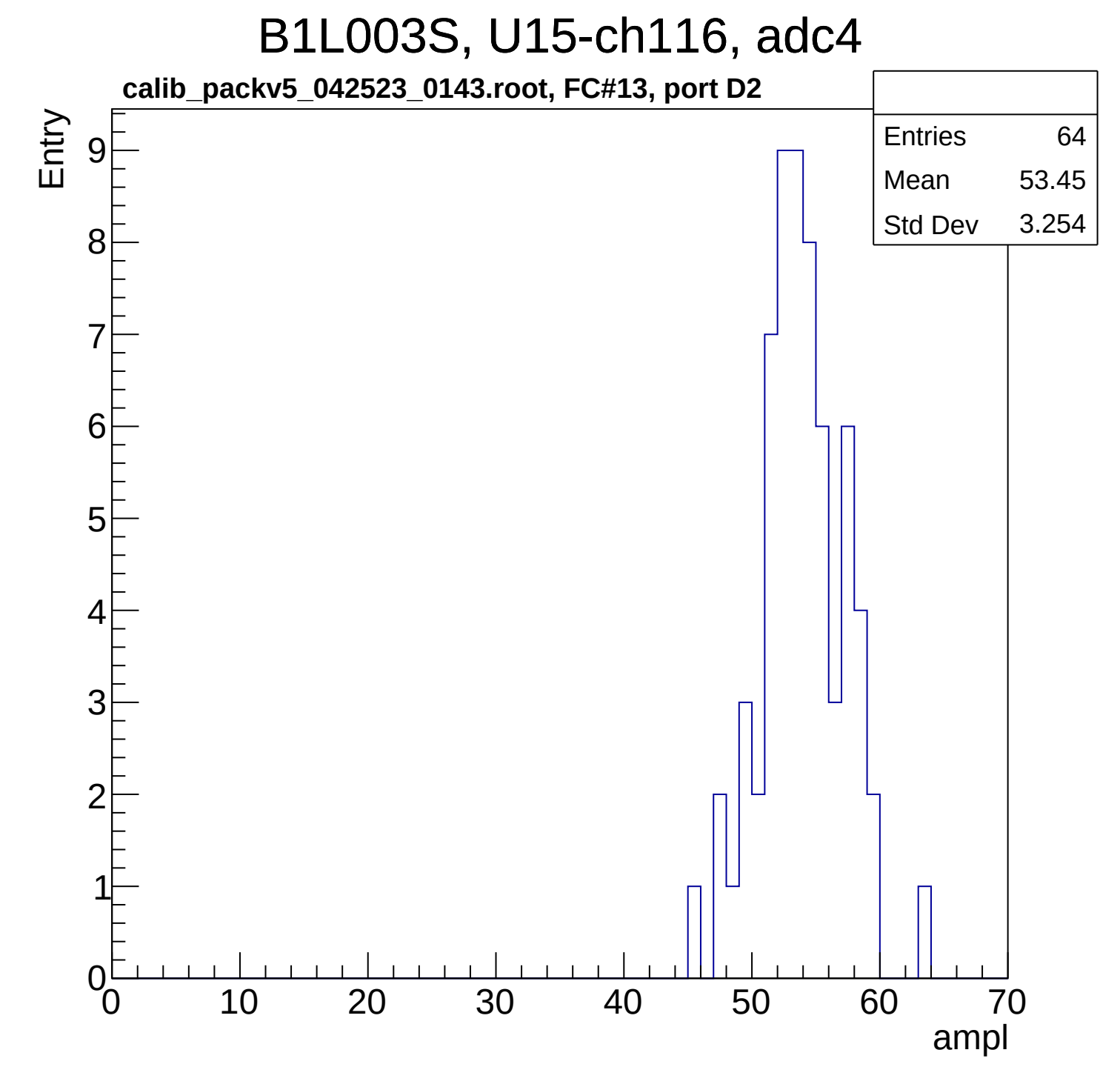
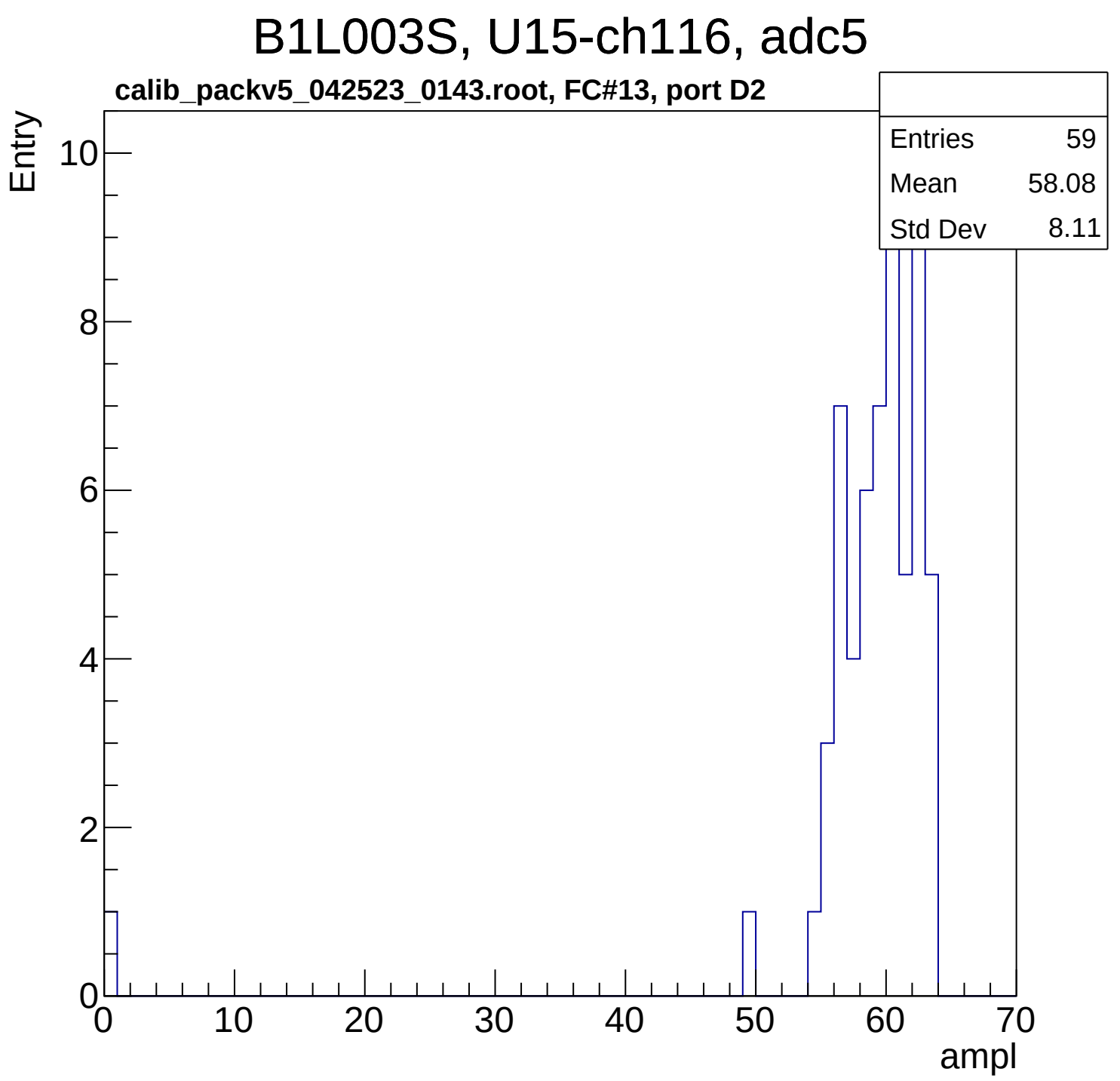
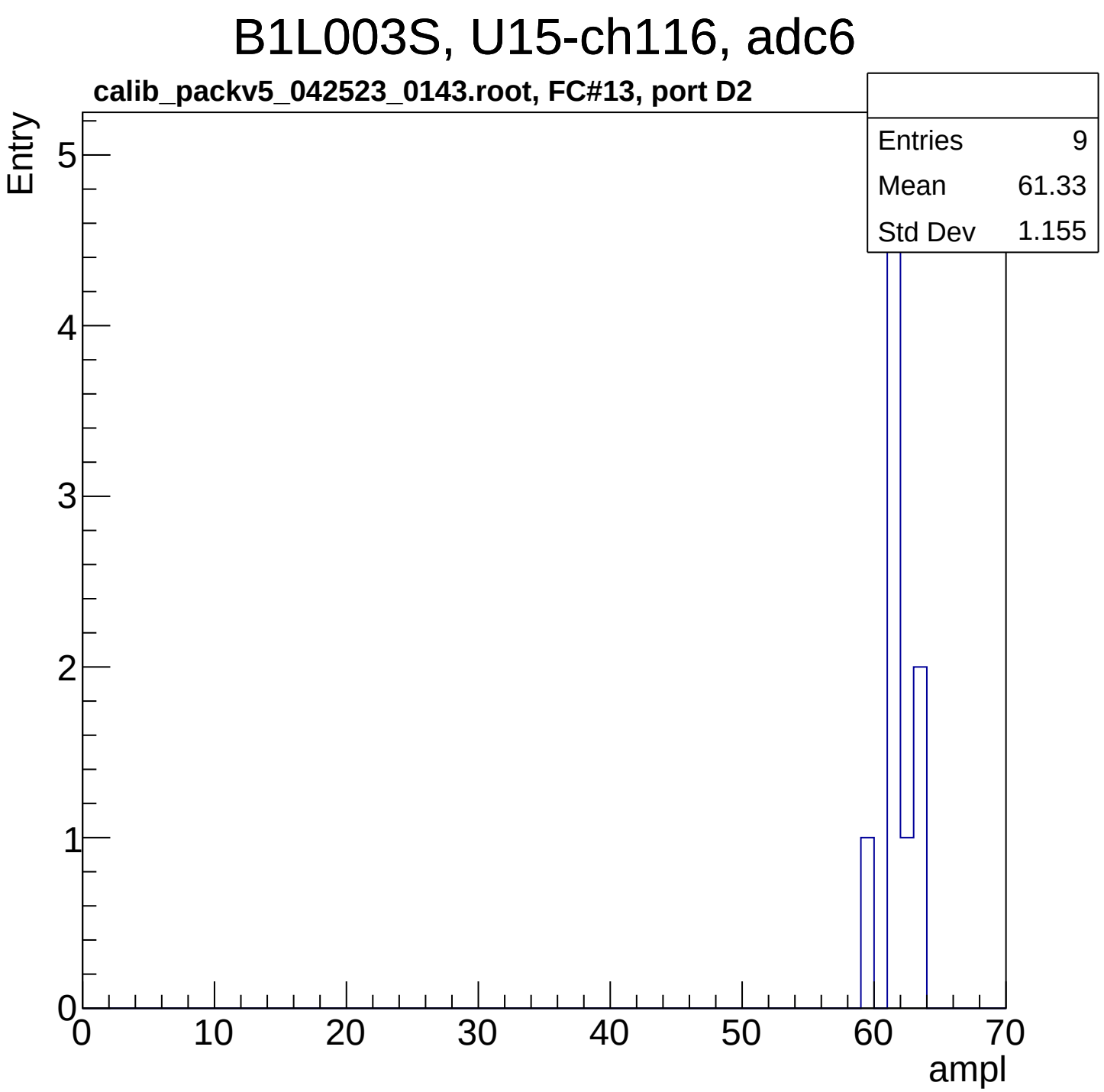
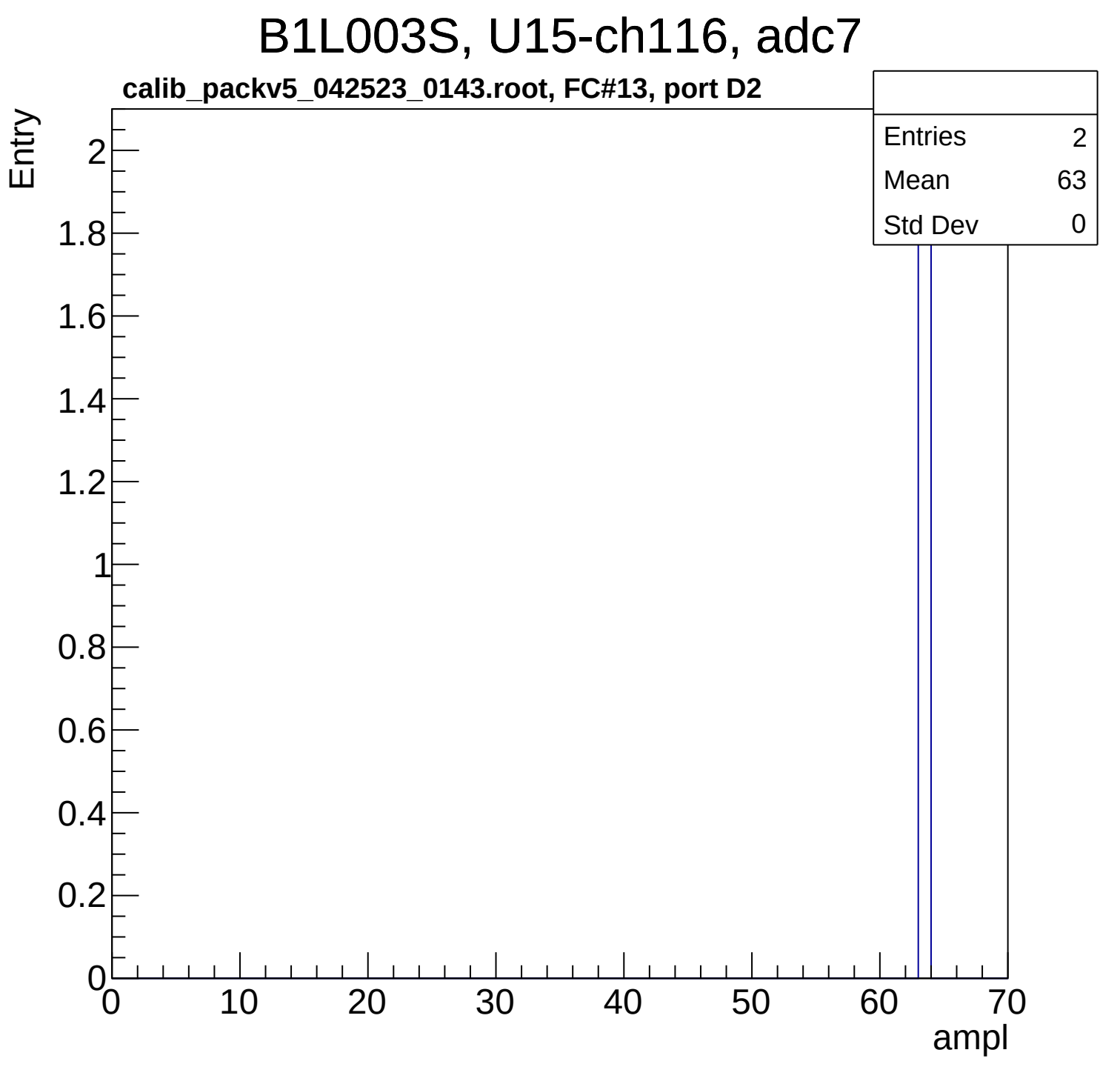

B1L003S, U15-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 53.45
Std Dev 3.254
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.08
Std Dev 8.11
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.33
Std Dev 1.155
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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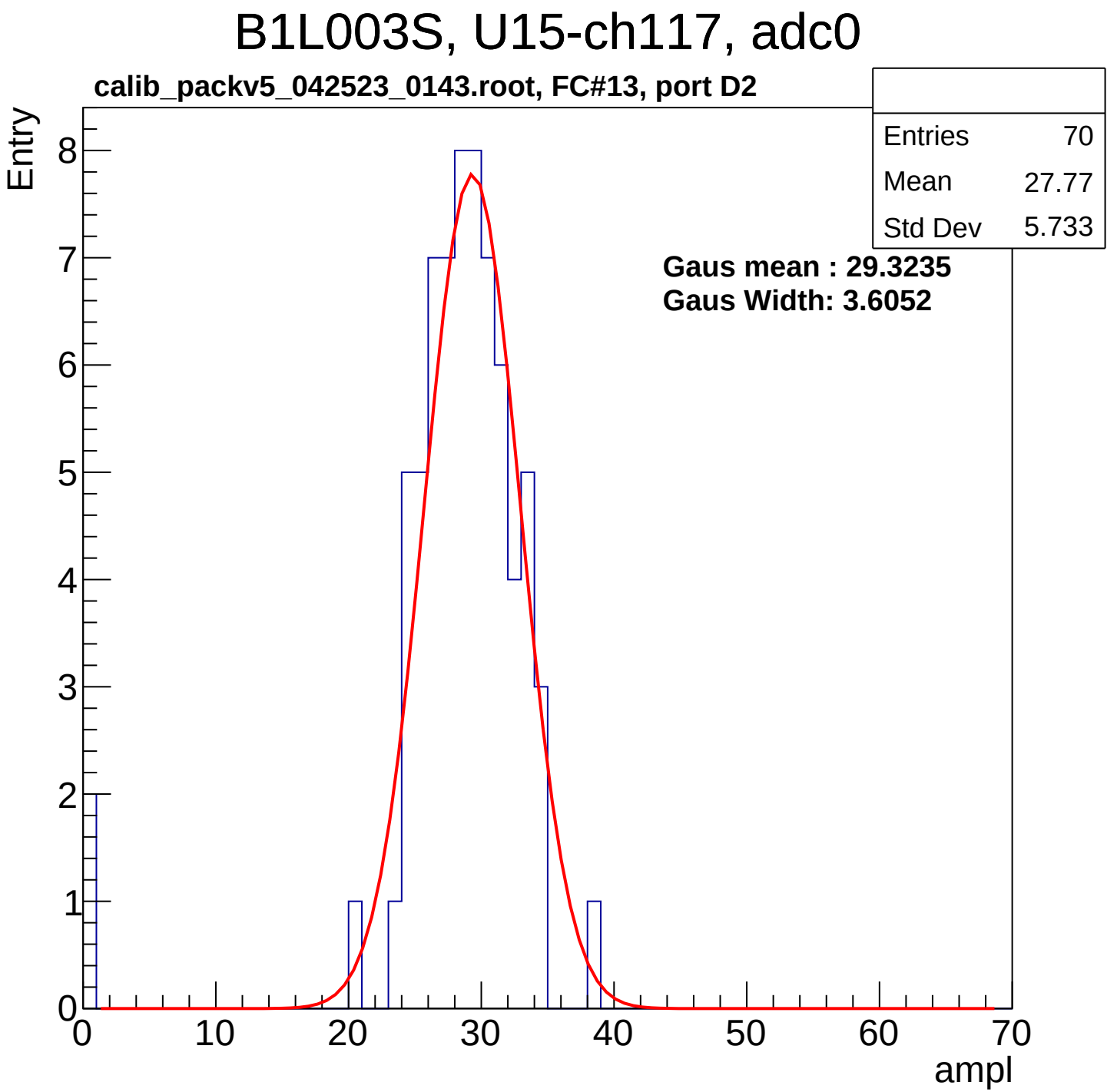
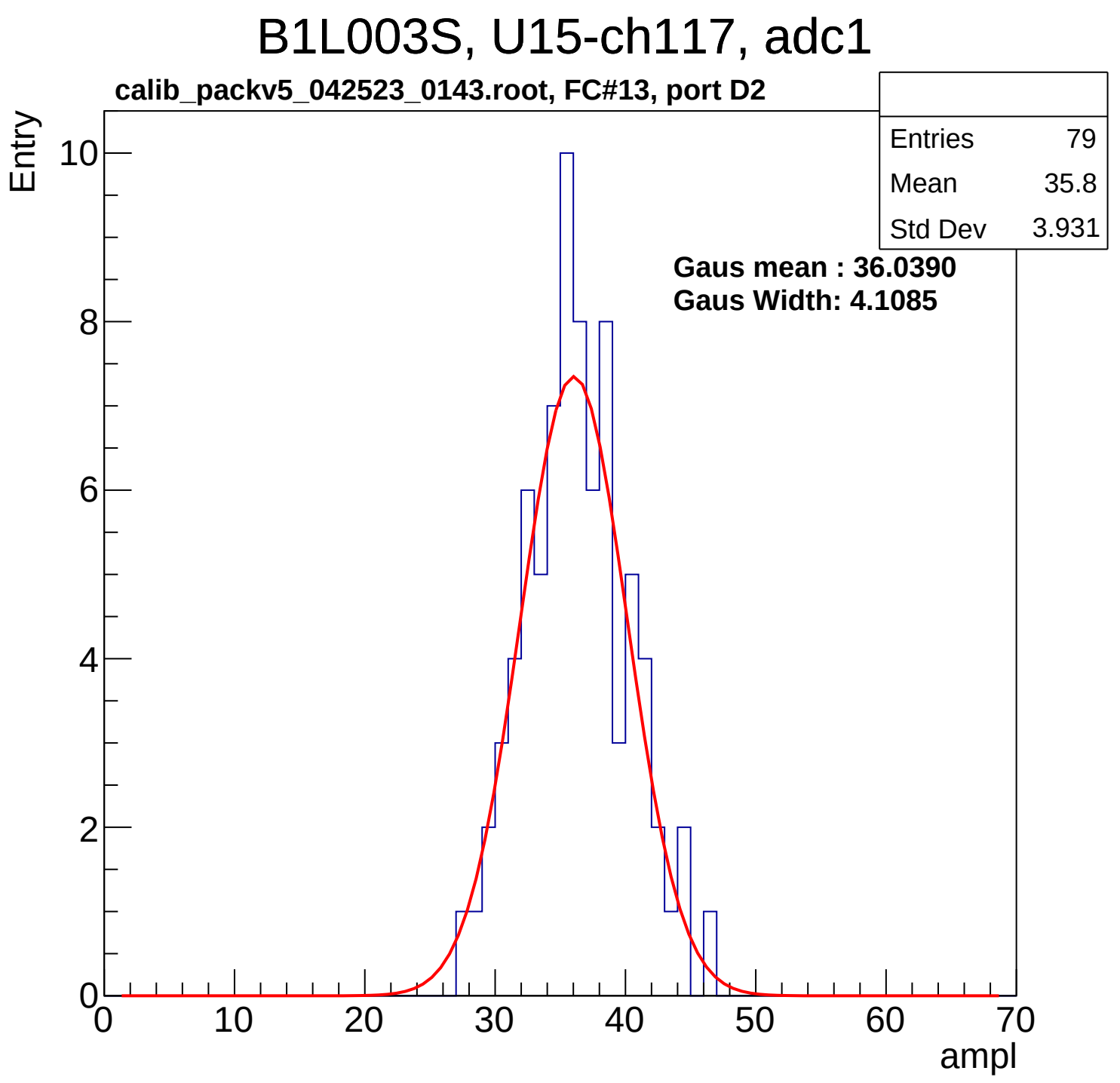
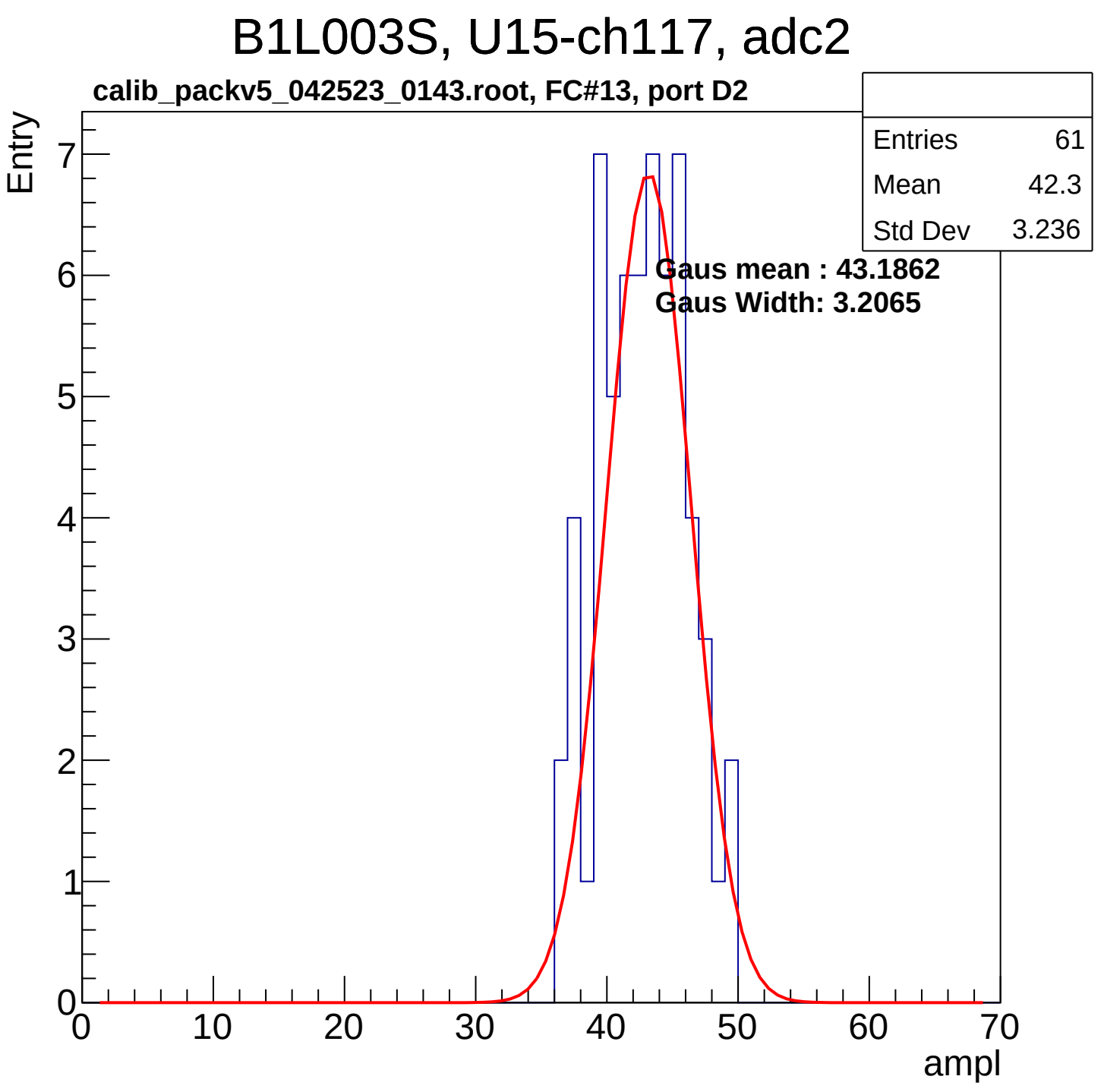
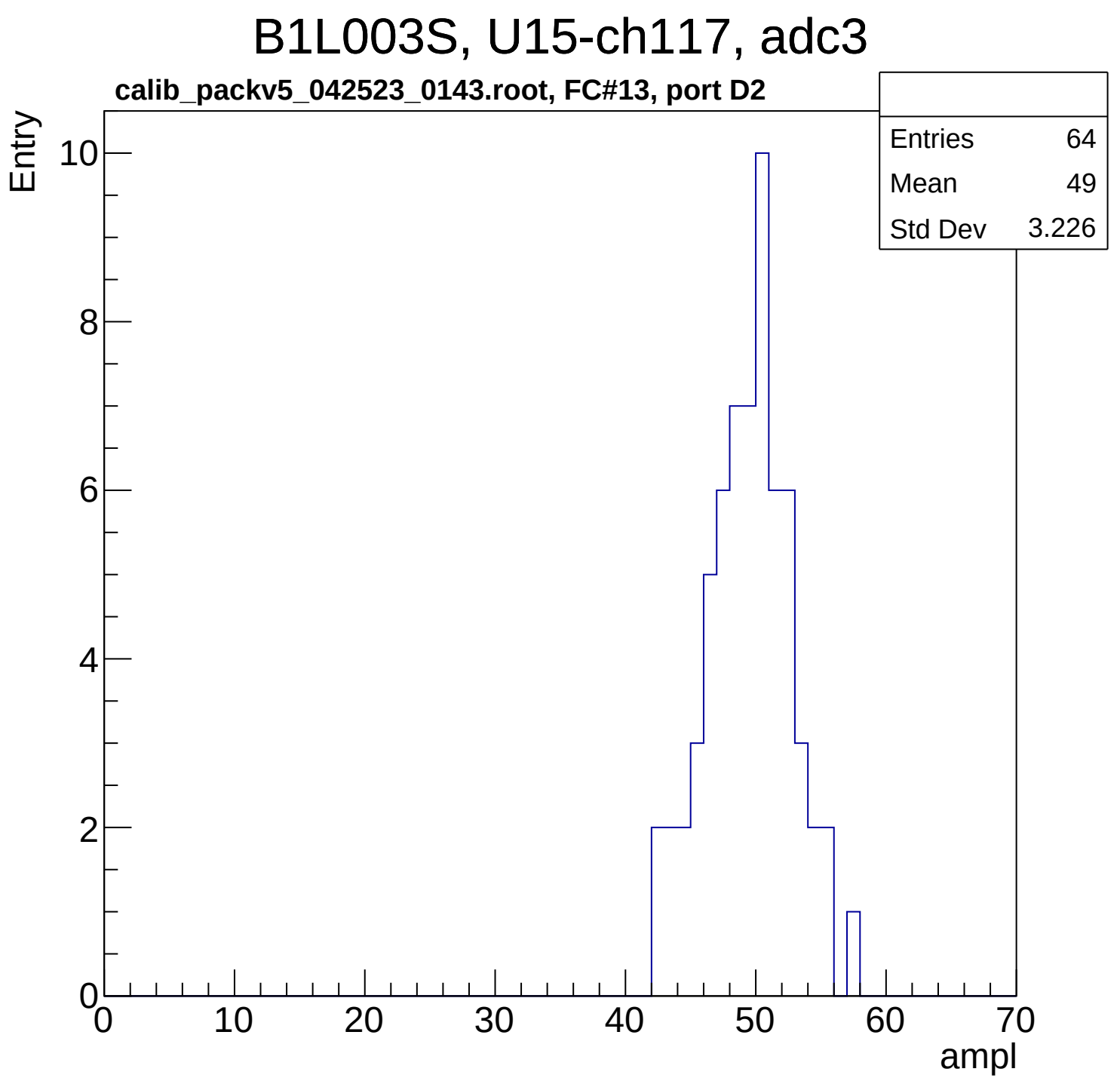
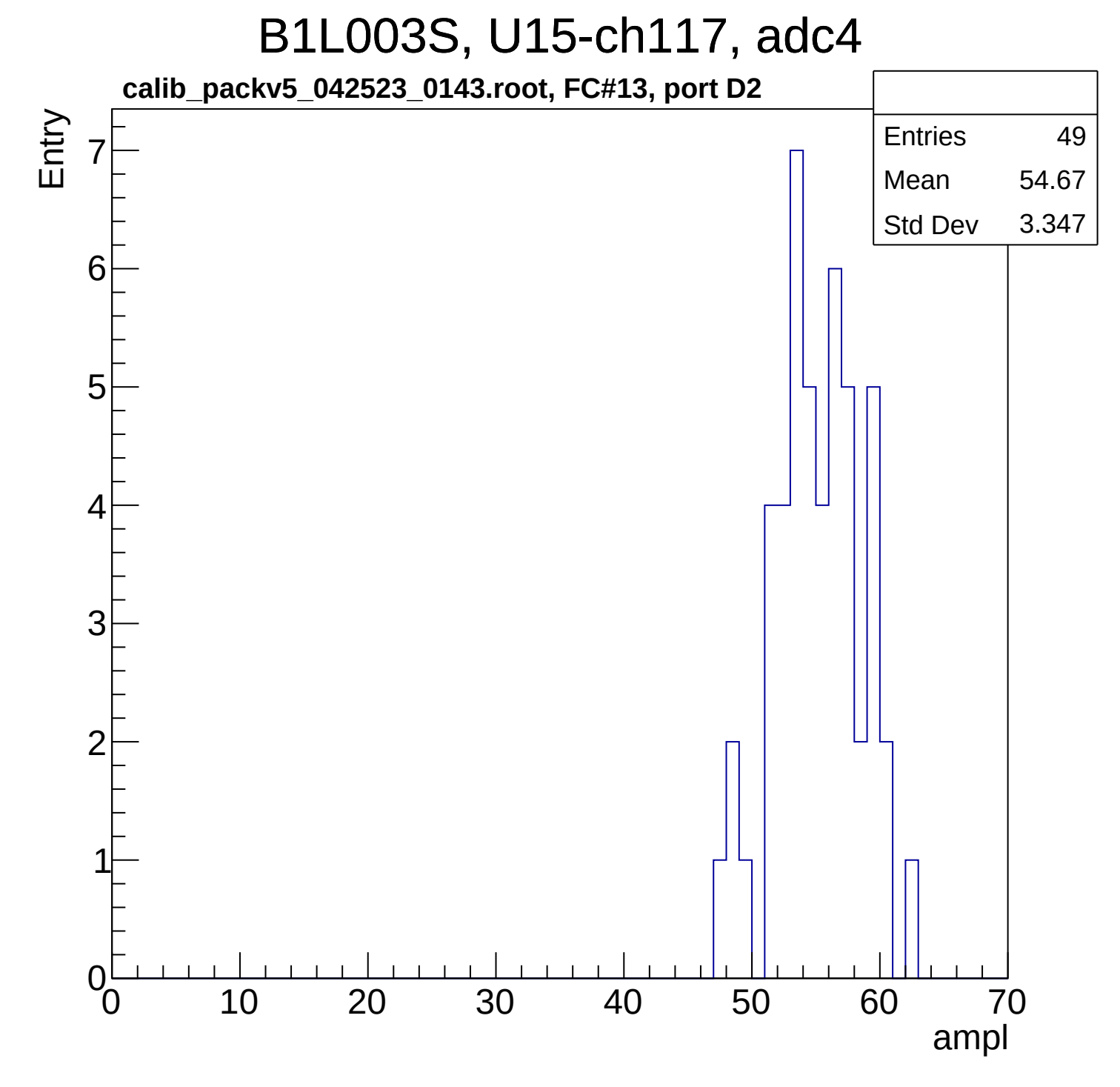
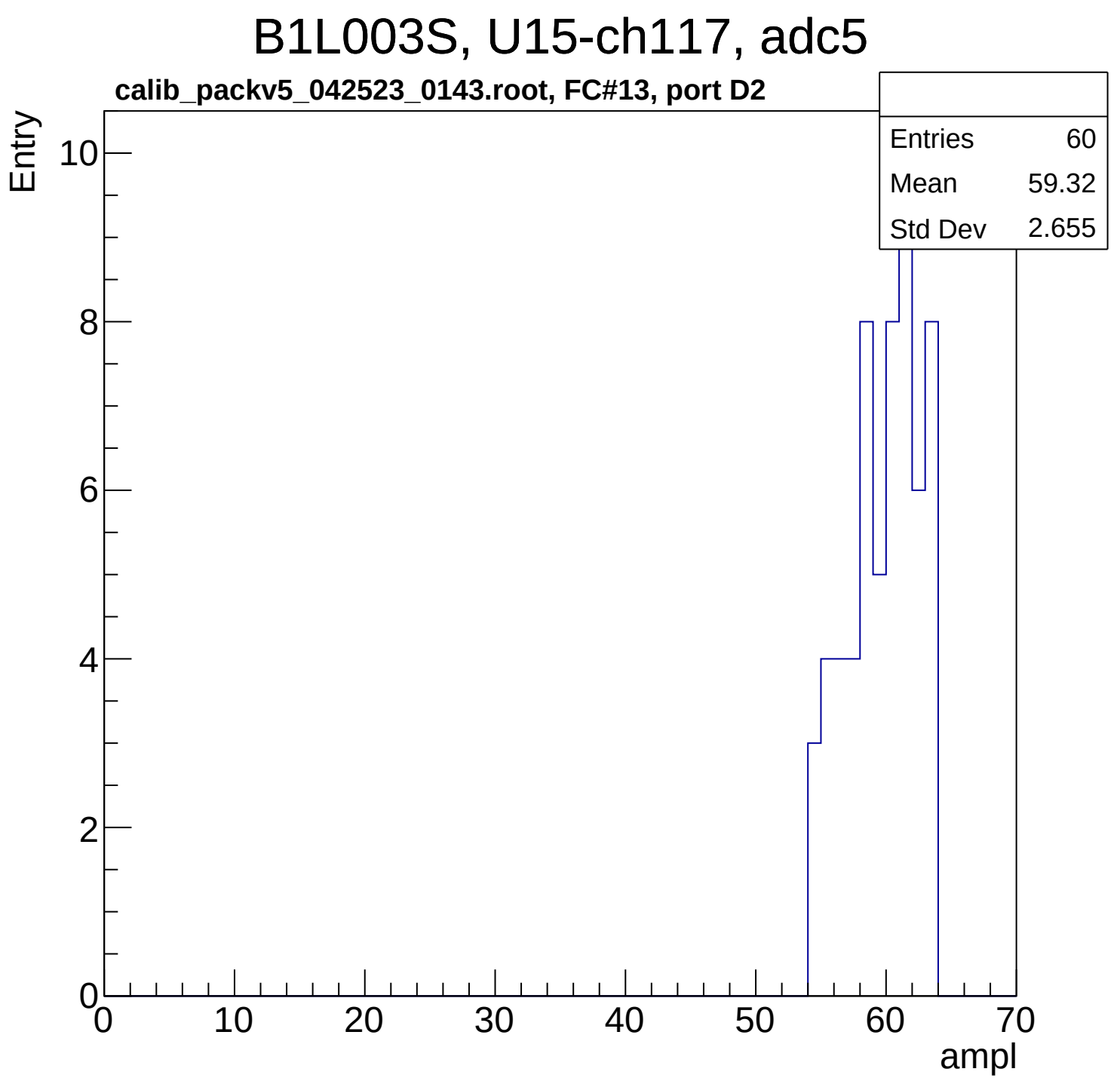

B1L003S, U15-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 27.77
Std Dev 5.733
Gaus mean : 29.3235
Gaus Width: 3.6052
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

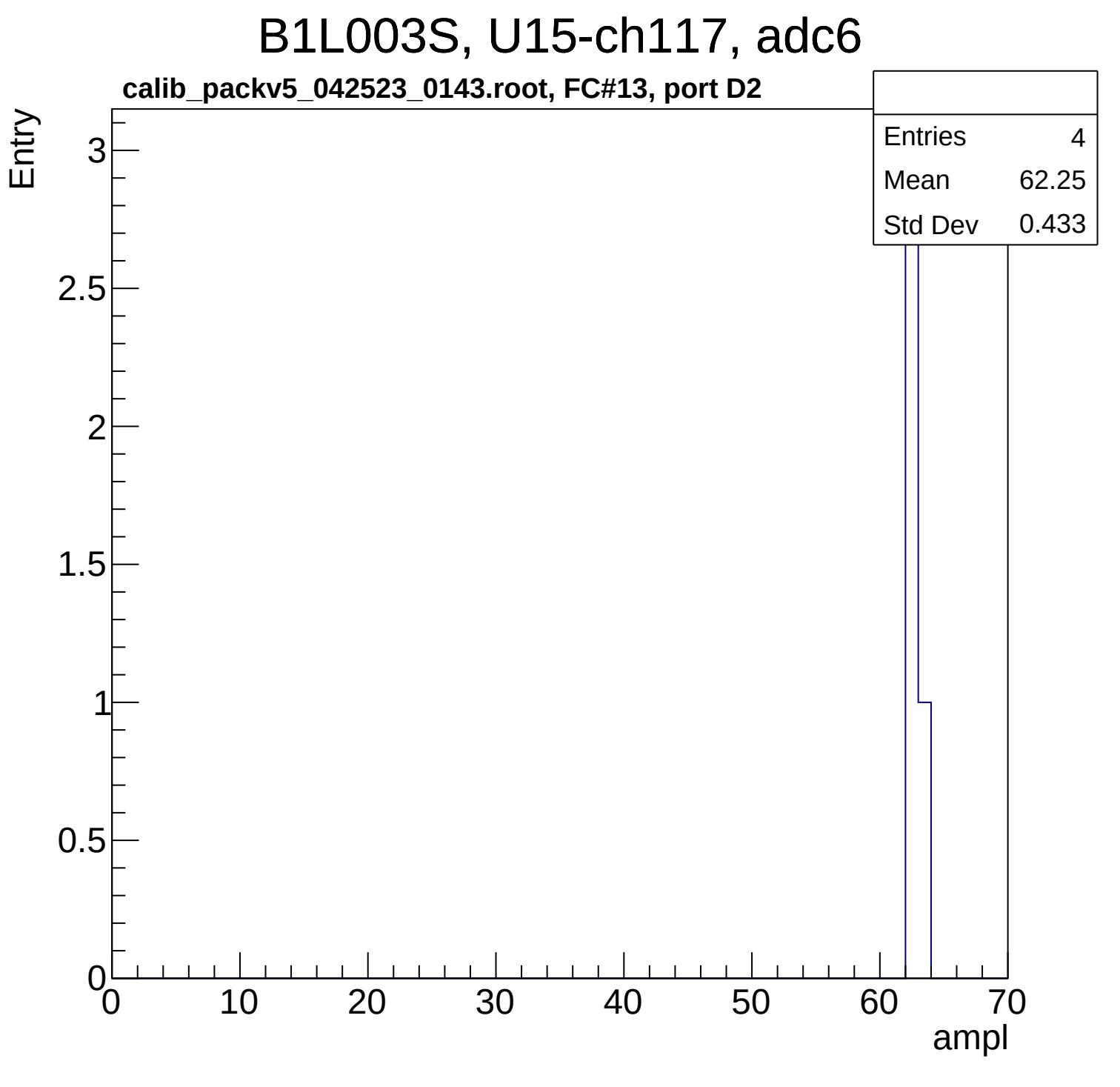

B1L003S, U15-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 35.8
Std Dev 3.931
Gaus mean : 36.0390
Gaus Width: 4.1085
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

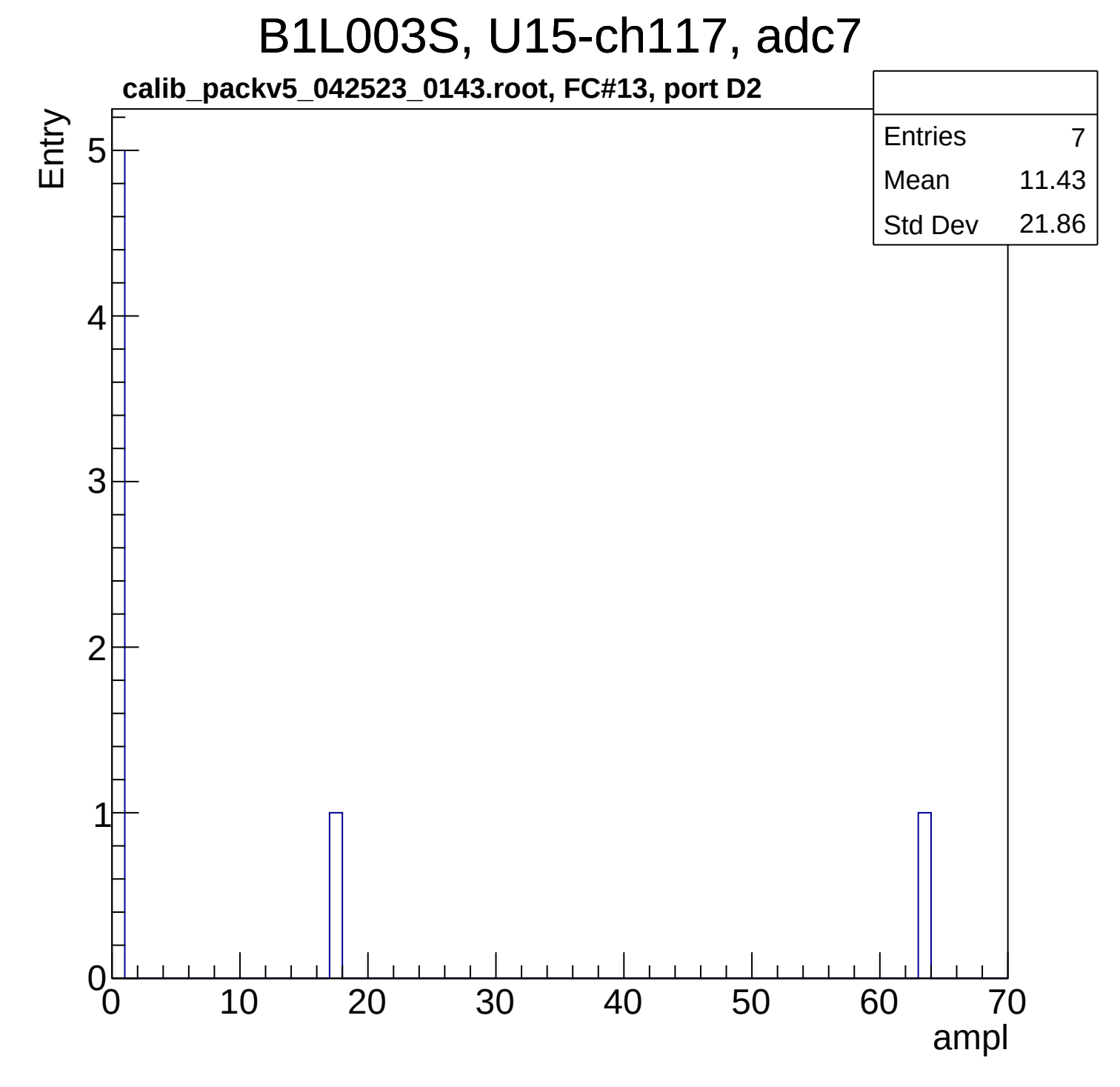

B1L003S, U15-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.3
Std Dev 3.236
Gaus mean : 43.1862
Gaus Width: 3.2065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

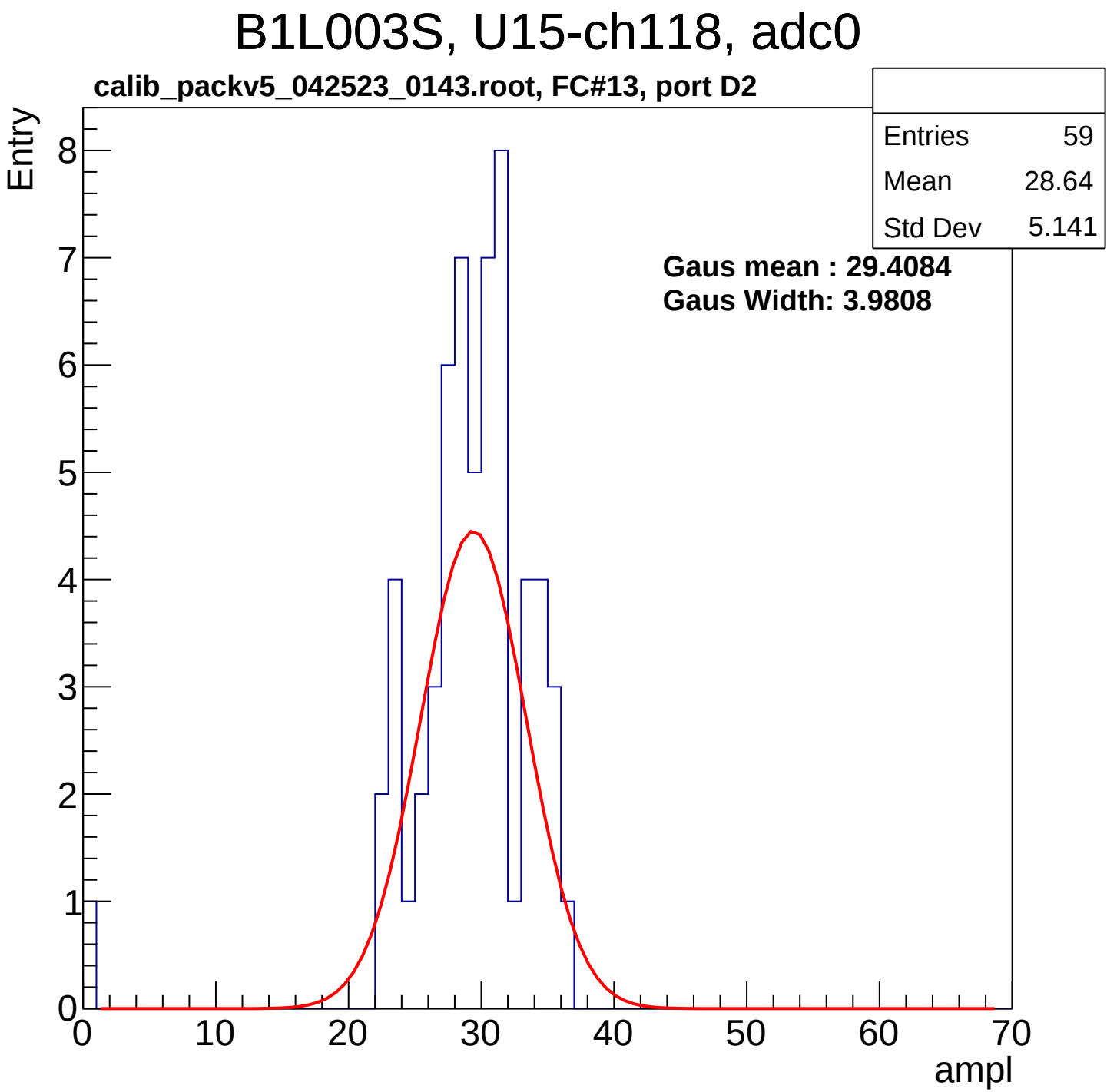

B1L003S, U15-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49
Std Dev 3.226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

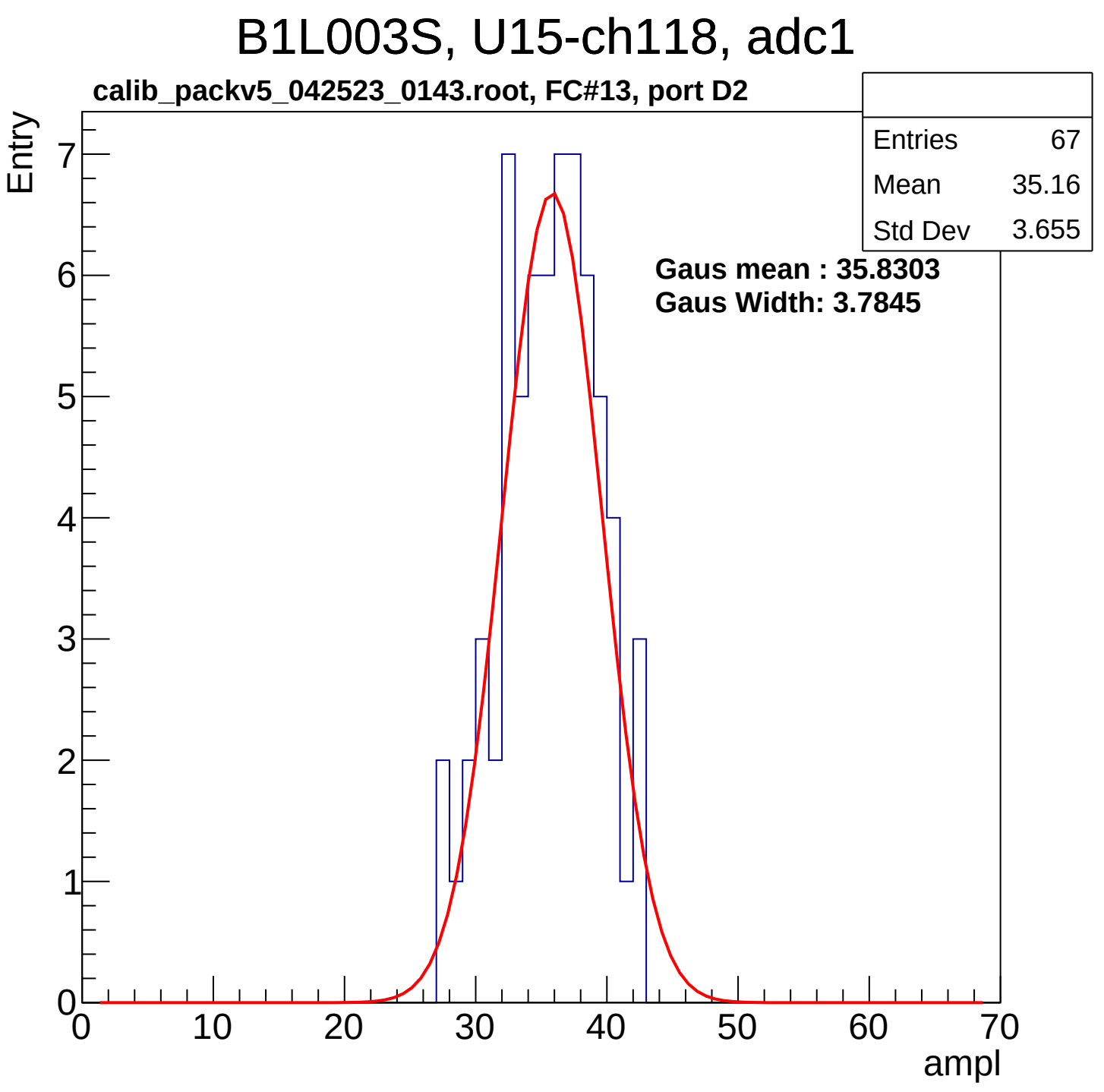

B1L003S, U15-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.67
Std Dev 3.347
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

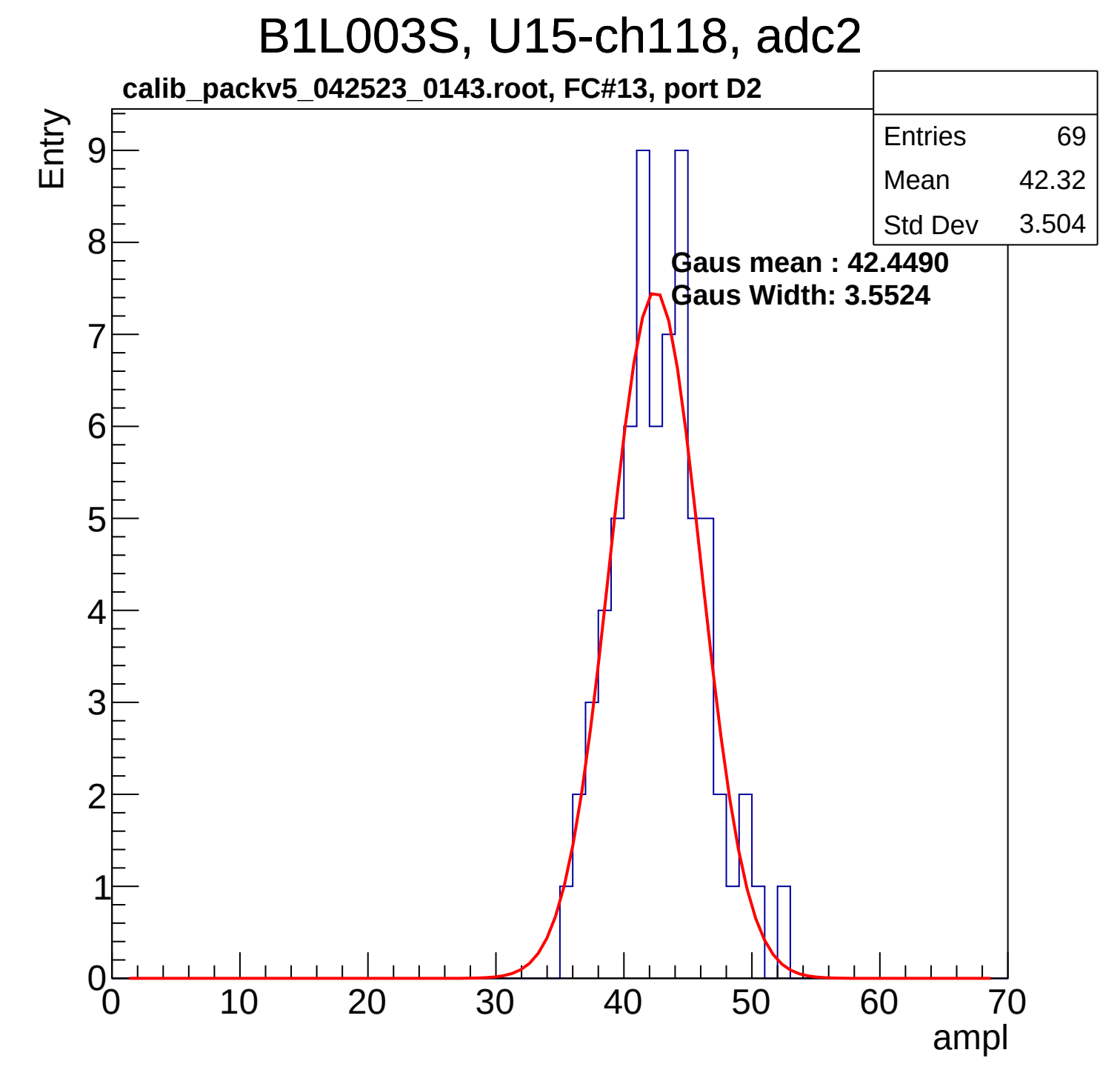

B1L003S, U15-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 59.32
Std Dev 2.655
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

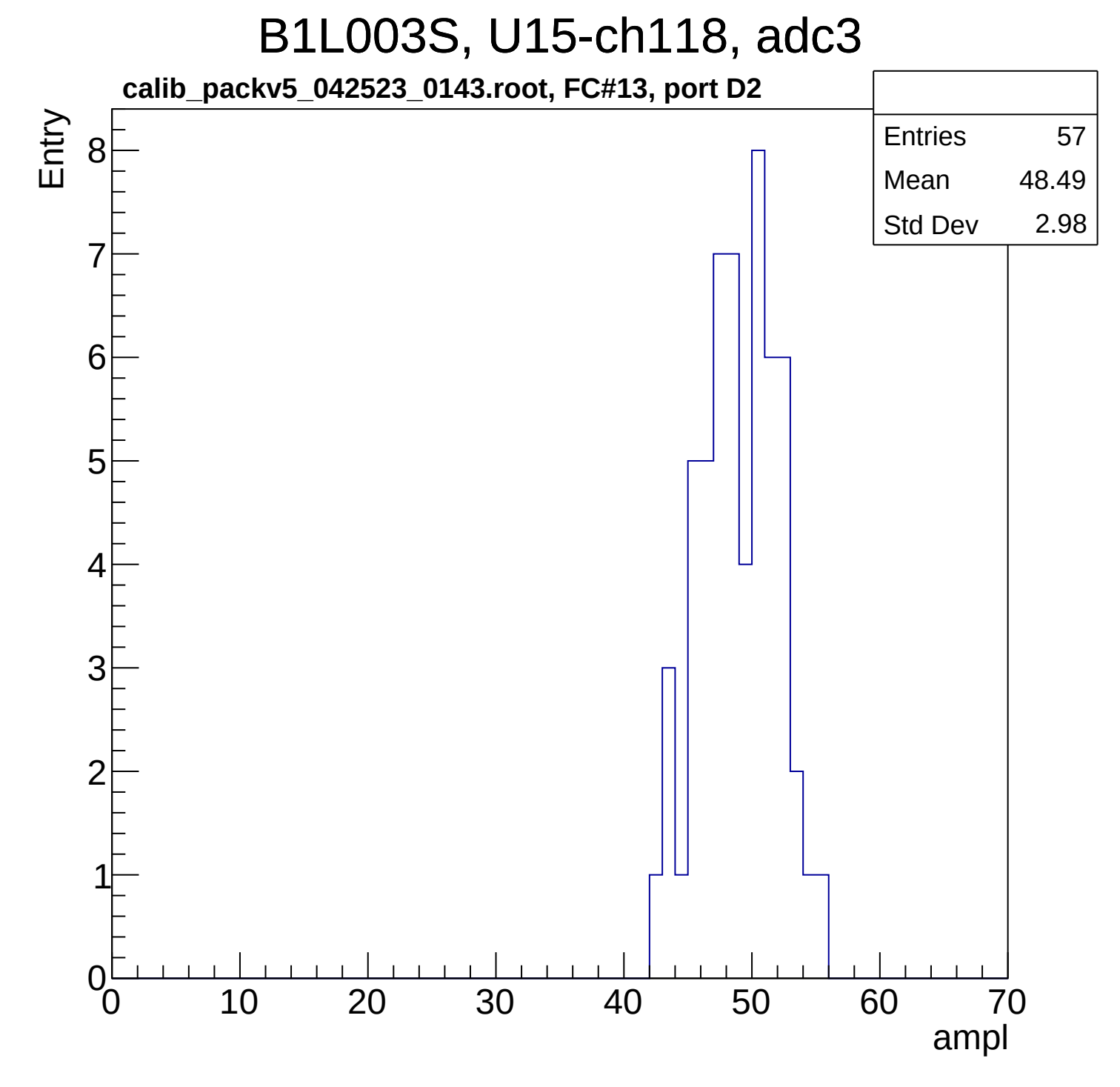

B1L003S, U15-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U15-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 11.43
Std Dev 21.86
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.64
Std Dev 5.141
Gaus mean : 29.4084
Gaus Width: 3.9808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.16
Std Dev 3.655
Gaus mean : 35.8303
Gaus Width: 3.7845
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.32
Std Dev 3.504
Gaus mean : 42.4490
Gaus Width: 3.5524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.49
Std Dev 2.98
Entry
ampl
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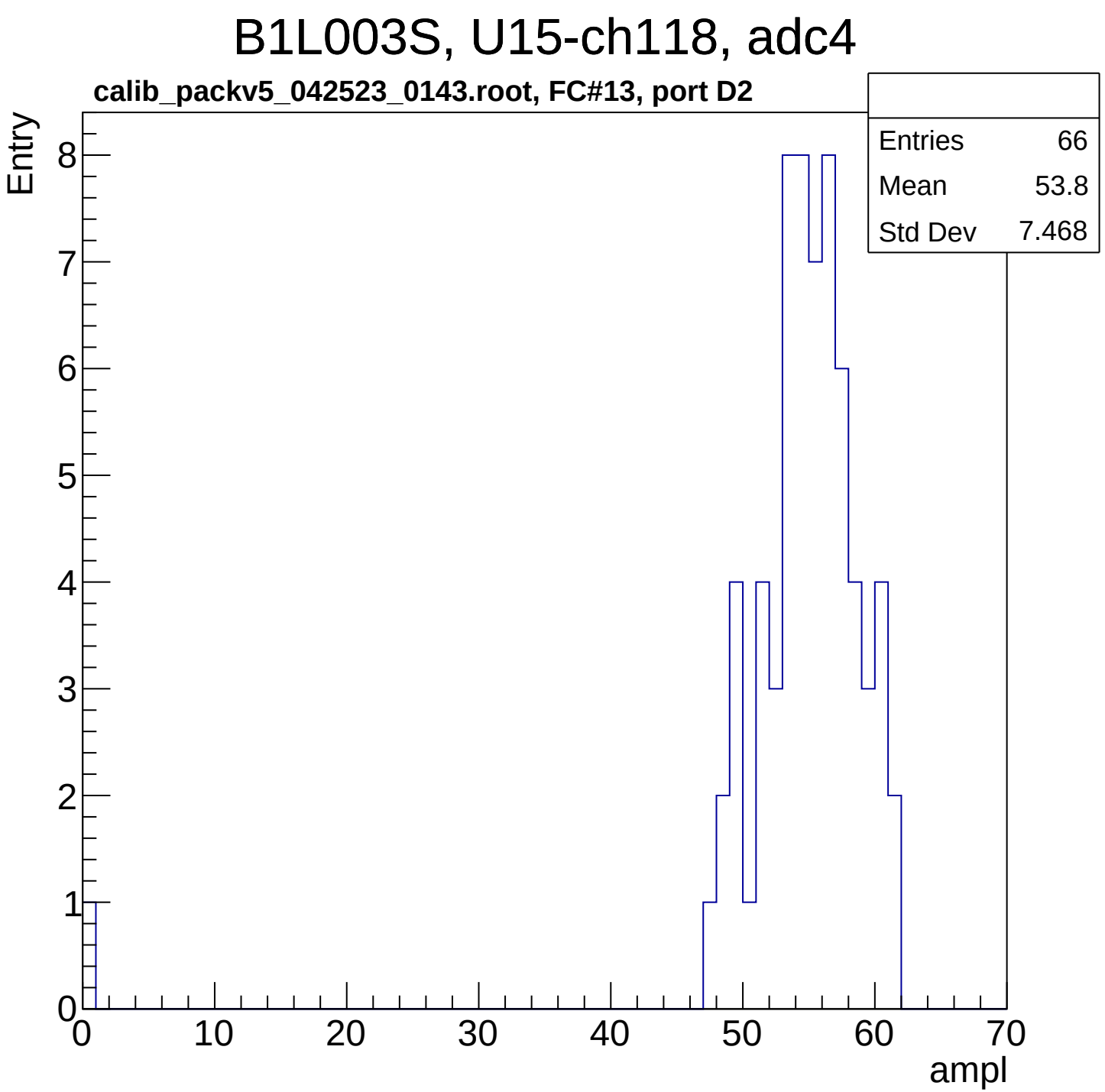
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.8
Std Dev 7.468
Entry
ampl
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1
2
3
4
5
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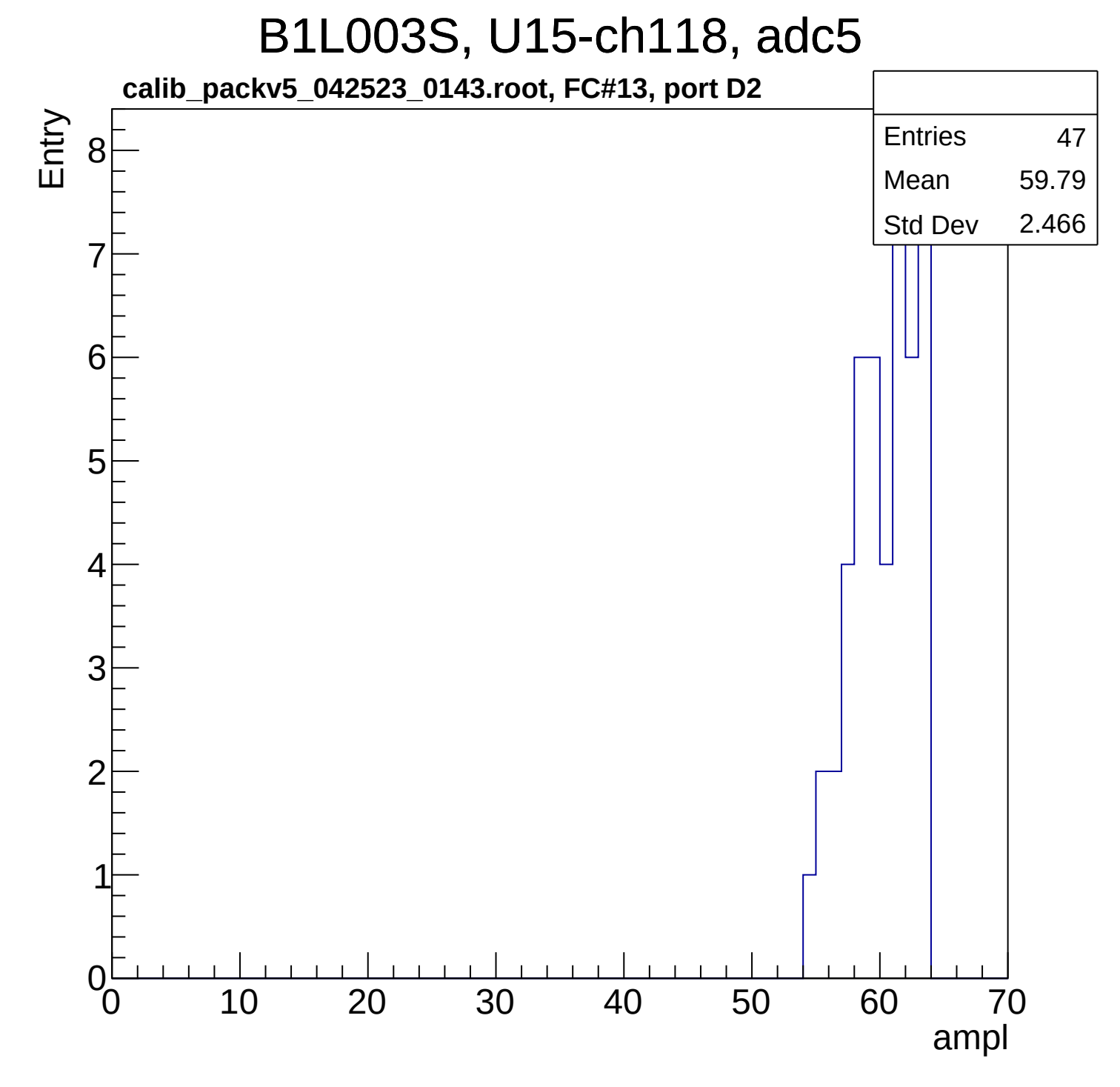
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.79
Std Dev 2.466
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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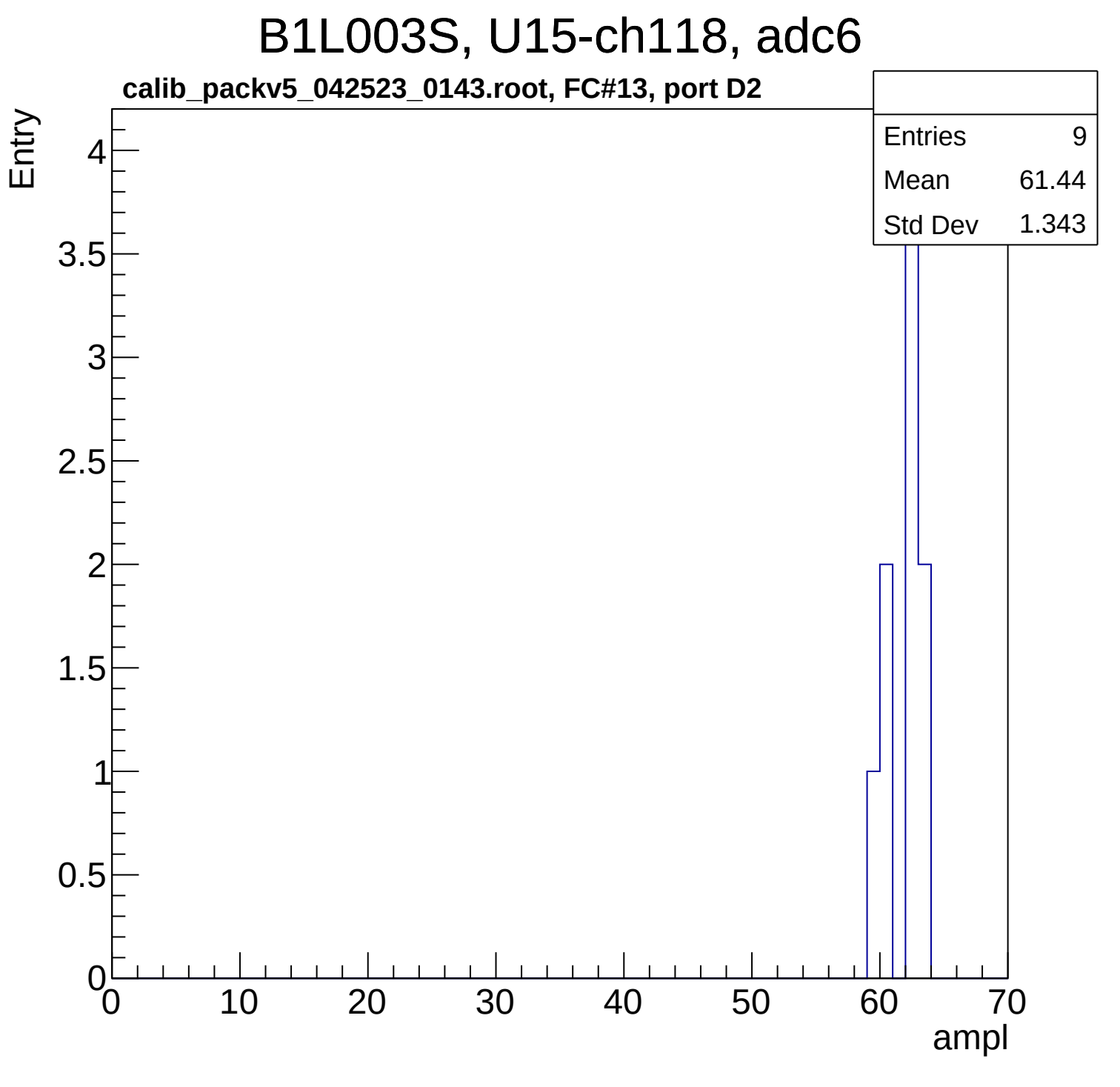
B1L003S, U15-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.44
Std Dev 1.343
Entry
ampl
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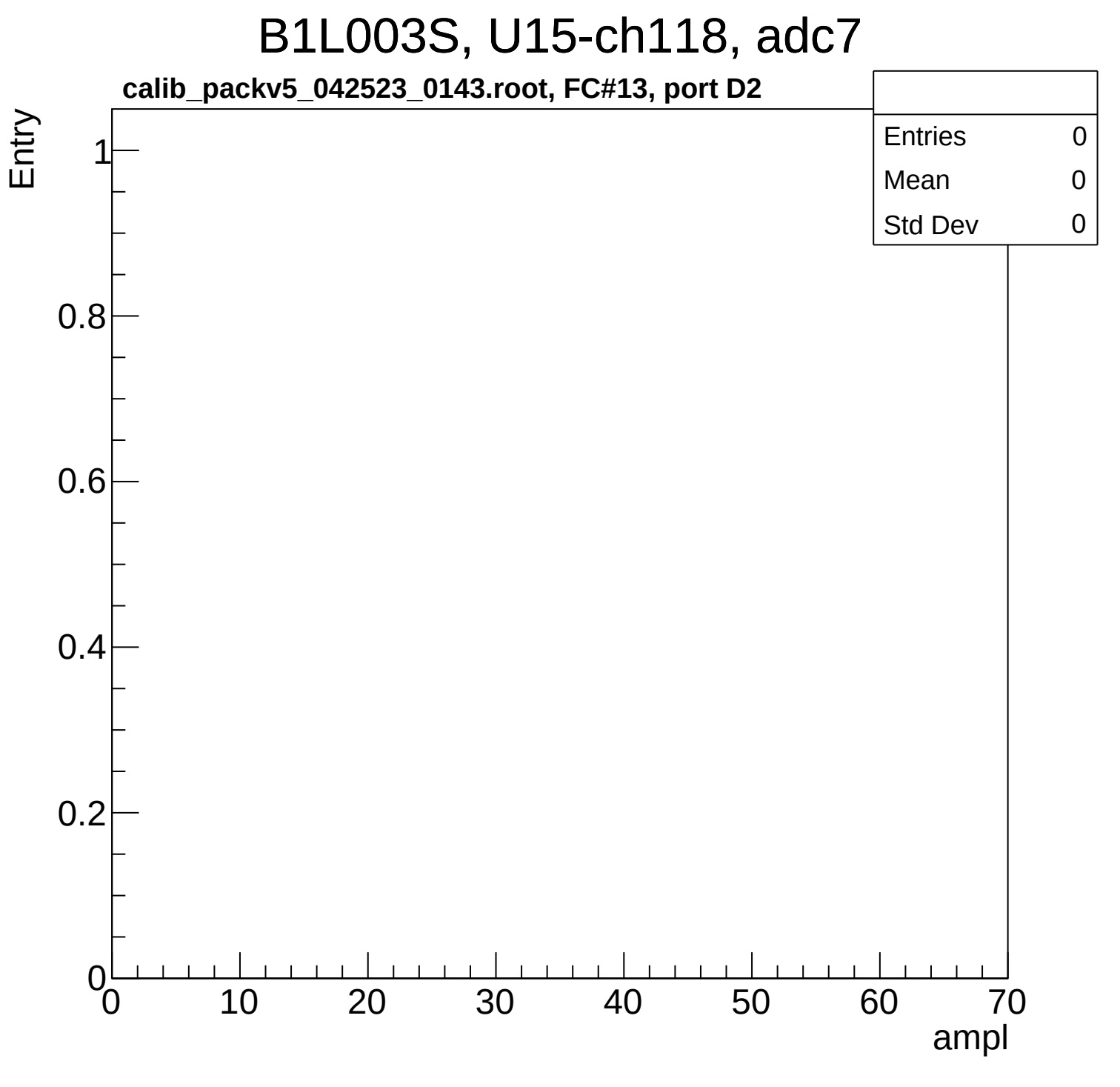
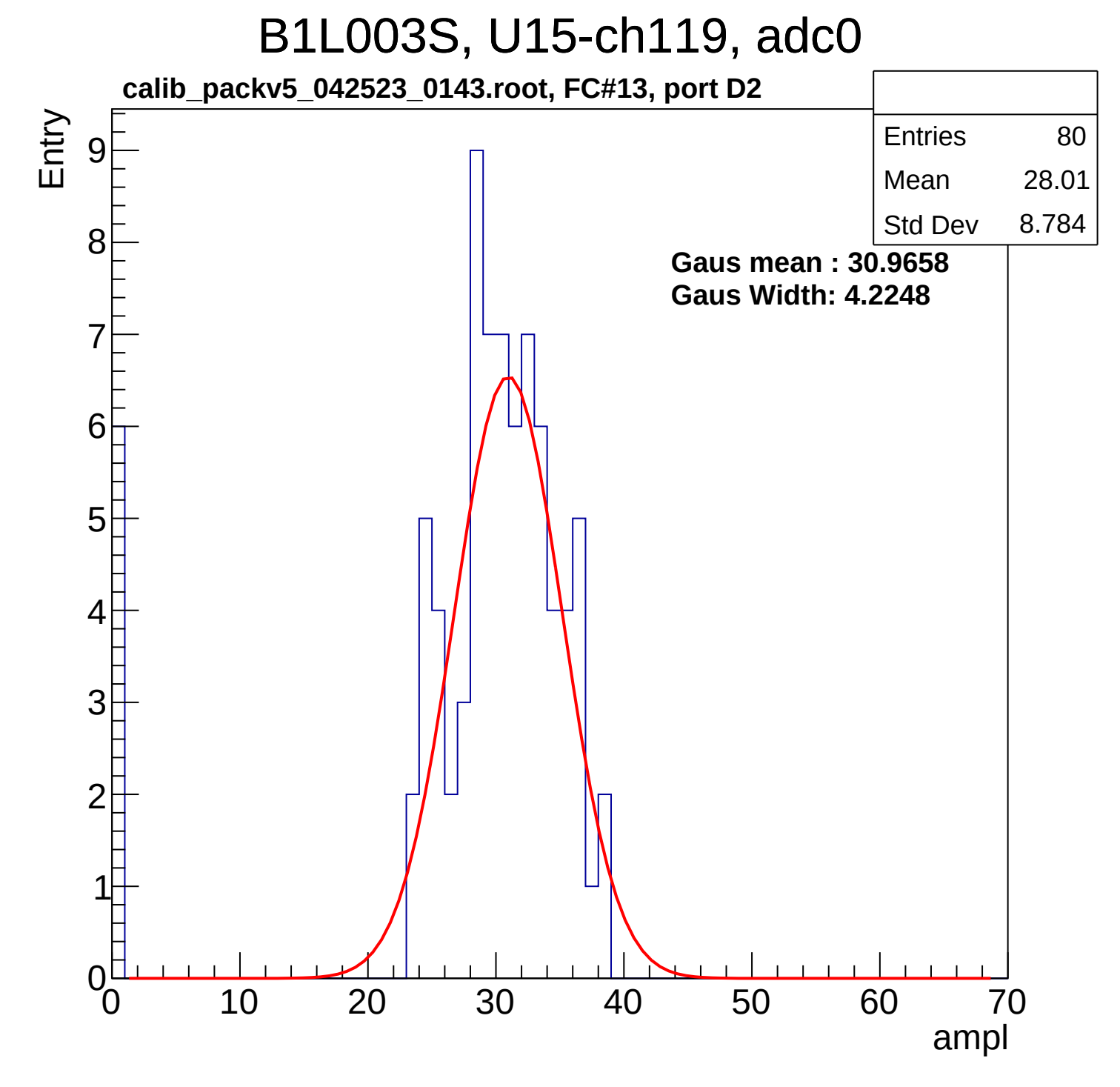
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 28.01
Std Dev 8.784
Gaus mean : 30.9658
Gaus Width: 4.2248
Entry
ampl
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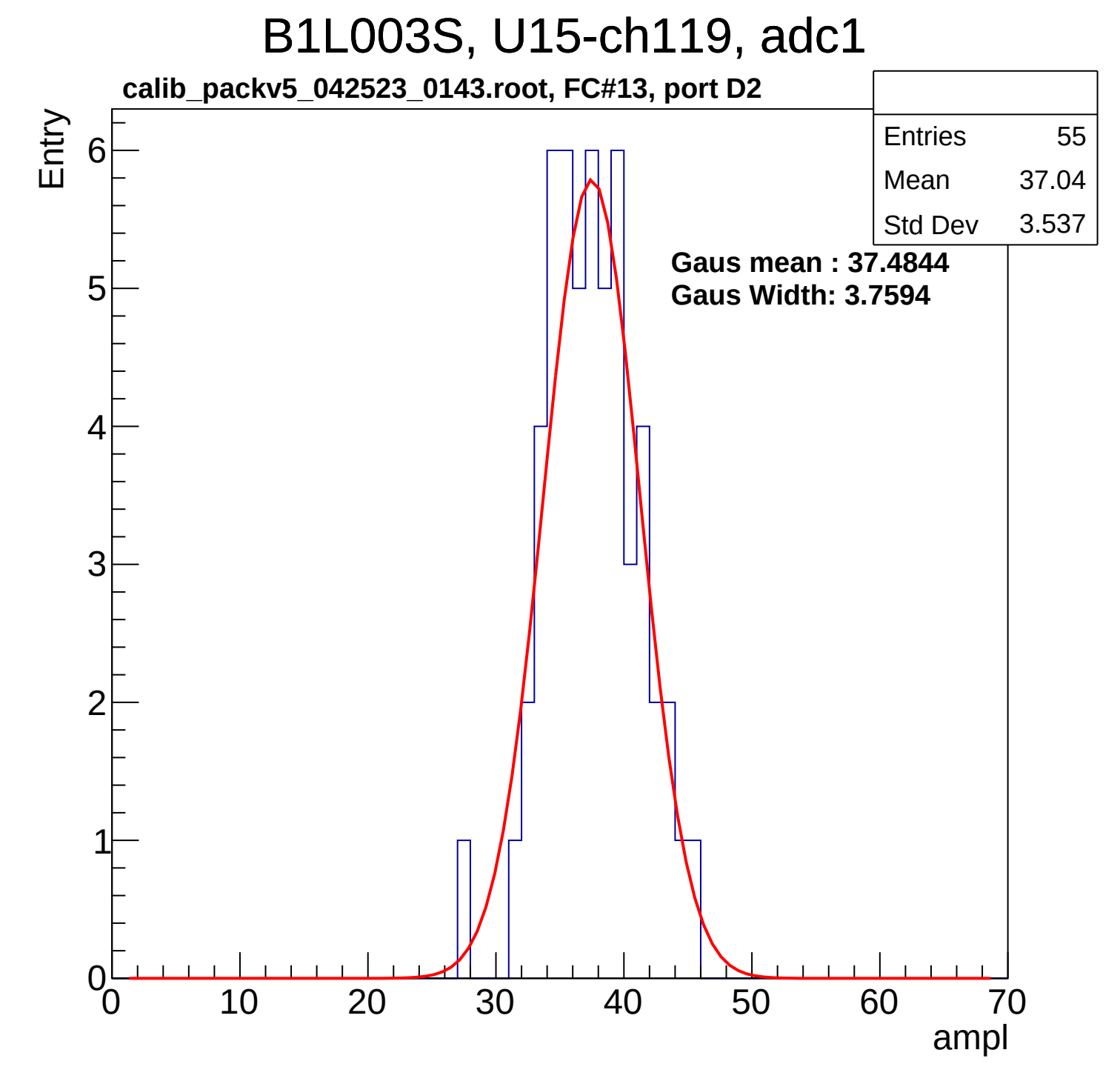
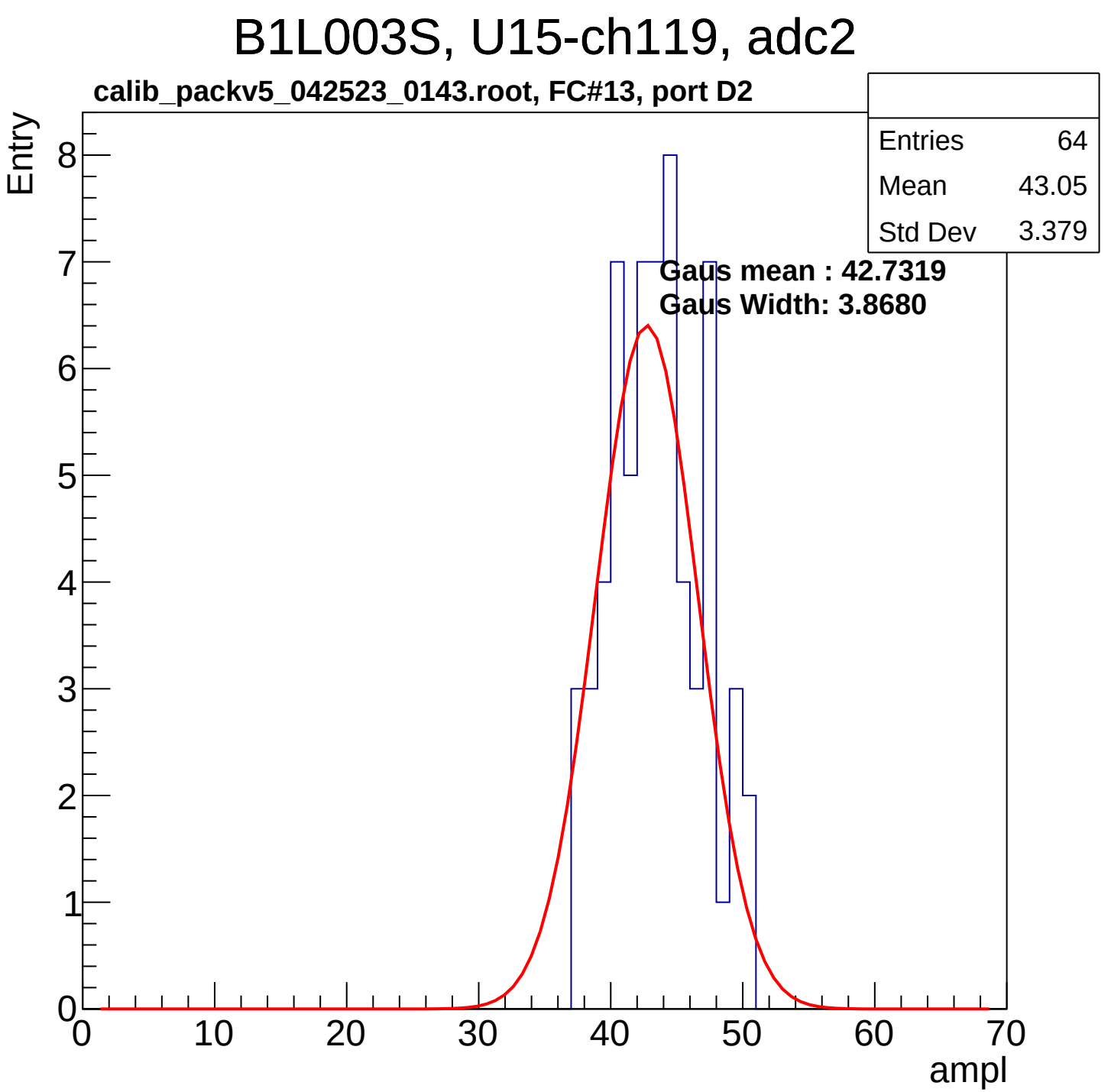
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U15-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.04
Std Dev 3.537
Gaus mean : 37.4844
Gaus Width: 3.7594
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U15-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.05
Std Dev 3.379
Gaus mean : 42.7319
Gaus Width: 3.8680
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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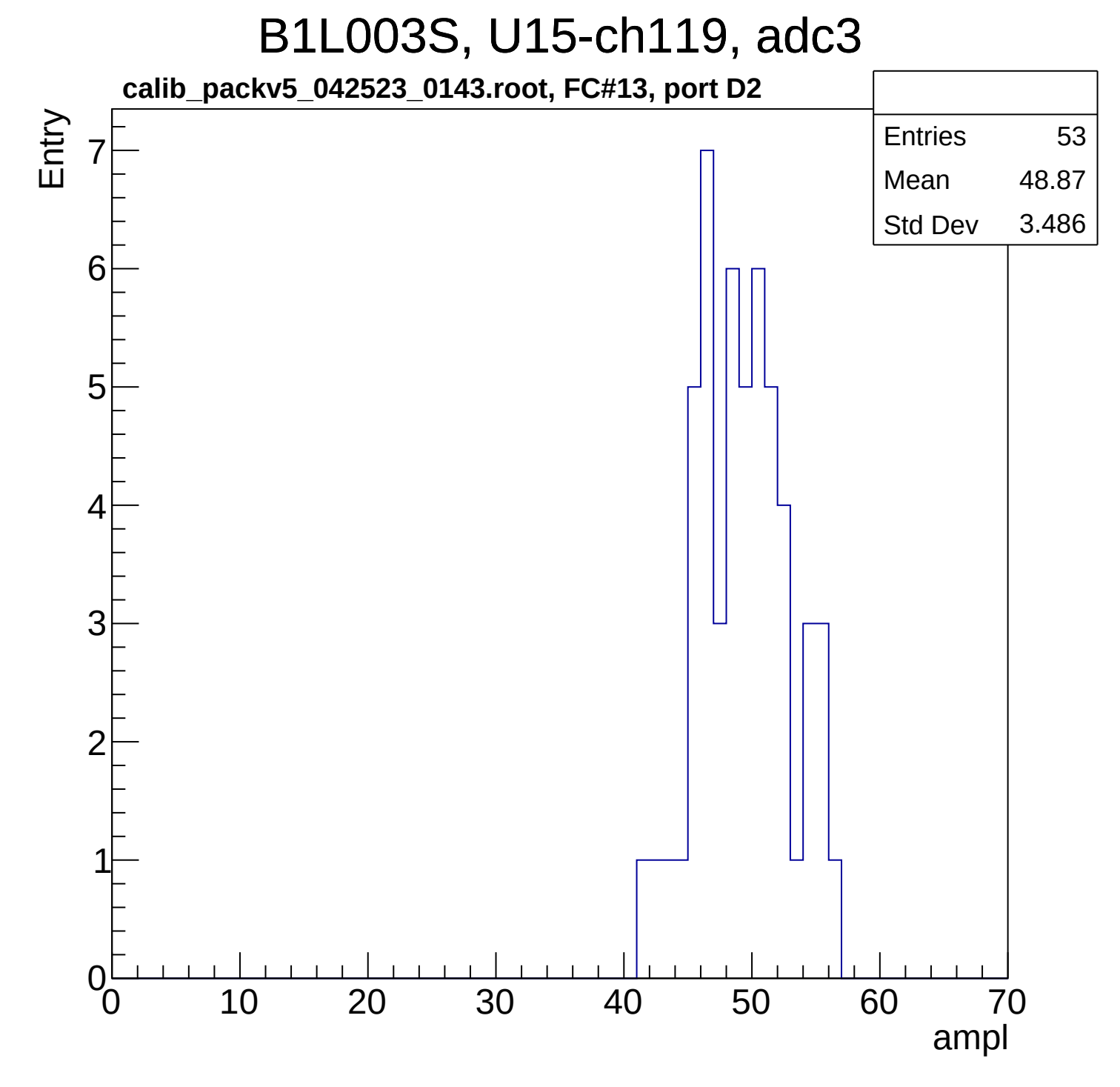
B1L003S, U15-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.87
Std Dev 3.486
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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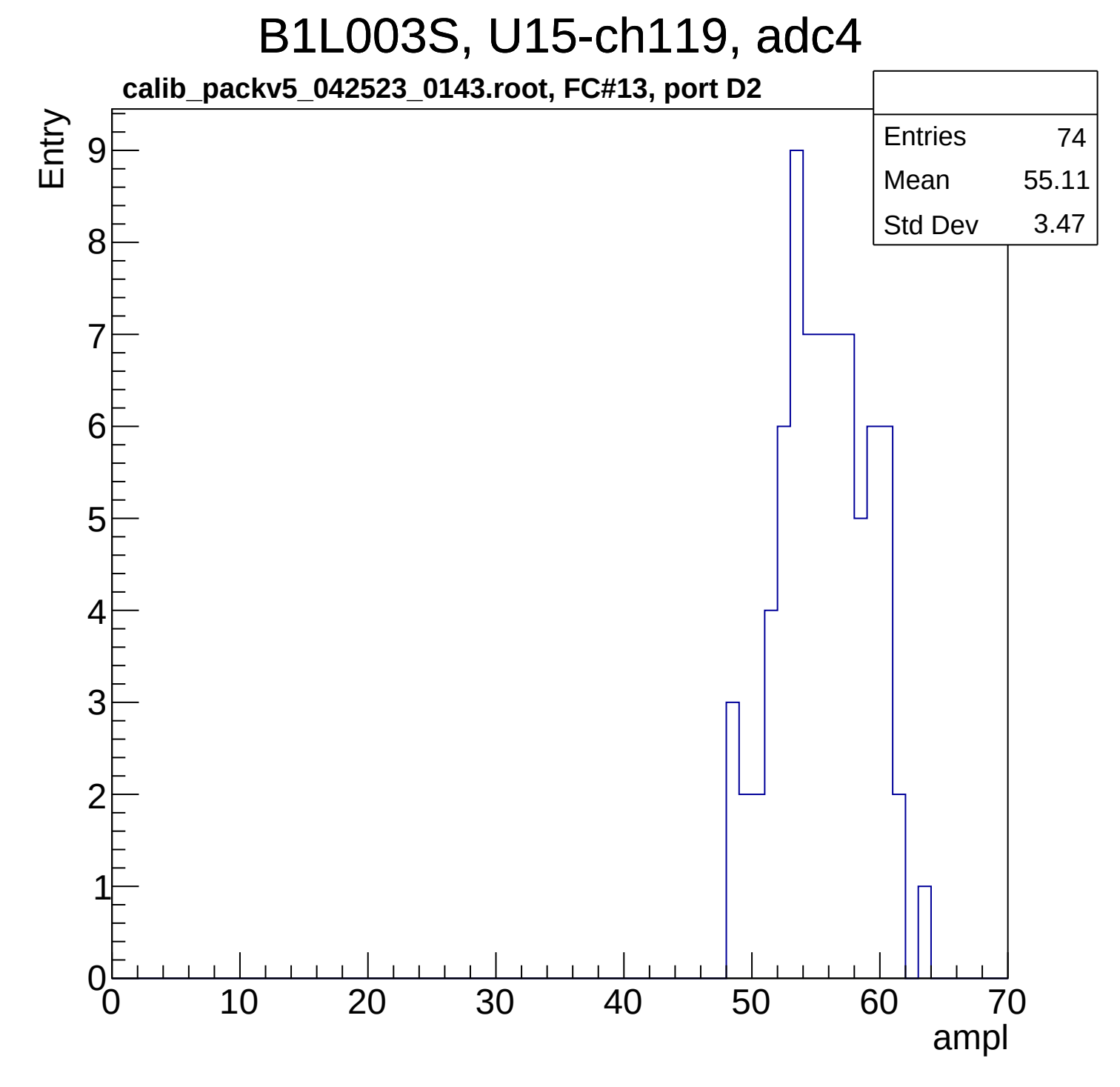
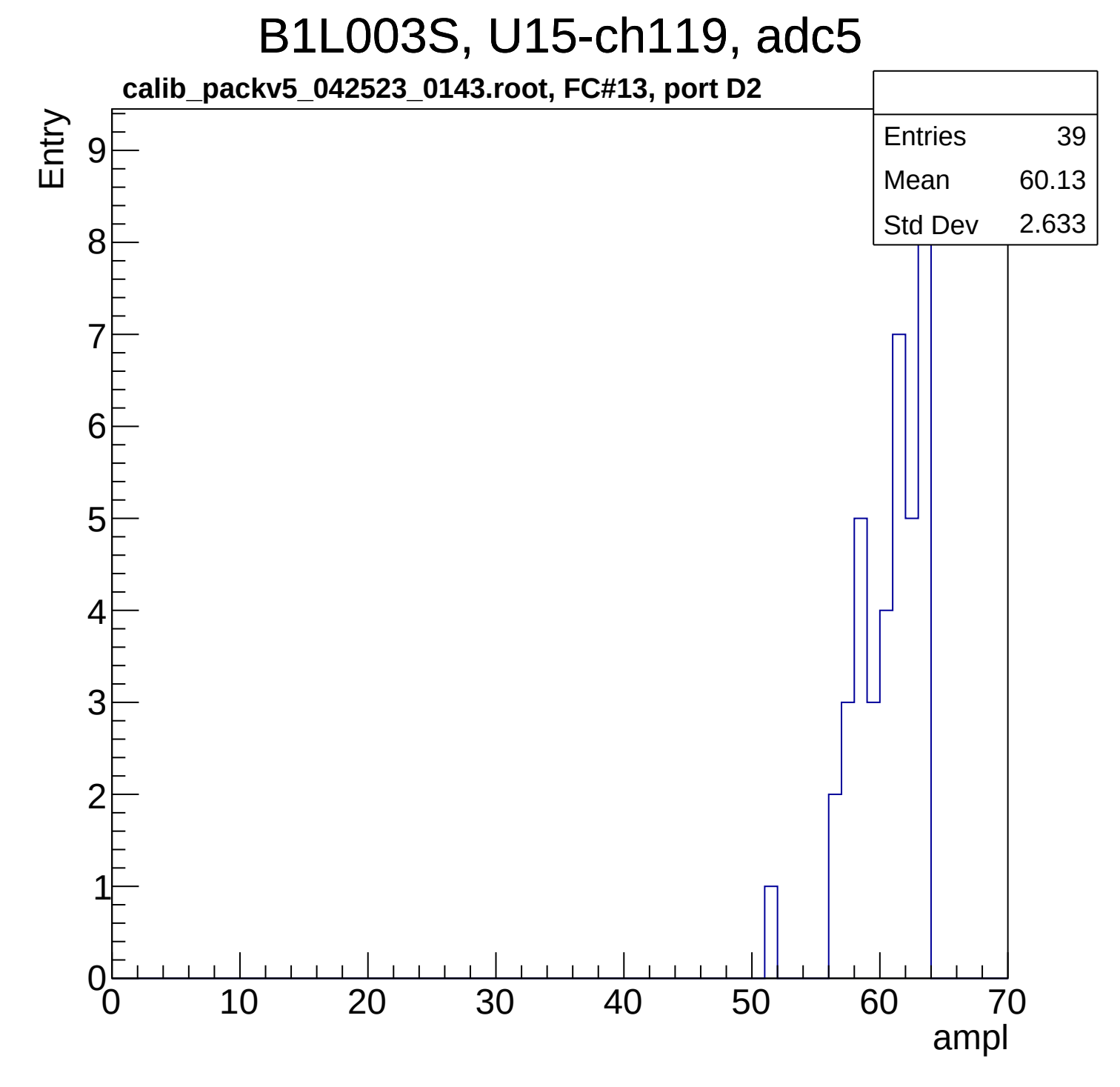

B1L003S, U15-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 55.11
Std Dev 3.47
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 60.13
Std Dev 2.633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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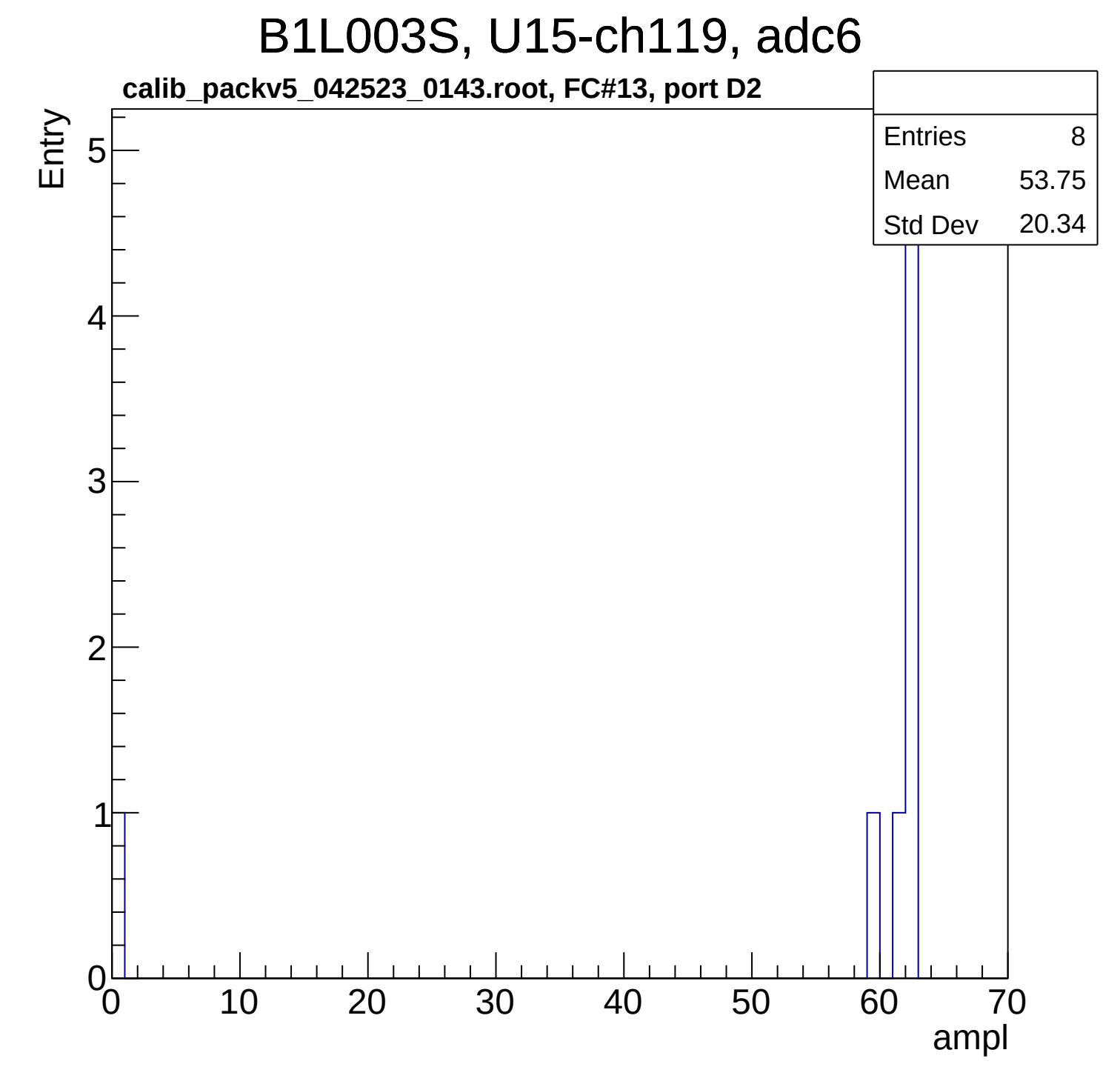
B1L003S, U15-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 53.75
Std Dev 20.34
Entry
ampl
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2
3
4
5
0
10
20
30
40
50
60
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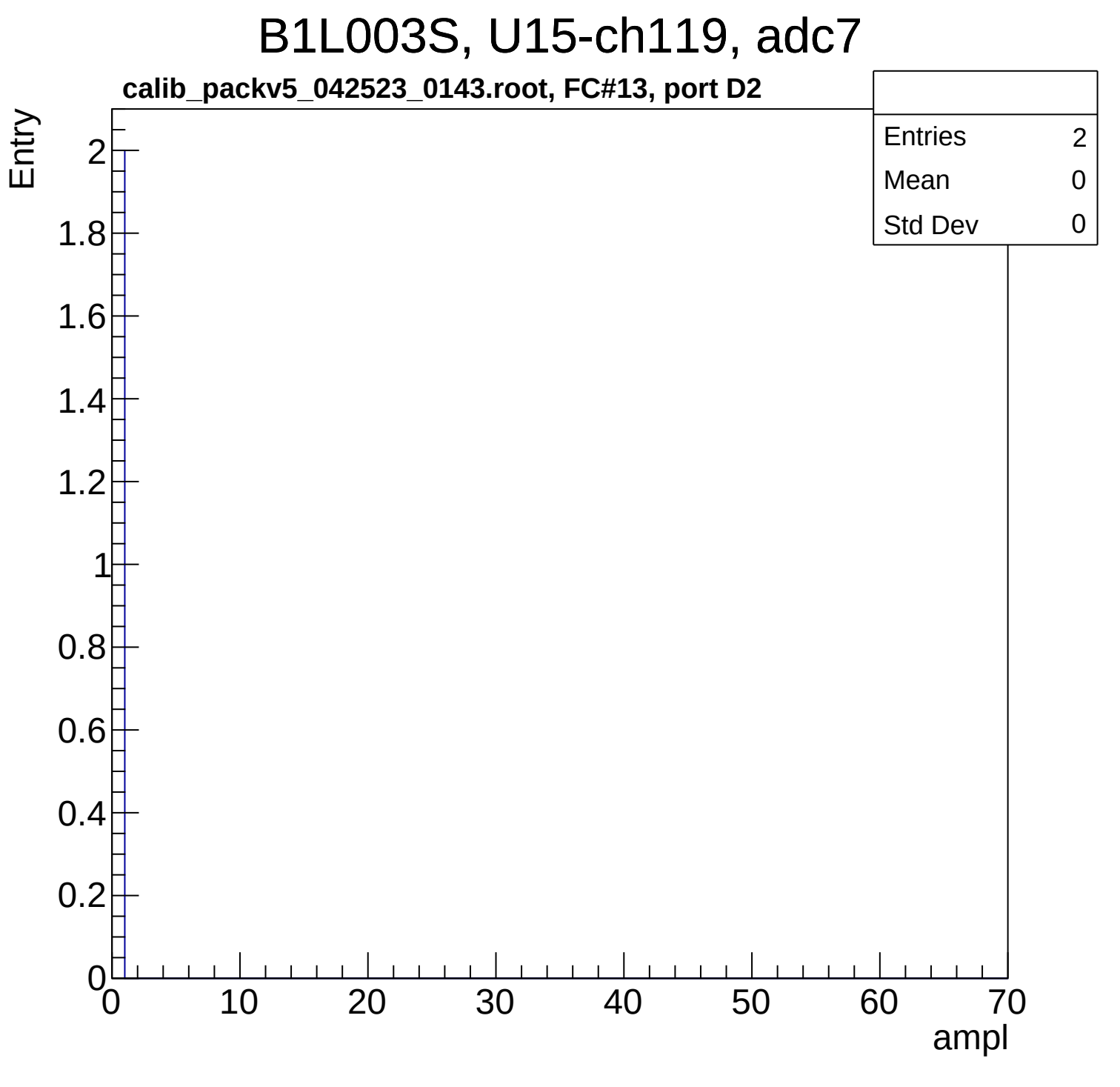
B1L003S, U15-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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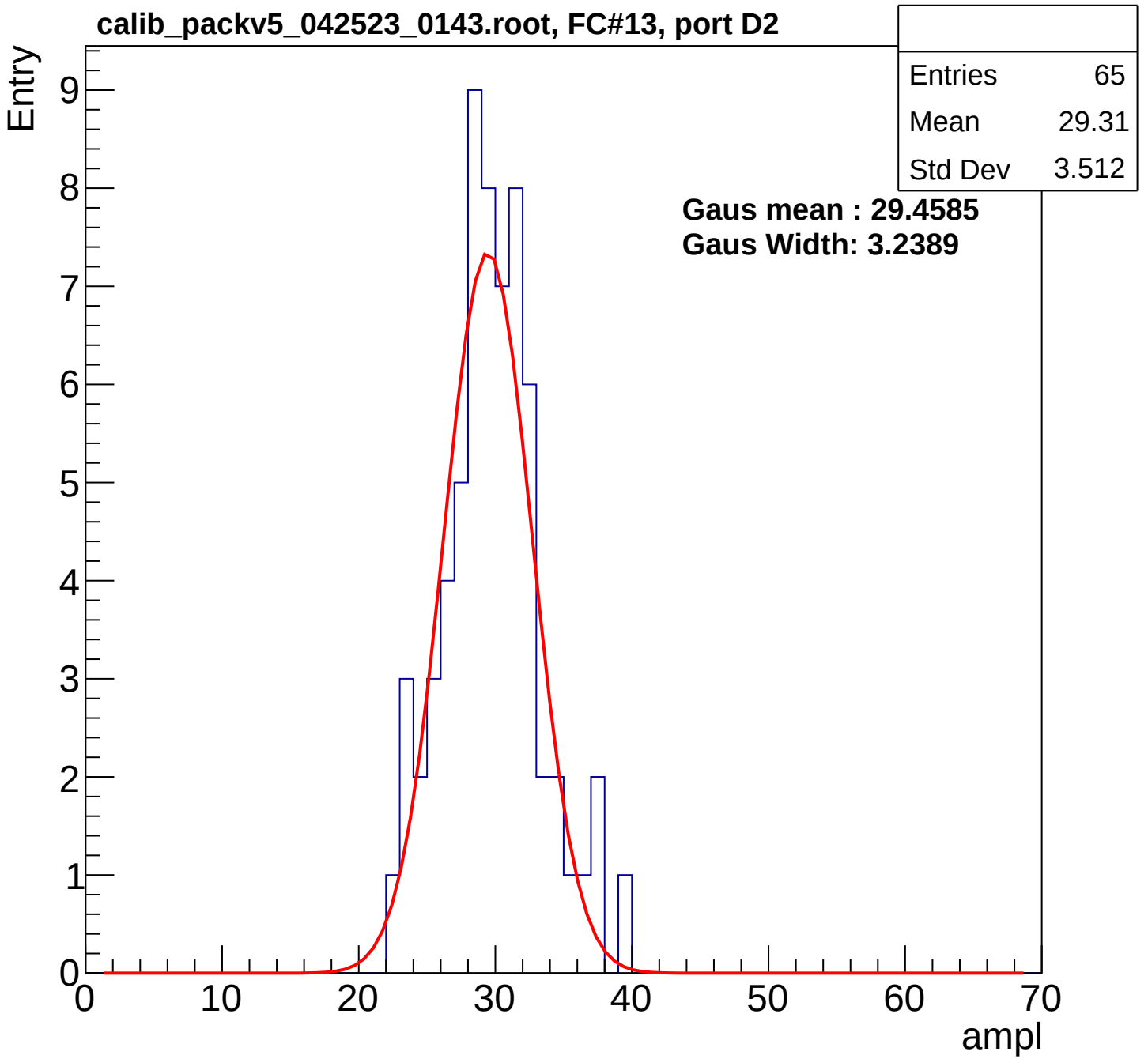
B1L003S, U15-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.31
Std Dev 3.512
Gaus mean : 29.4585
Gaus Width: 3.2389
Entry
ampl
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2
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4
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6
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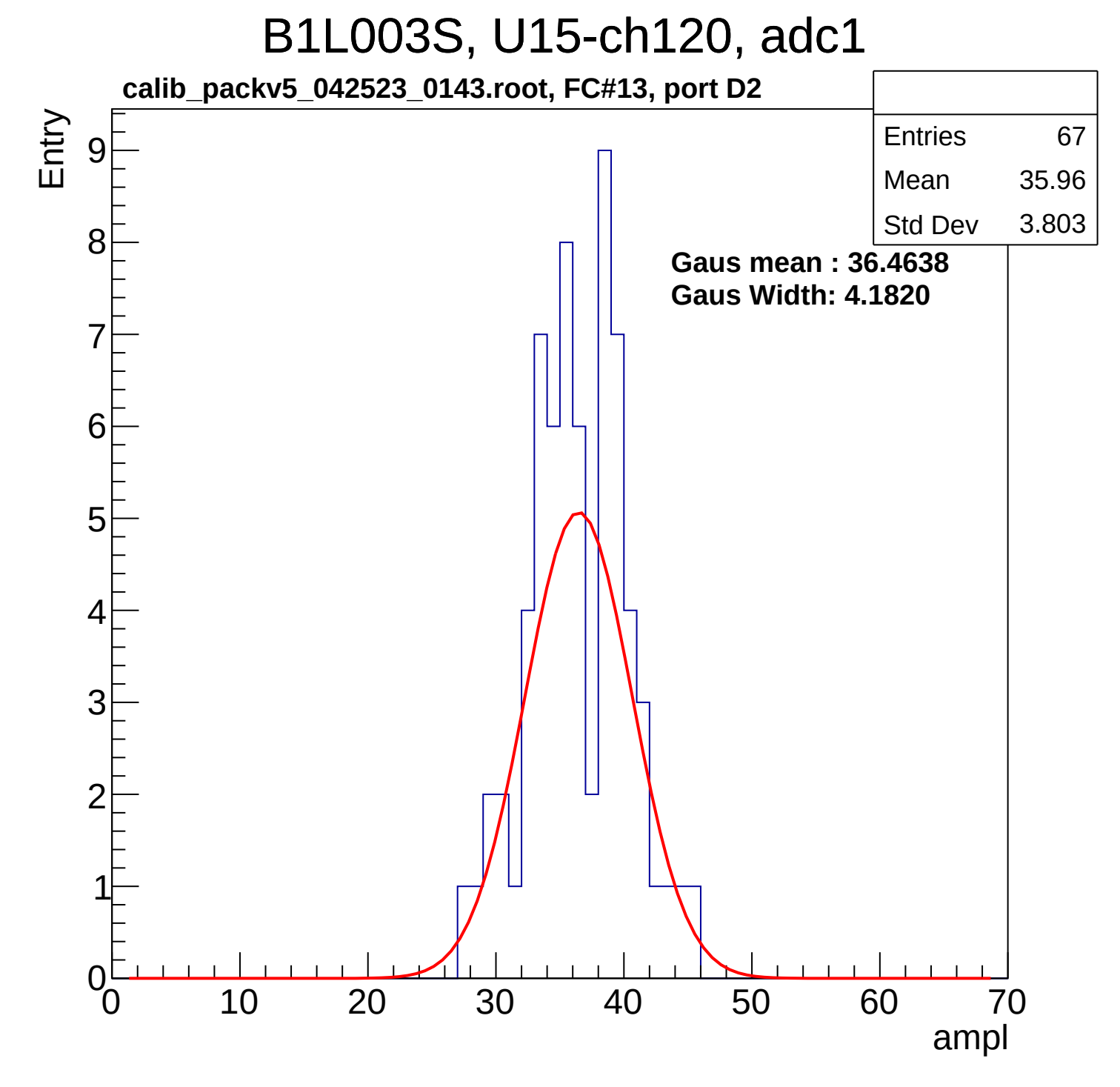
B1L003S, U15-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.96
Std Dev 3.803
Gaus mean : 36.4638
Gaus Width: 4.1820
Entry
ampl
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0
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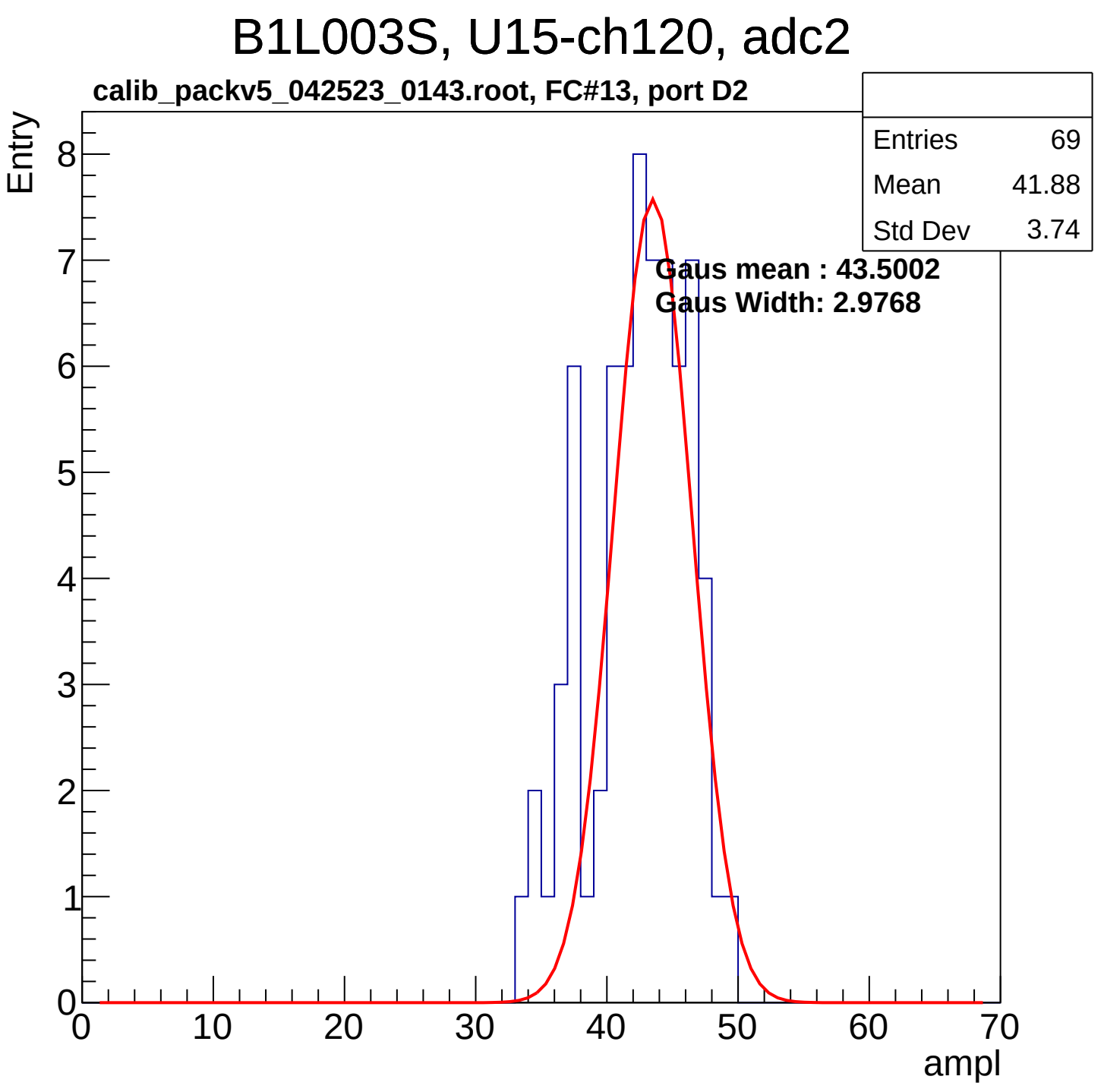
B1L003S, U15-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 41.88
Std Dev 3.74
Gaus mean : 43.5002
Gaus Width: 2.9768
Entry
ampl
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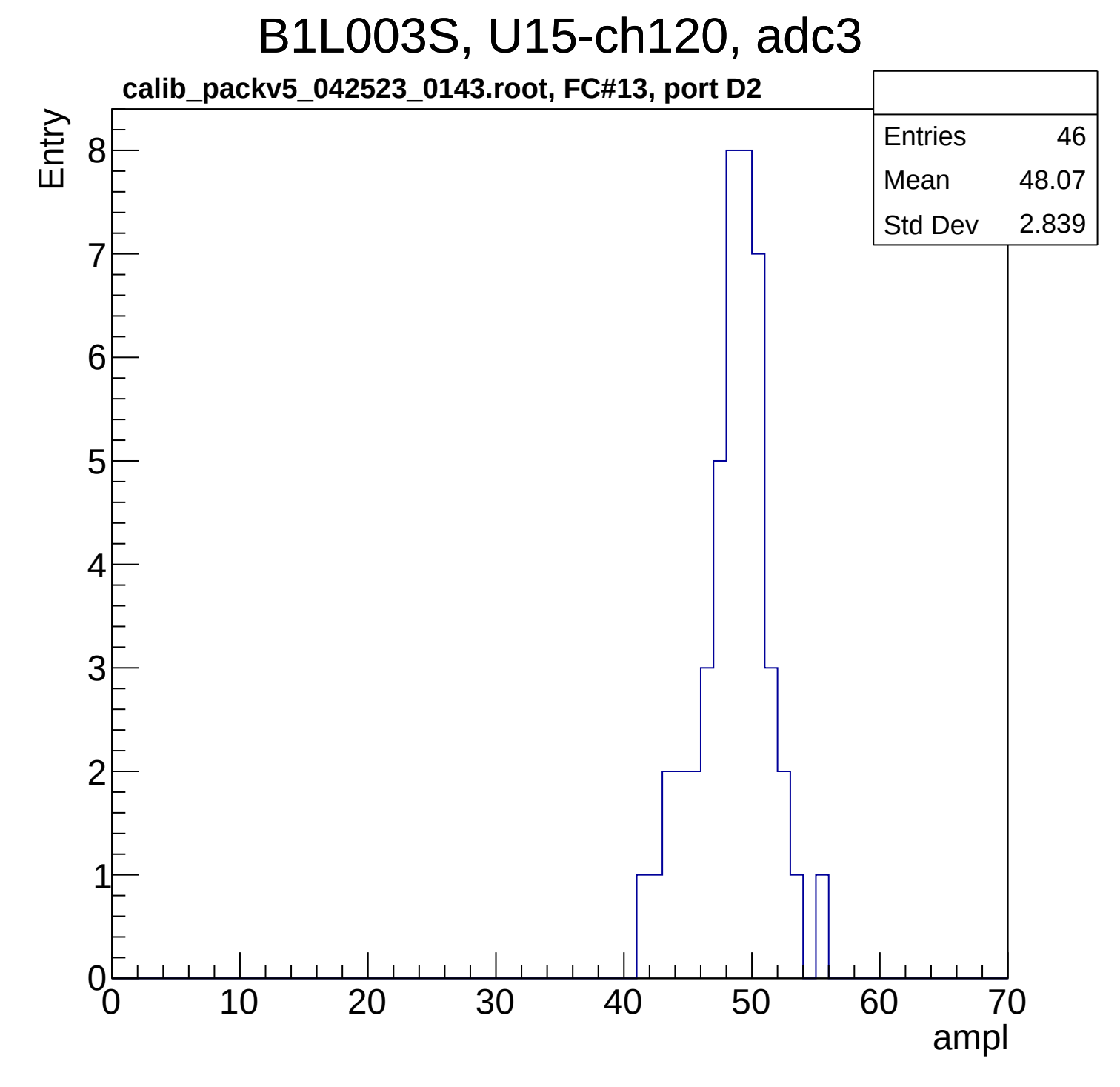
B1L003S, U15-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 48.07
Std Dev 2.839
Entry
ampl
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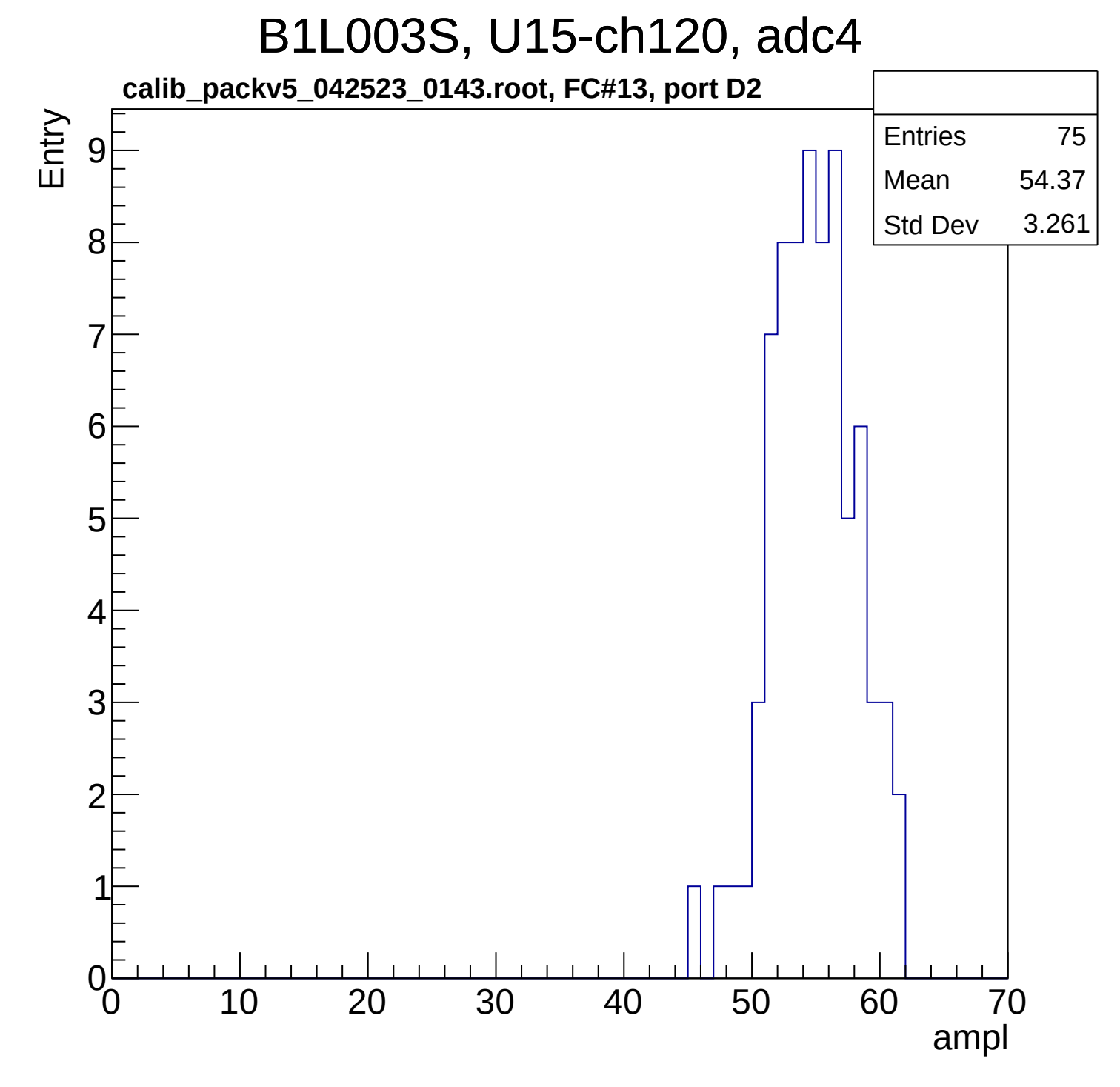
B1L003S, U15-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 54.37
Std Dev 3.261
Entry
ampl
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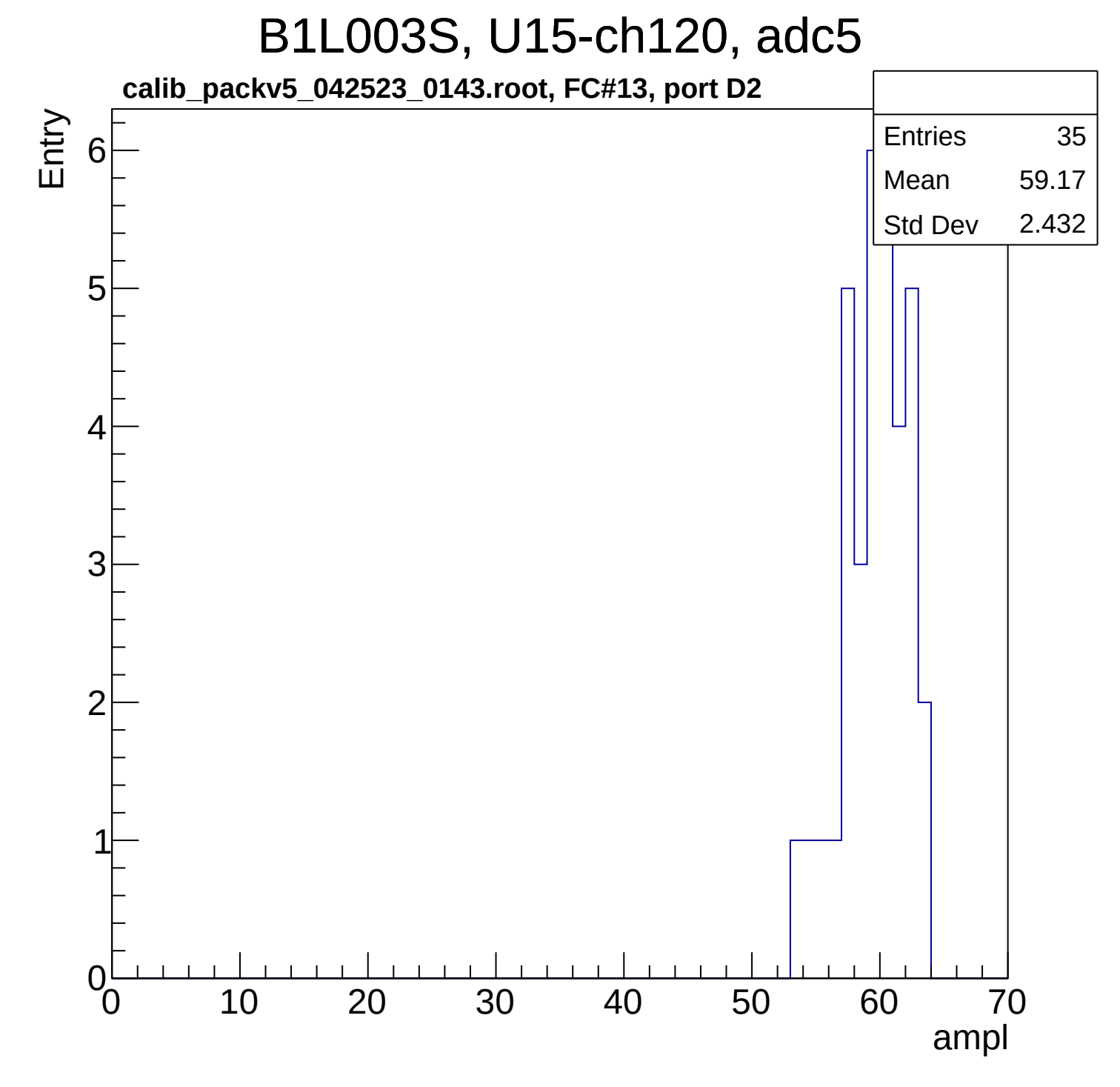
B1L003S, U15-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 59.17
Std Dev 2.432
Entry
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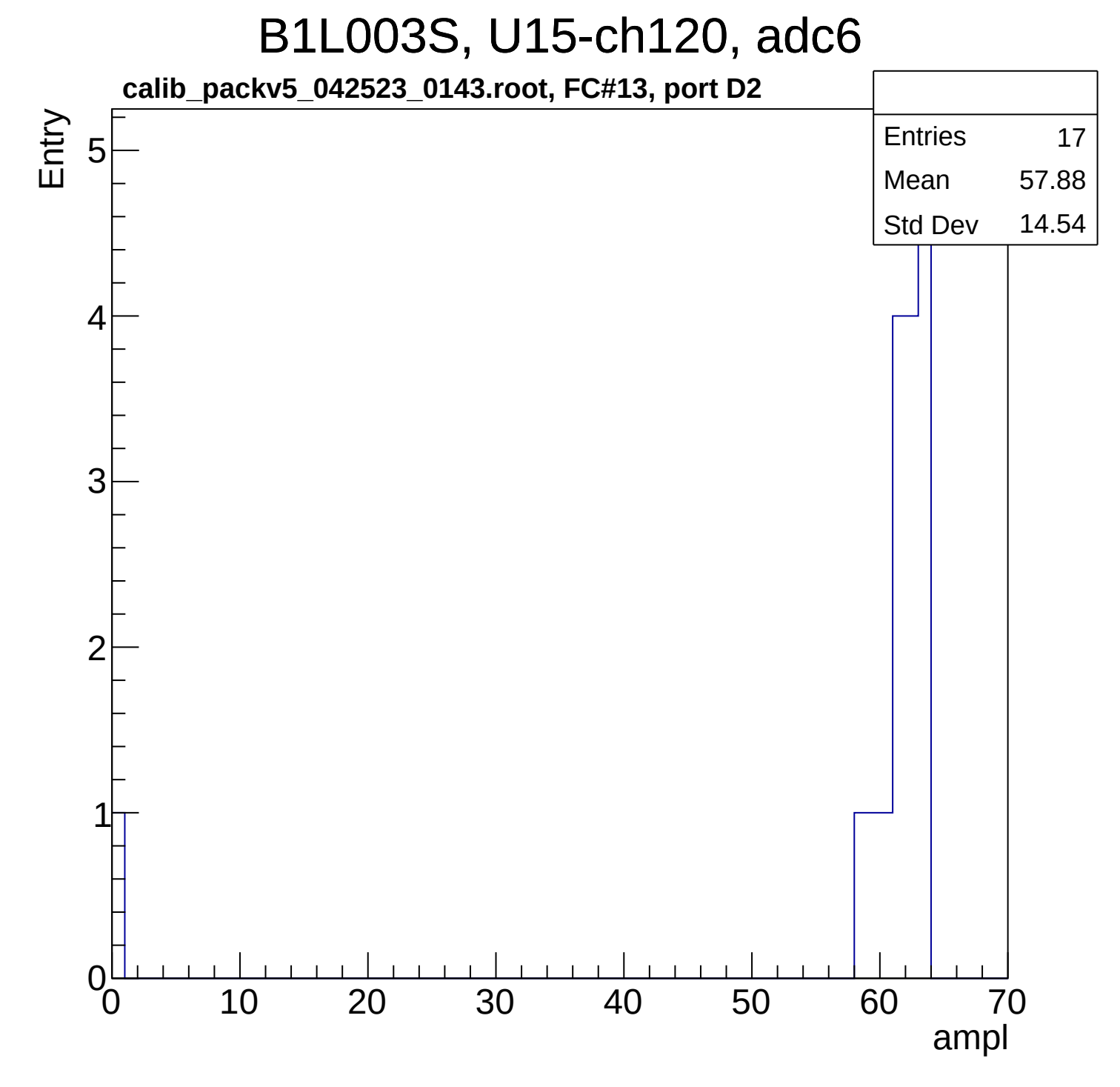
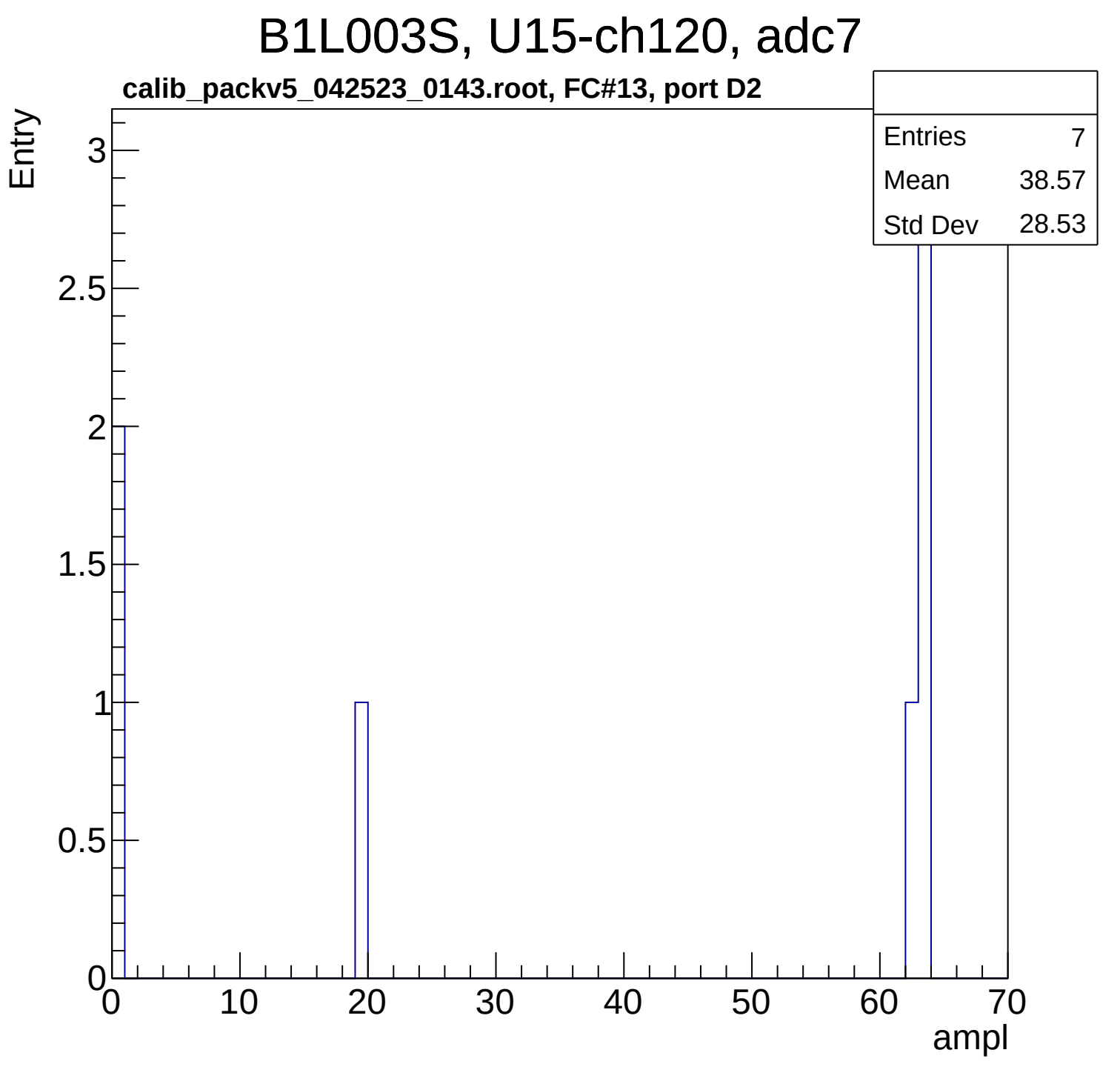
70
ampl


B1L003S, U15-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.88
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 38.57
Std Dev 28.53
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
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60
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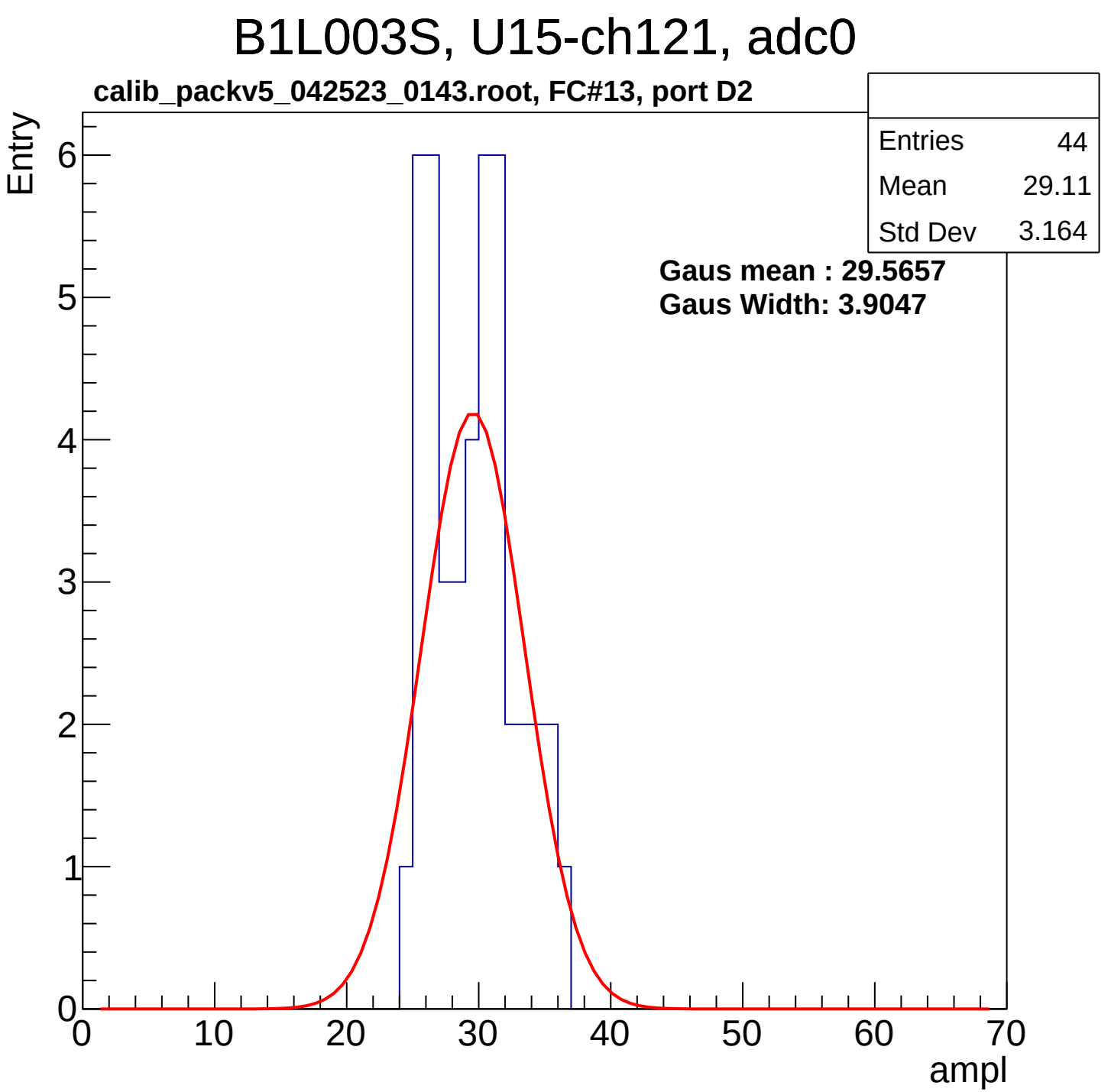
B1L003S, U15-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 29.11
Std Dev 3.164
Gaus mean : 29.5657
Gaus Width: 3.9047
Entry
ampl
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1
2
3
4
5
6
0
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20
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40
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60
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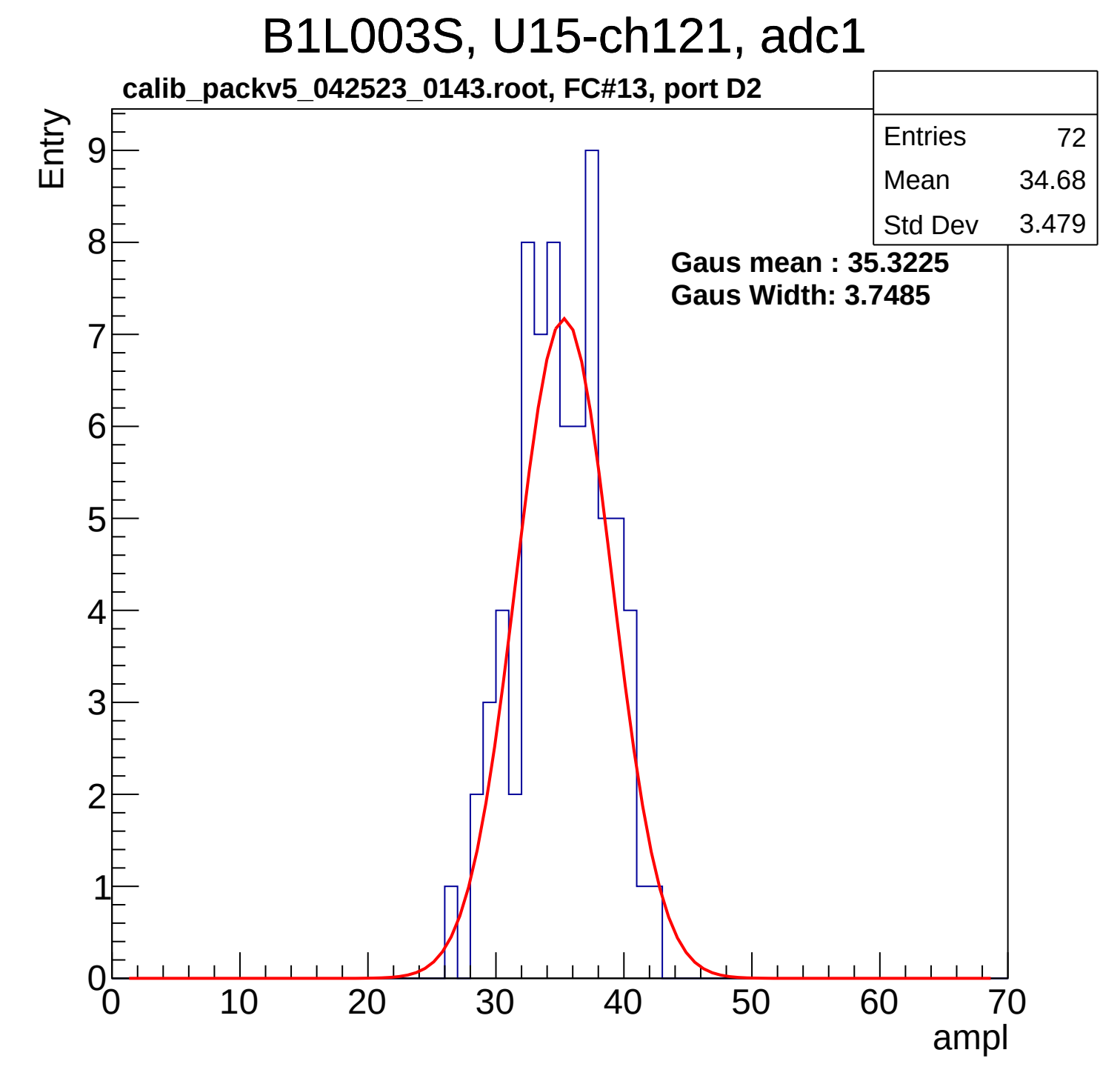
B1L003S, U15-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.68
Std Dev 3.479
Gaus mean : 35.3225
Gaus Width: 3.7485
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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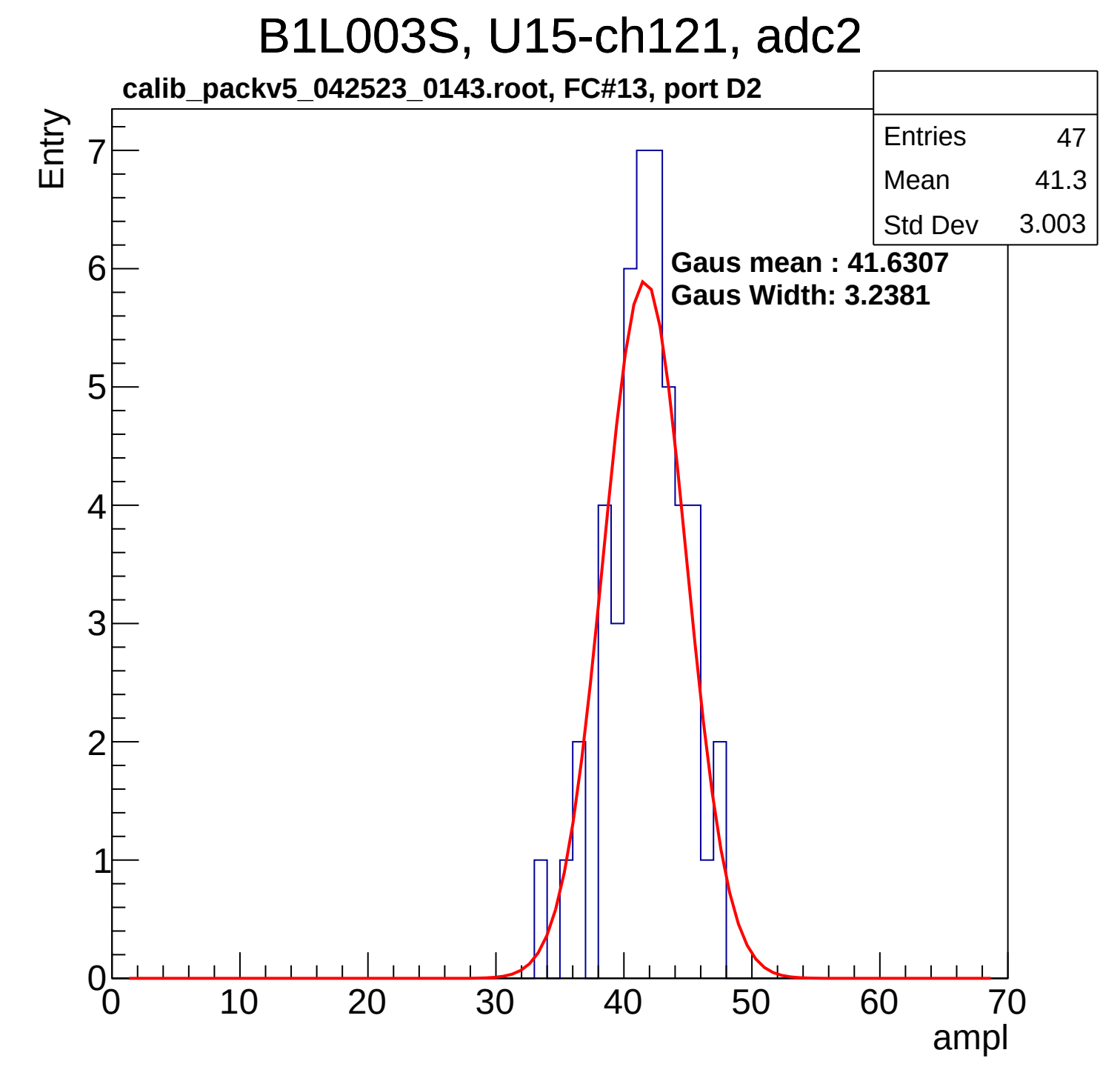
B1L003S, U15-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 41.3
Std Dev 3.003
Gaus mean : 41.6307
Gaus Width: 3.2381
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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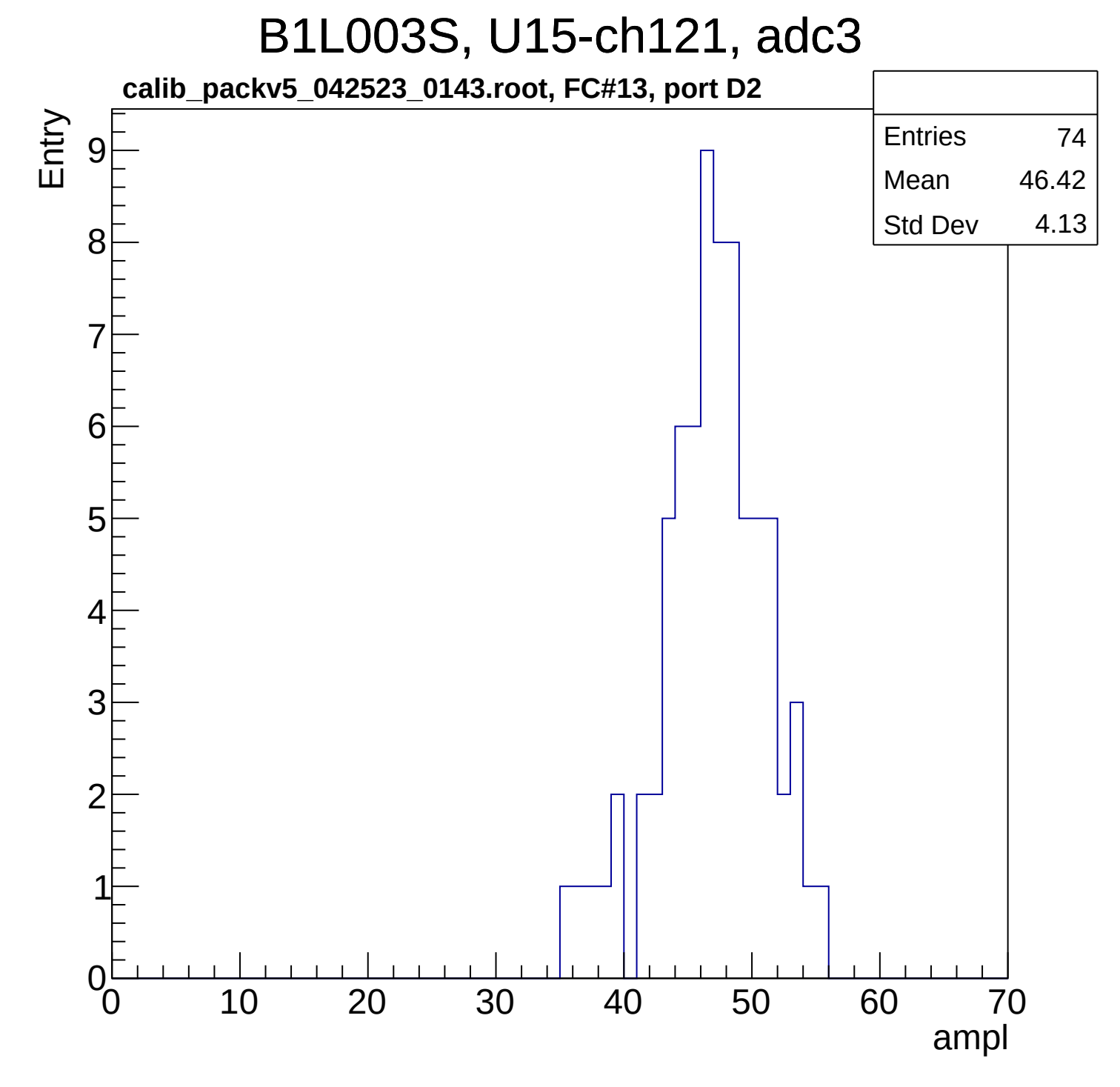
B1L003S, U15-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 46.42
Std Dev 4.13
Entry
ampl
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10
20
30
40
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60
70
1
2
3
4
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6
7
8
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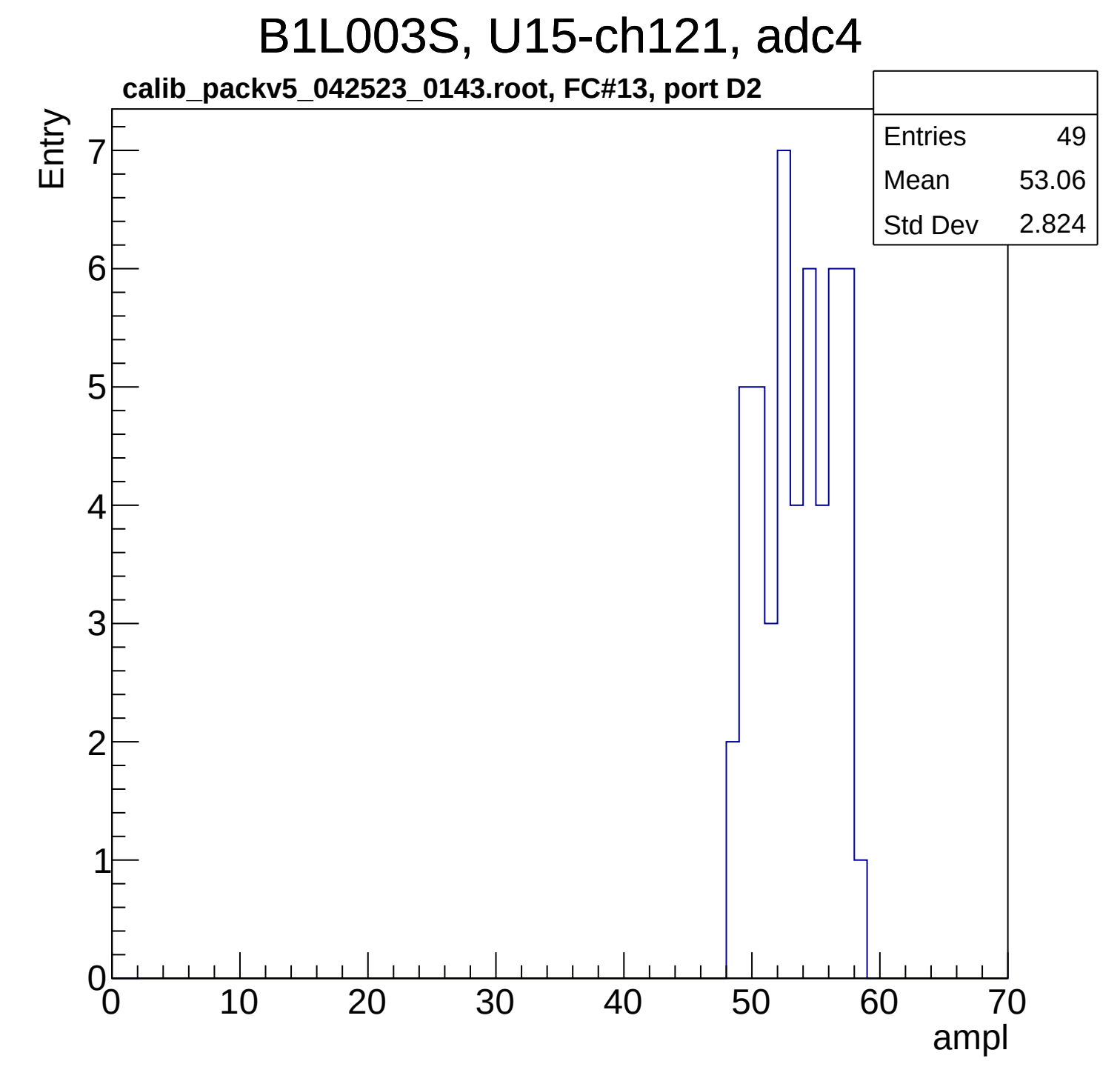
B1L003S, U15-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.06
Std Dev 2.824
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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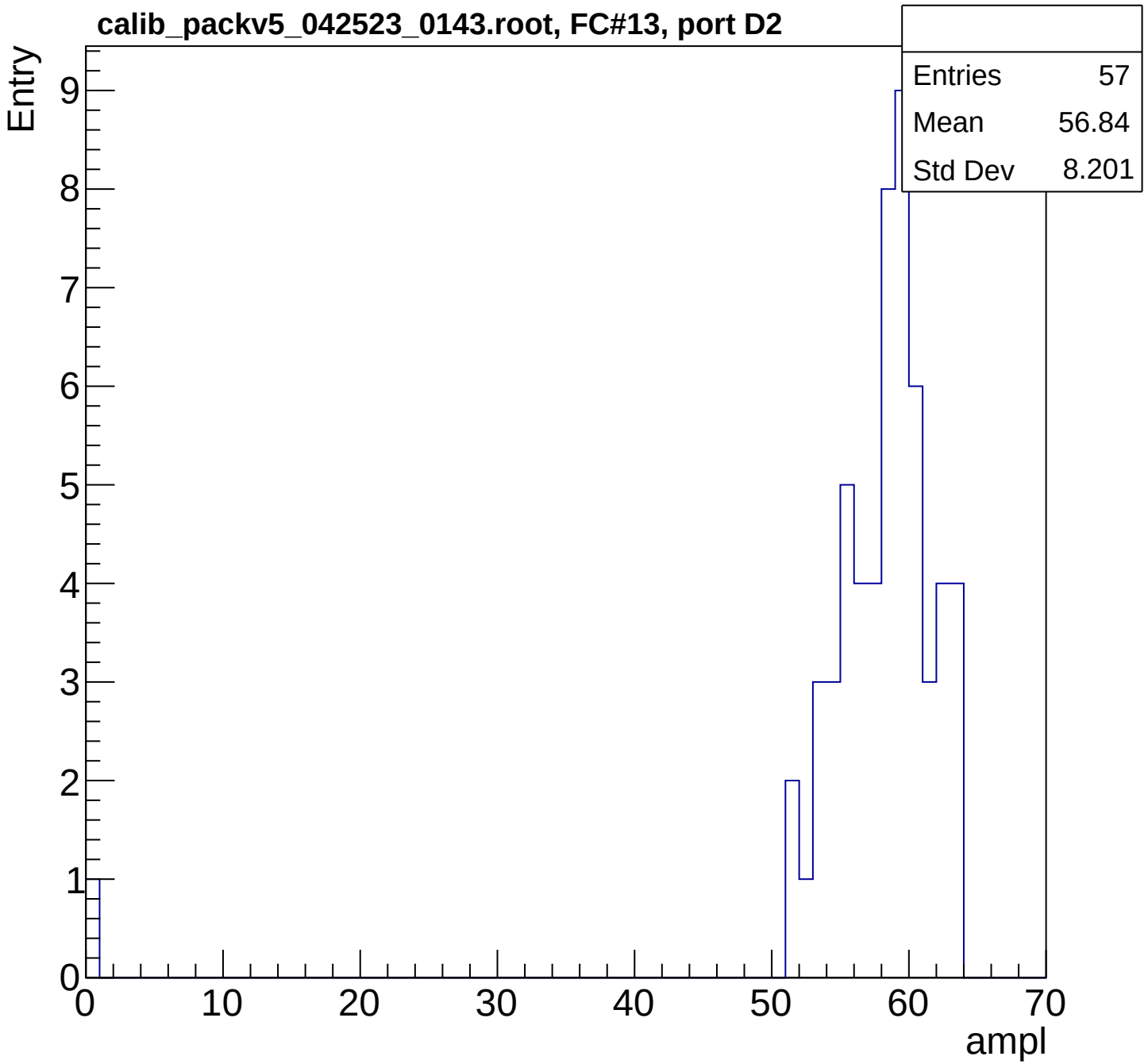
B1L003S, U15-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 56.84
Std Dev 8.201
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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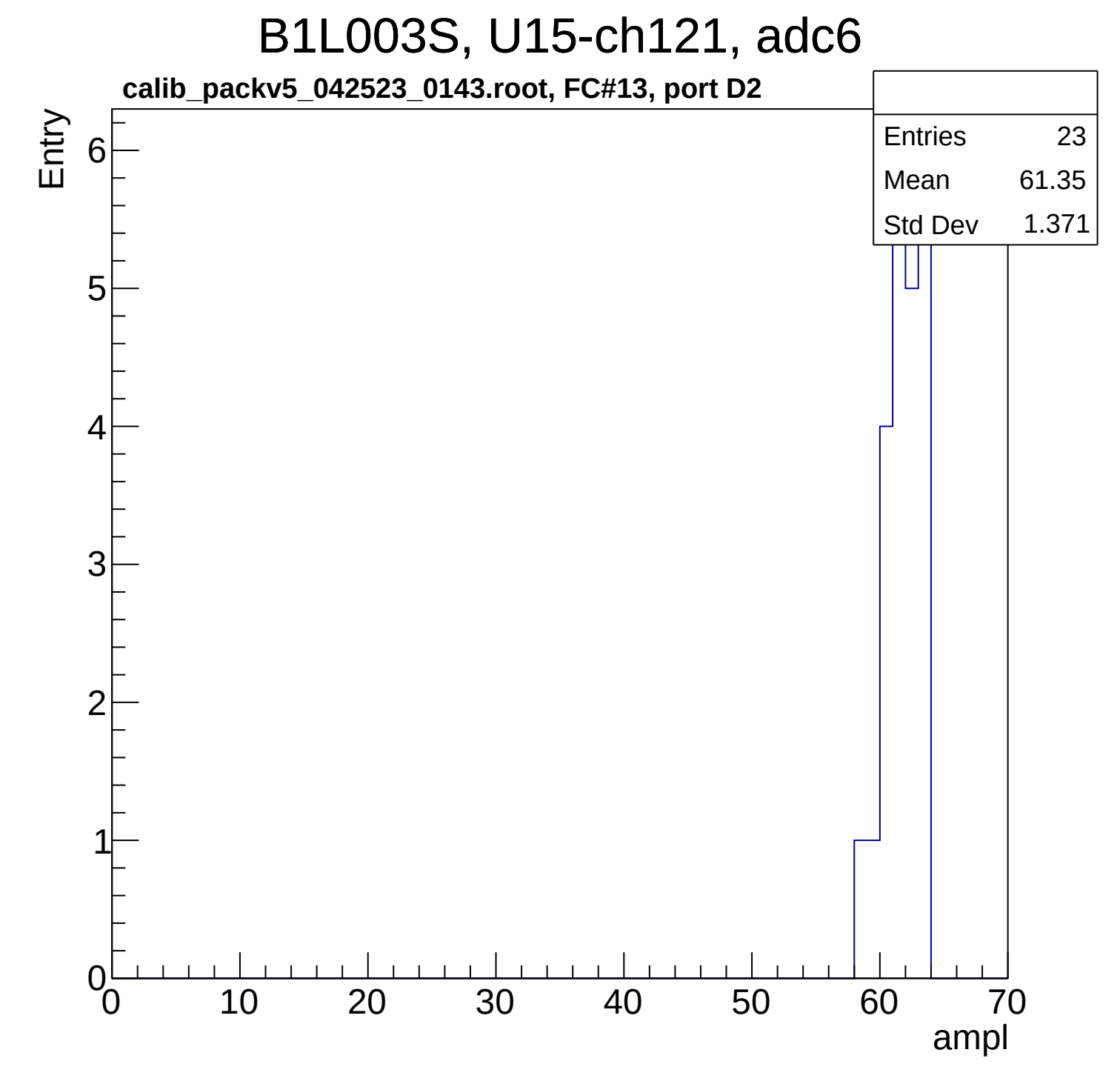
B1L003S, U15-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 61.35
Std Dev 1.371
Entry
0
1
2
3
4
5
6
0
10
20
30
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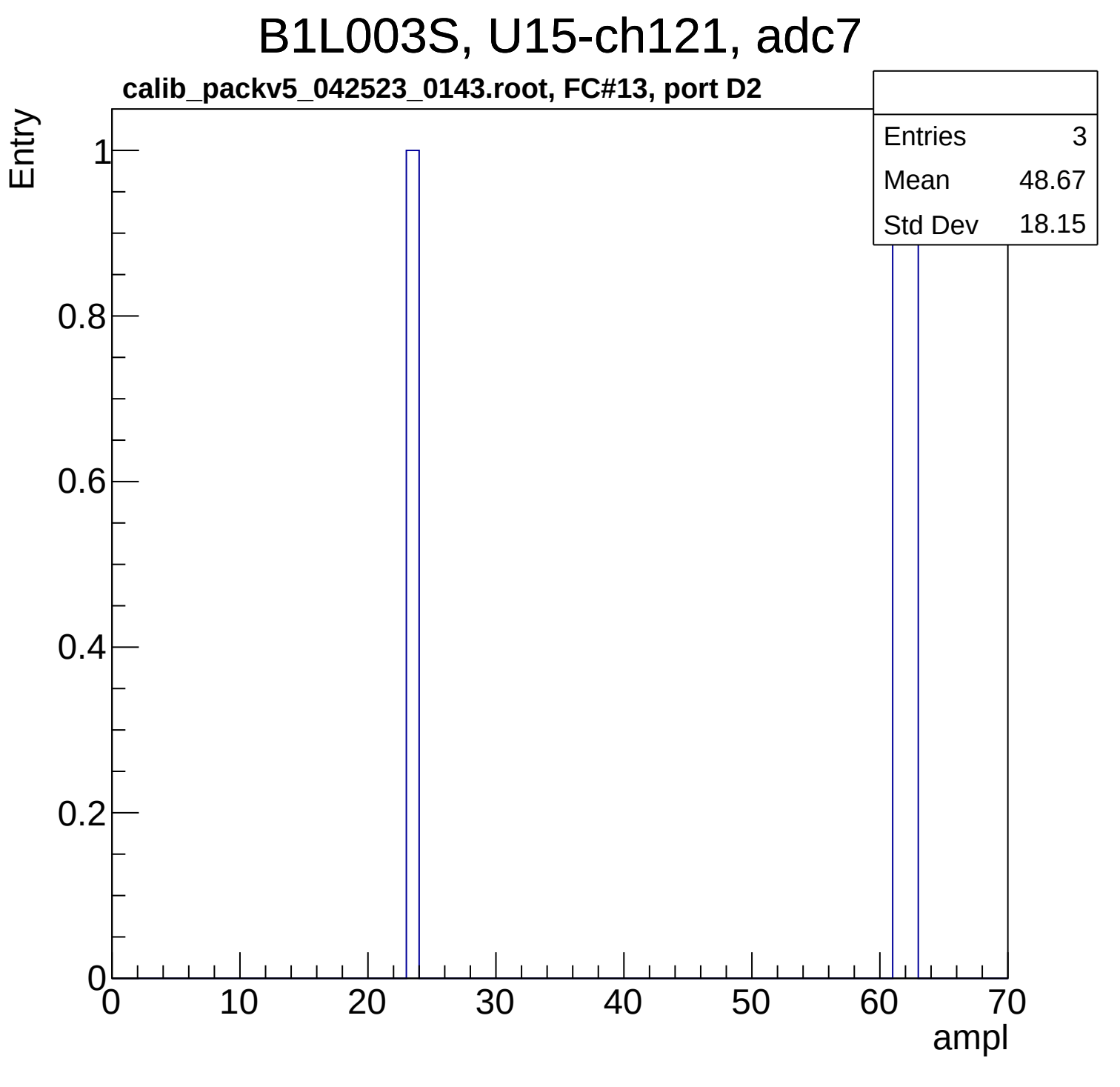
50
60
70
ampl


B1L003S, U15-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 48.67
Std Dev 18.15
Entry
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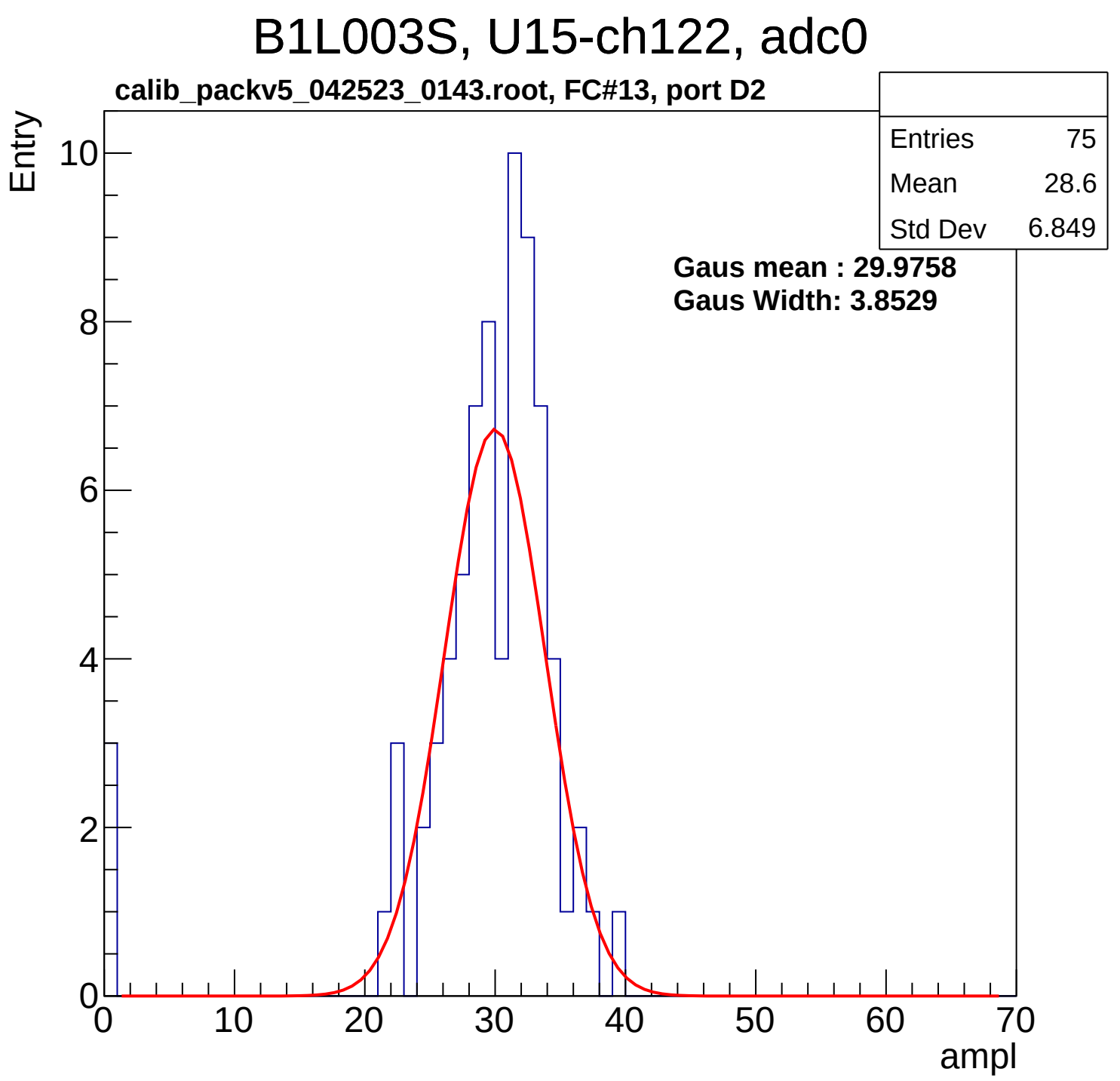
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.6
Std Dev 6.849
Gaus mean : 29.9758
Gaus Width: 3.8529
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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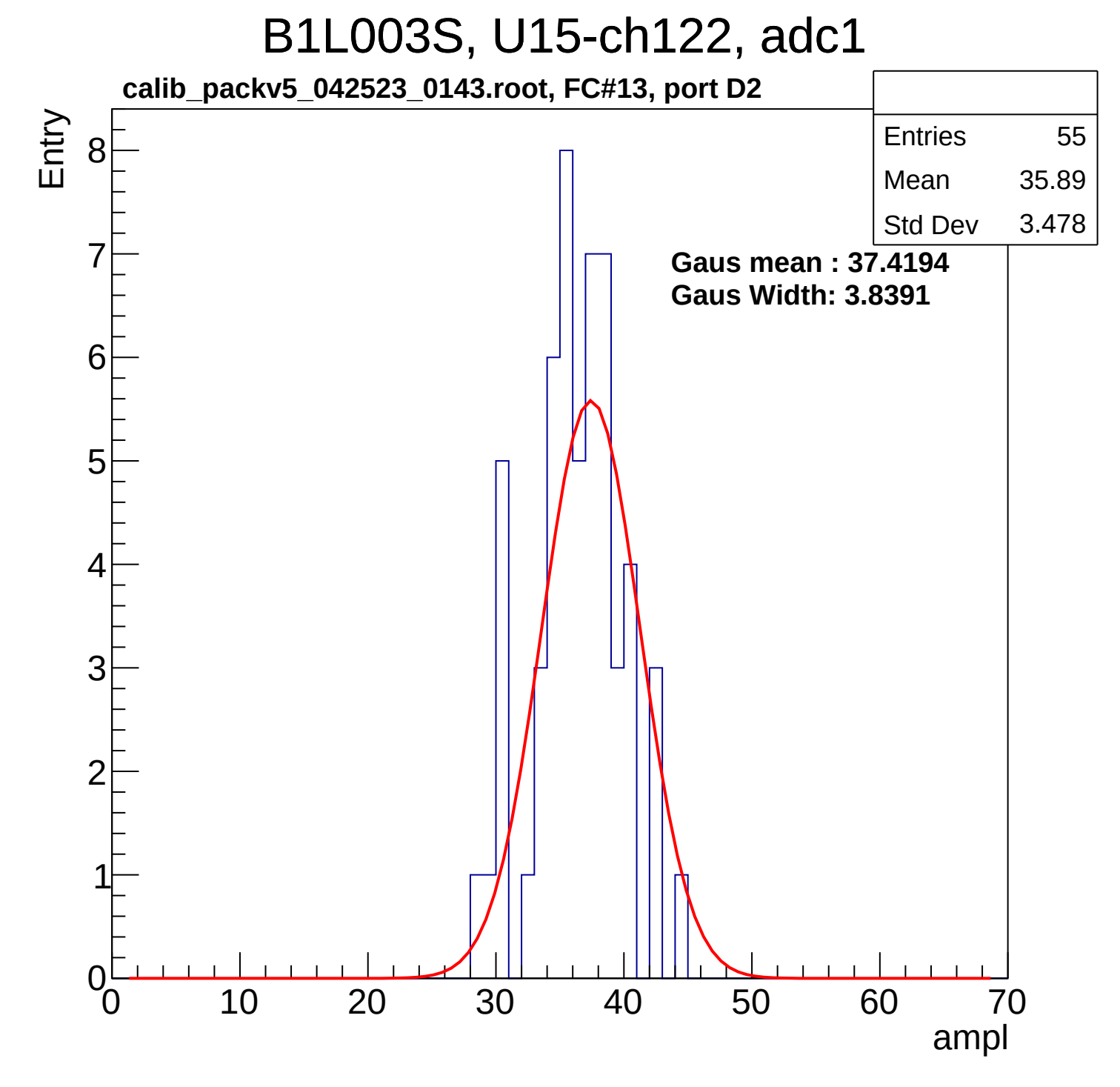
B1L003S, U15-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 35.89
Std Dev 3.478
Gaus mean : 37.4194
Gaus Width: 3.8391
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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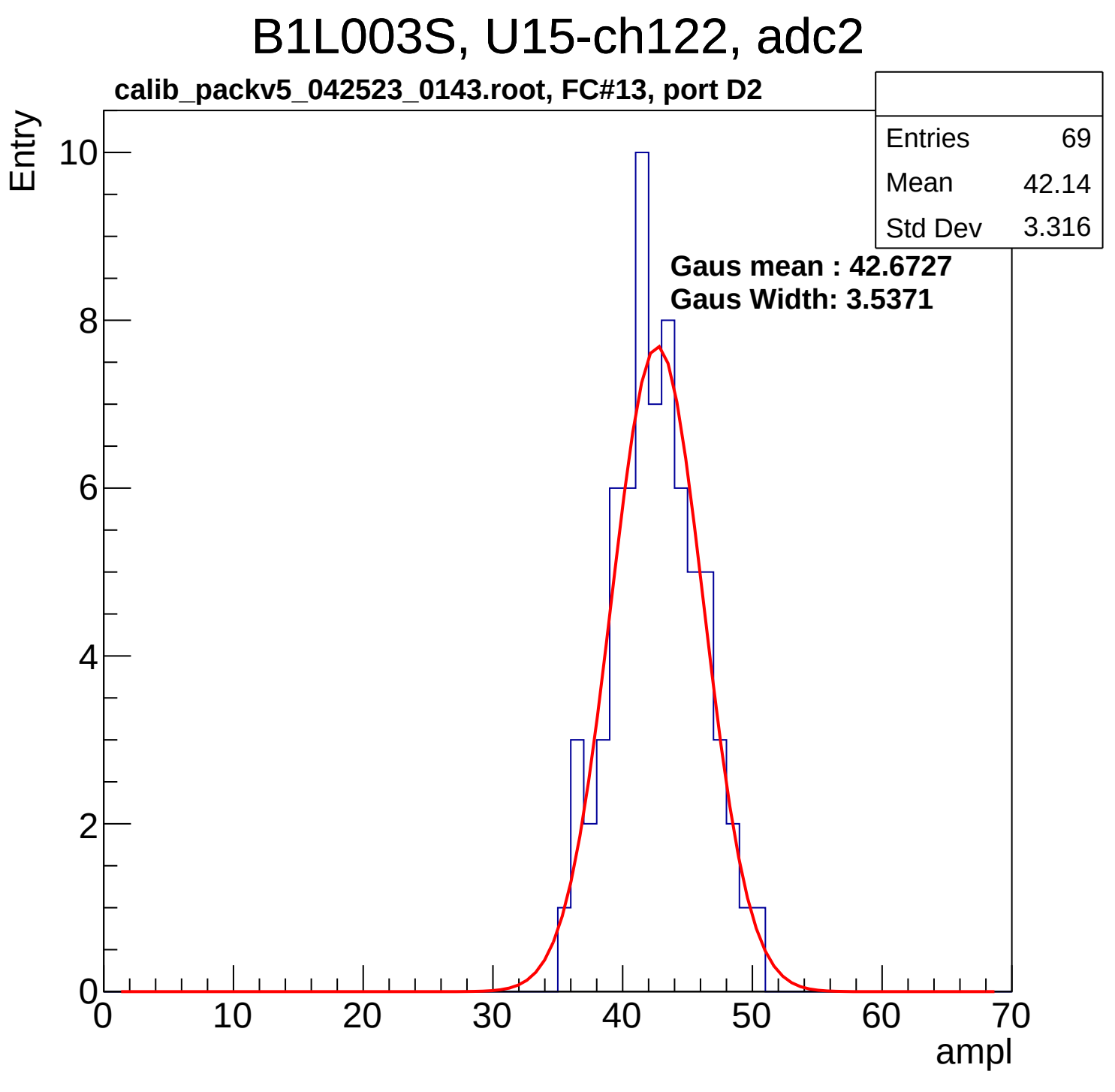
B1L003S, U15-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.14
Std Dev 3.316
Gaus mean : 42.6727
Gaus Width: 3.5371
Entry
ampl
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2
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6
8
10
0
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20
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40
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60
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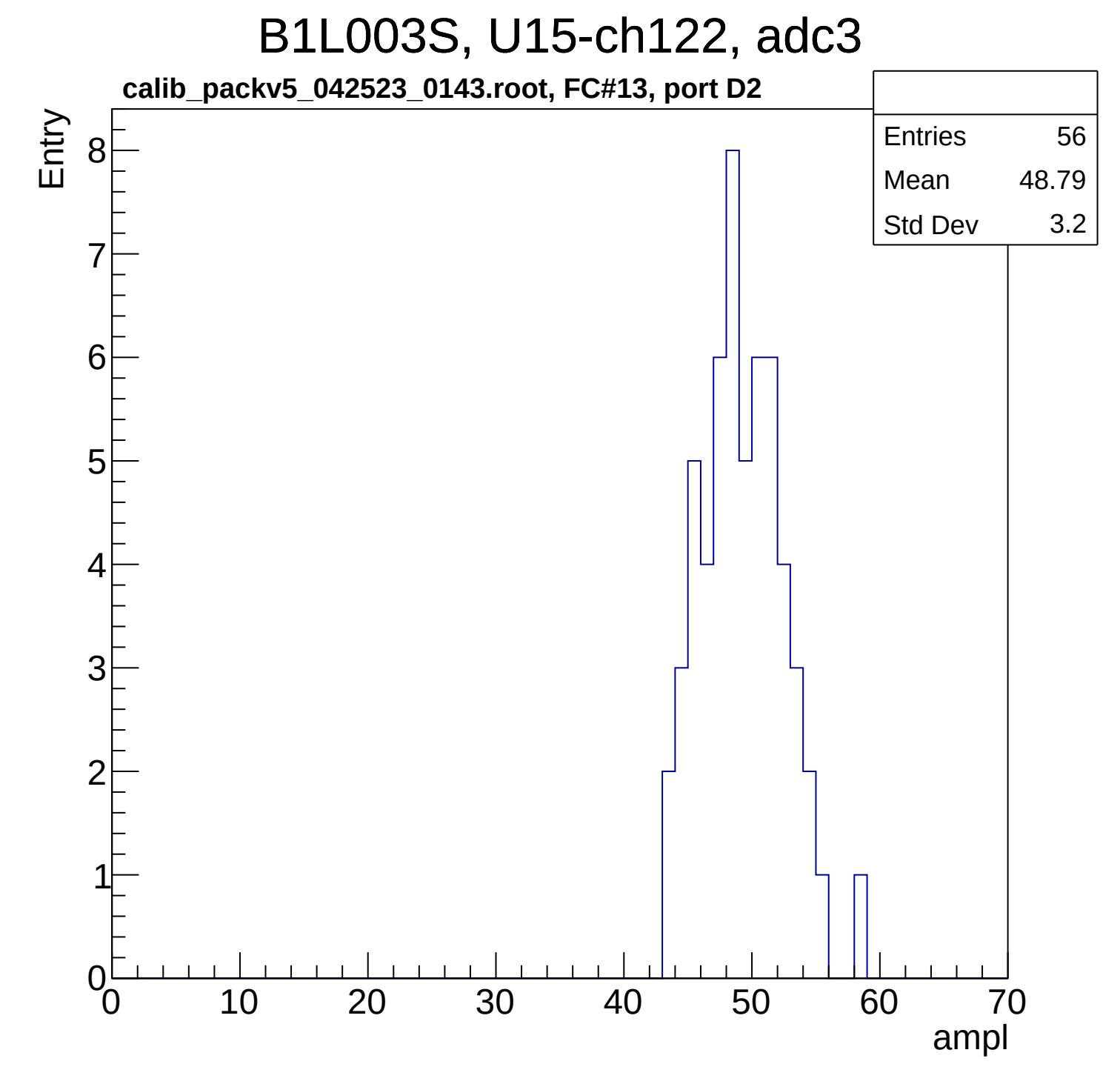
B1L003S, U15-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.79
Std Dev 3.2
Entry
ampl
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7
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0
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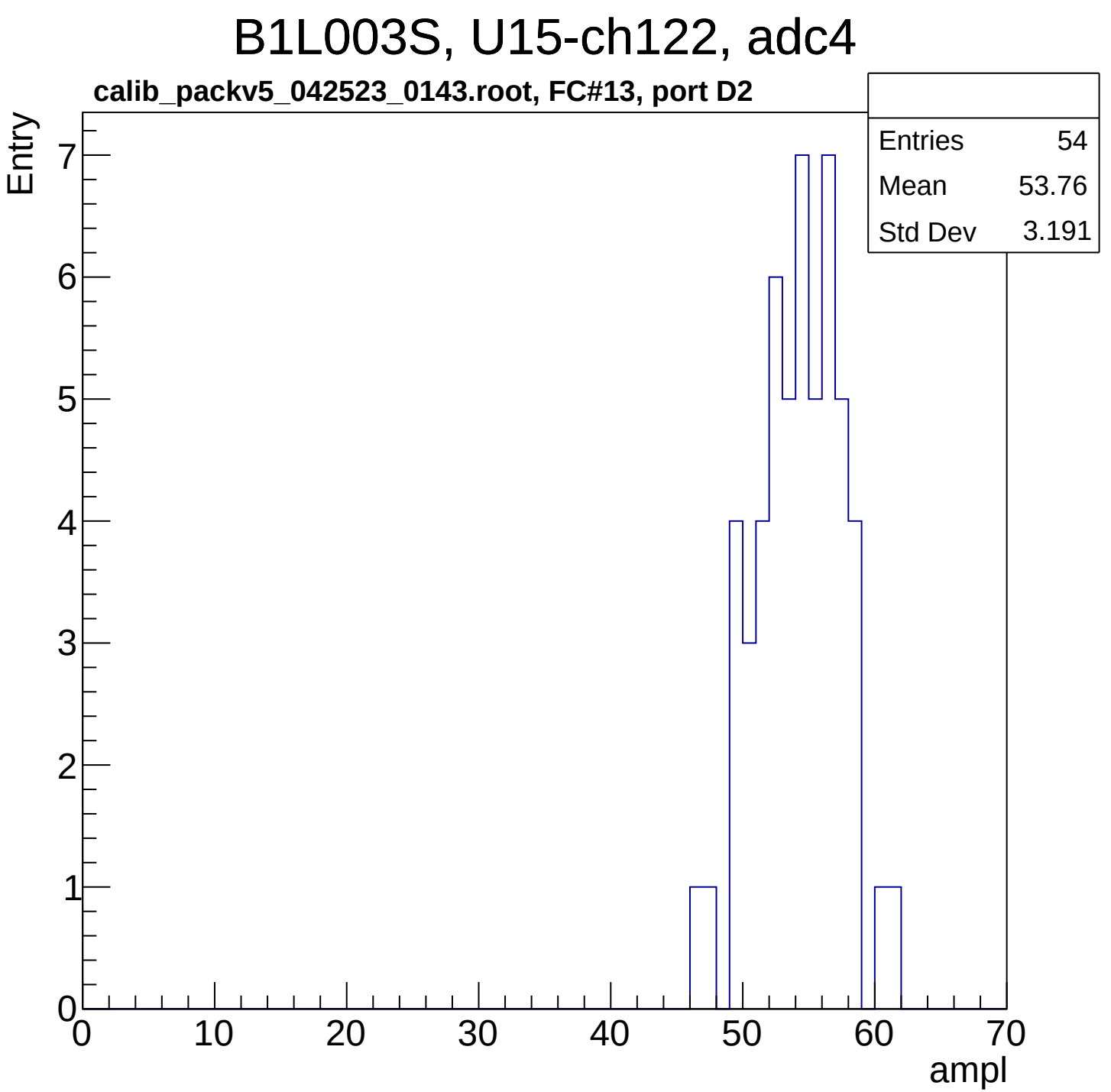
B1L003S, U15-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.76
Std Dev 3.191
Entry
ampl
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6
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0
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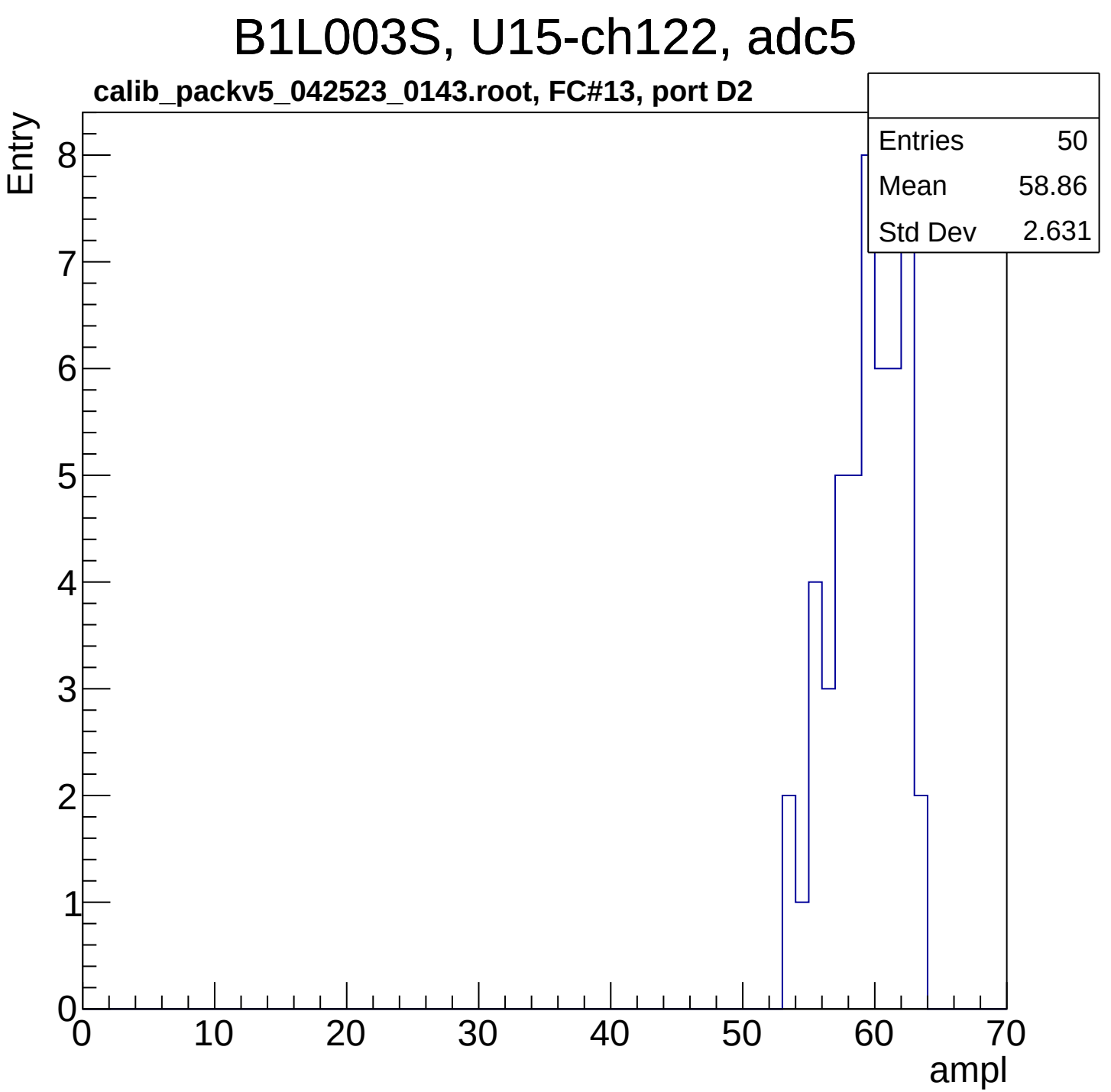
B1L003S, U15-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.86
Std Dev 2.631
Entry
ampl
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4
5
6
7
8
0
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20
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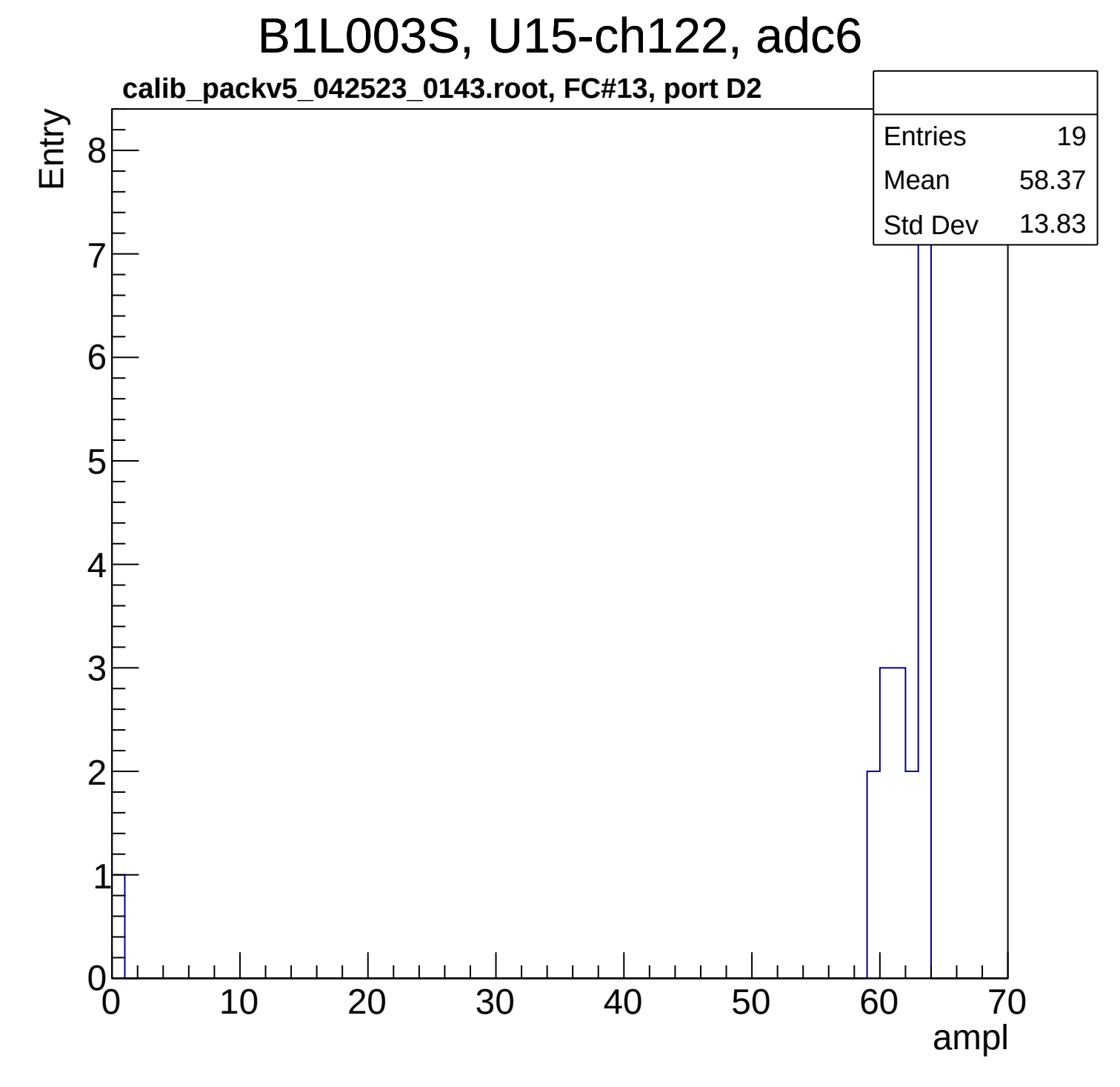
B1L003S, U15-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 19
Mean 58.37
Std Dev 13.83
Entry
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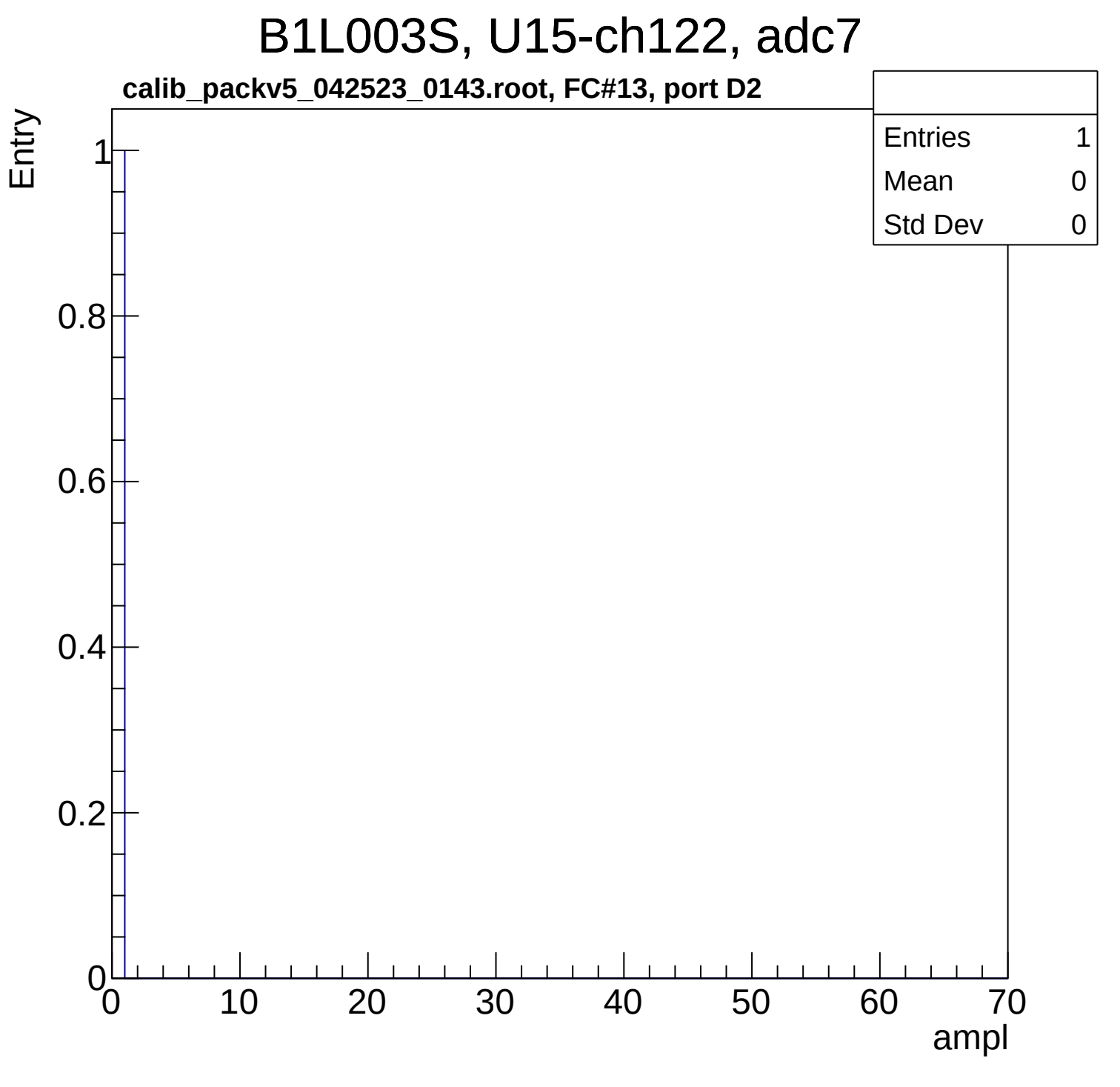
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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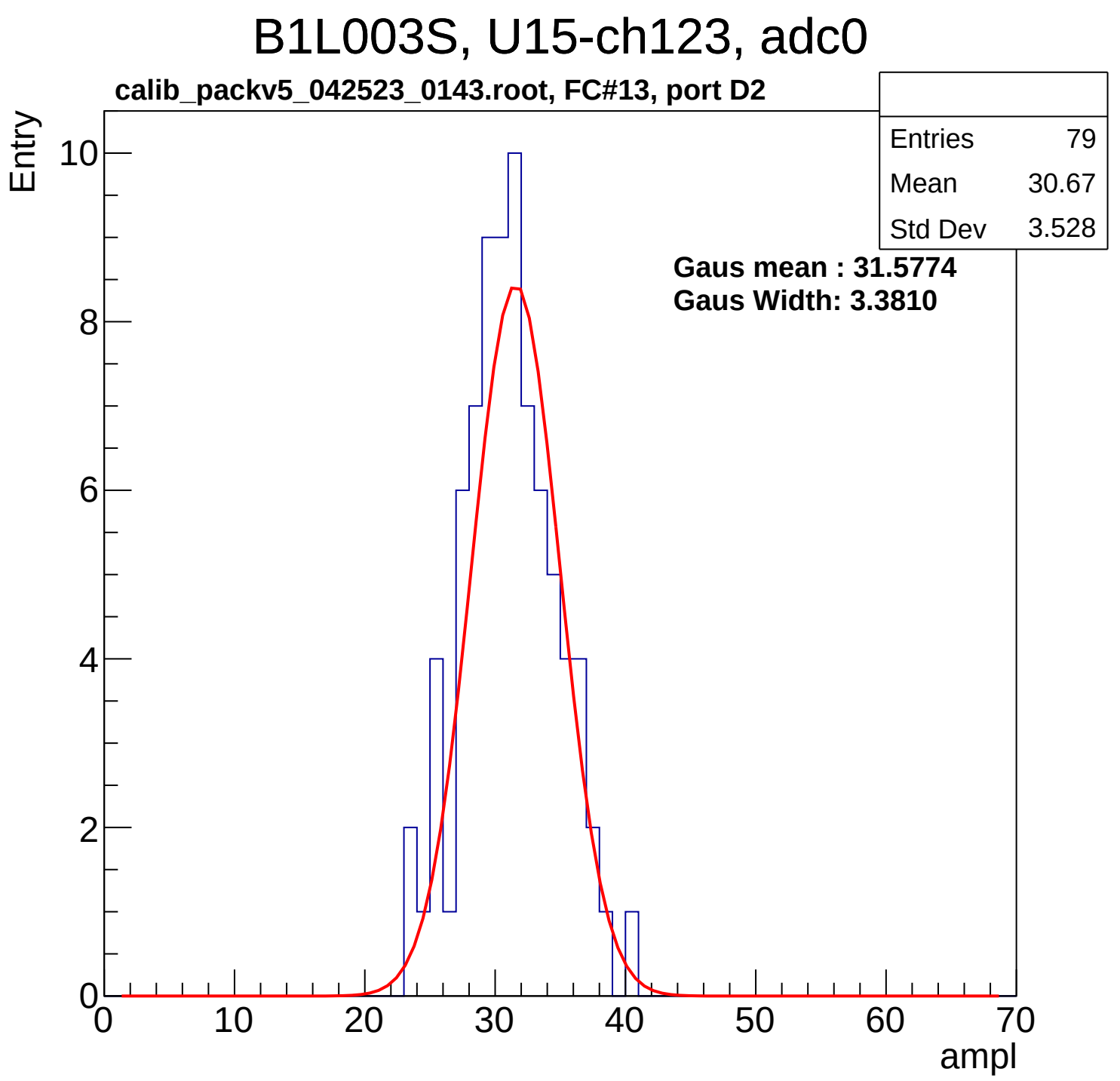
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 30.67
Std Dev 3.528
Gaus mean : 31.5774
Gaus Width: 3.3810
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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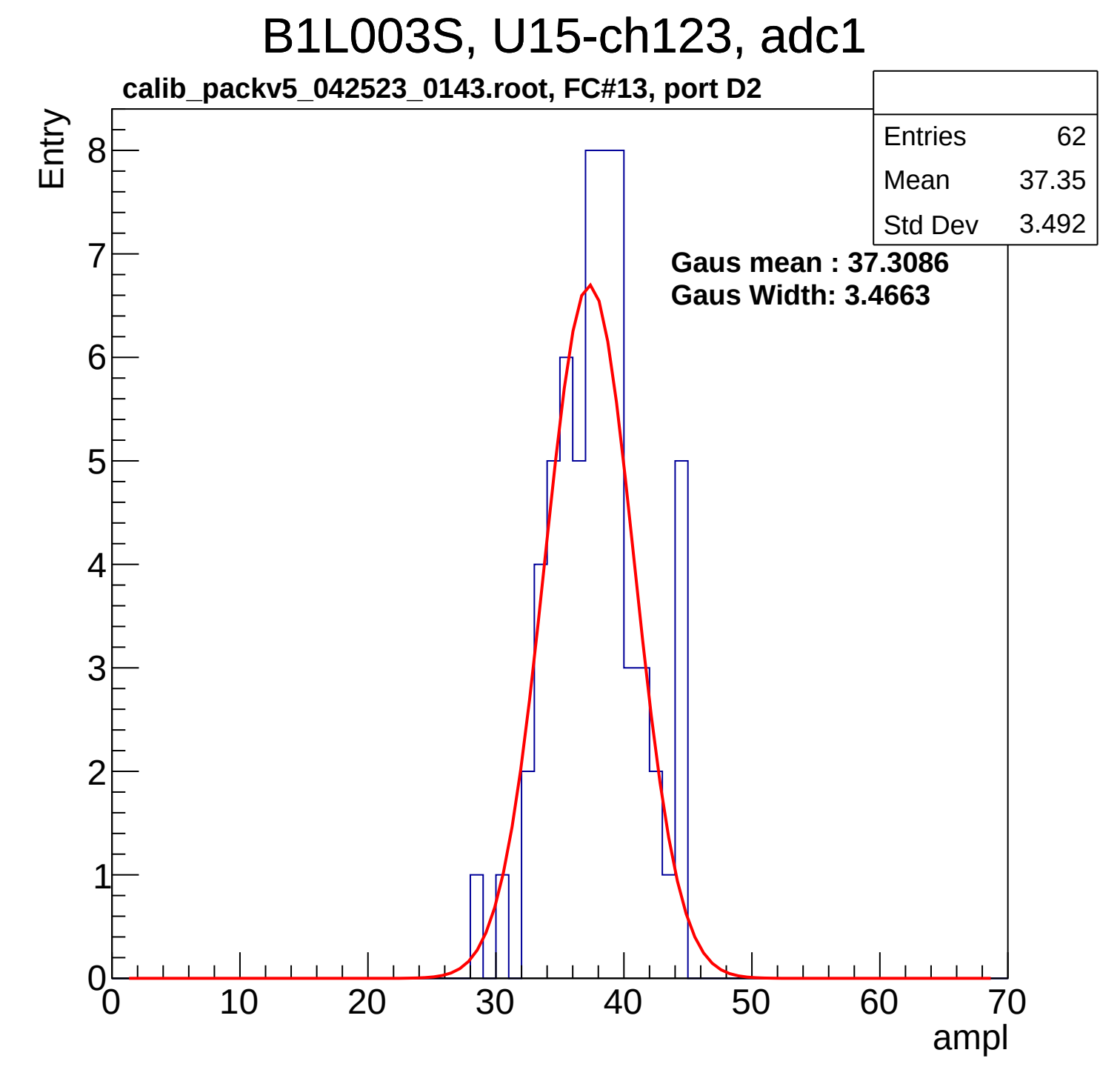
B1L003S, U15-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.35
Std Dev 3.492
Gaus mean : 37.3086
Gaus Width: 3.4663
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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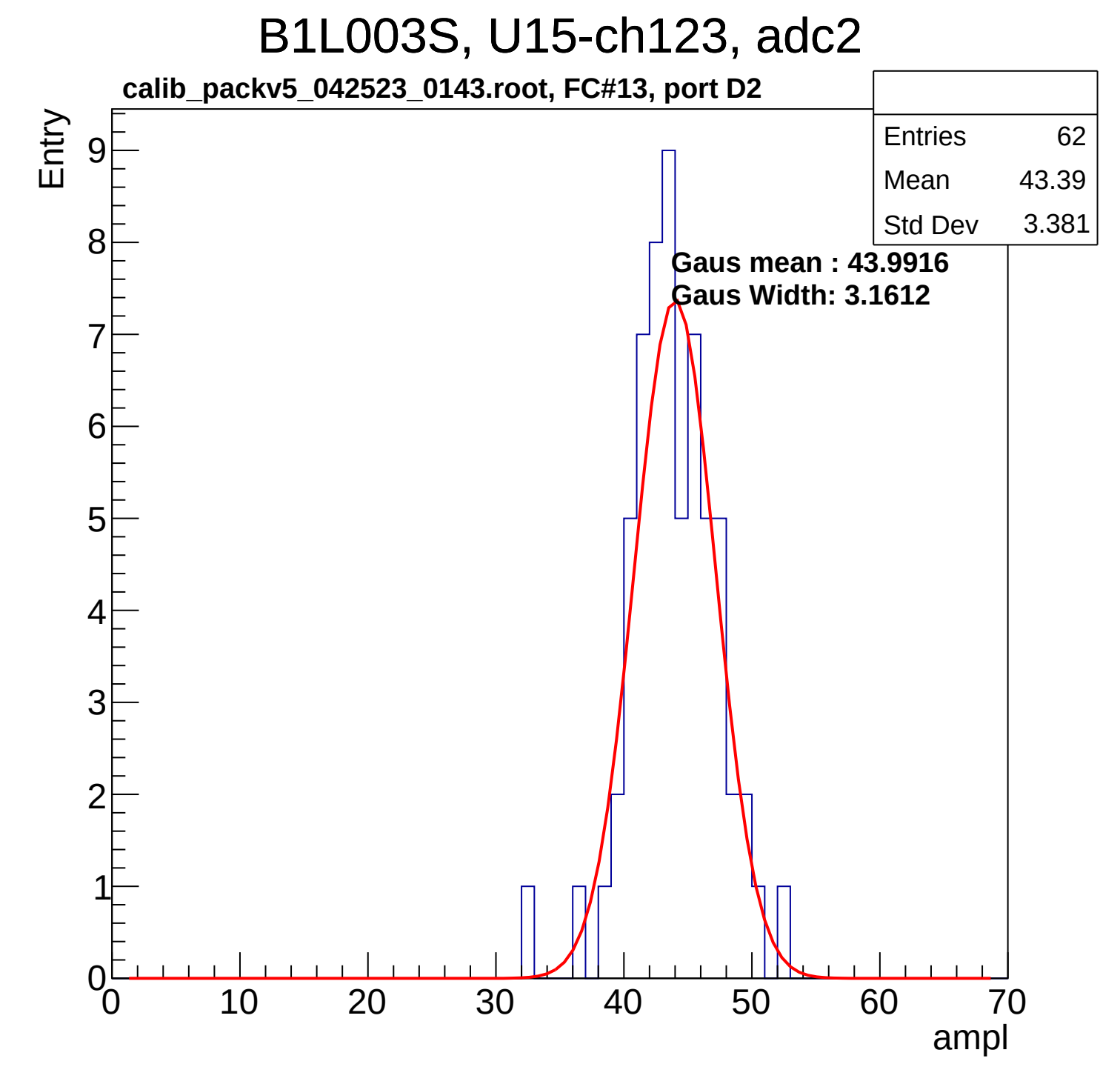
B1L003S, U15-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.39
Std Dev 3.381
Gaus mean : 43.9916
Gaus Width: 3.1612
Entry
ampl
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3
4
5
6
7
8
9
0
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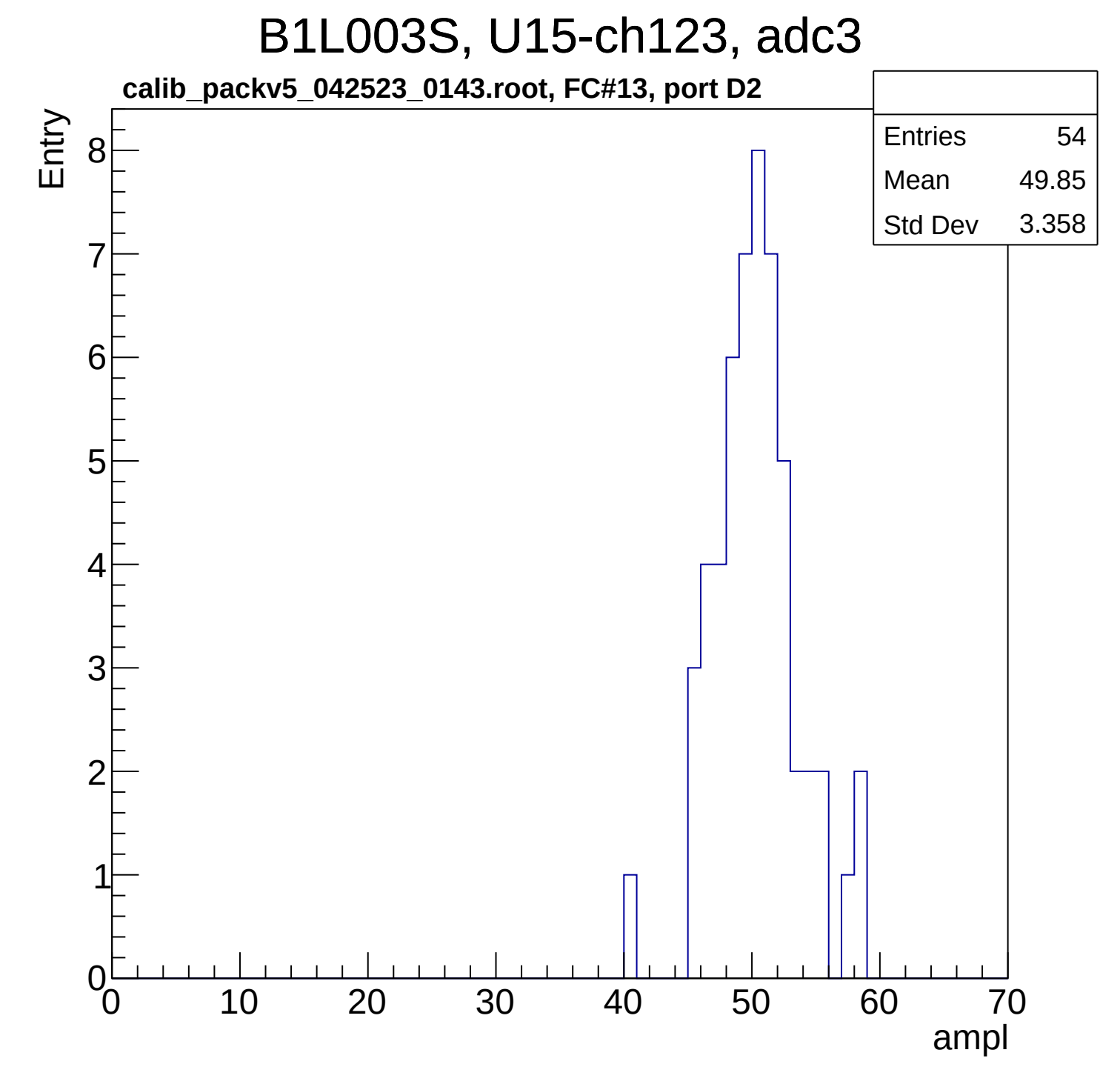
B1L003S, U15-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.85
Std Dev 3.358
Entry
ampl
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3
4
5
6
7
8
0
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20
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60
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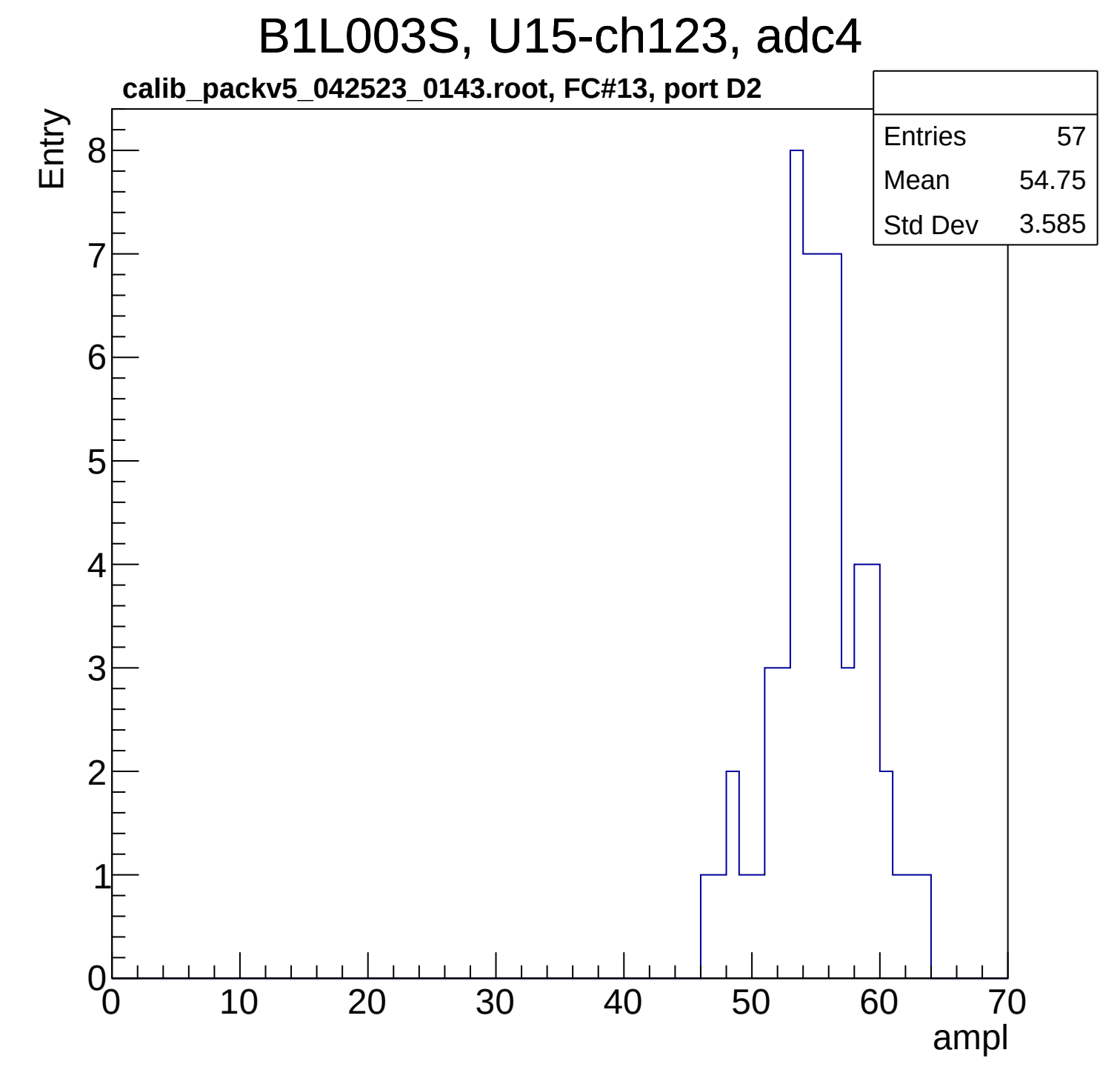
B1L003S, U15-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.75
Std Dev 3.585
Entry
ampl
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3
4
5
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7
8
0
10
20
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40
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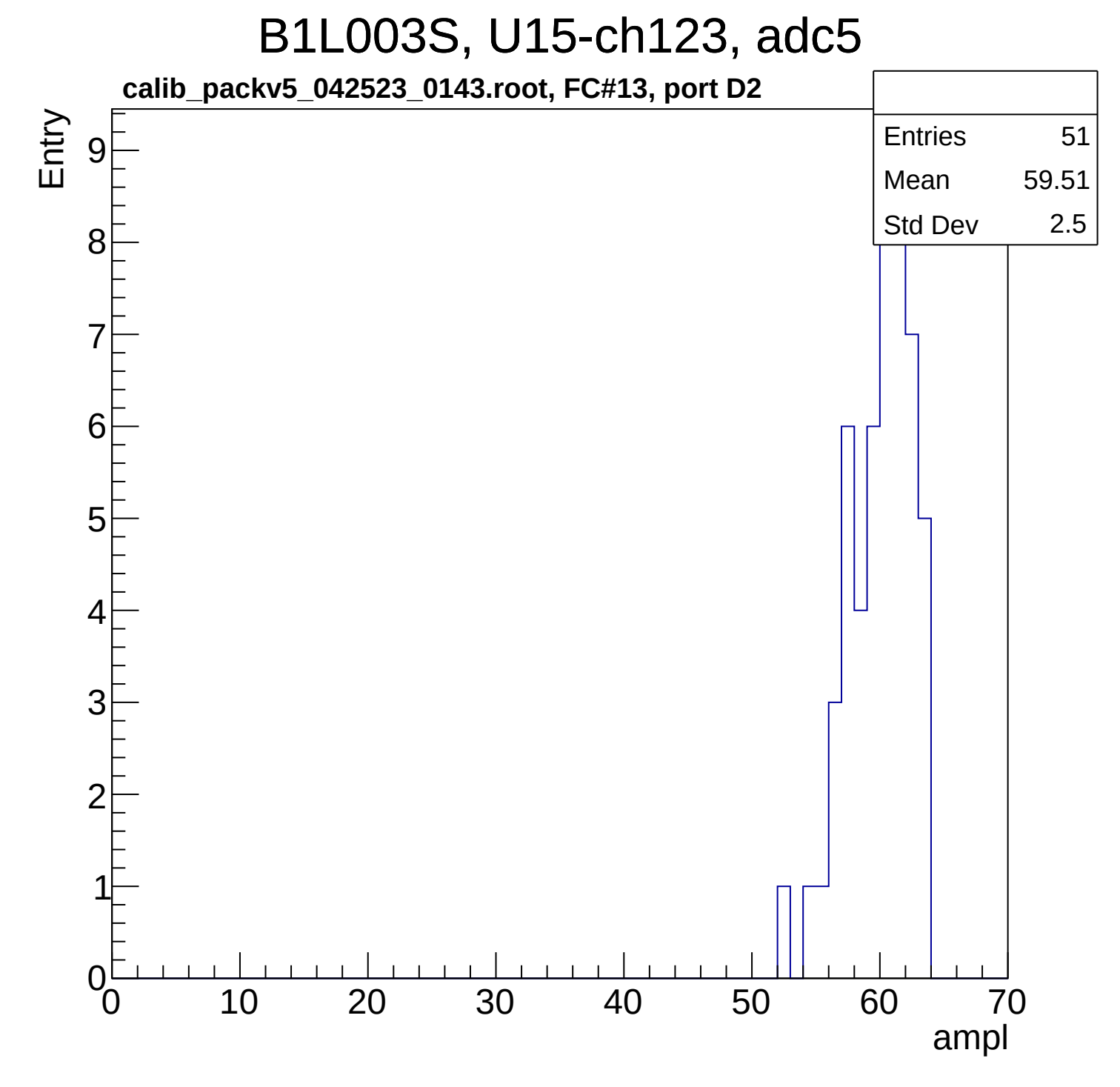
B1L003S, U15-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.51
Std Dev 2.5
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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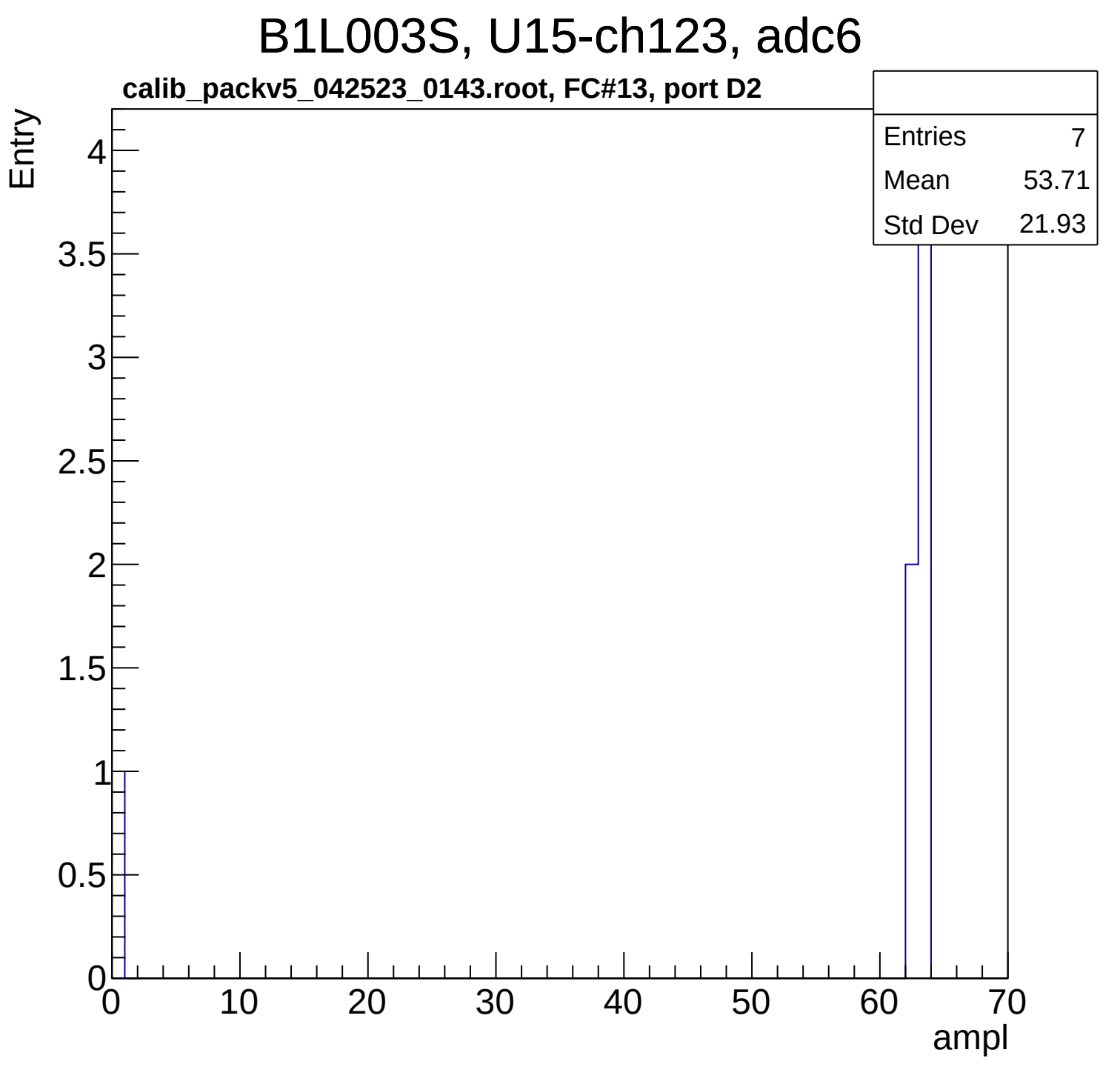
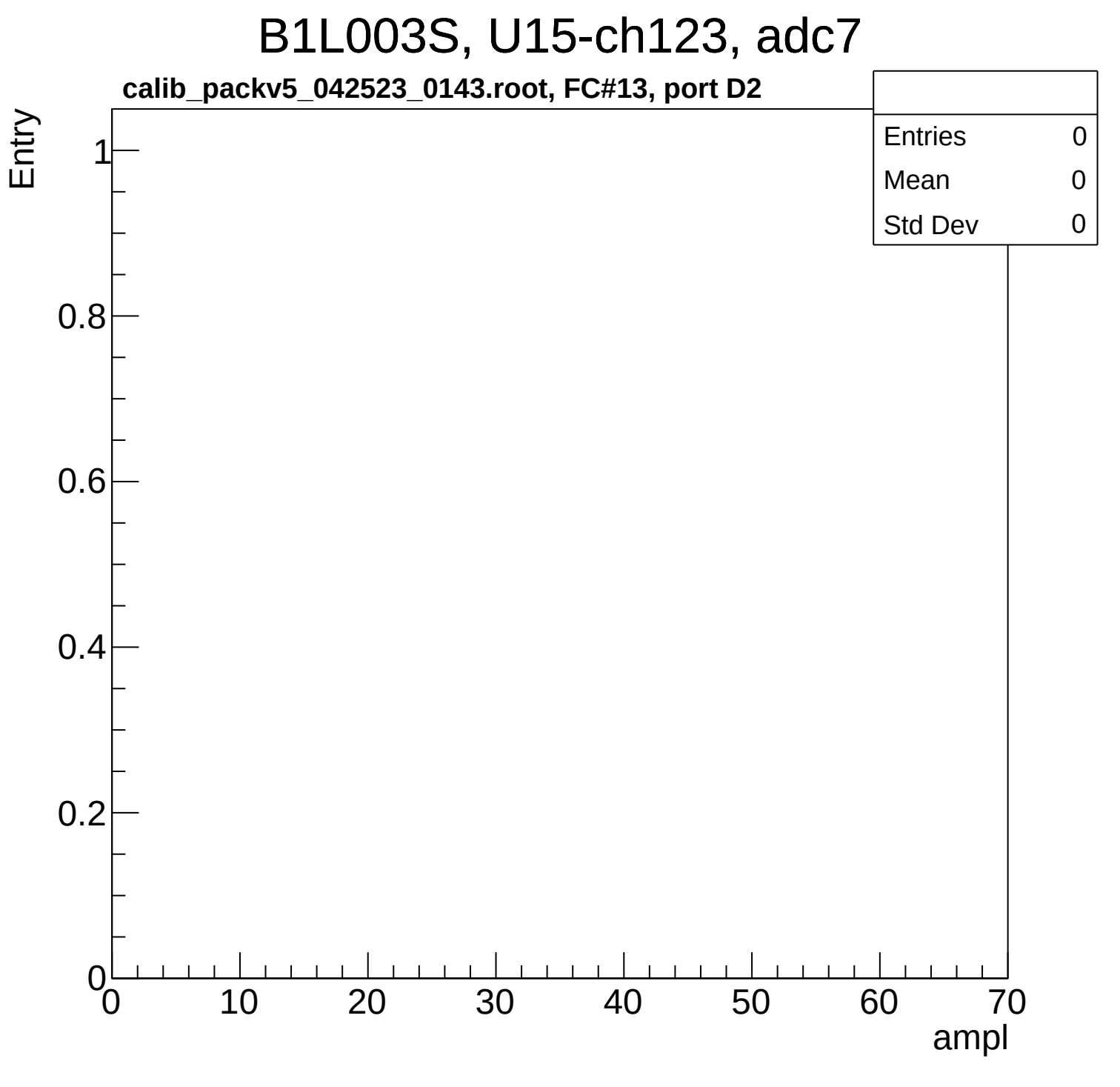

B1L003S, U15-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 53.71
Std Dev 21.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U15-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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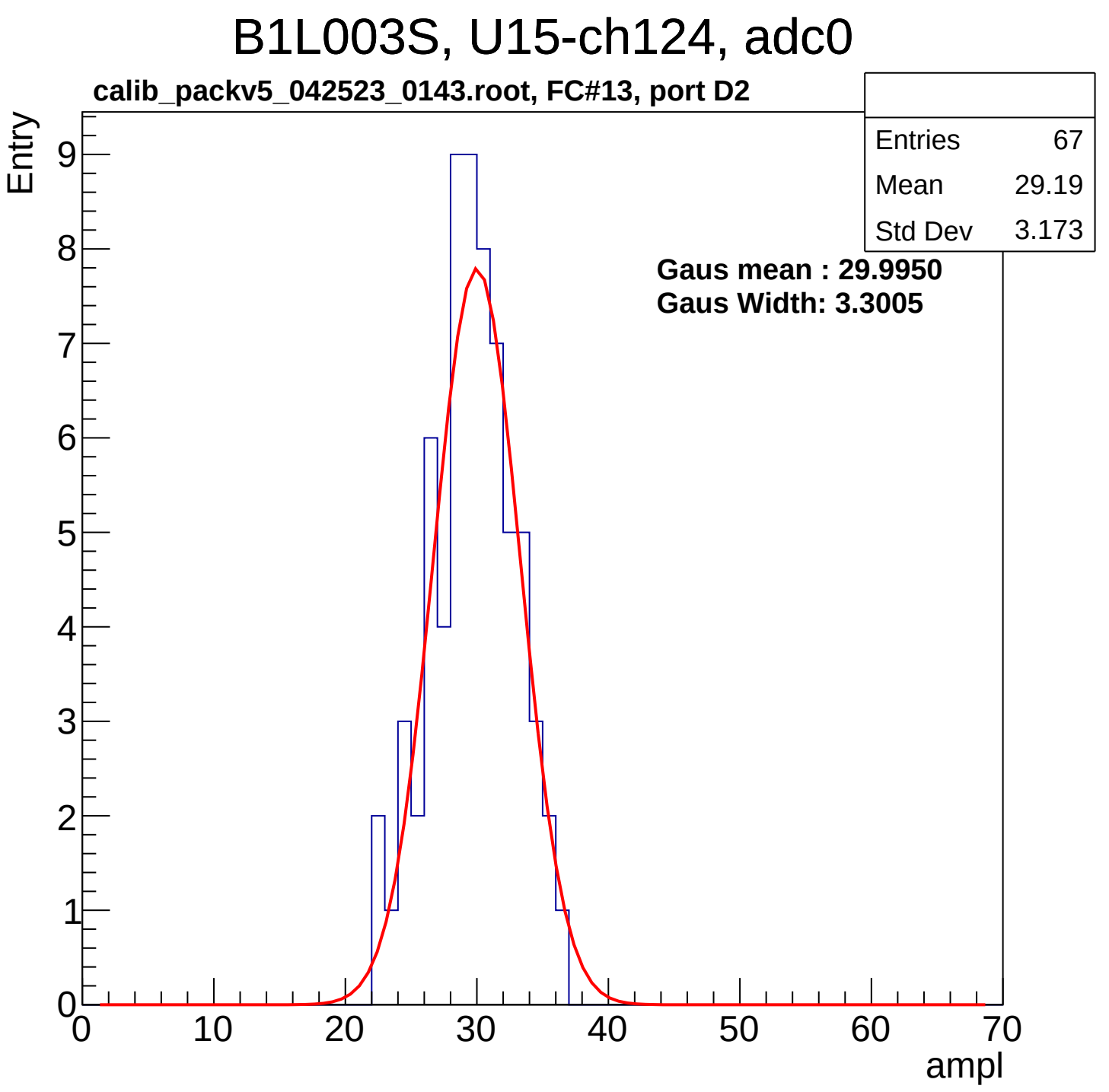
50
60
70
ampl


B1L003S, U15-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.19
Std Dev 3.173
Gaus mean : 29.9950
Gaus Width: 3.3005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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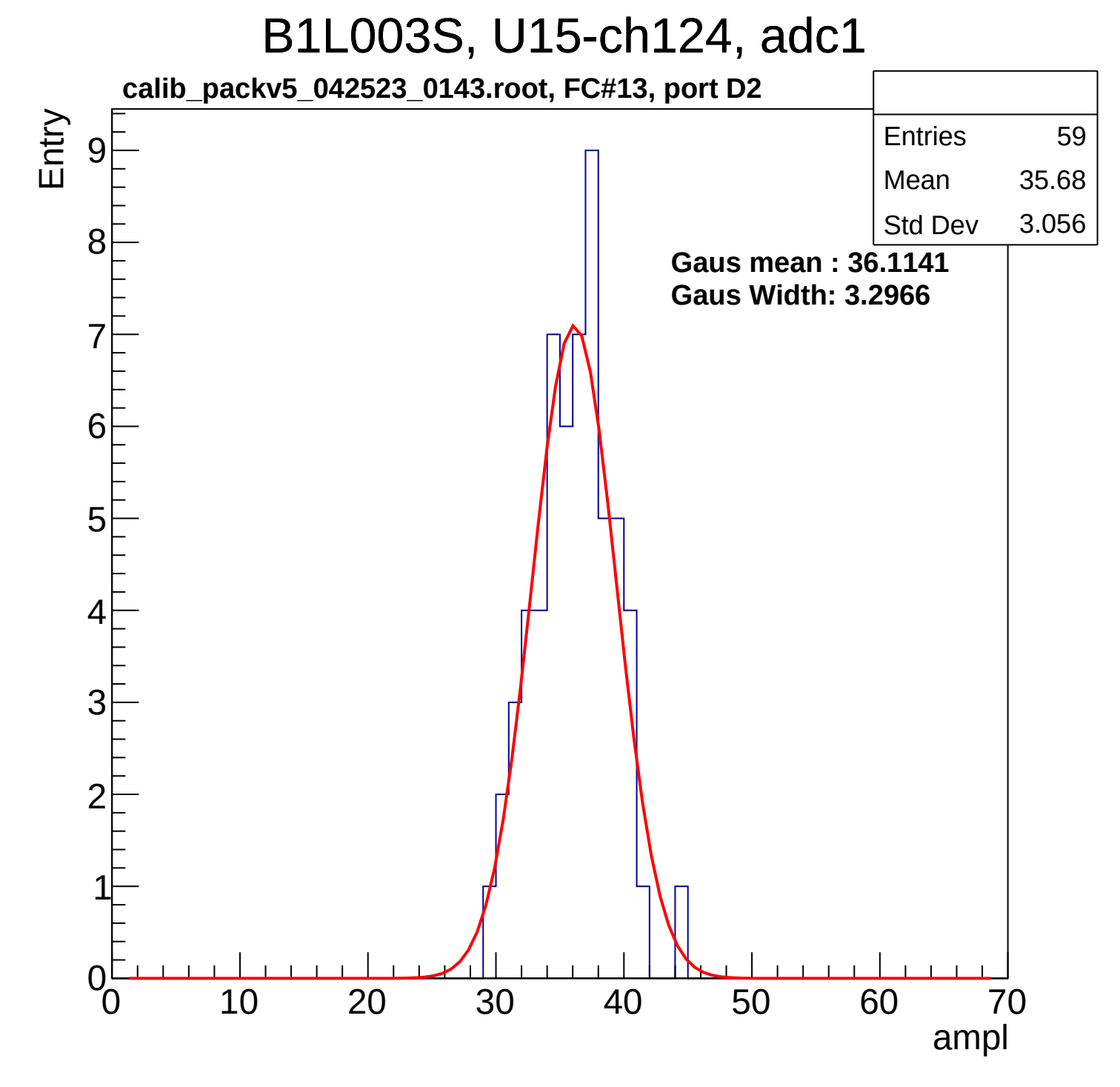
B1L003S, U15-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 35.68
Std Dev 3.056
Gaus mean : 36.1141
Gaus Width: 3.2966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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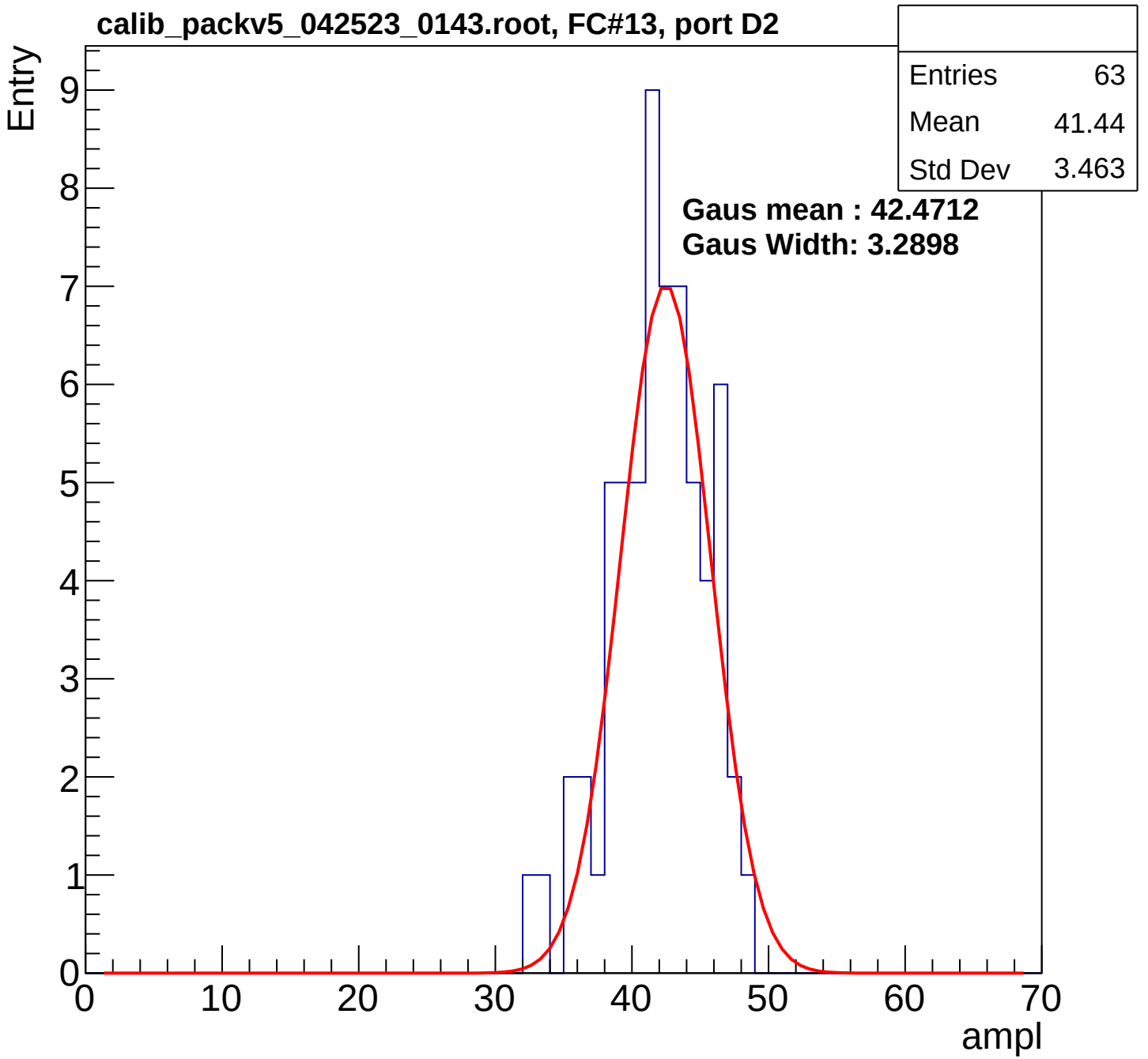
B1L003S, U15-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 41.44
Std Dev 3.463
Gaus mean : 42.4712
Gaus Width: 3.2898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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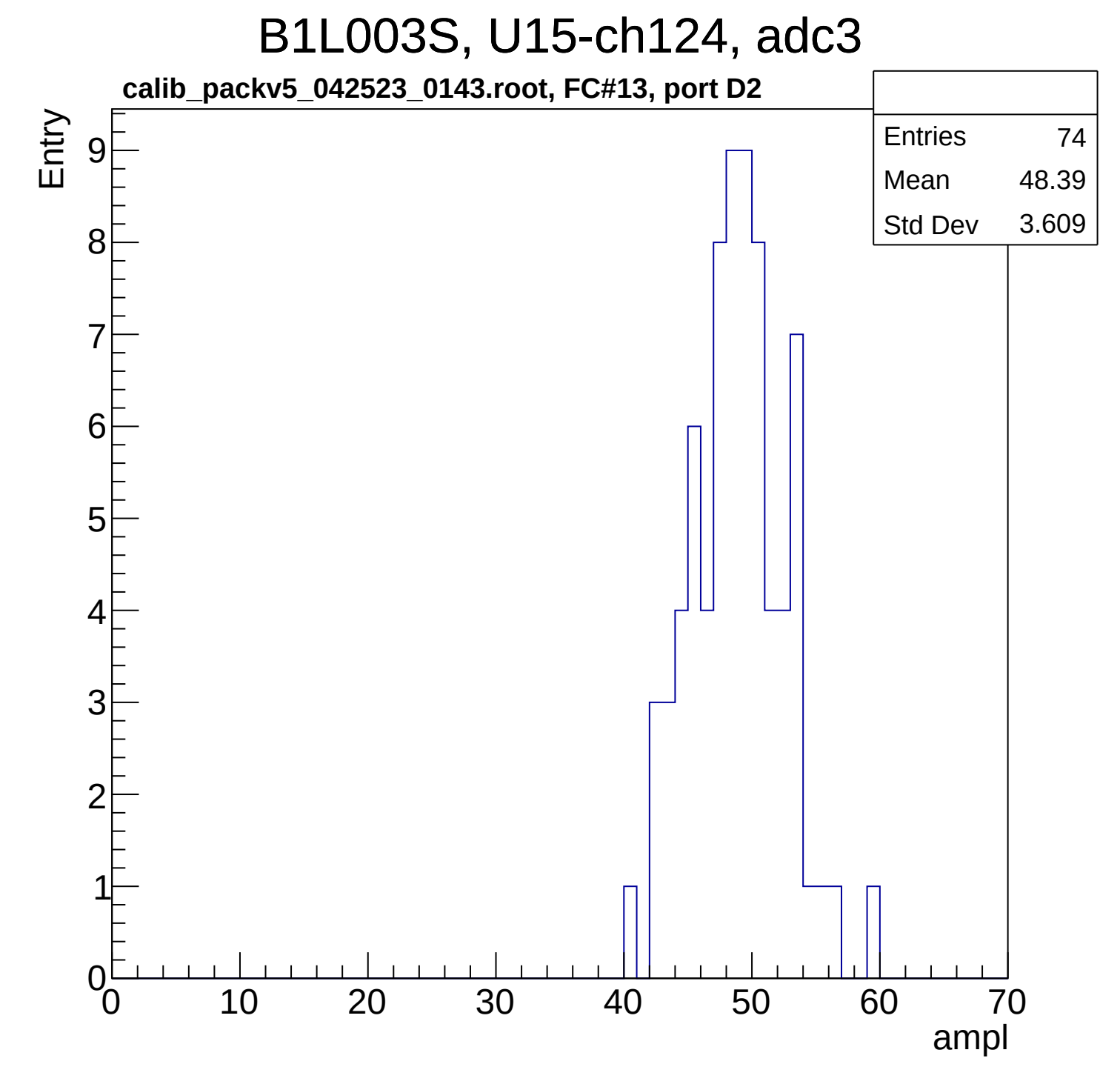
B1L003S, U15-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.39
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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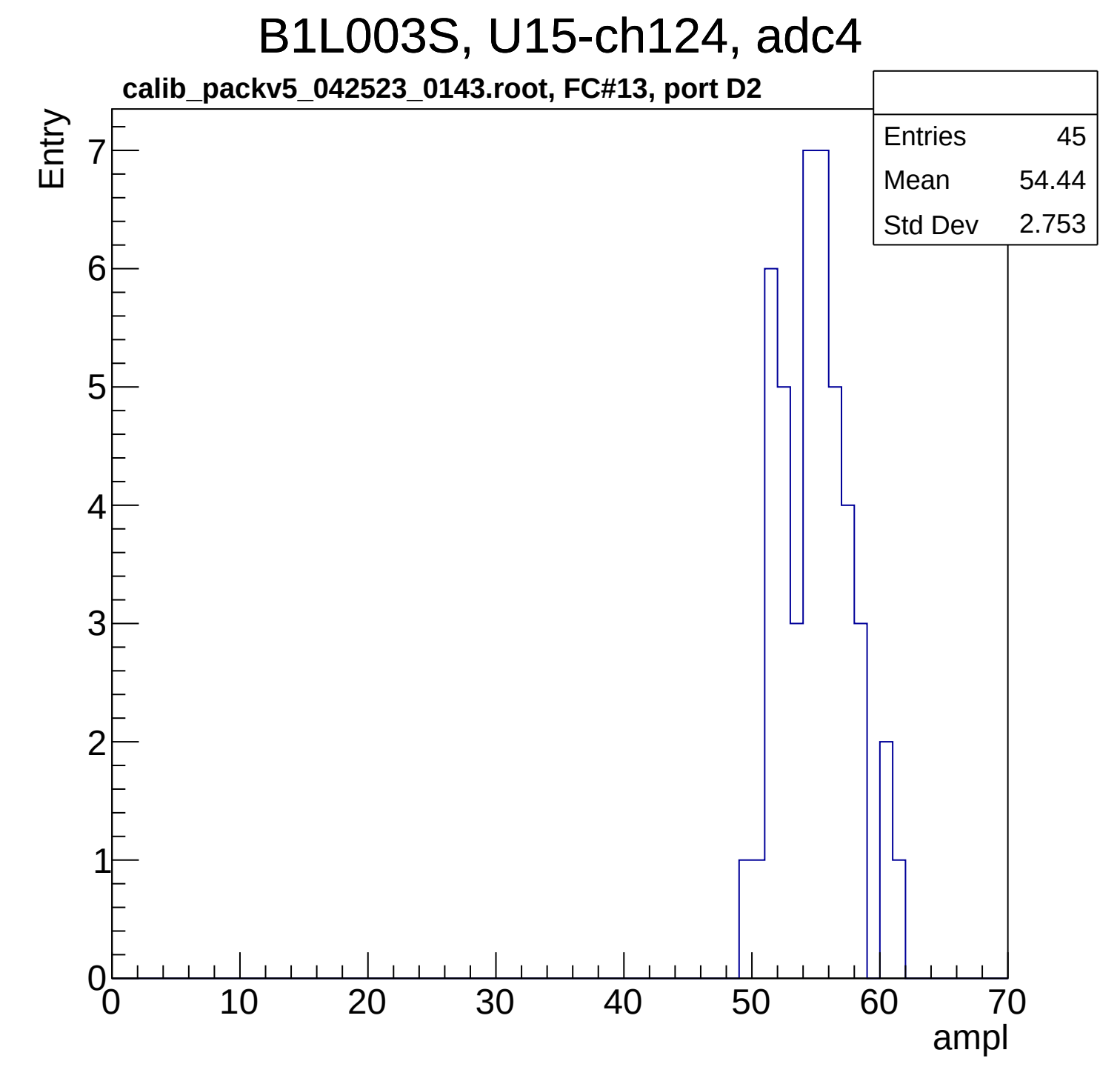
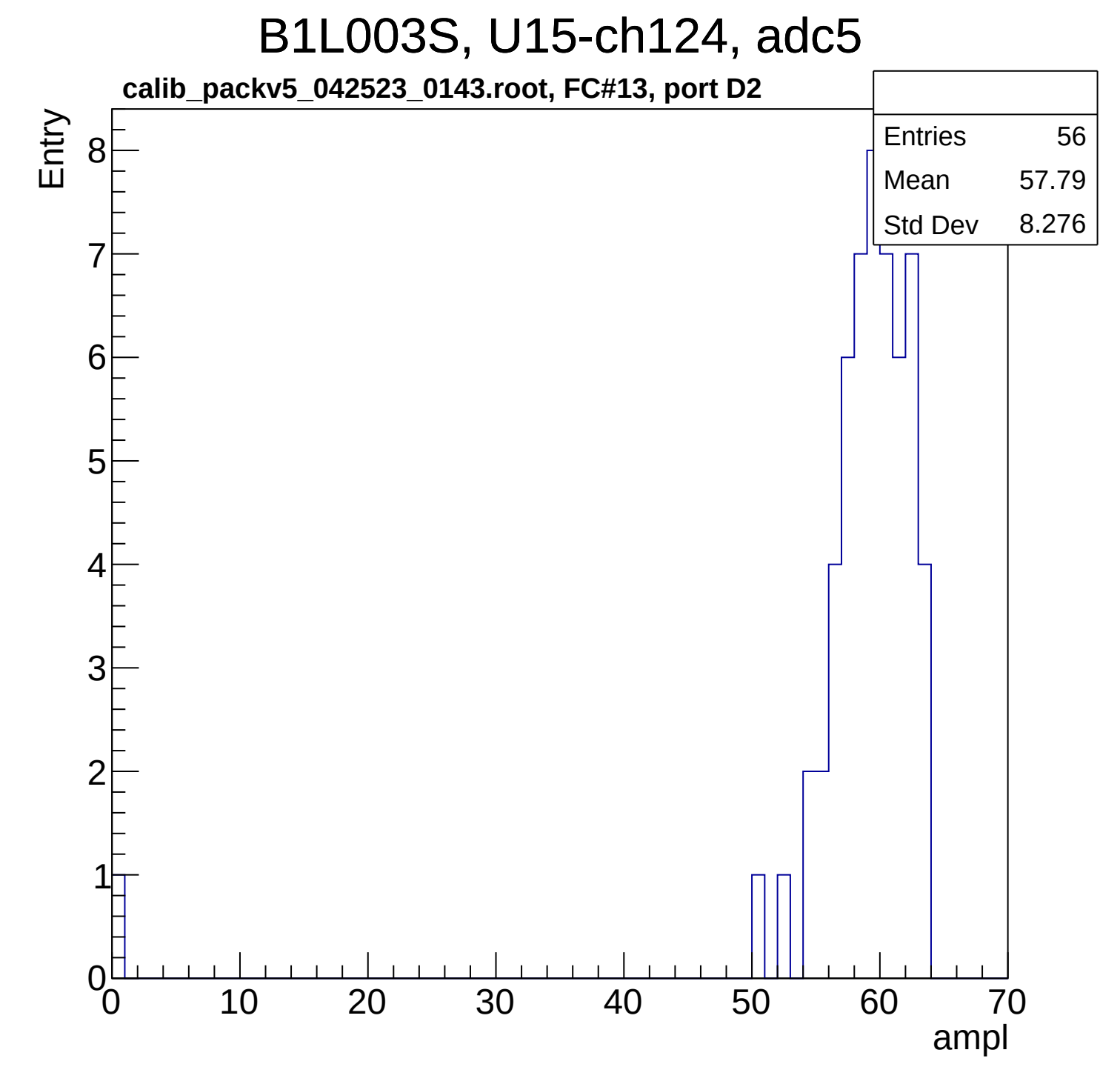

B1L003S, U15-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 54.44
Std Dev 2.753
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 57.79
Std Dev 8.276
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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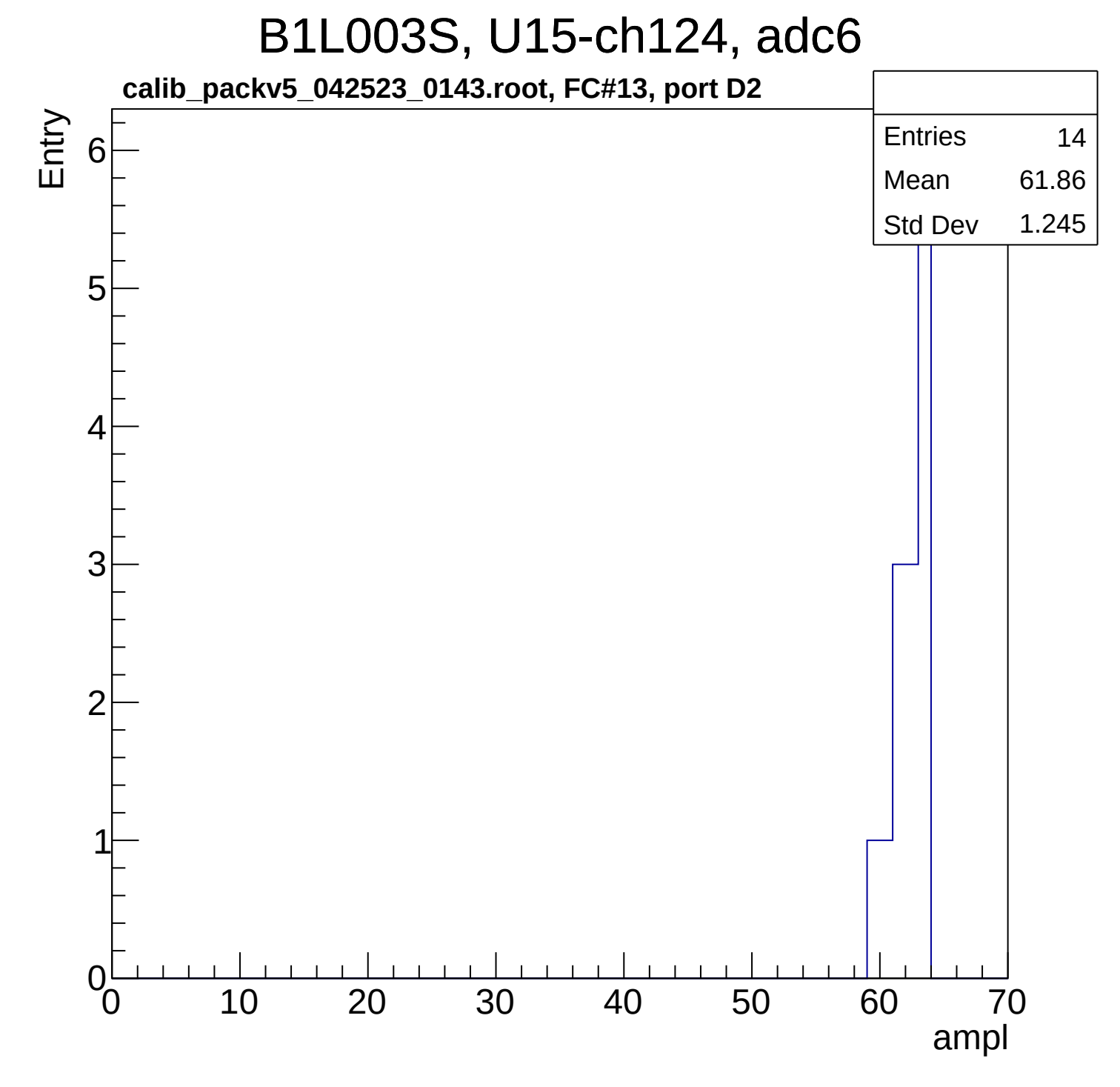
B1L003S, U15-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.86
Std Dev 1.245
Entry
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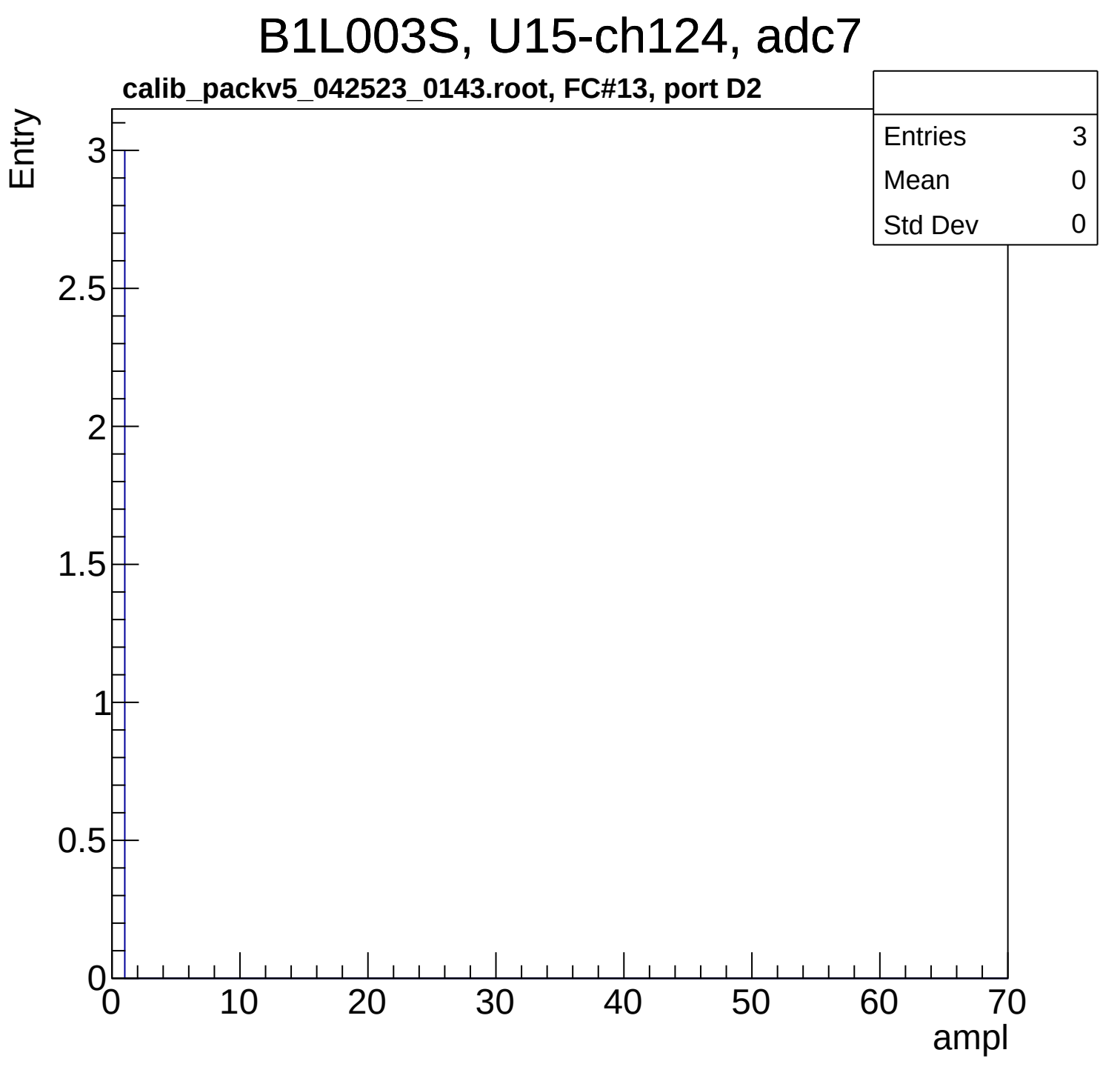
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
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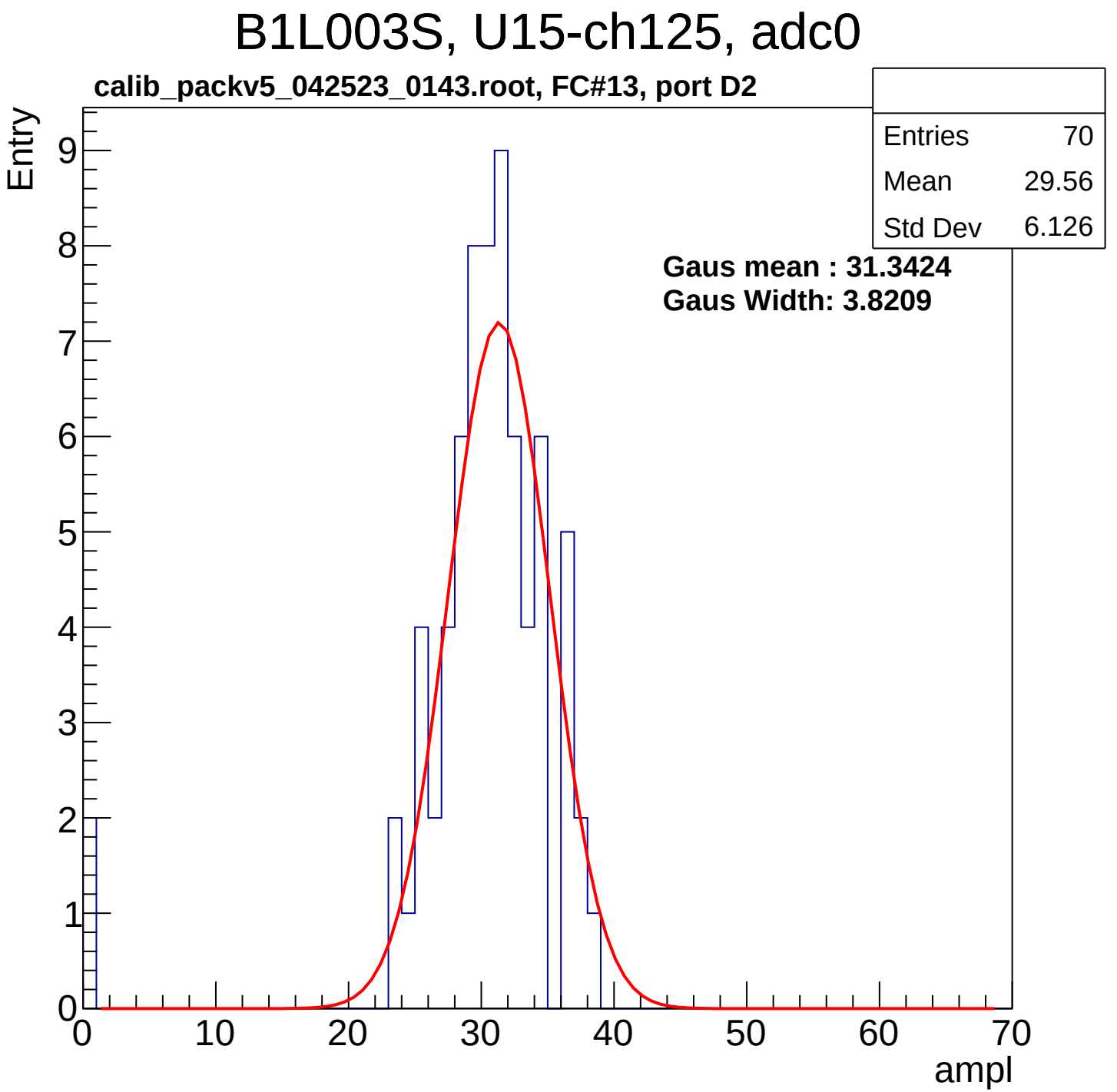
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.56
Std Dev 6.126
Gaus mean : 31.3424
Gaus Width: 3.8209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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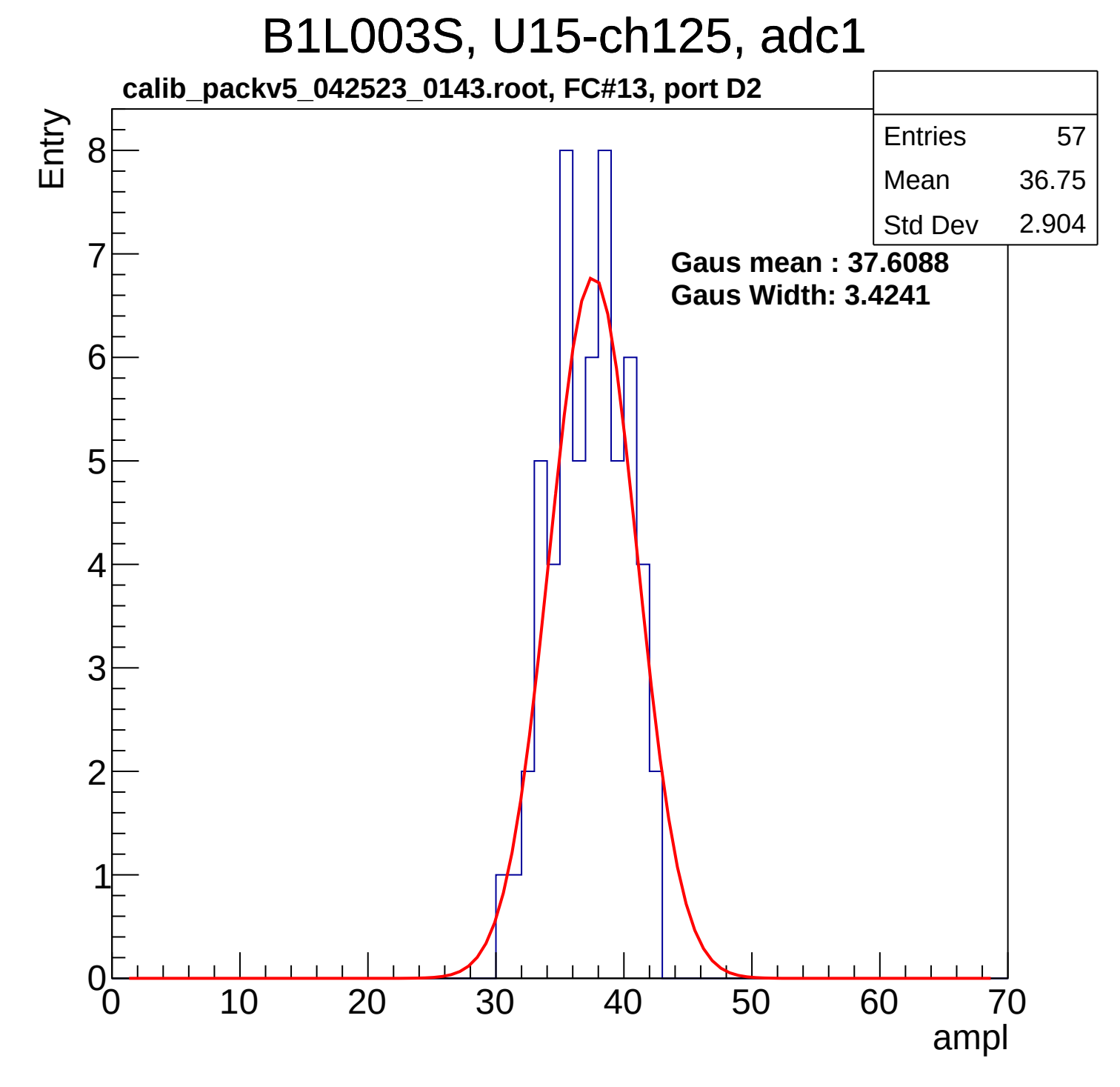
B1L003S, U15-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.75
Std Dev 2.904
Gaus mean : 37.6088
Gaus Width: 3.4241
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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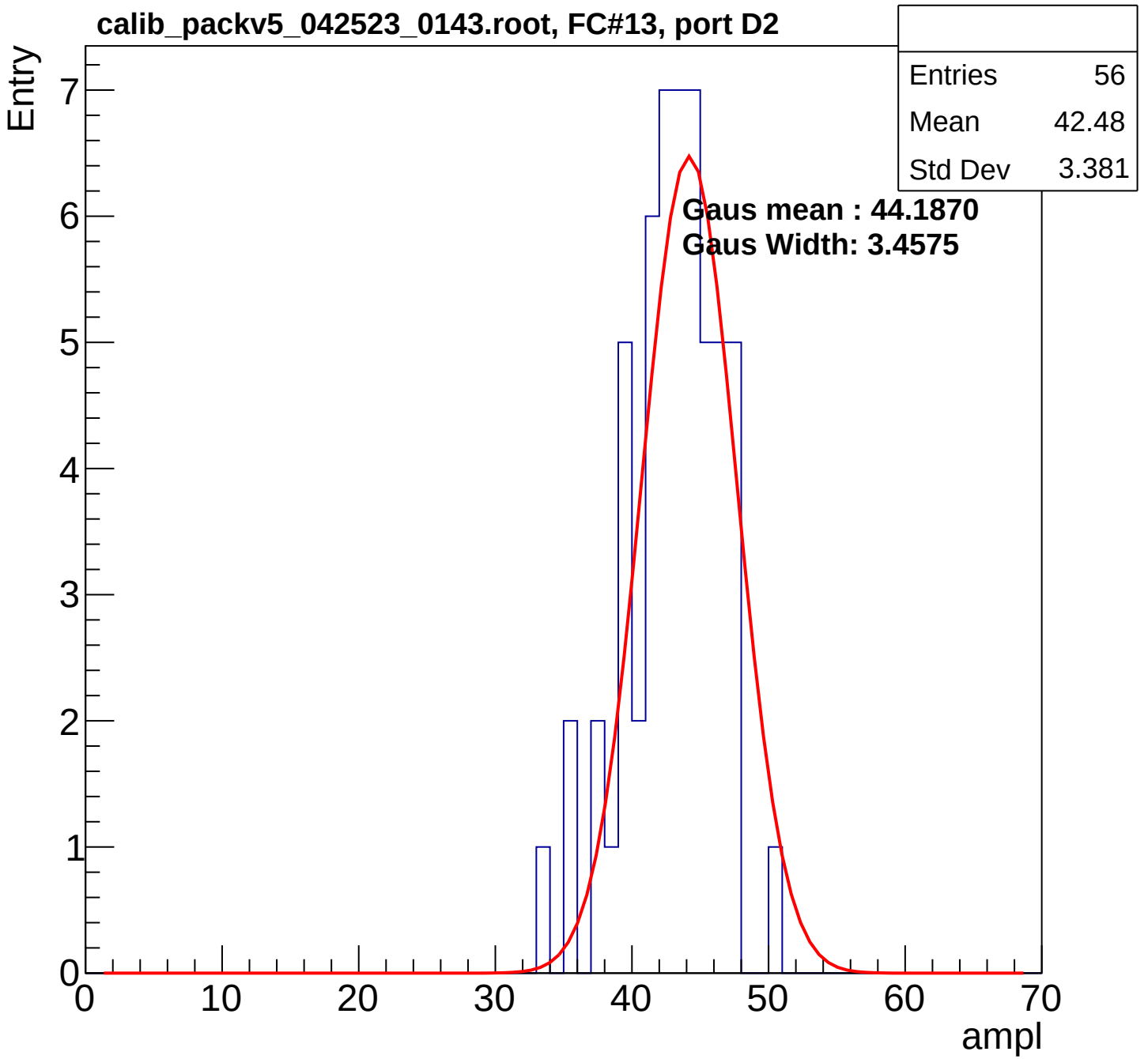
B1L003S, U15-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.48
Std Dev 3.381
Gaus mean : 44.1870
Gaus Width: 3.4575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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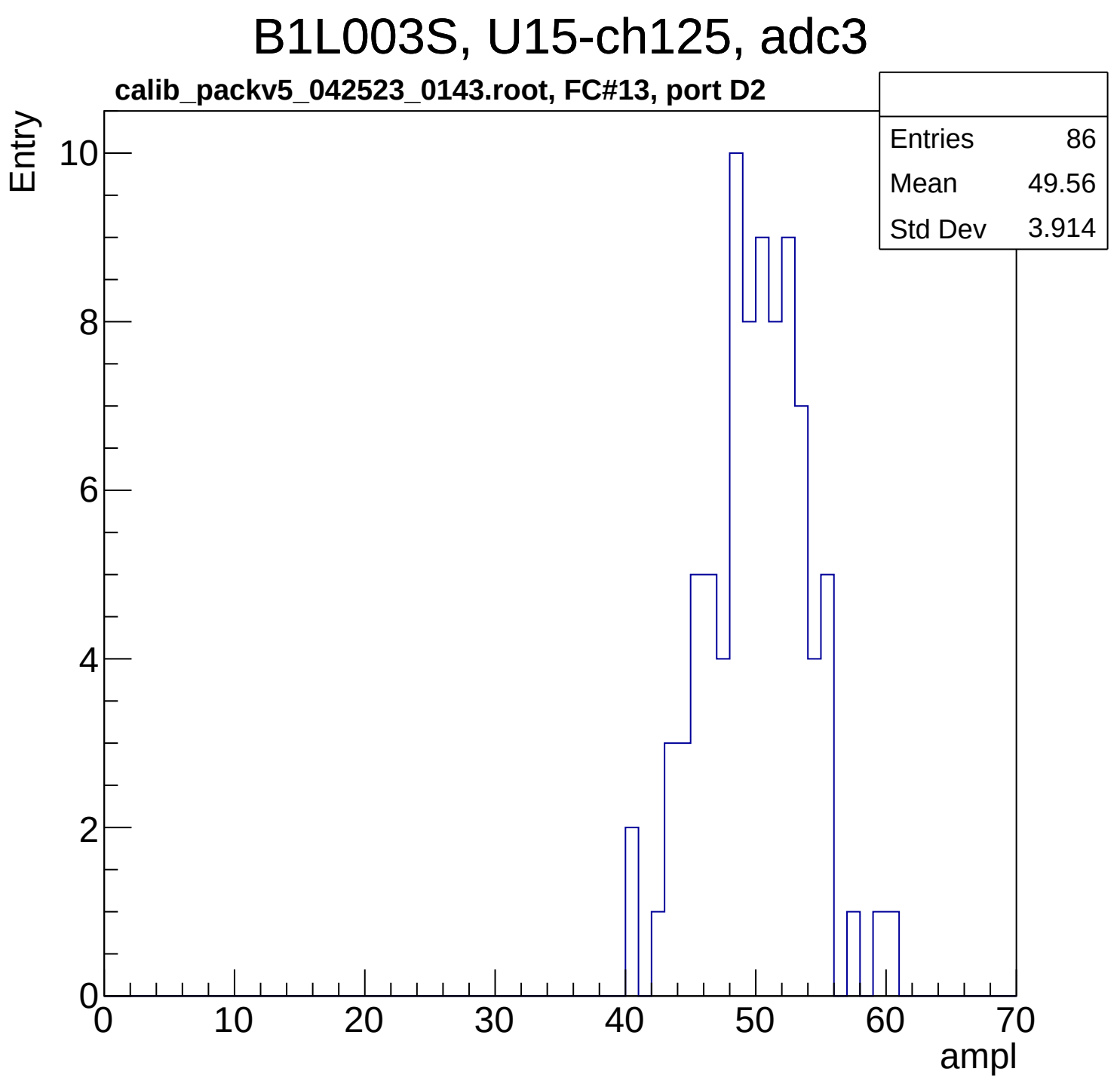
B1L003S, U15-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 49.56
Std Dev 3.914
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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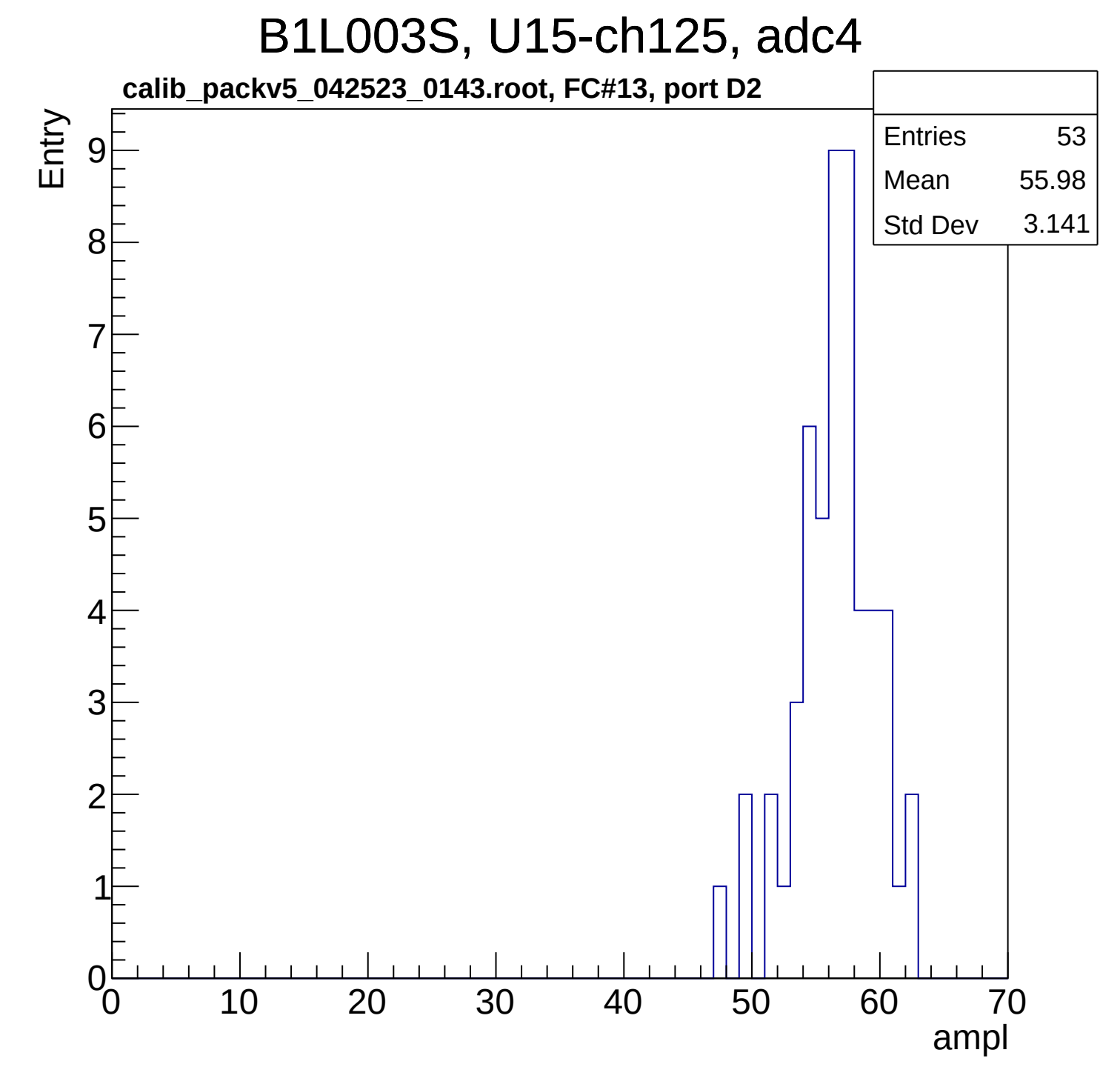
B1L003S, U15-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.98
Std Dev 3.141
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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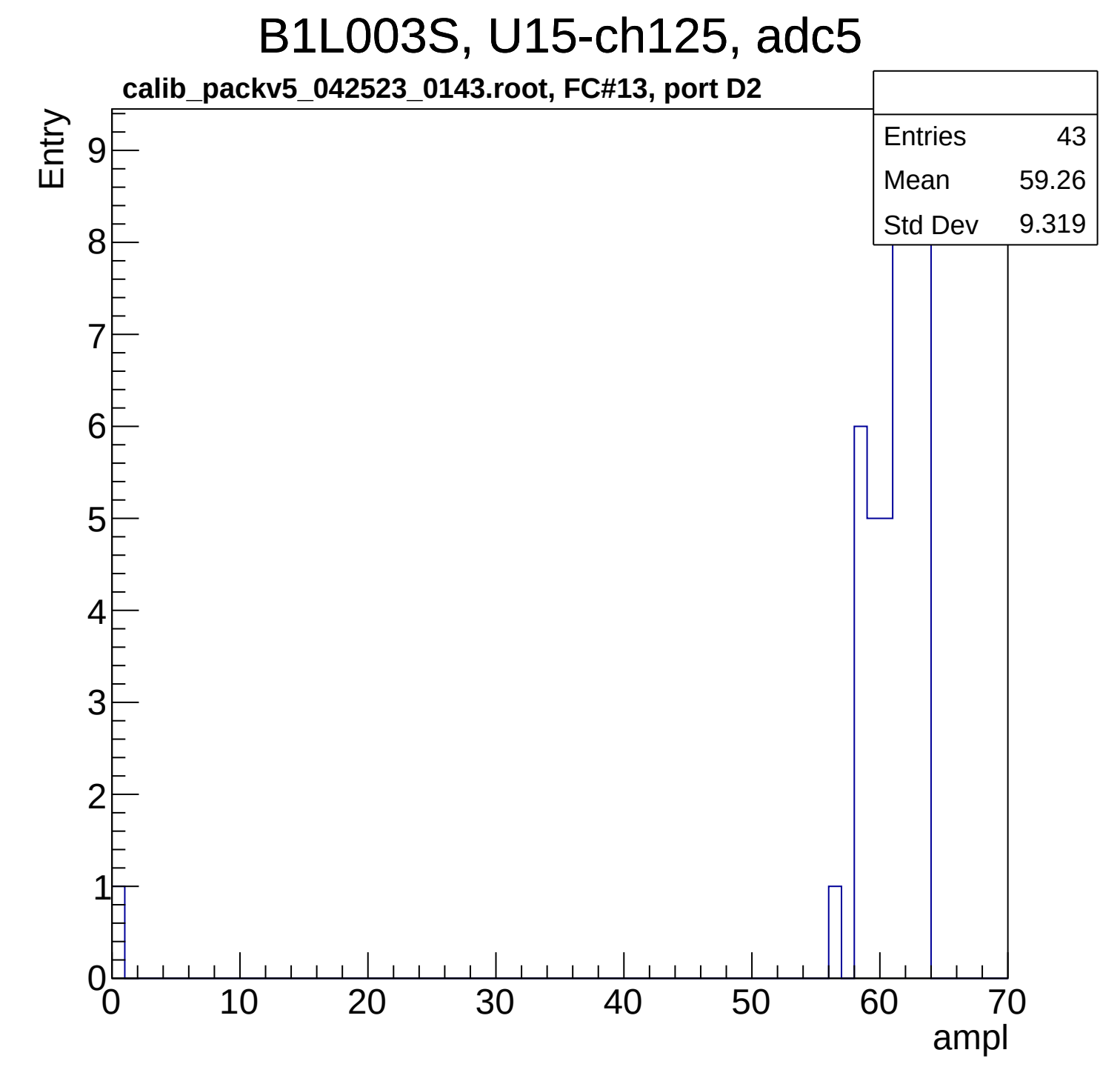
B1L003S, U15-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.26
Std Dev 9.319
Entry
ampl
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3
4
5
6
7
8
9
0
10
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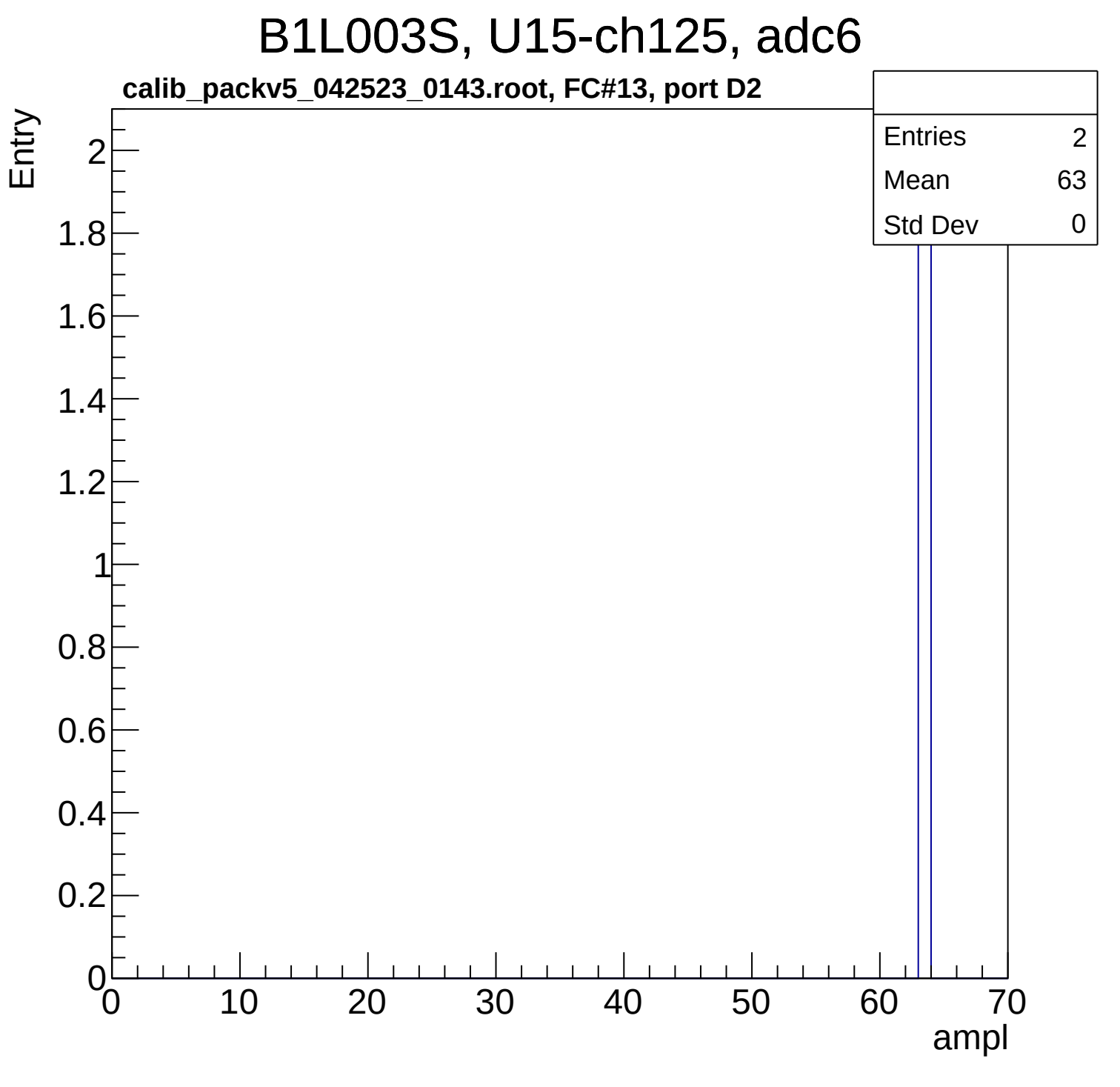
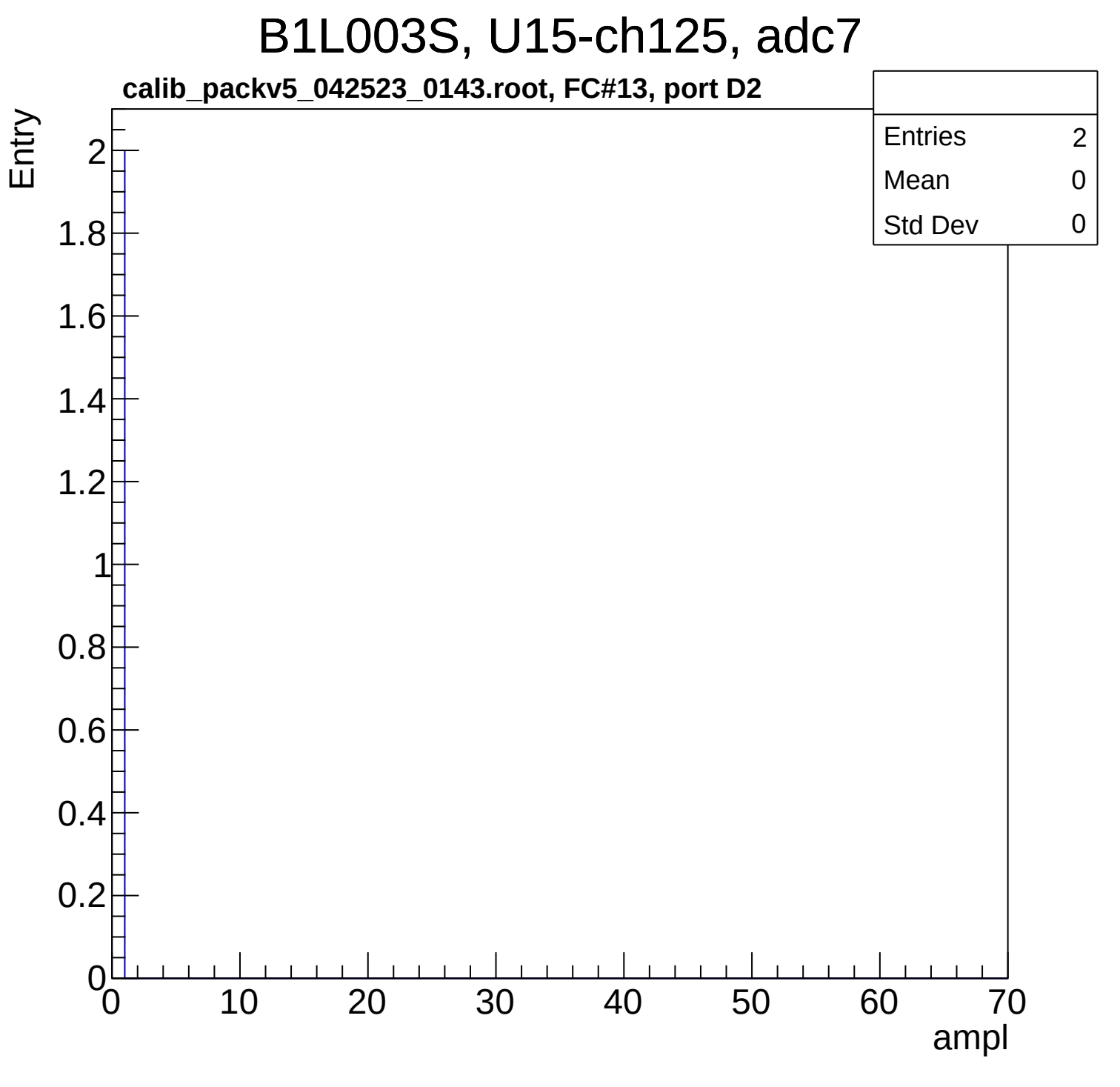

B1L003S, U15-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U15-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
50
60
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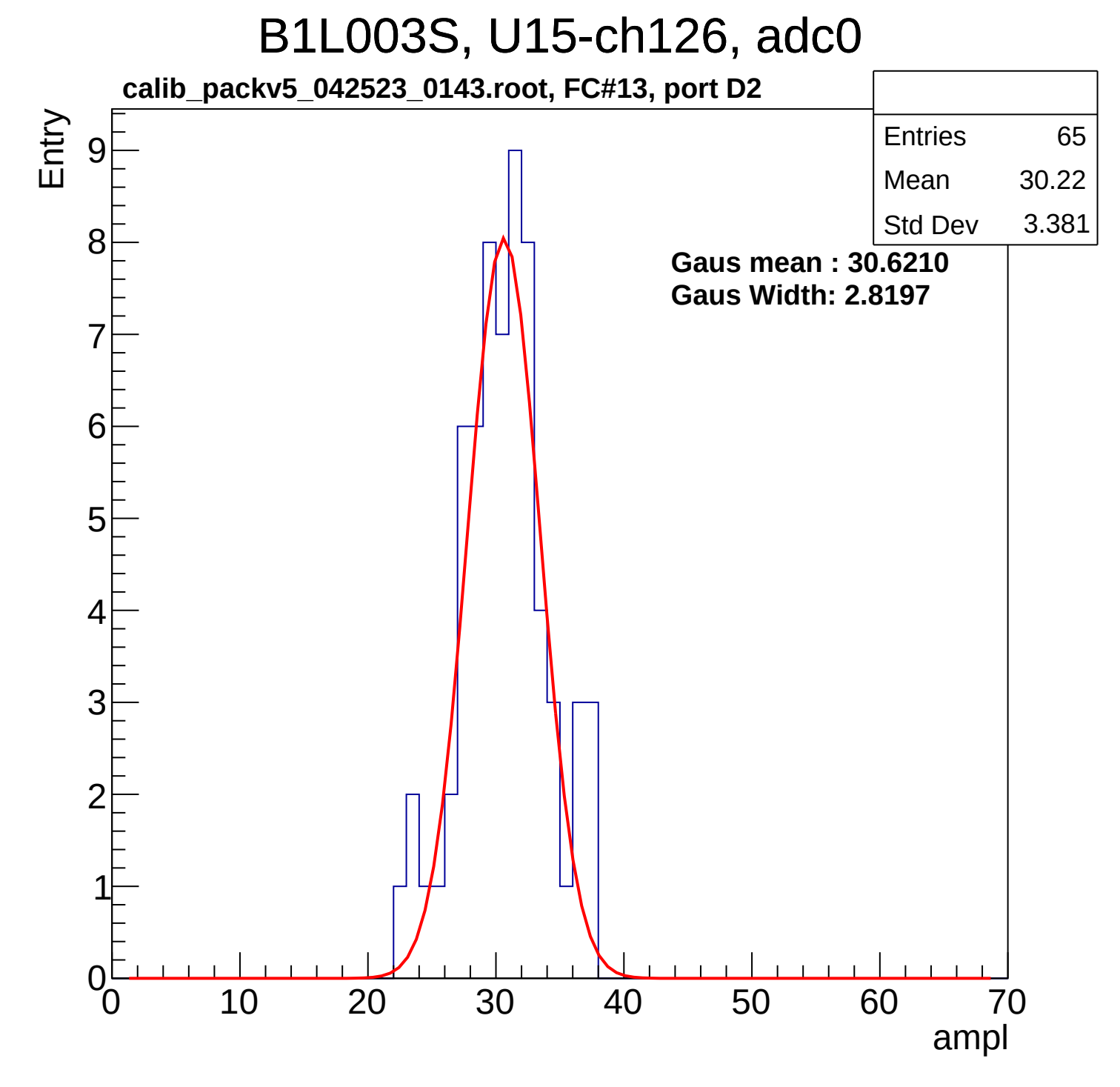
B1L003S, U15-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.22
Std Dev 3.381
Gaus mean : 30.6210
Gaus Width: 2.8197
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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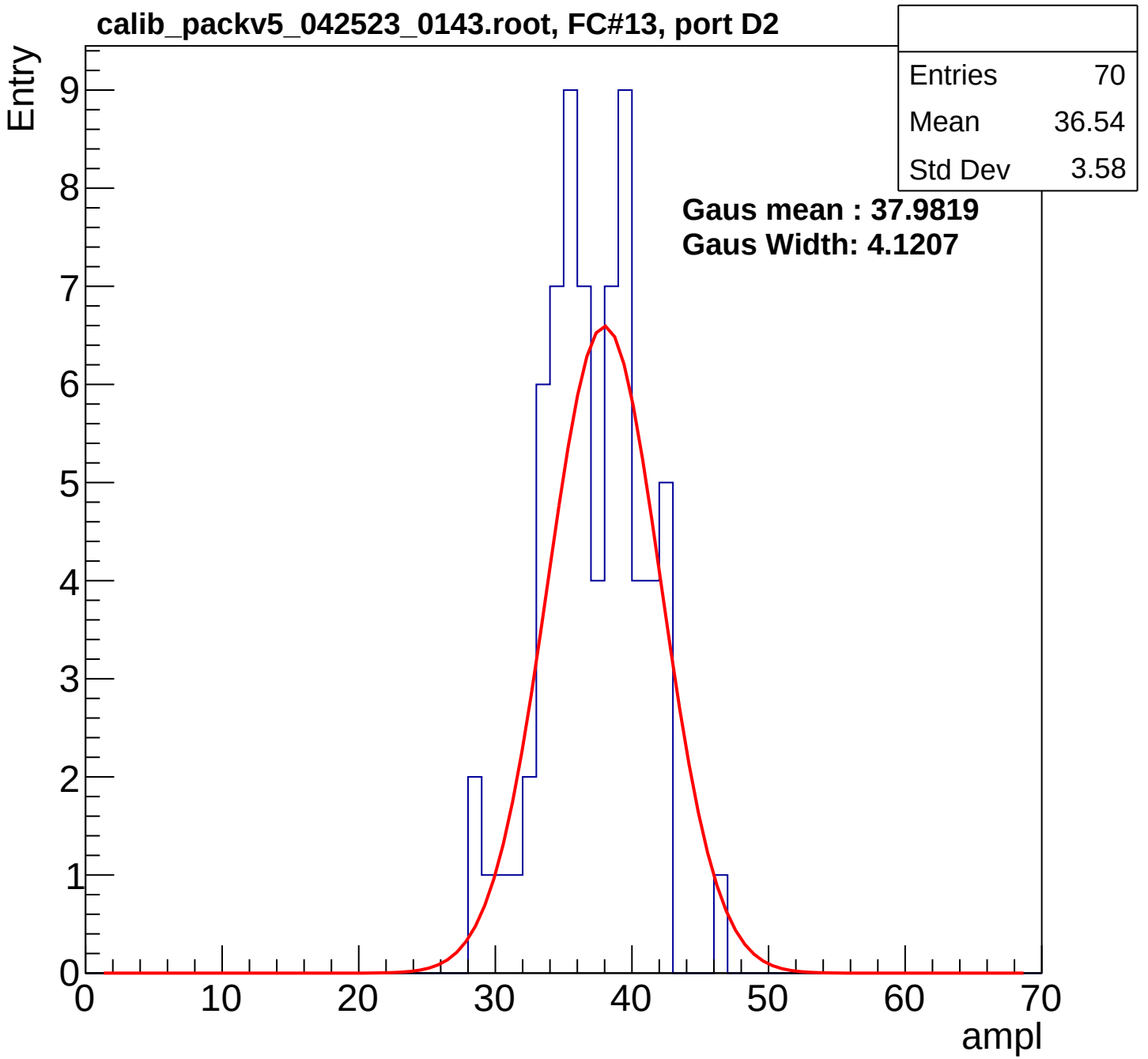
60
70


B1L003S, U15-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.54
Std Dev 3.58
Gaus mean : 37.9819
Gaus Width: 4.1207
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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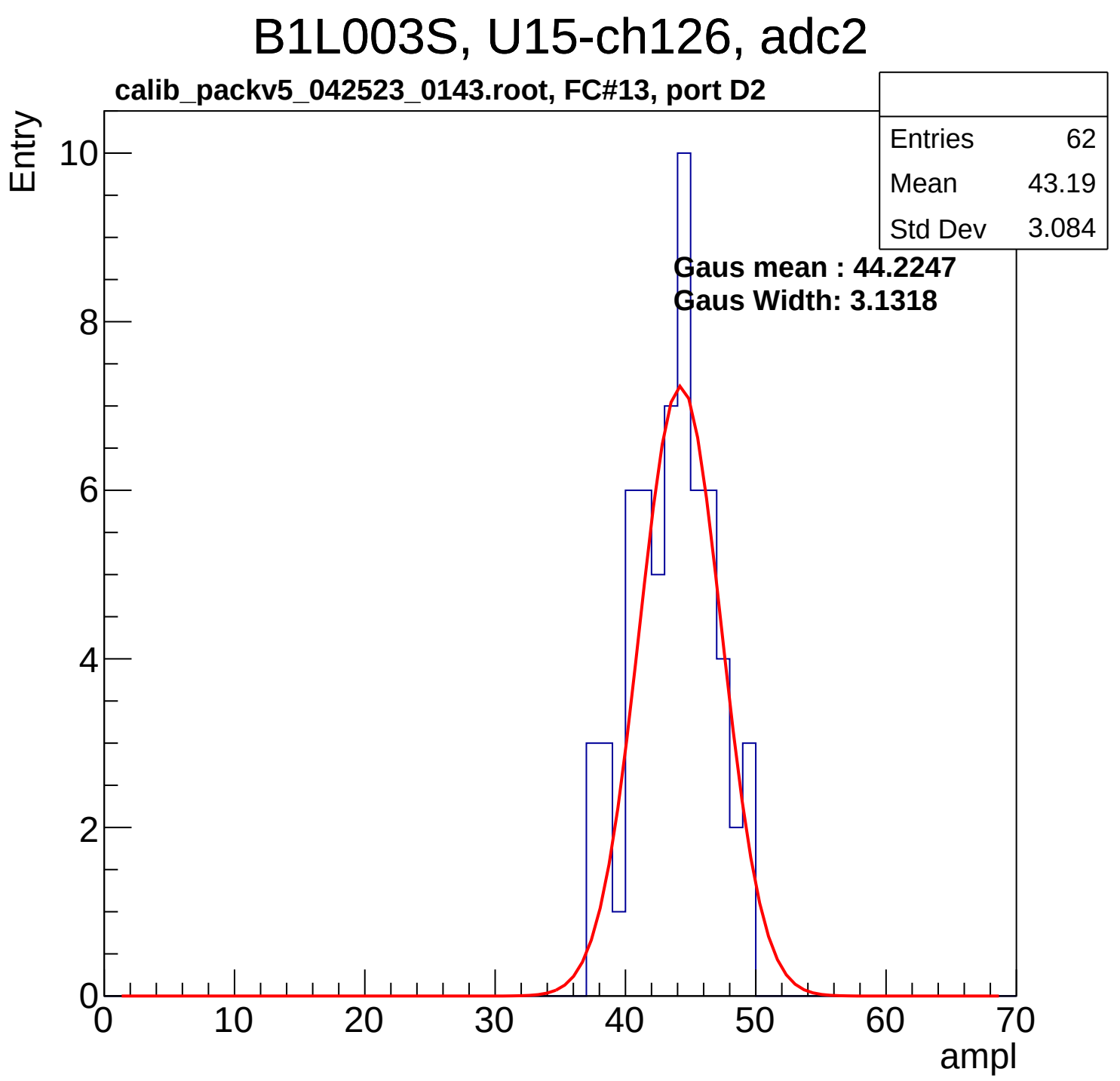
B1L003S, U15-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.19
Std Dev 3.084
Gaus mean : 44.2247
Gaus Width: 3.1318
Entry
ampl
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2
4
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10
0
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40
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60
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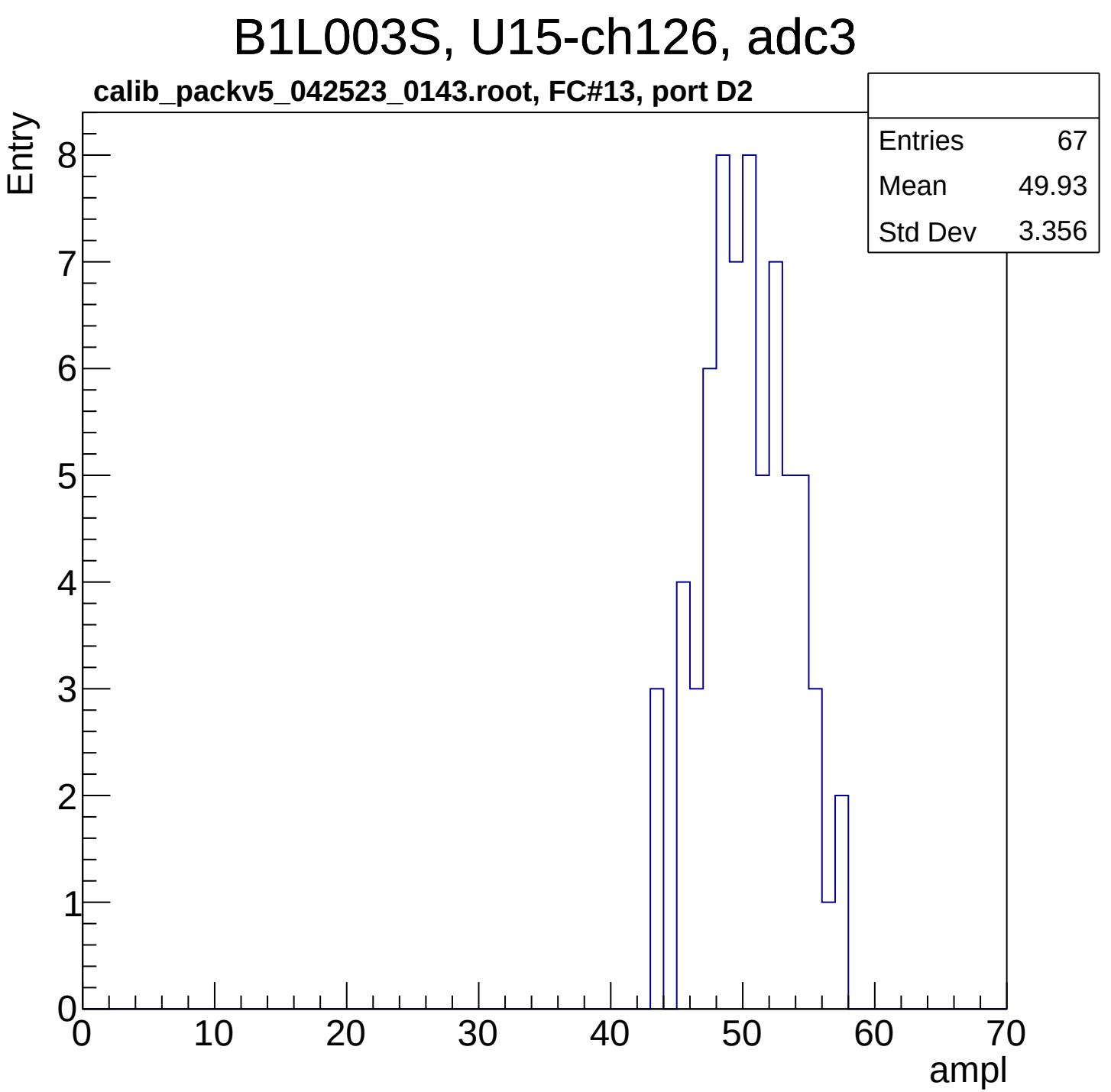
B1L003S, U15-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 49.93
Std Dev 3.356
Entry
ampl
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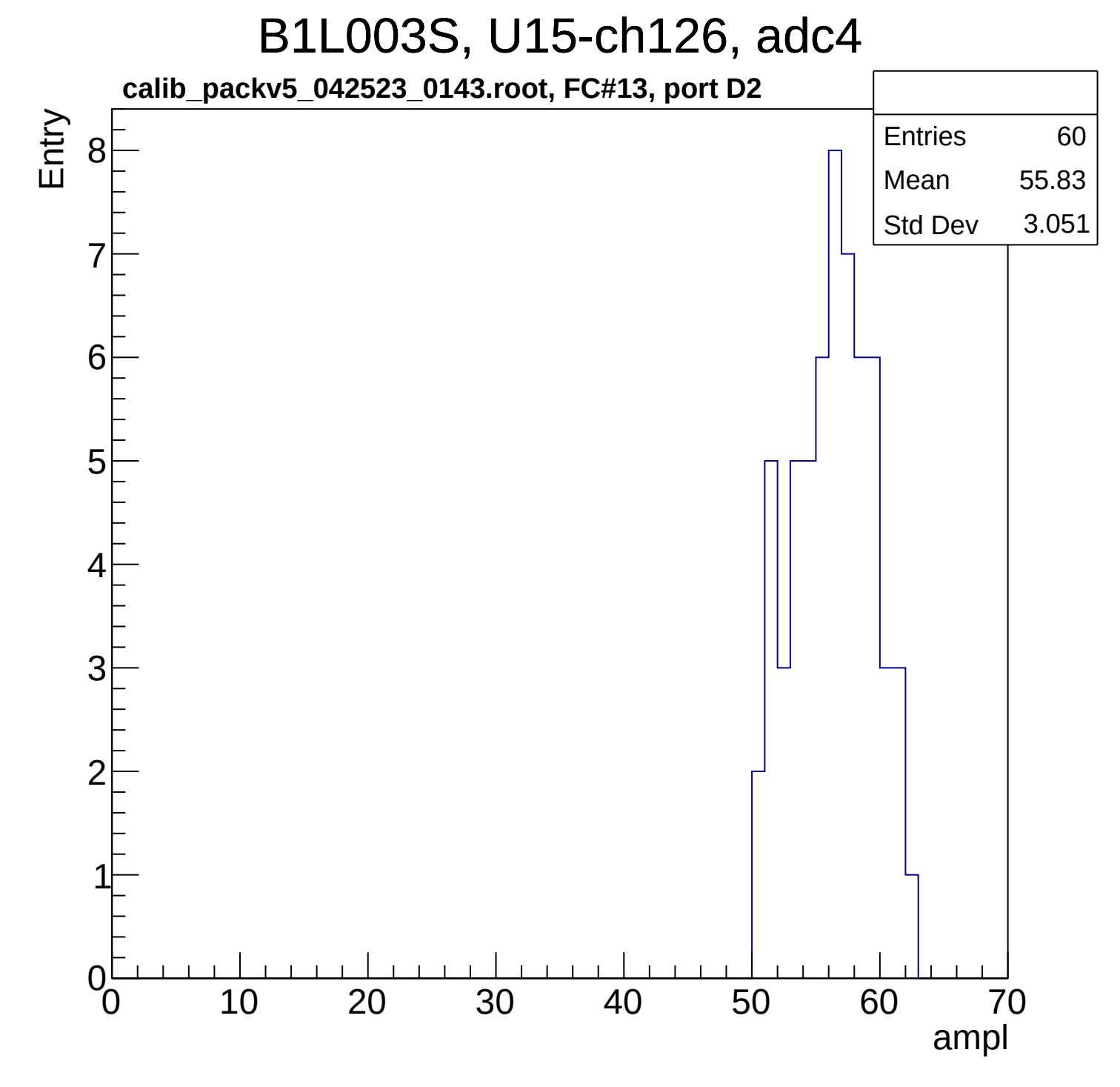
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U15-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55.83
Std Dev 3.051
Entry
ampl
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2
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5
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7
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0
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20
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40
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60
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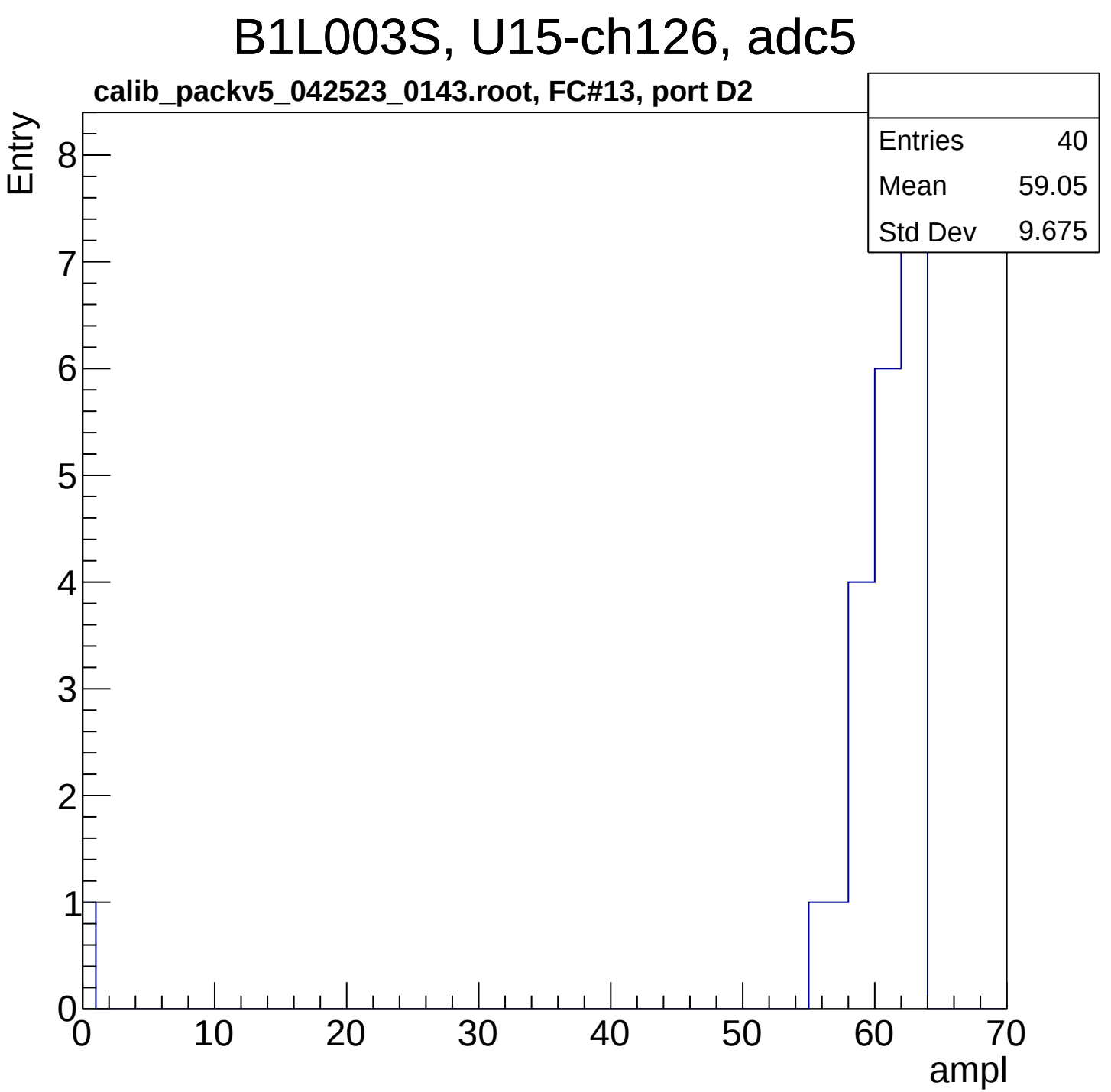
B1L003S, U15-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.05
Std Dev 9.675
Entry
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2
3
4
5
6
7
8
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10
20
30
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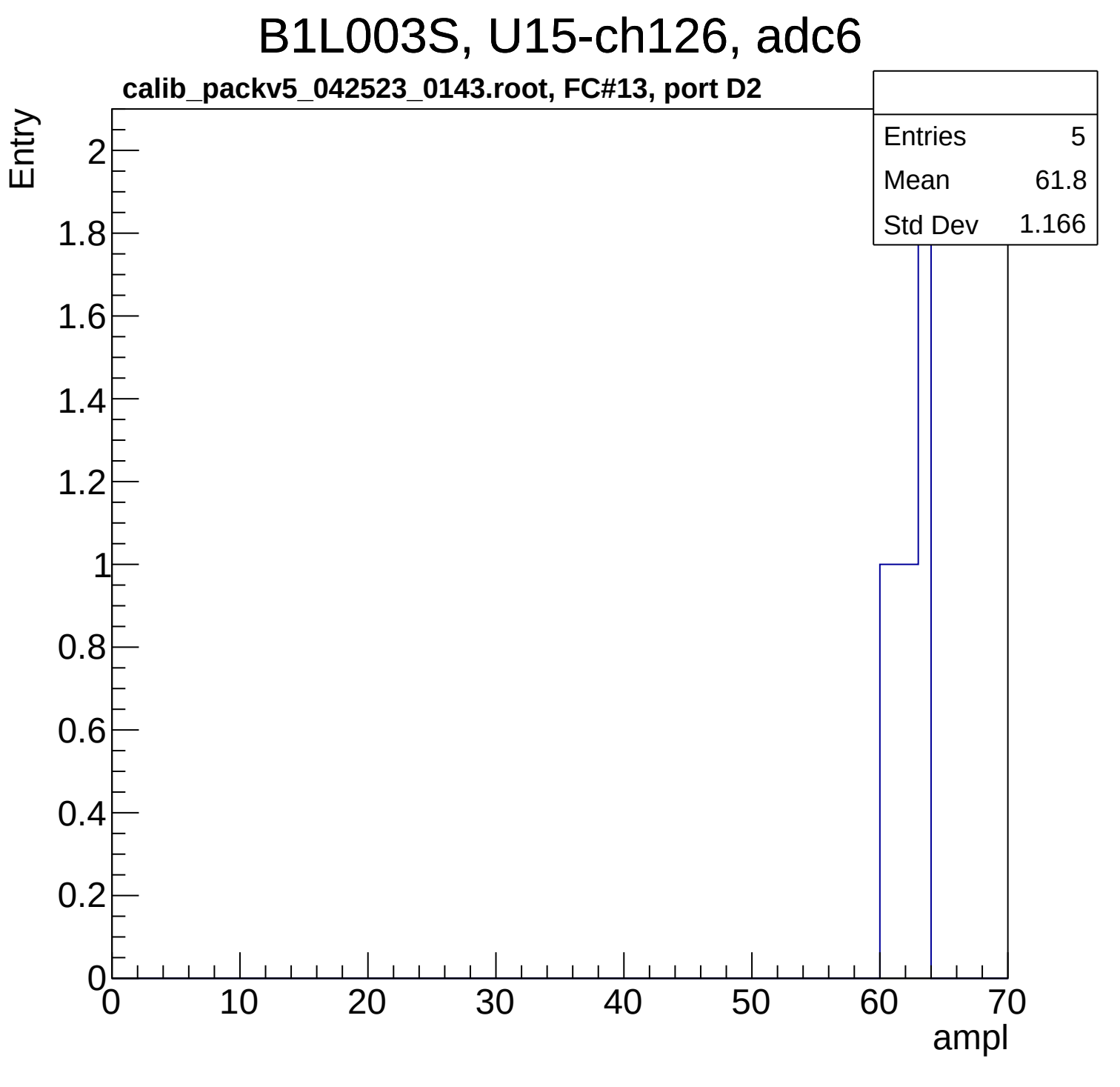
50
60
70
ampl


B1L003S, U15-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
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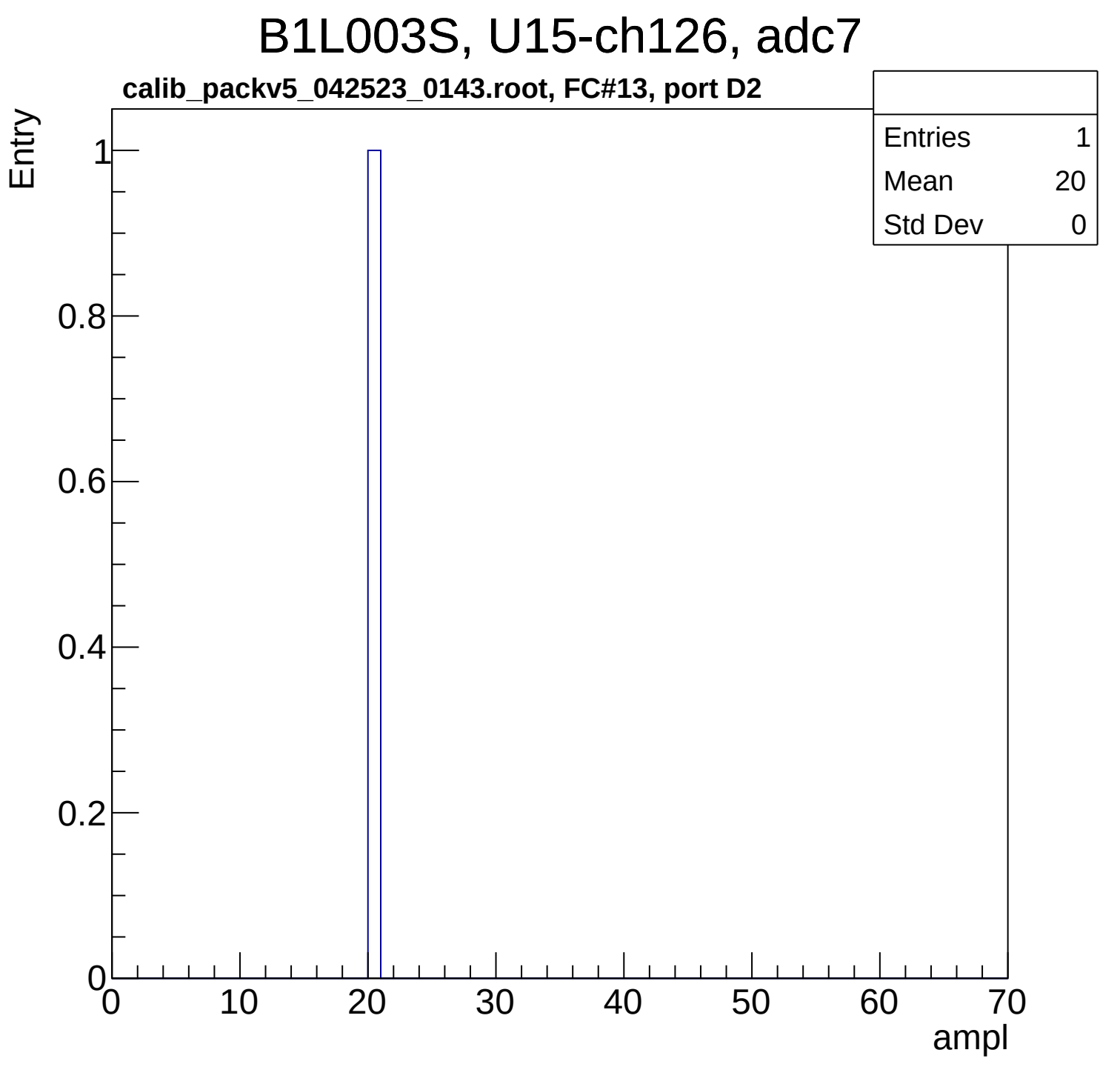
60
70


B1L003S, U15-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
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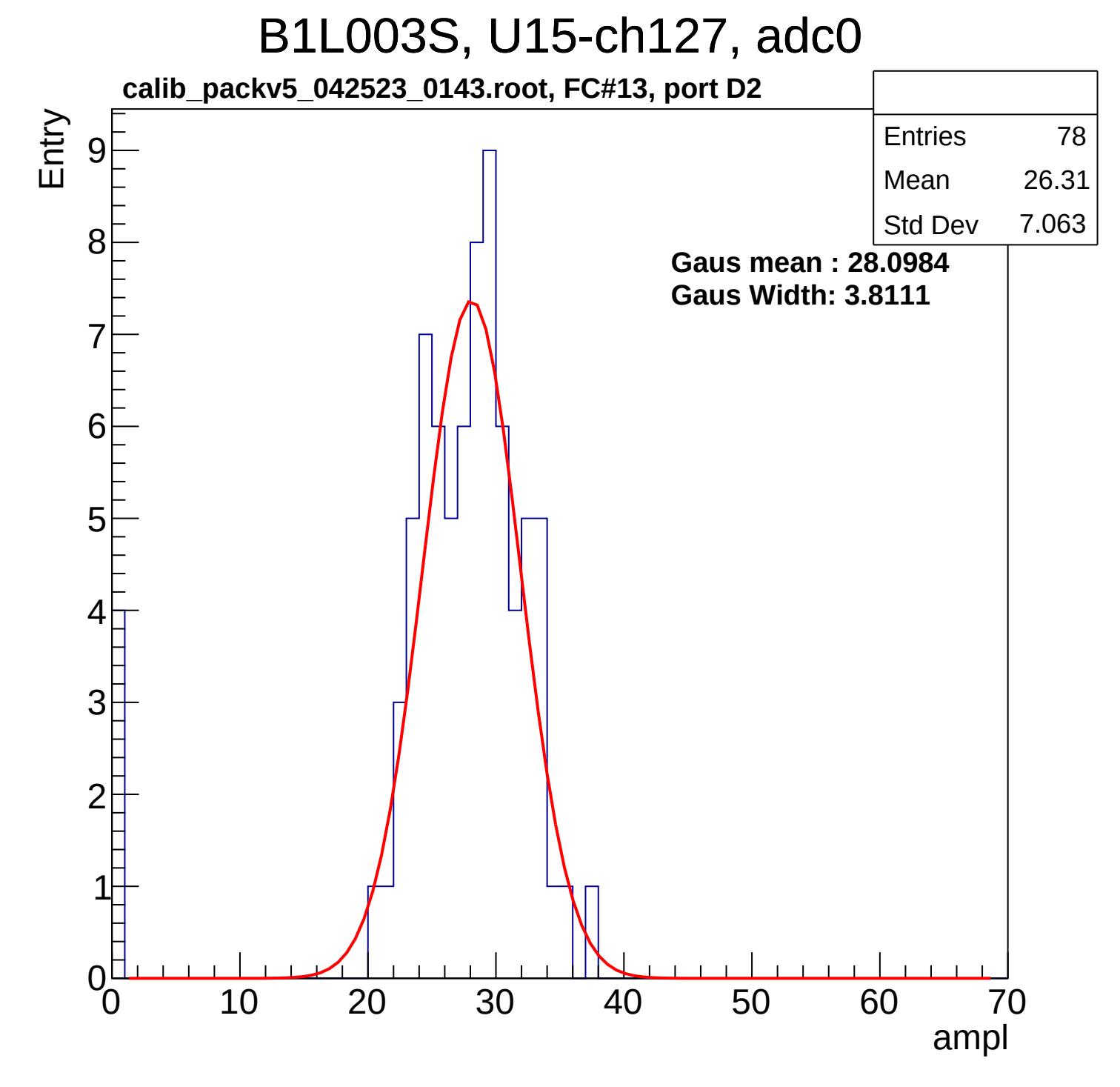
50
60
70
ampl


B1L003S, U15-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 26.31
Std Dev 7.063
Gaus mean : 28.0984
Gaus Width: 3.8111
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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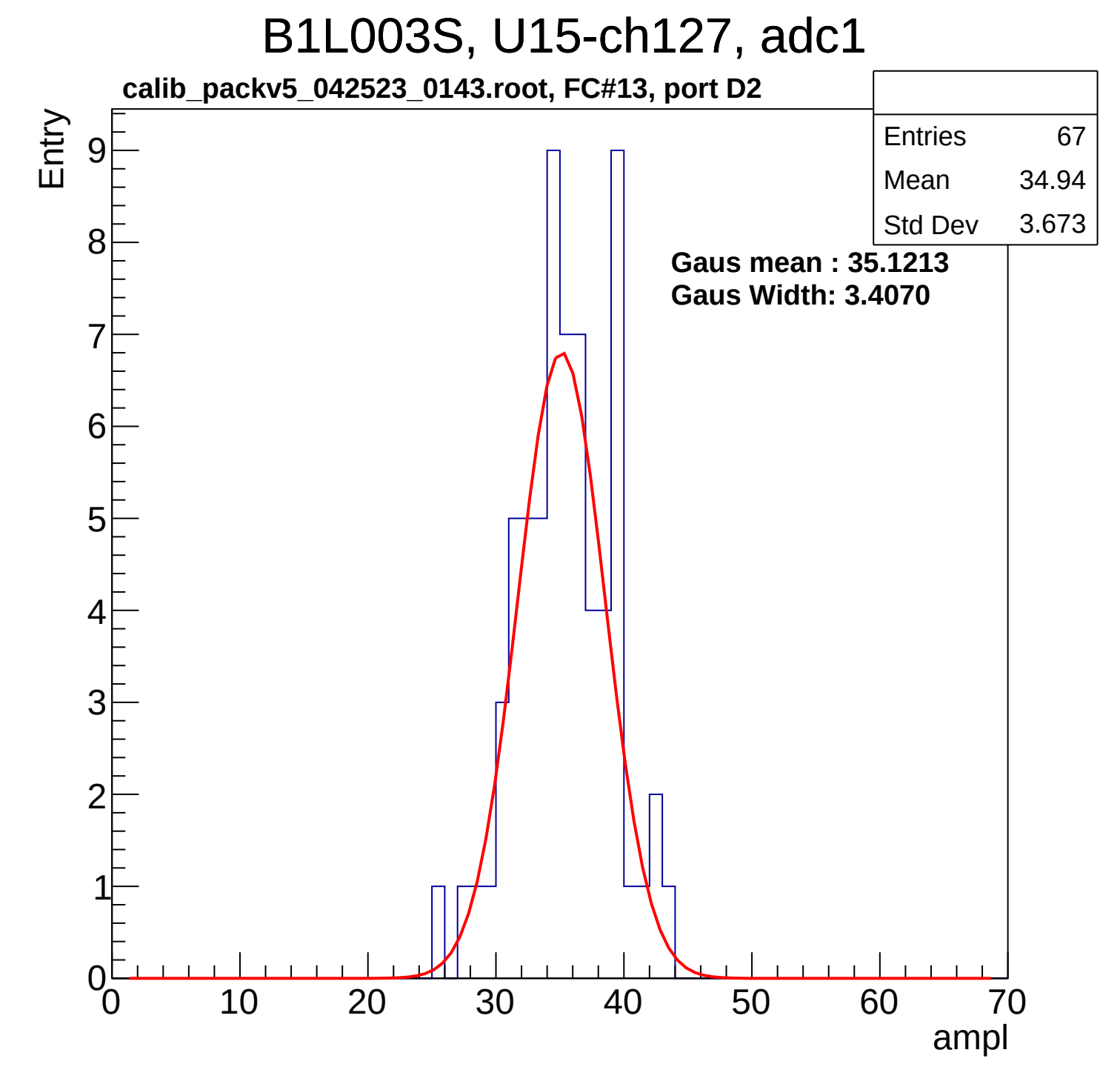
B1L003S, U15-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 34.94
Std Dev 3.673
Gaus mean : 35.1213
Gaus Width: 3.4070
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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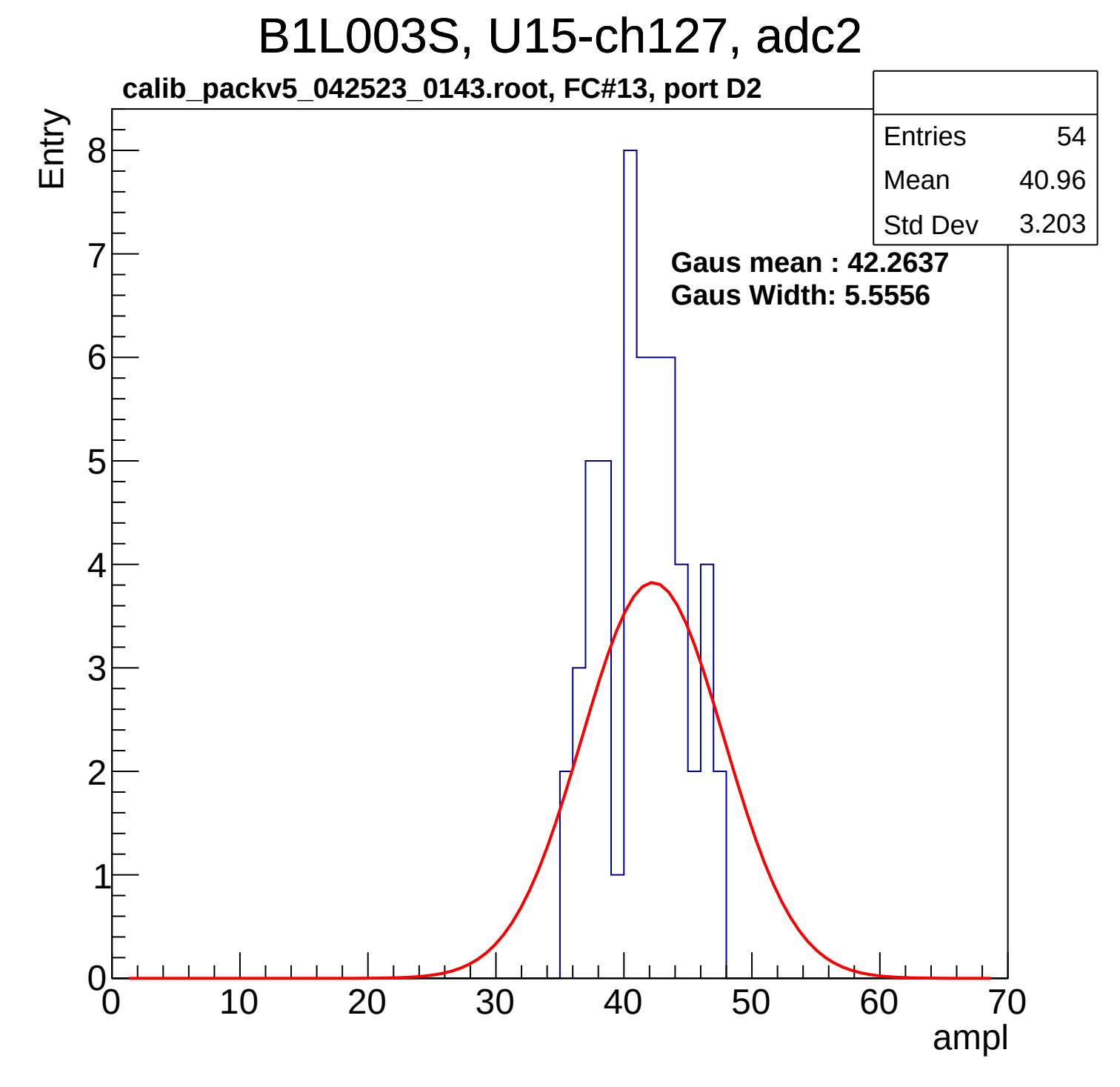
B1L003S, U15-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.96
Std Dev 3.203
Gaus mean : 42.2637
Gaus Width: 5.5556
Entry
ampl
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2
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4
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60
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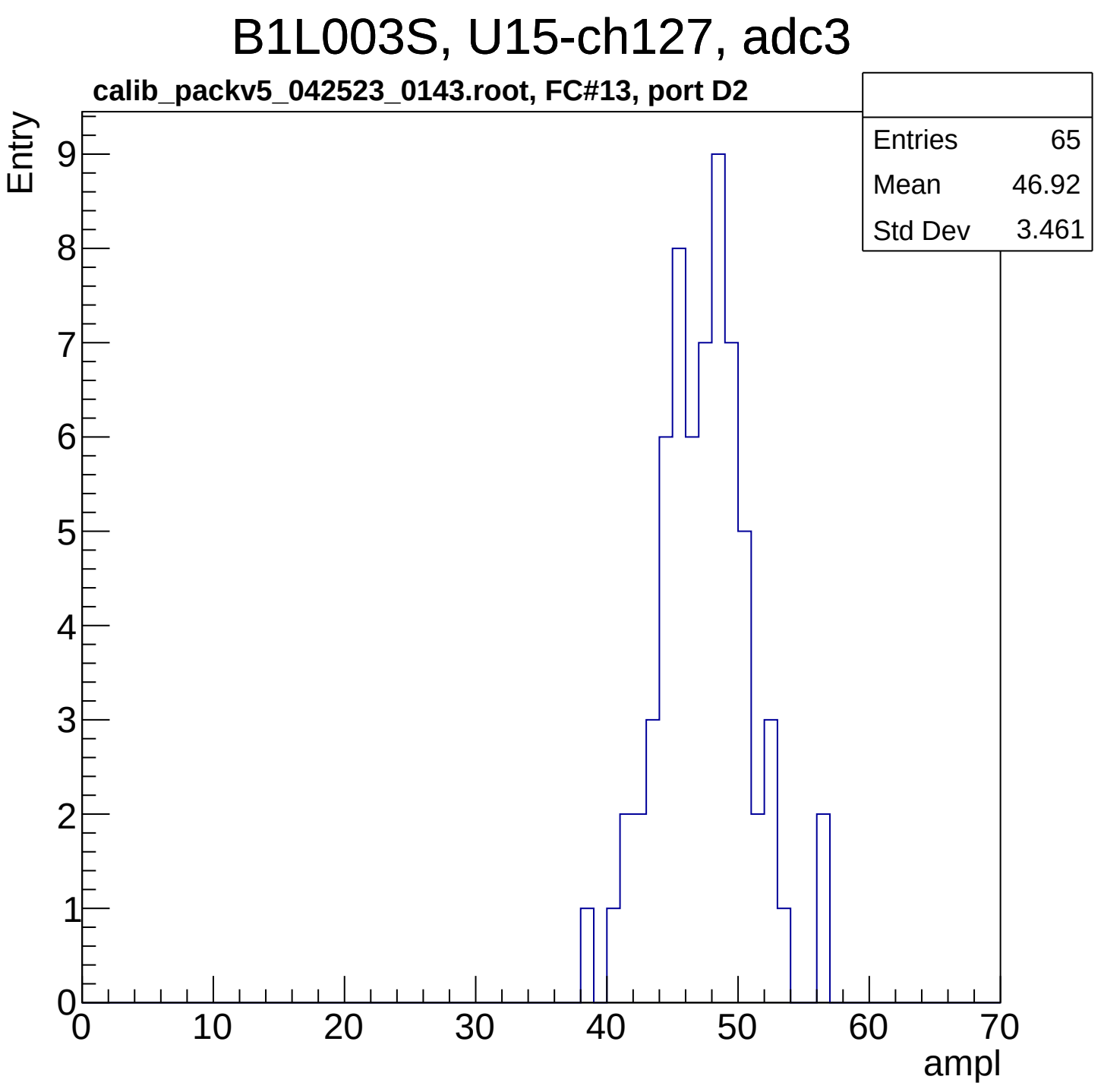
B1L003S, U15-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 46.92
Std Dev 3.461
Entry
ampl
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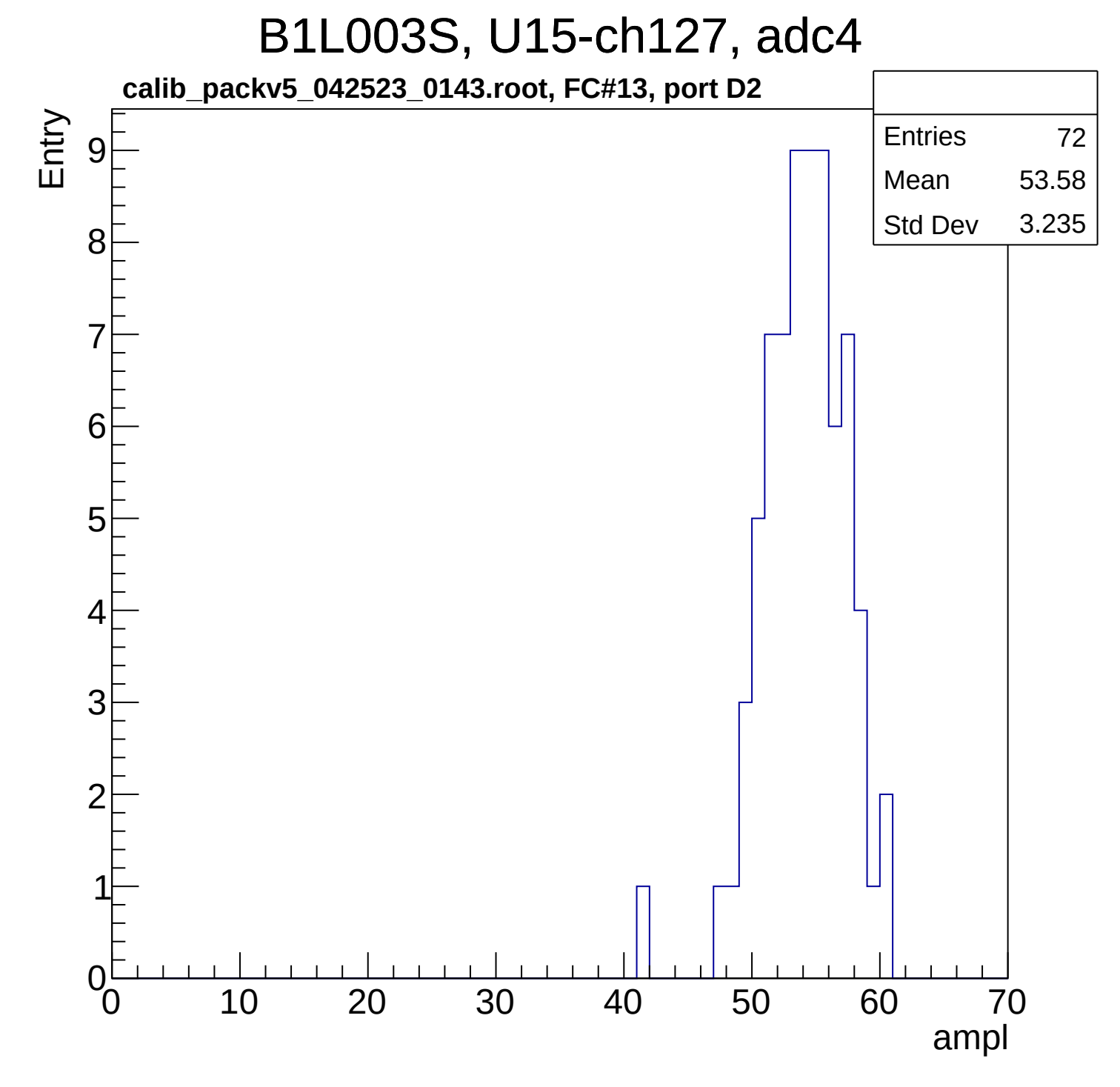
B1L003S, U15-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 53.58
Std Dev 3.235
Entry
ampl
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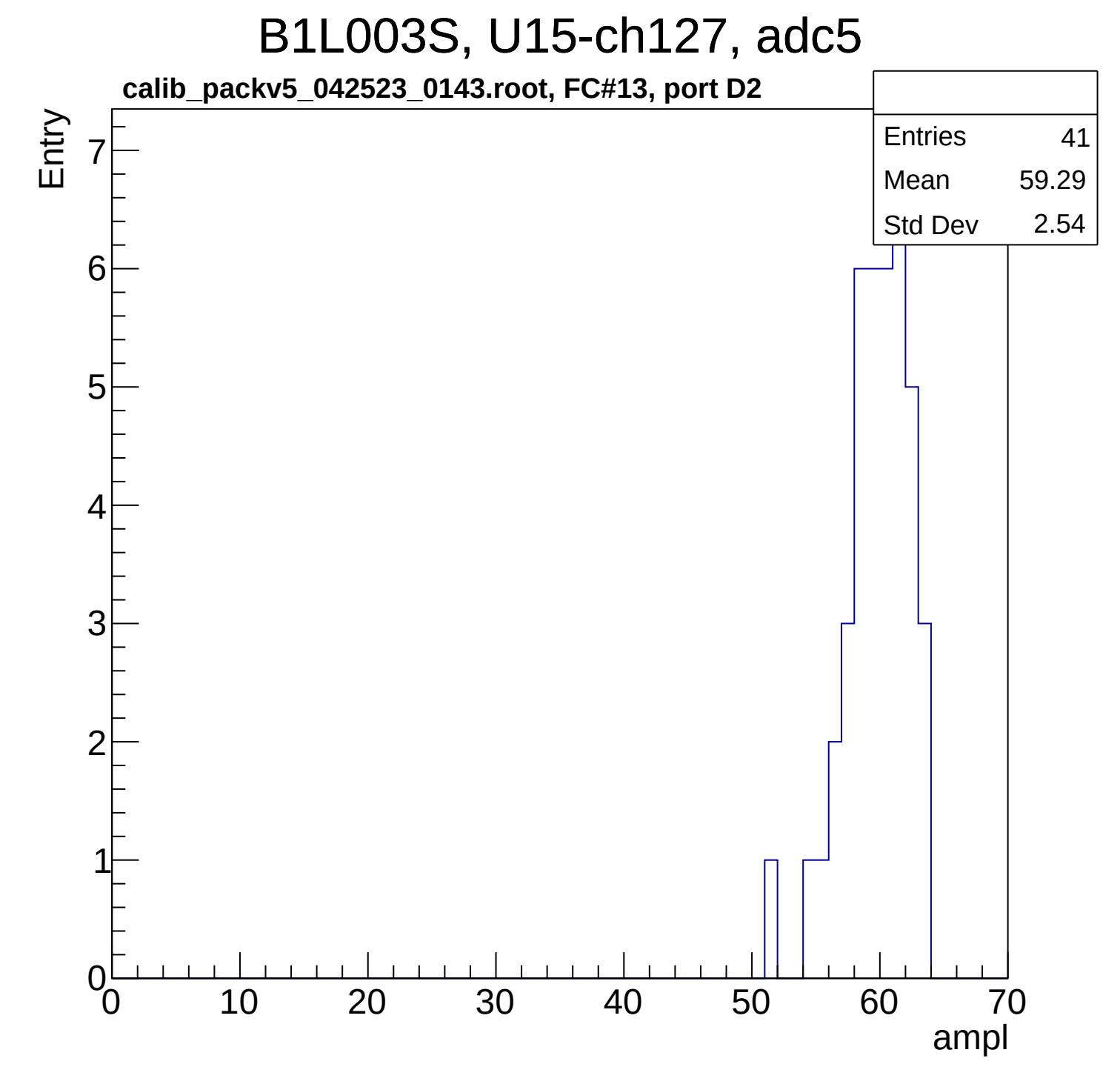
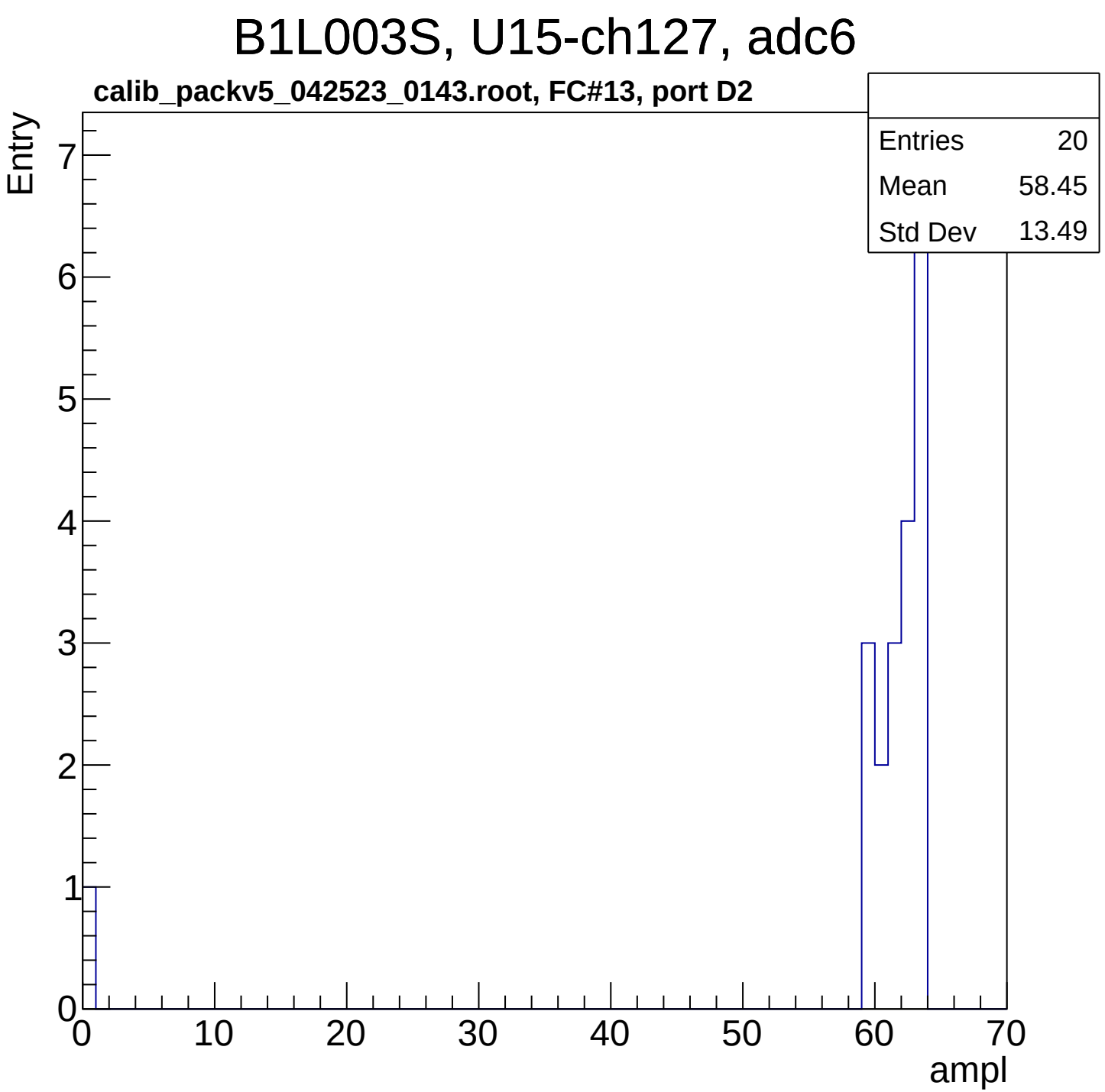
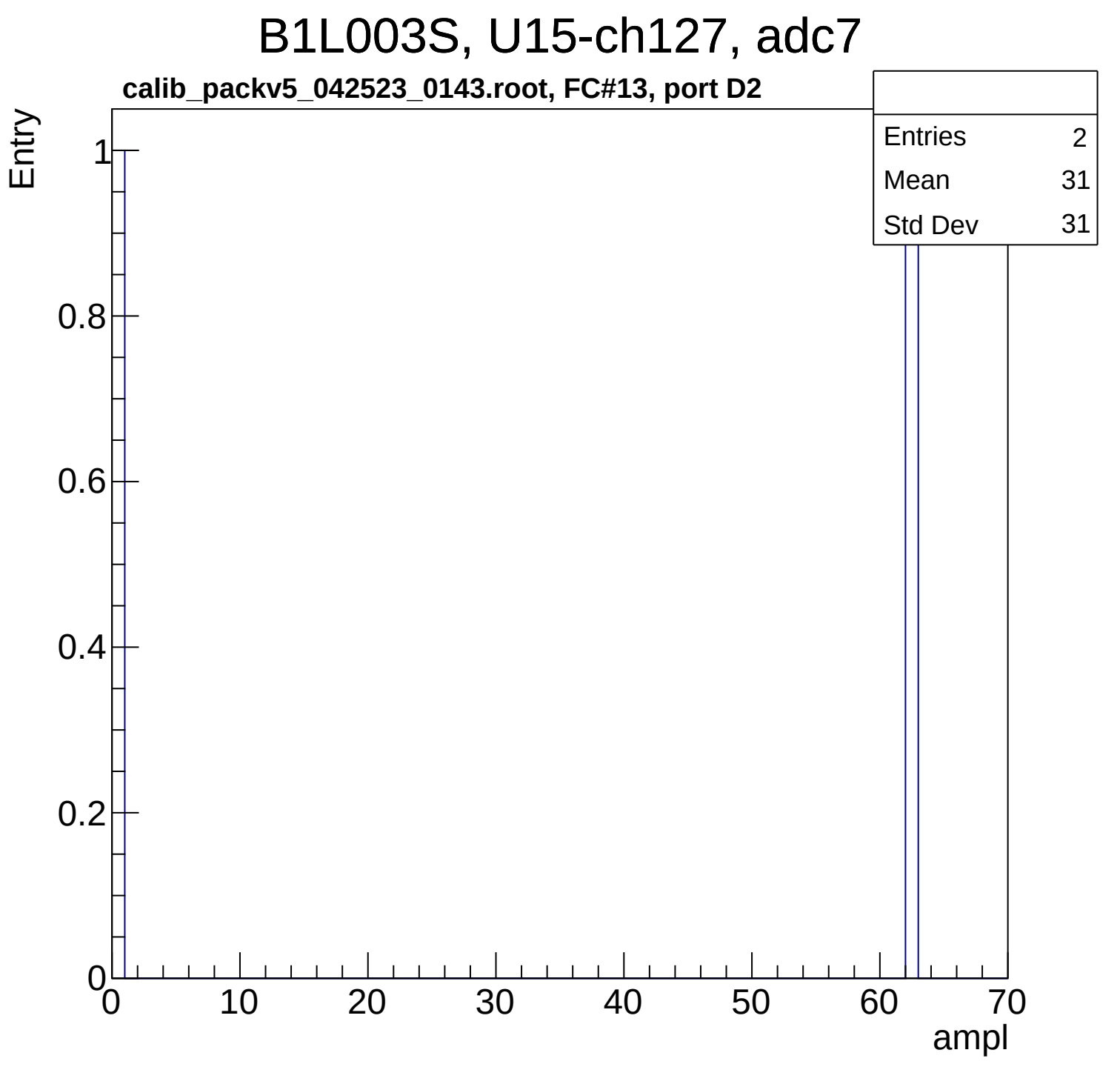
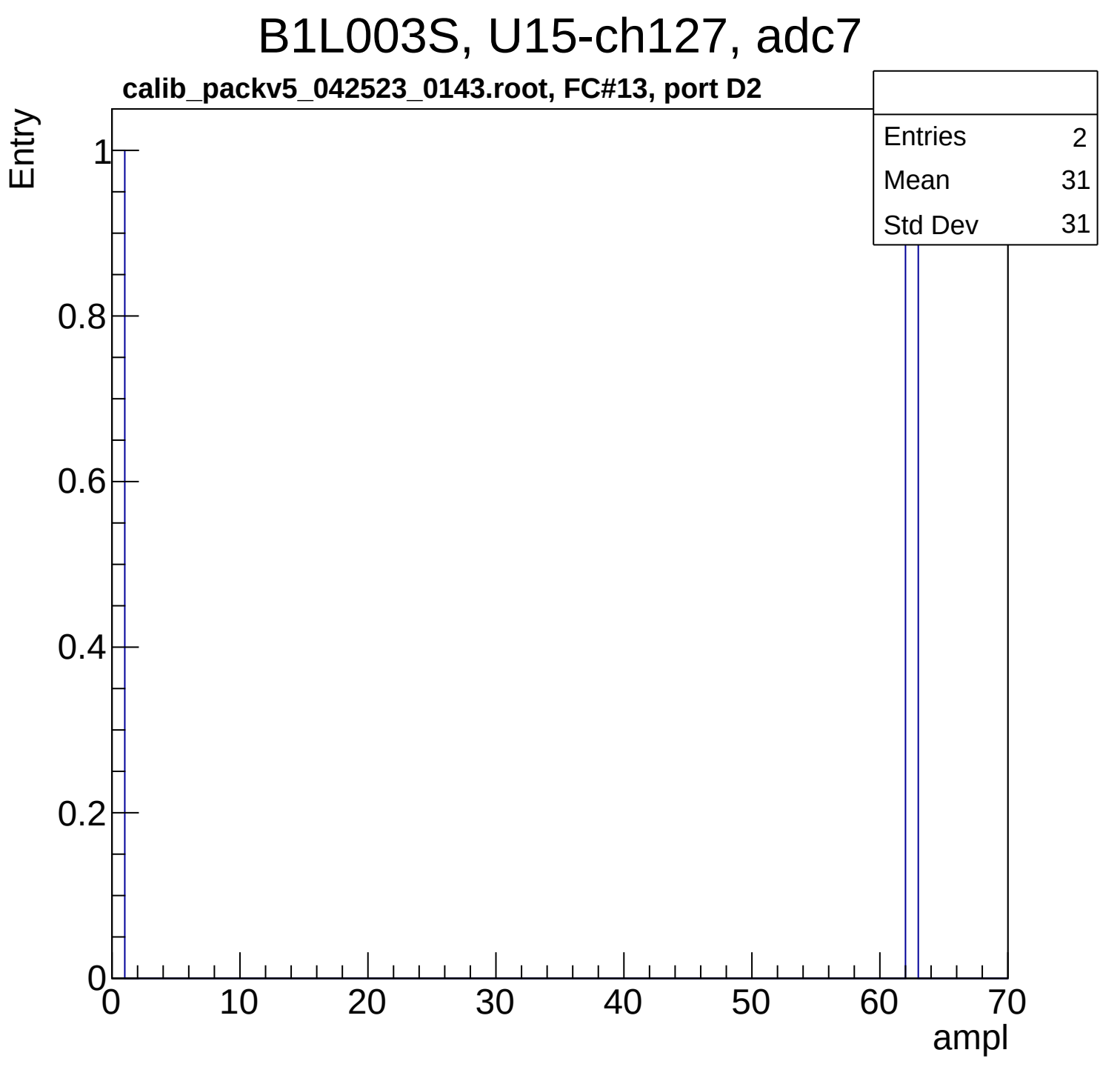

B1L003S, U15-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.29
Std Dev 2.54
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 58.45
Std Dev 13.49
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U15-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl