

B0L001S, U23-ch0

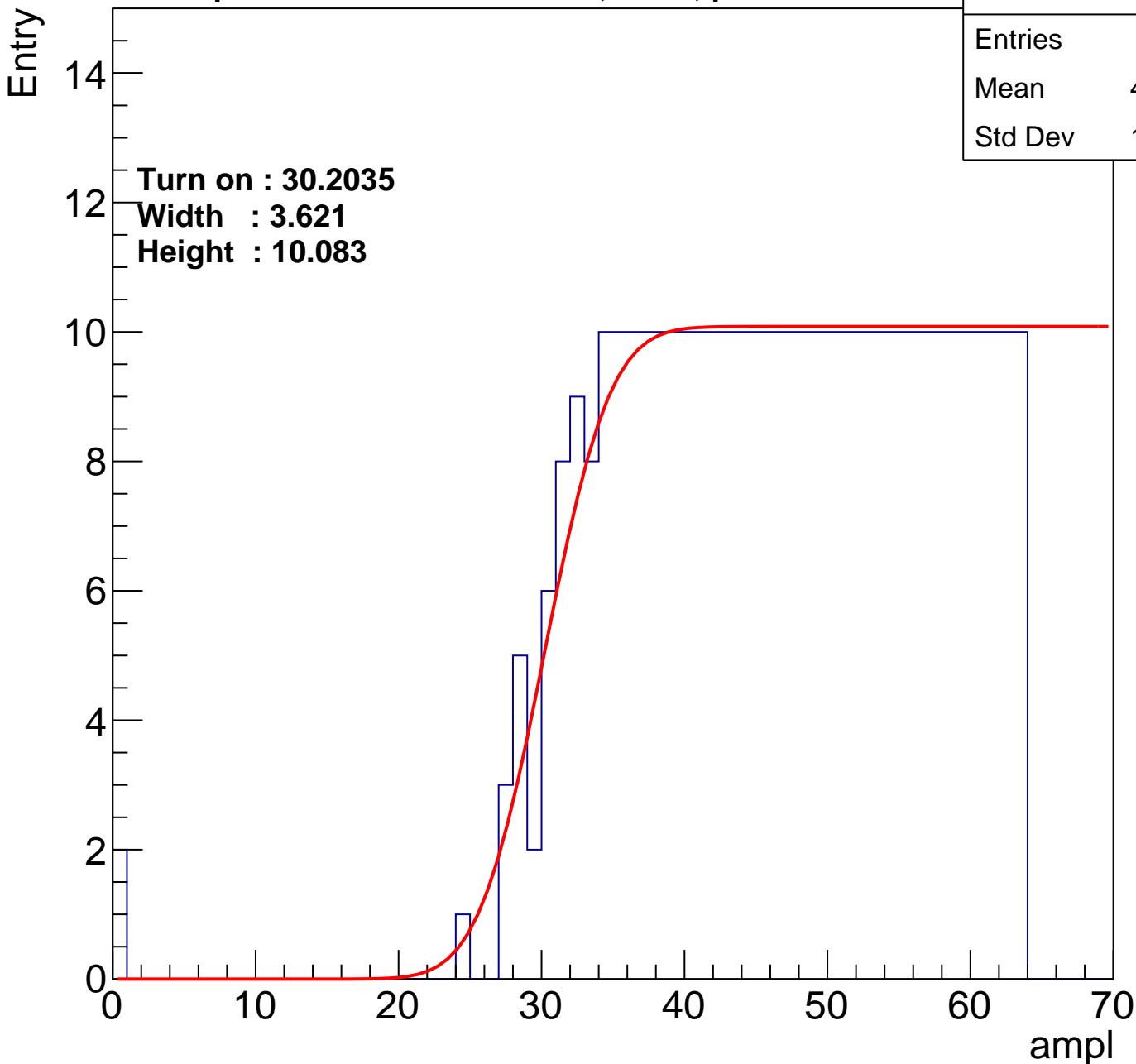
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 344 |
| Mean | 46.03 |
| Std Dev | 10.62 |

Turn on : 30.2035

Width : 3.621

Height : 10.083



B0L001S, U23-ch1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.46 |
| Std Dev | 10.72 |

Turn on : 28.5723

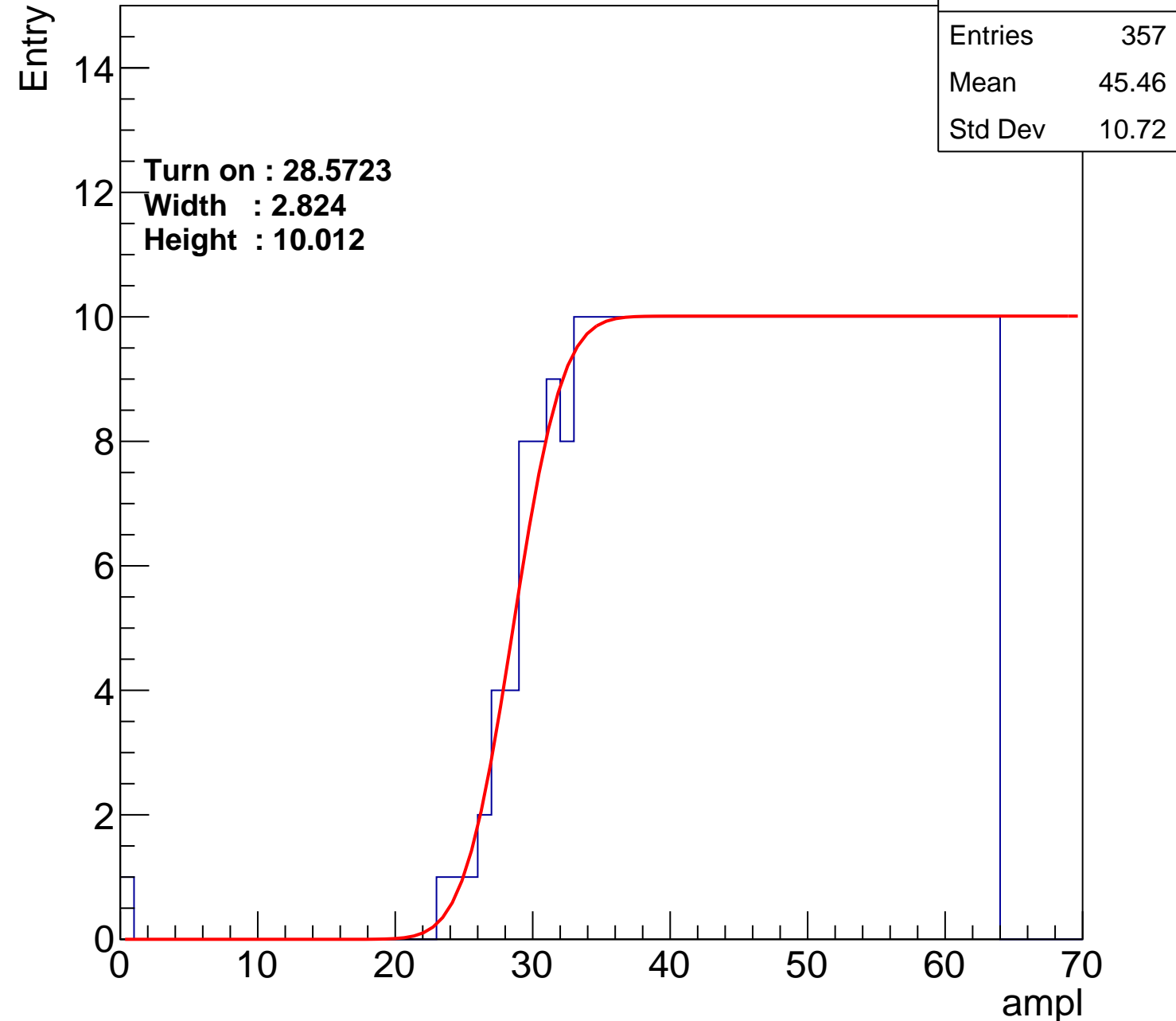
Width : 2.824

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch2

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 363 |
| Mean | 44.96 |
| Std Dev | 11.48 |

Turn on : 28.5731

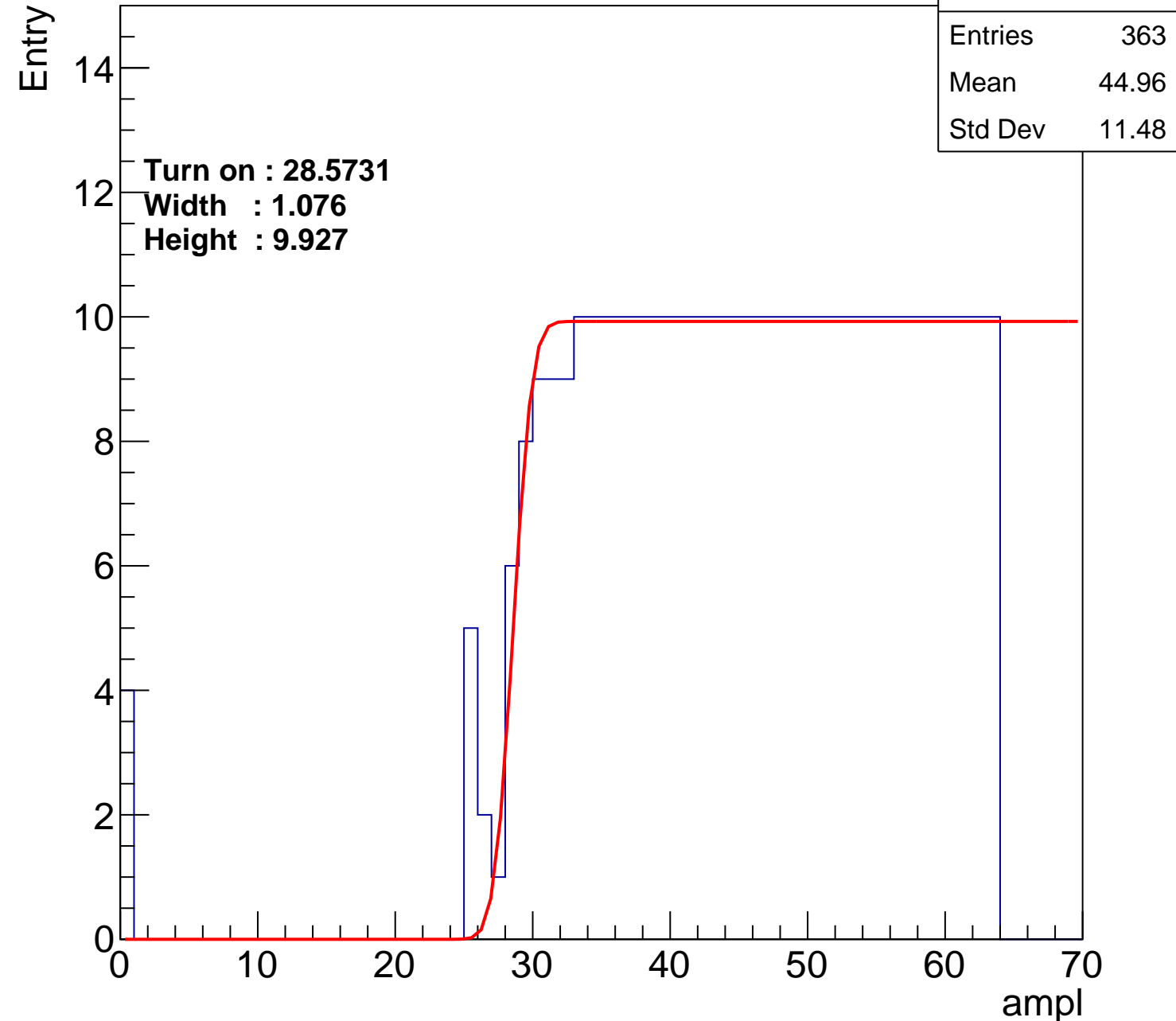
Width : 1.076

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch3

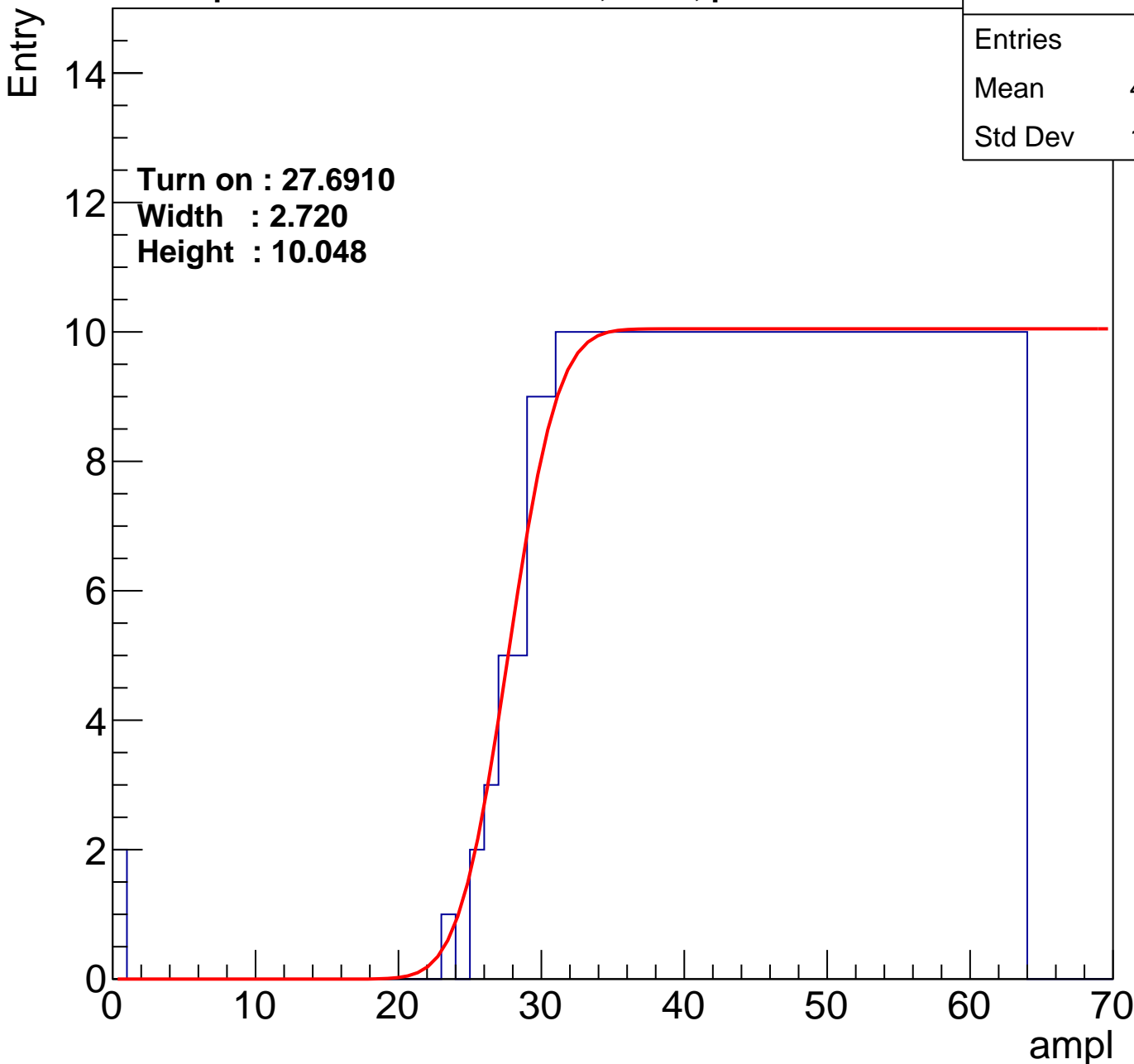
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.99 |
| Std Dev | 11.09 |

Turn on : 27.6910

Width : 2.720

Height : 10.048



B0L001S, U23-ch4

calib_packv5_042523_0143.root, FC#9, port A1

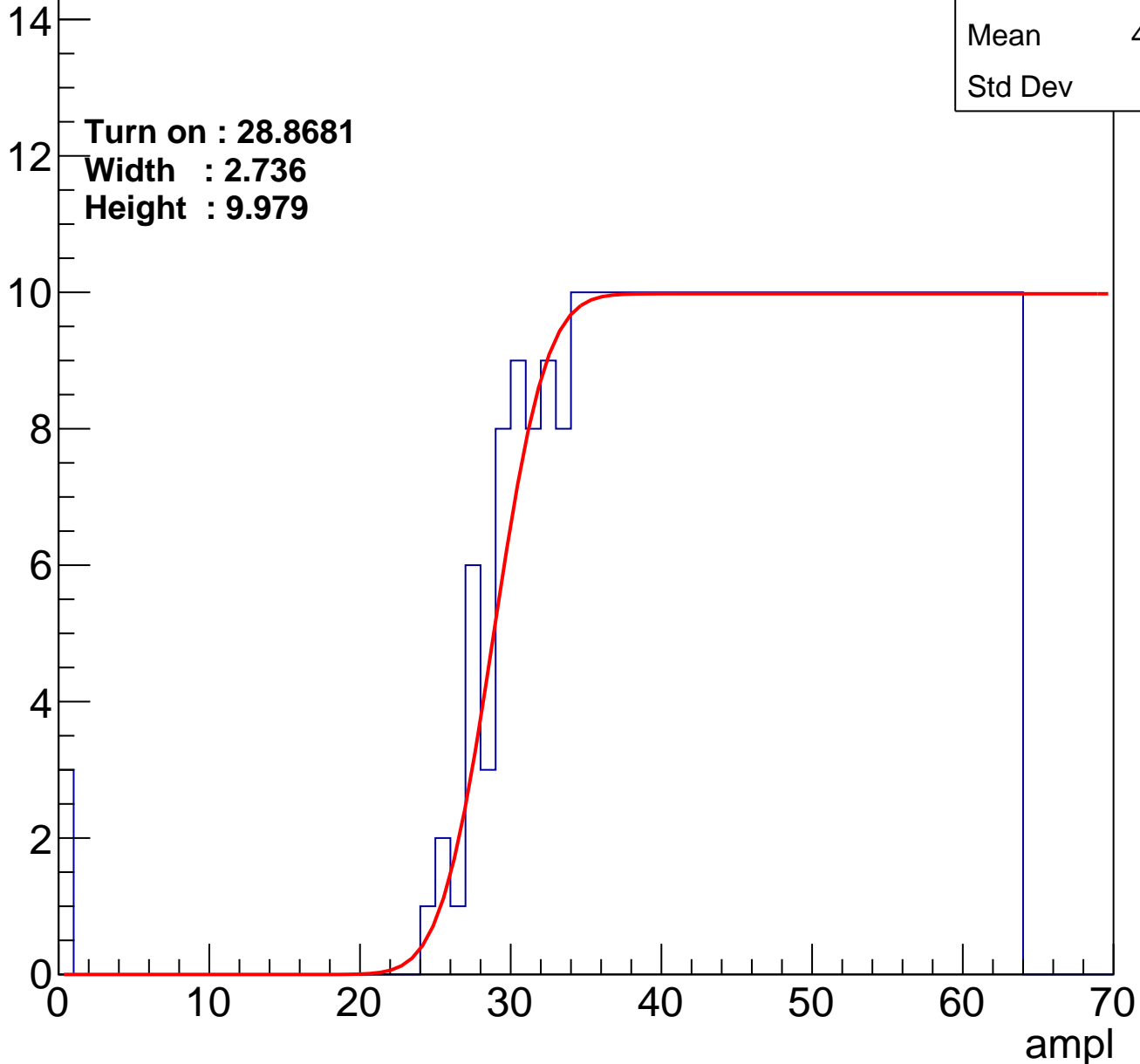
| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.25 |
| Std Dev | 11.2 |

Turn on : 28.8681

Width : 2.736

Height : 9.979

Entry



B0L001S, U23-ch5

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 349 |
| Mean | 45.76 |
| Std Dev | 10.78 |

Turn on : 29.8698

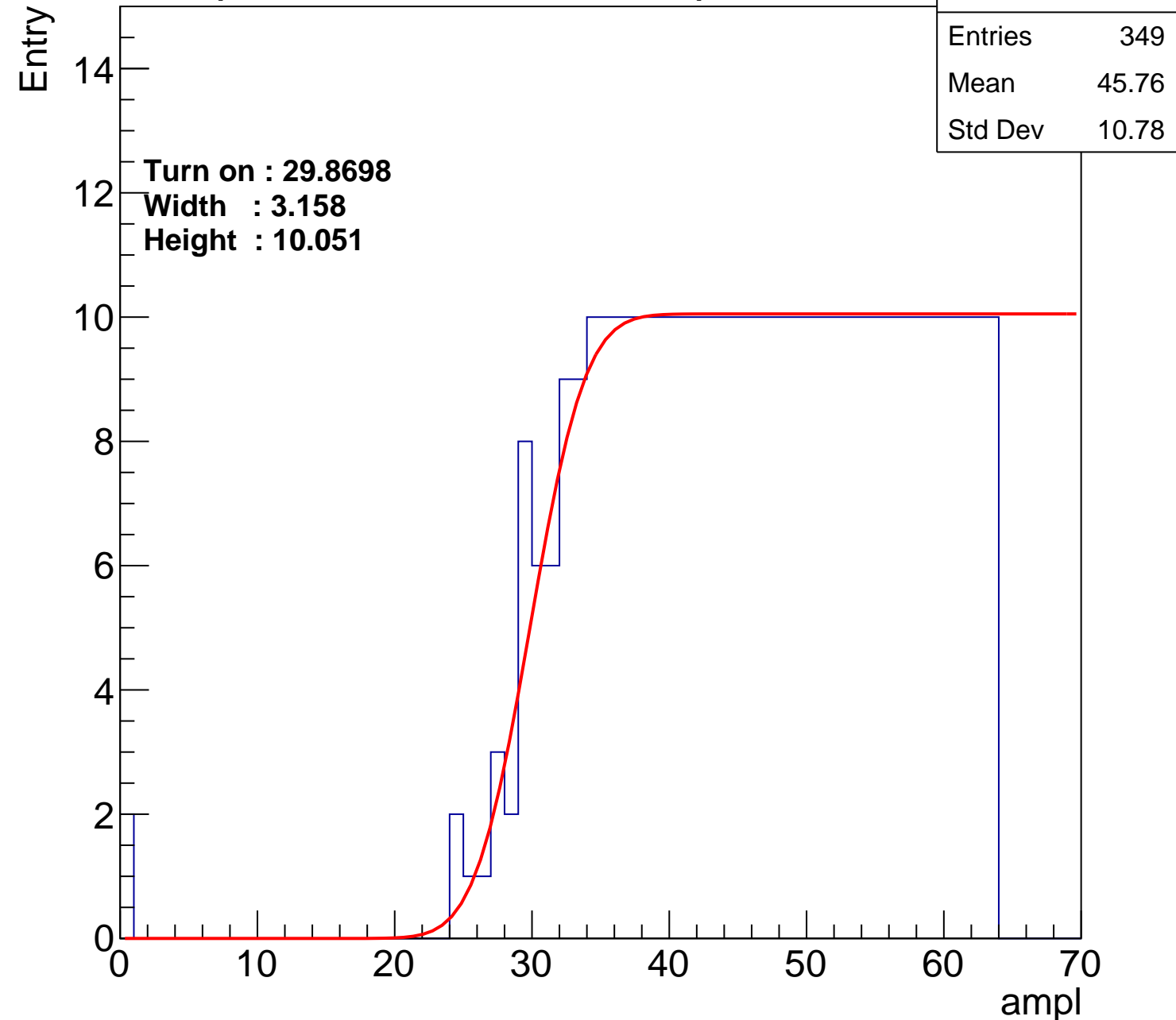
Width : 3.158

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch6

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.53 |
| Std Dev | 11.4 |

Turn on : 26.5694

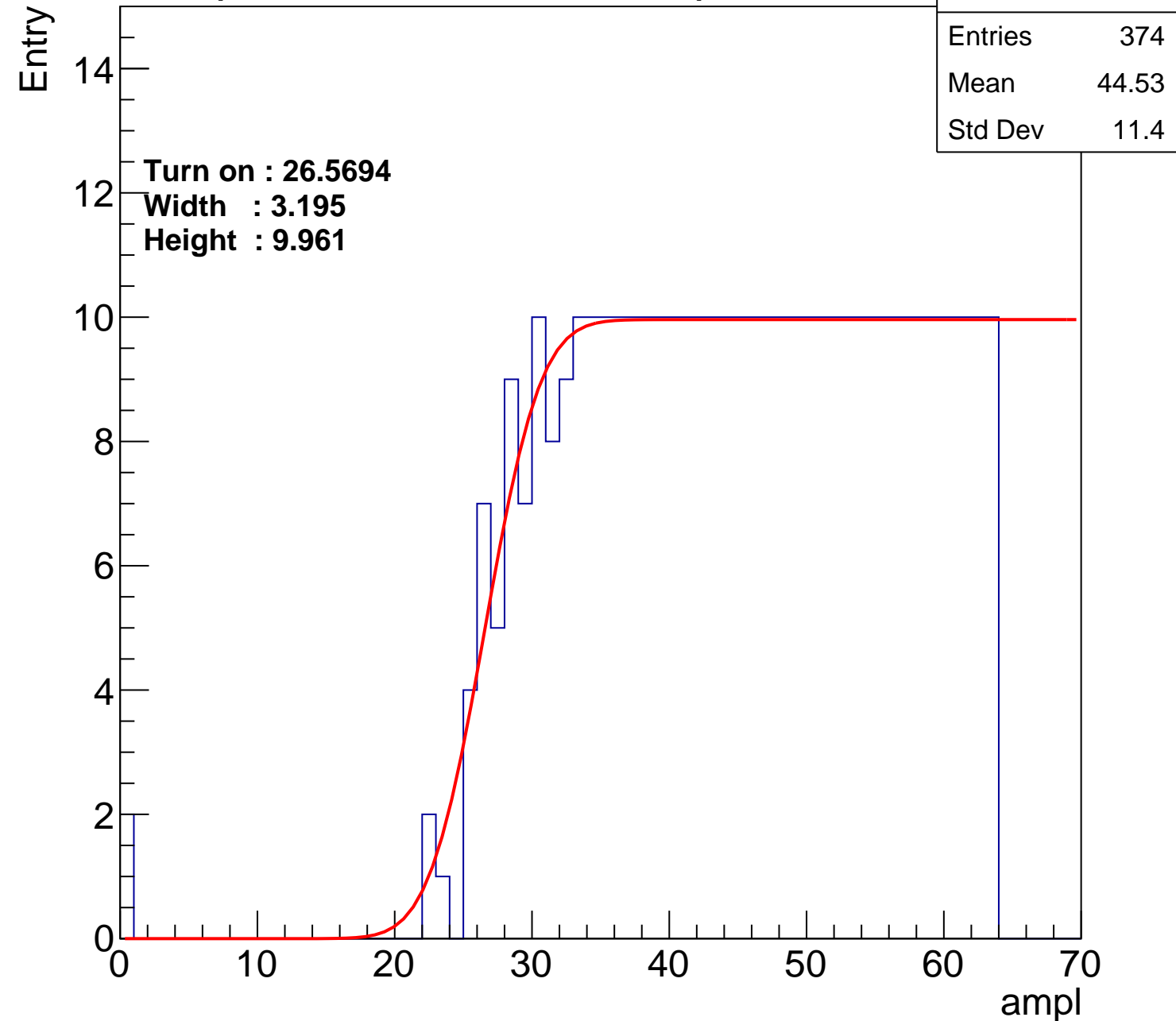
Width : 3.195

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch7

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.48 |
| Std Dev | 10.74 |

Turn on : 28.9397

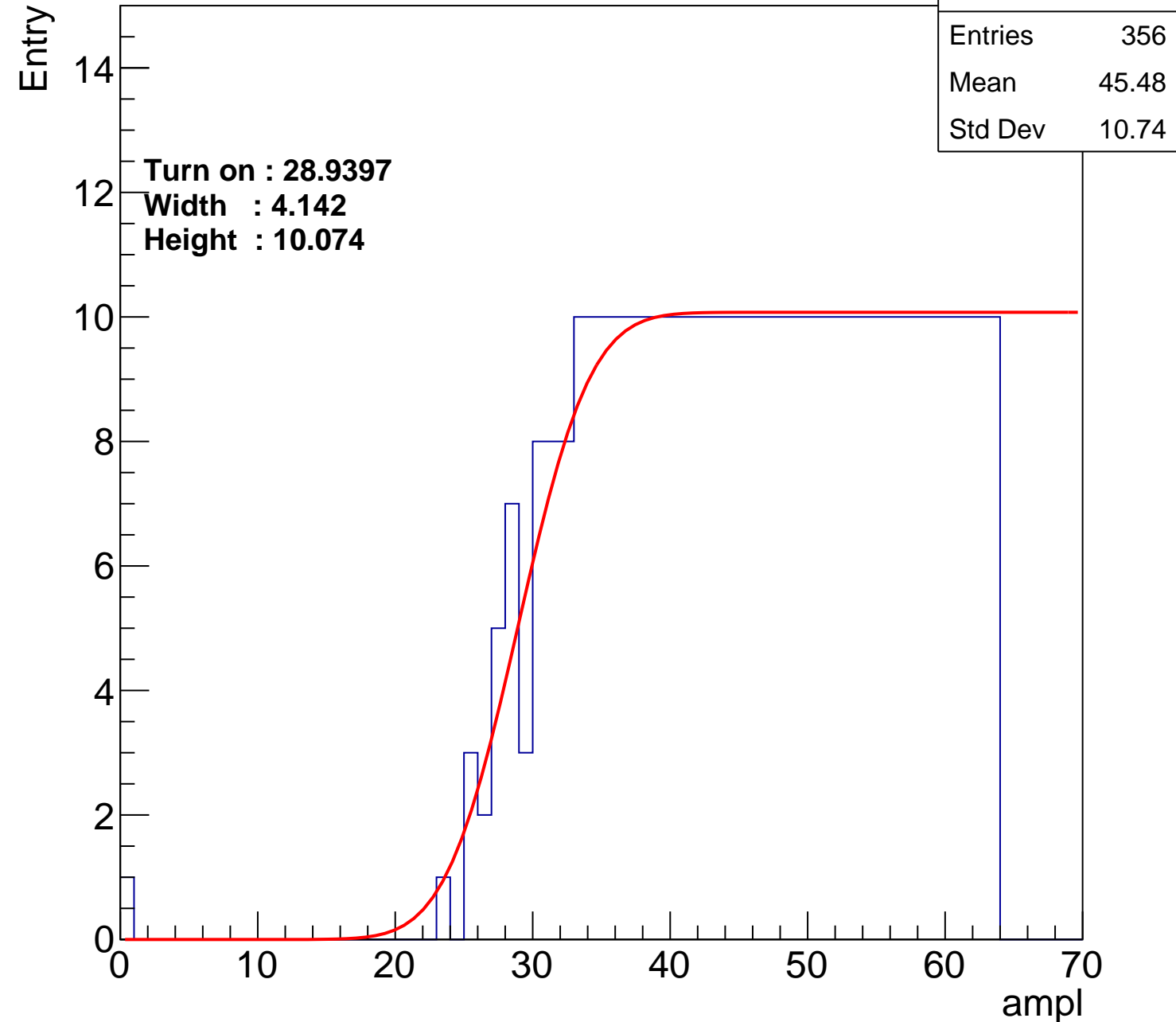
Width : 4.142

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch8

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 341 |
| Mean | 46.17 |
| Std Dev | 10.55 |

Turn on : 30.5964

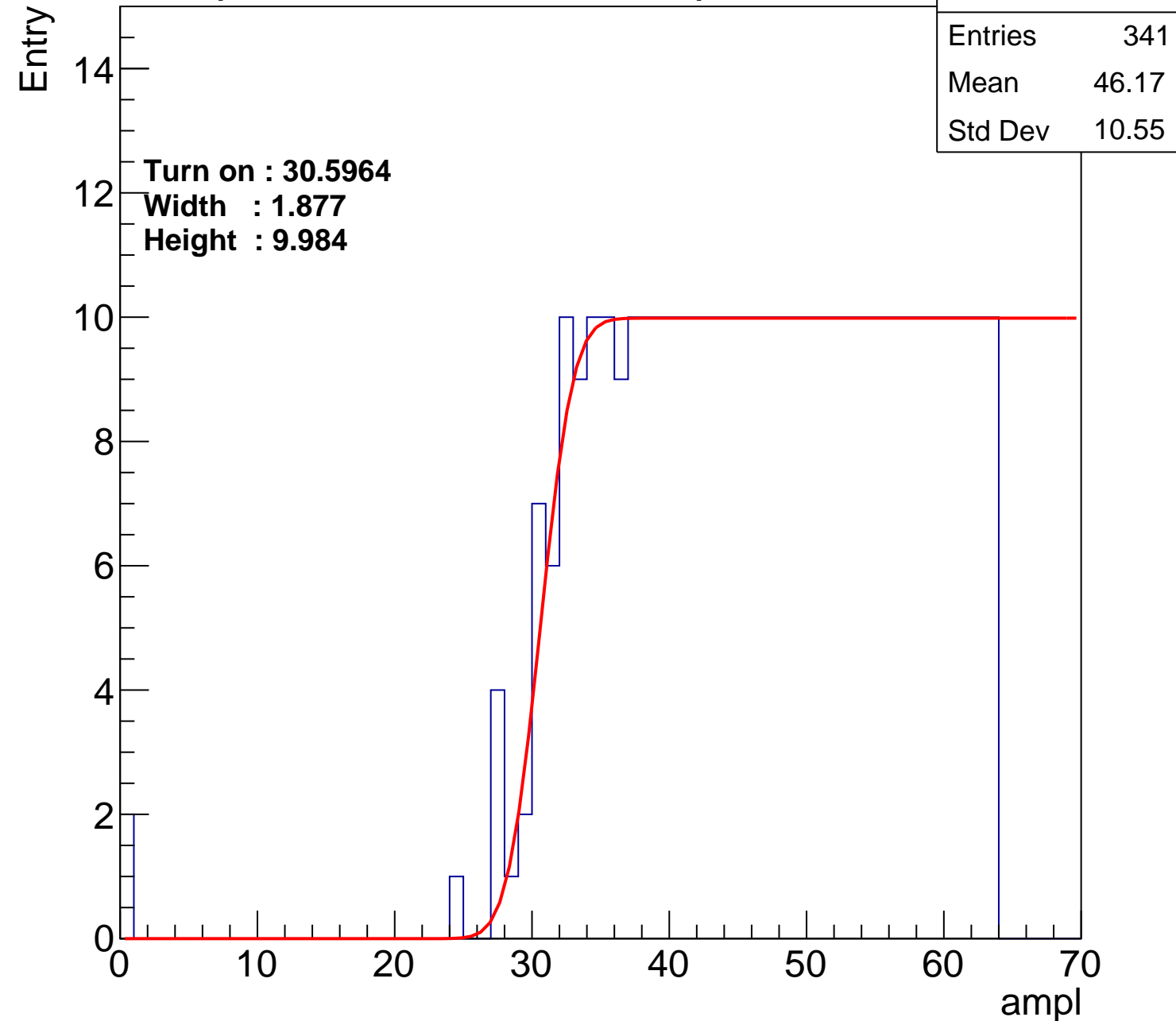
Width : 1.877

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch9

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.36 |
| Std Dev | 10.73 |

Turn on : 28.1094

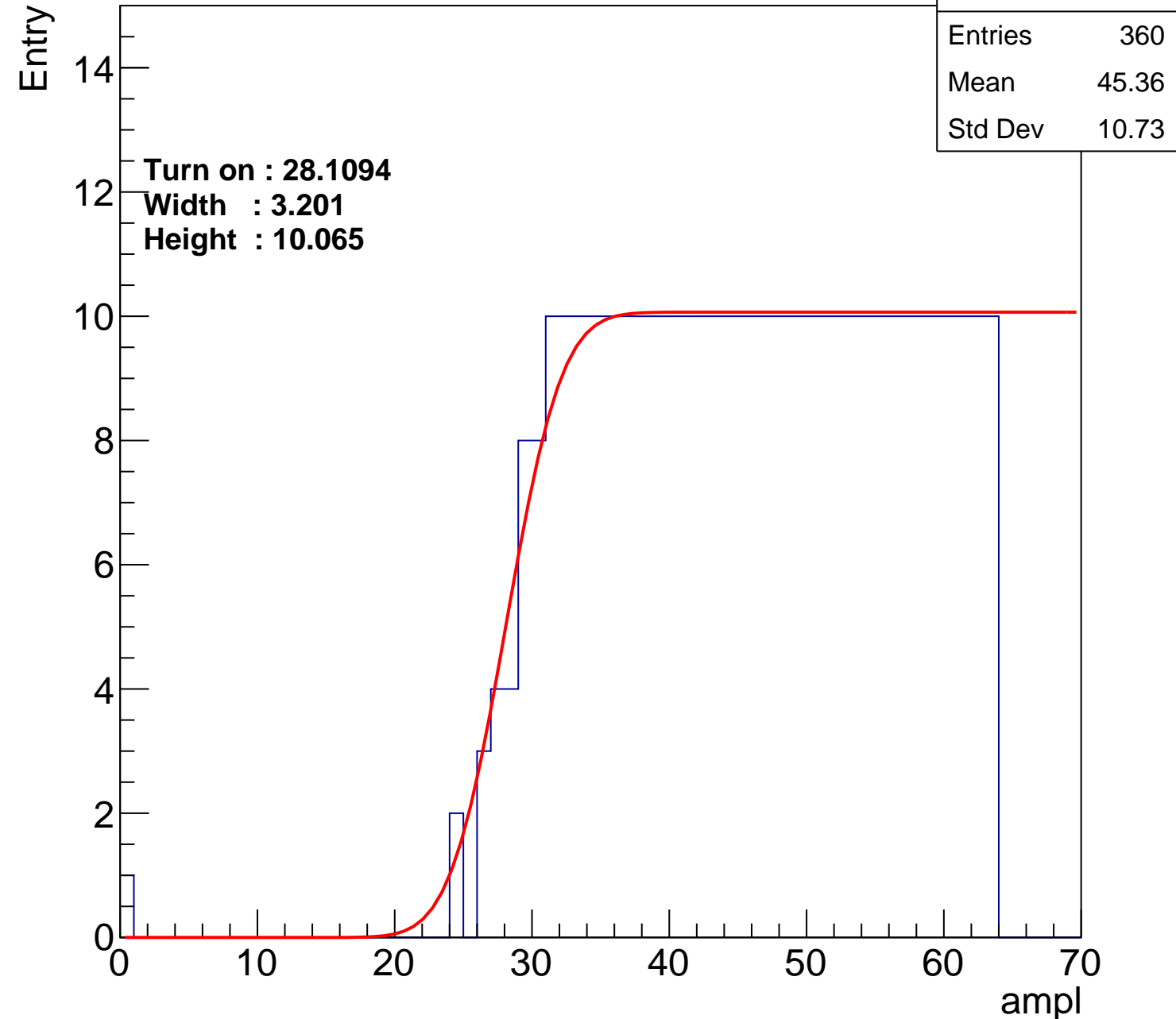
Width : 3.201

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch10

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 355 |
| Mean | 45.43 |
| Std Dev | 10.97 |

Turn on : 29.3941

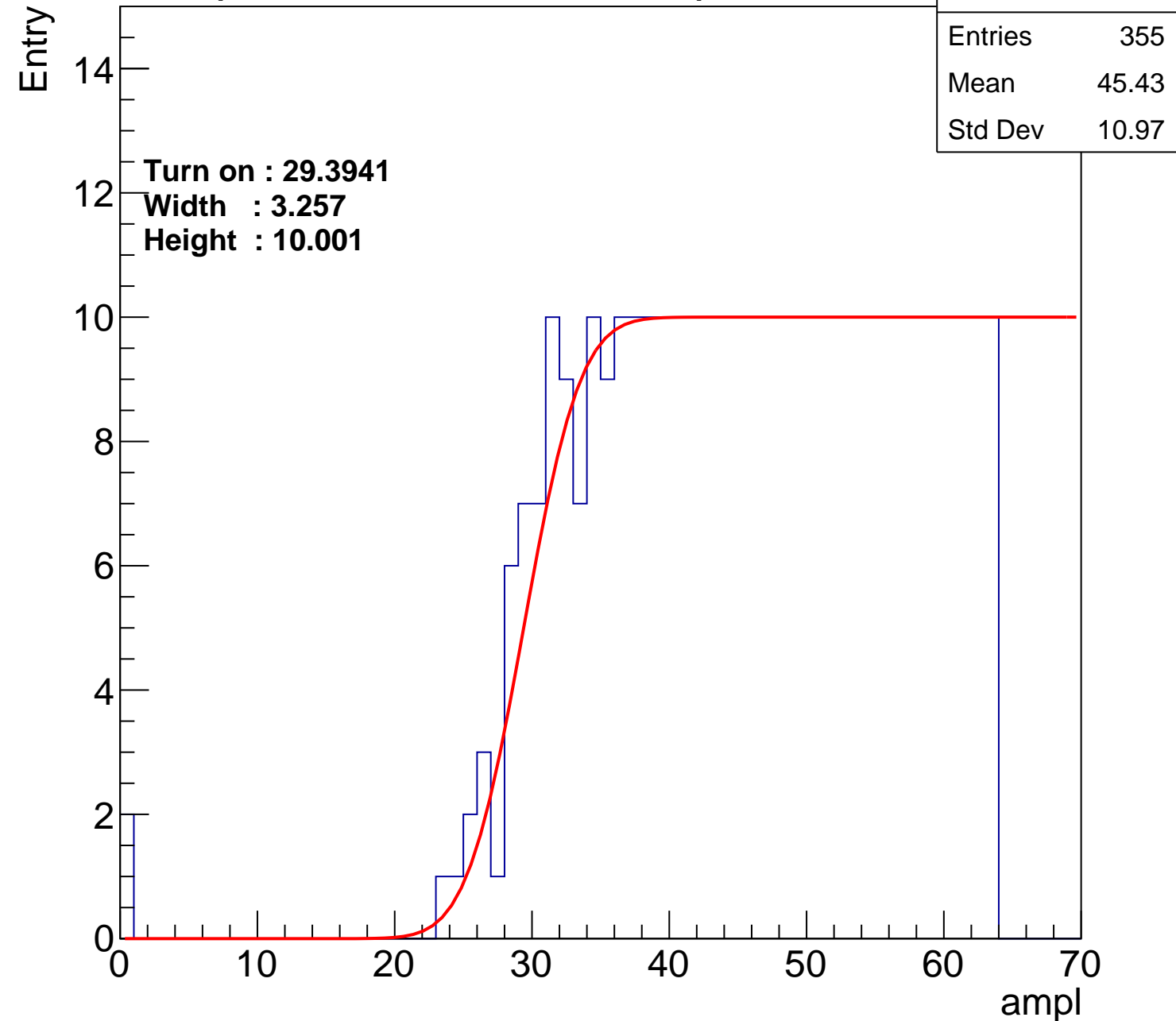
Width : 3.257

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch11

calib_packv5_042523_0143.root, FC#9, port A1

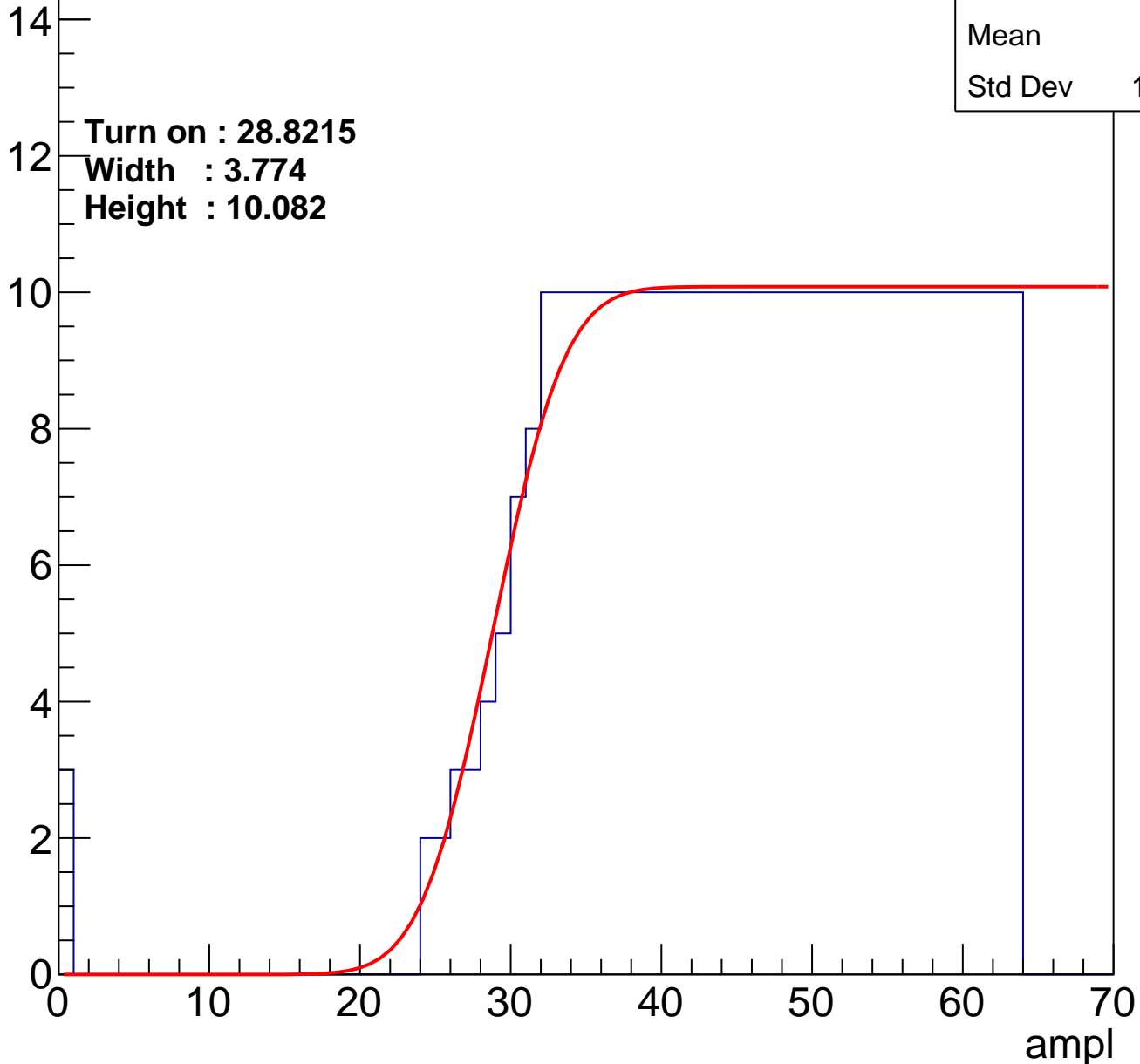
| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.3 |
| Std Dev | 11.18 |

Turn on : 28.8215

Width : 3.774

Height : 10.082

Entry



B0L001S, U23-ch12

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 347 |
| Mean | 45.86 |
| Std Dev | 10.72 |

Turn on : 29.9544

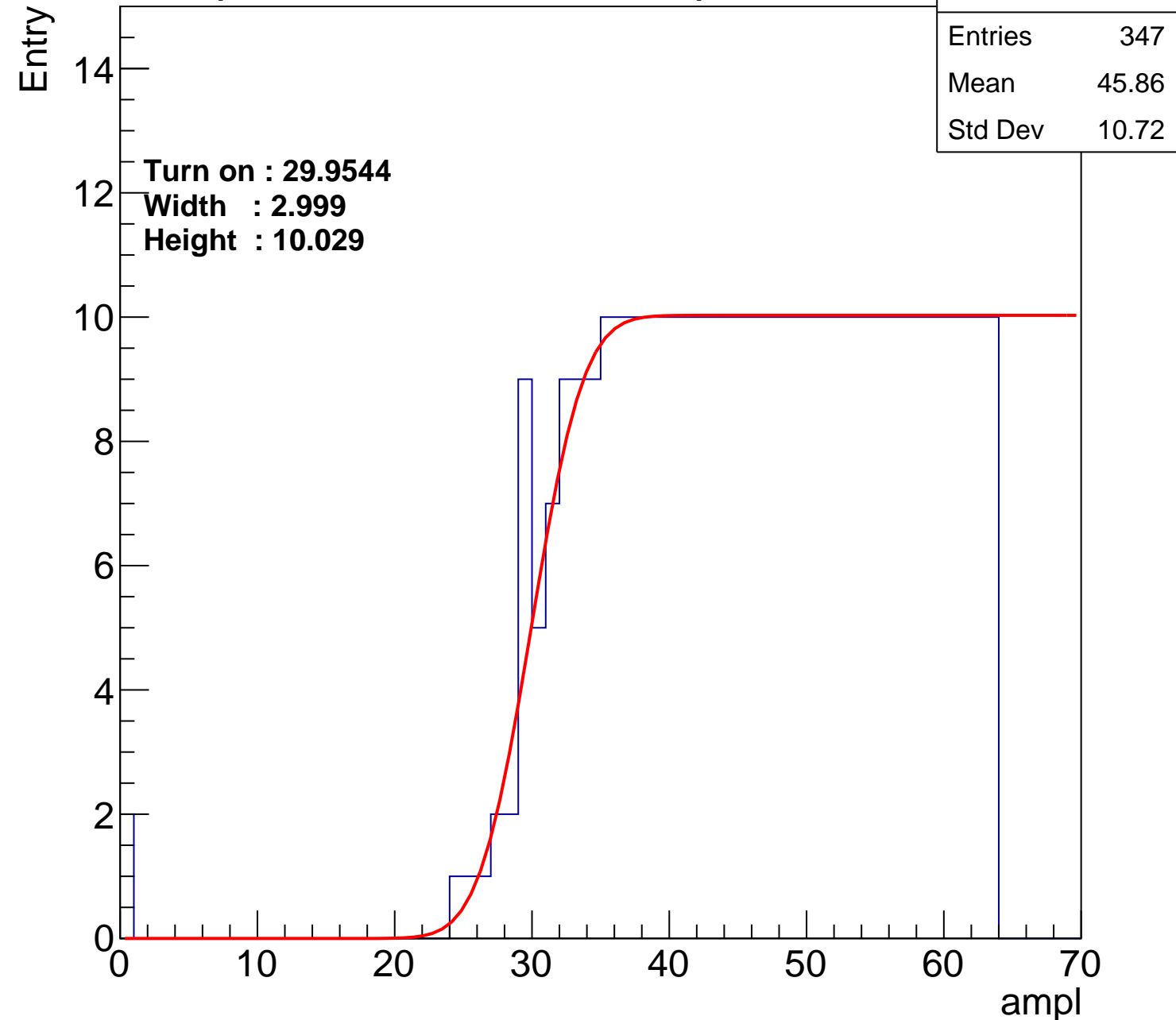
Width : 2.999

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch13

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.05 |
| Std Dev | 10.97 |

Turn on : 27.9090

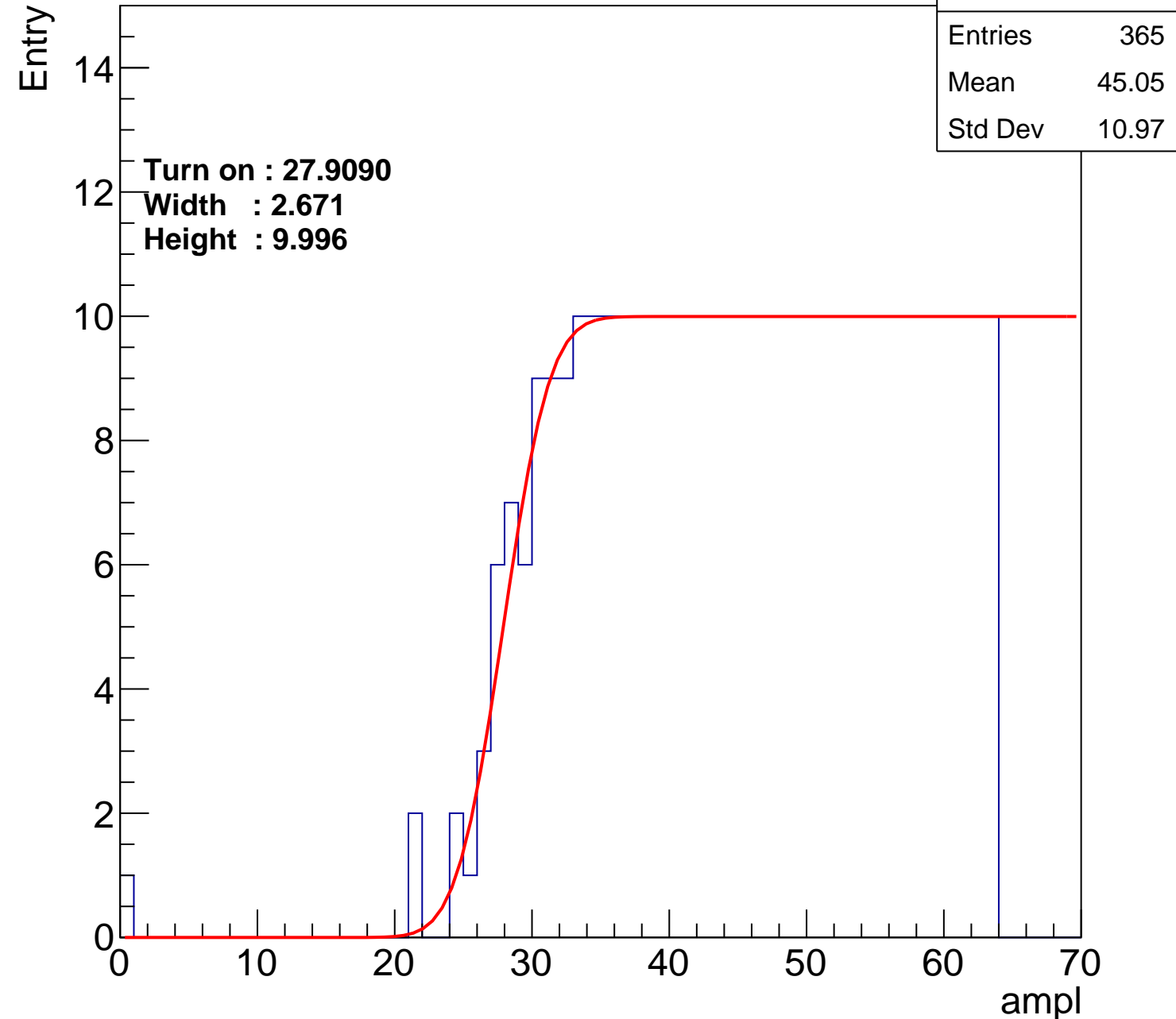
Width : 2.671

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch14

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.05 |
| Std Dev | 11.31 |

Turn on : 28.7362

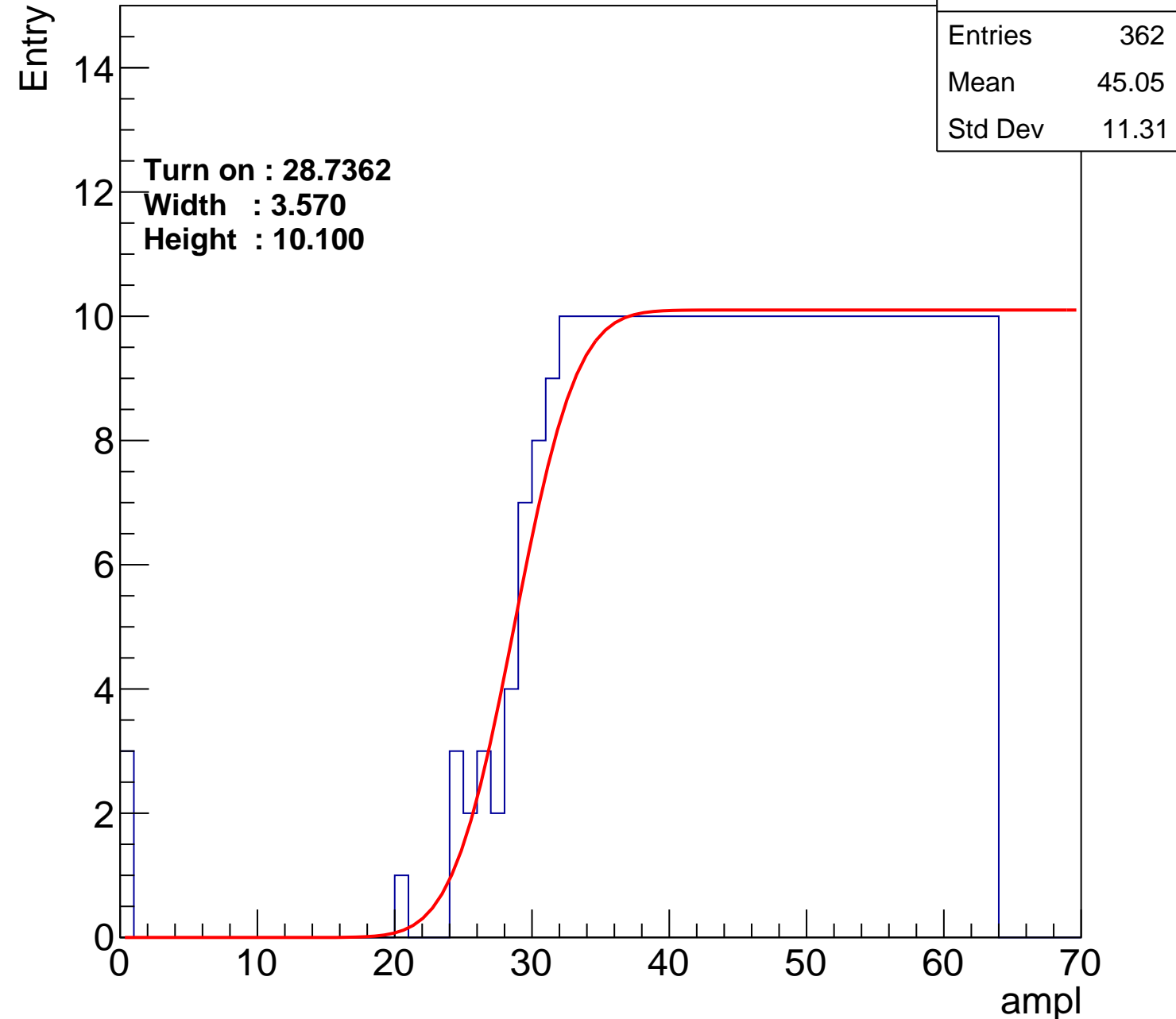
Width : 3.570

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch15

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.29 |
| Std Dev | 10.84 |

Turn on : 28.4505

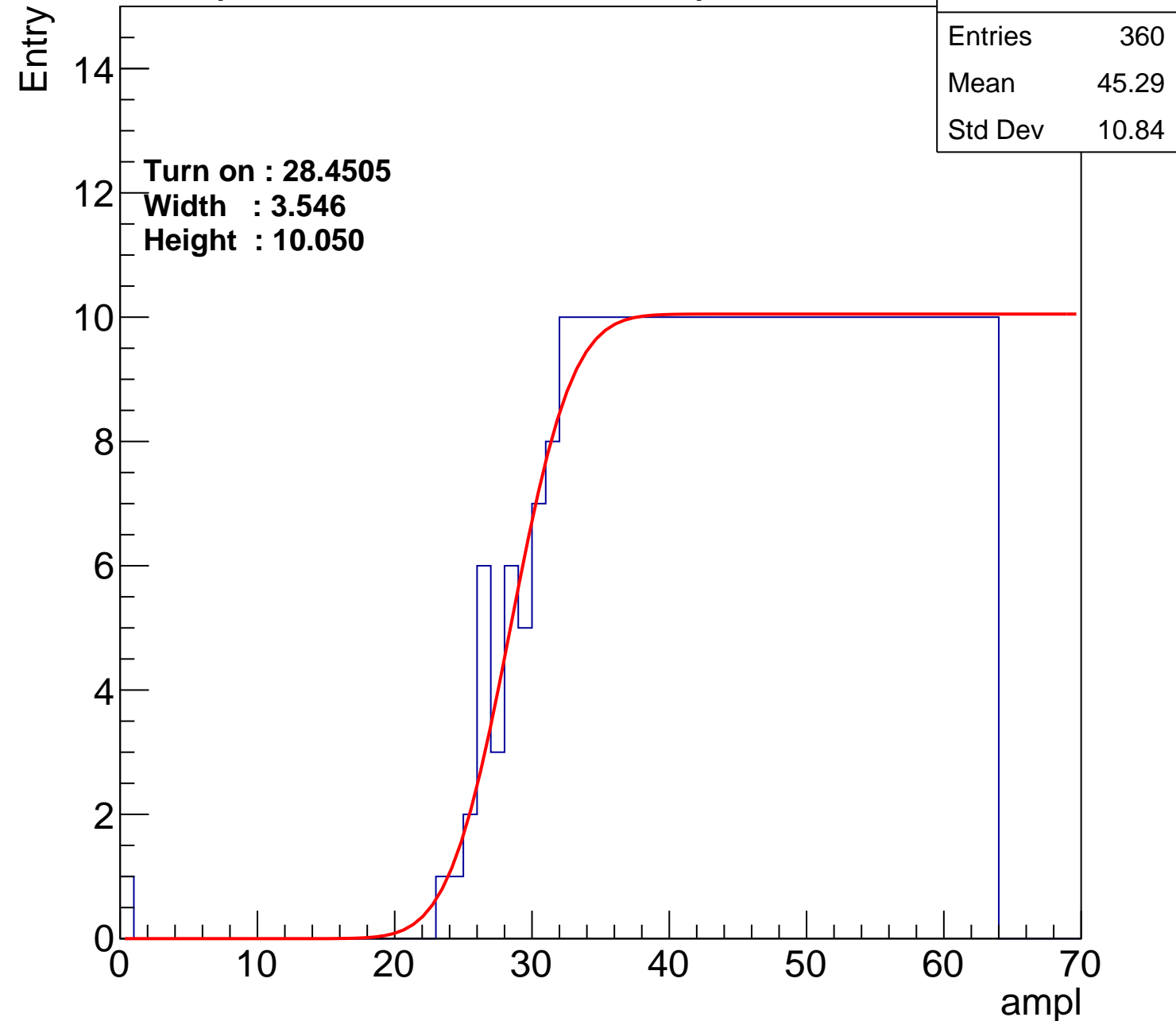
Width : 3.546

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch16

calib_packv5_042523_0143.root, FC#9, port A1

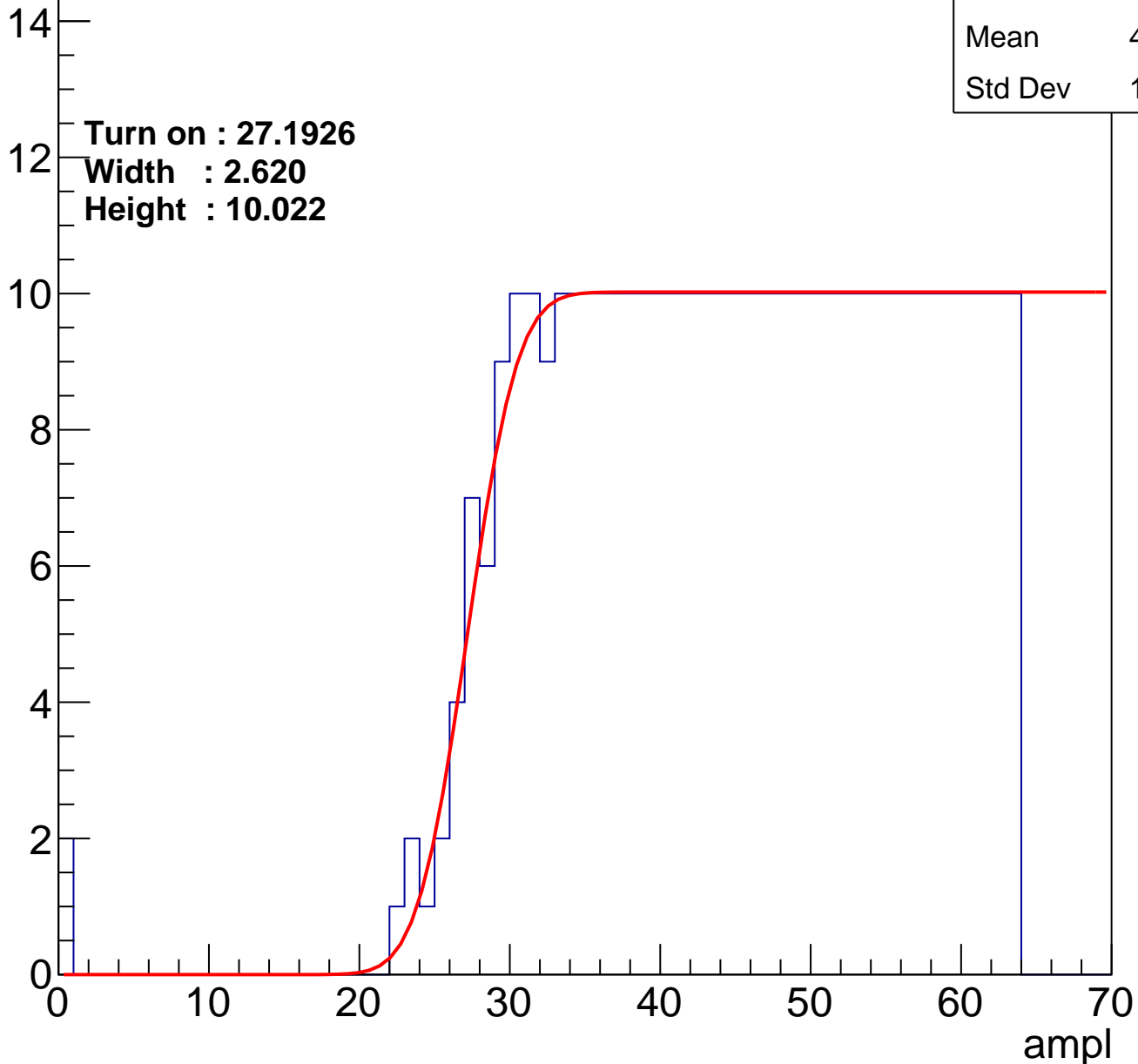
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.62 |
| Std Dev | 11.32 |

Turn on : 27.1926

Width : 2.620

Height : 10.022

Entry



B0L001S, U23-ch17

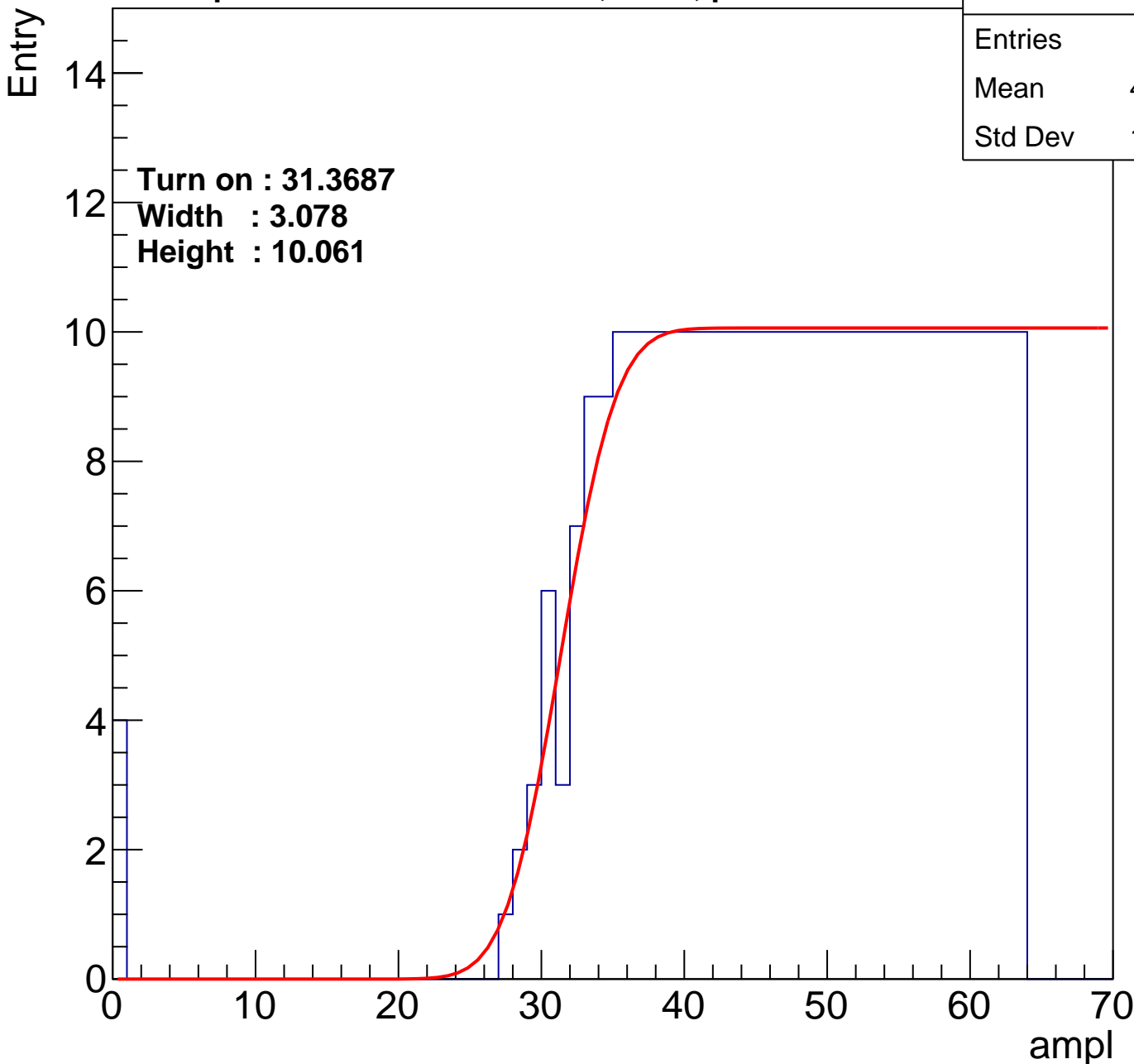
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 334 |
| Mean | 46.35 |
| Std Dev | 10.89 |

Turn on : 31.3687

Width : 3.078

Height : 10.061



B0L001S, U23-ch18

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.15 |
| Std Dev | 10.85 |

Turn on : 27.4692

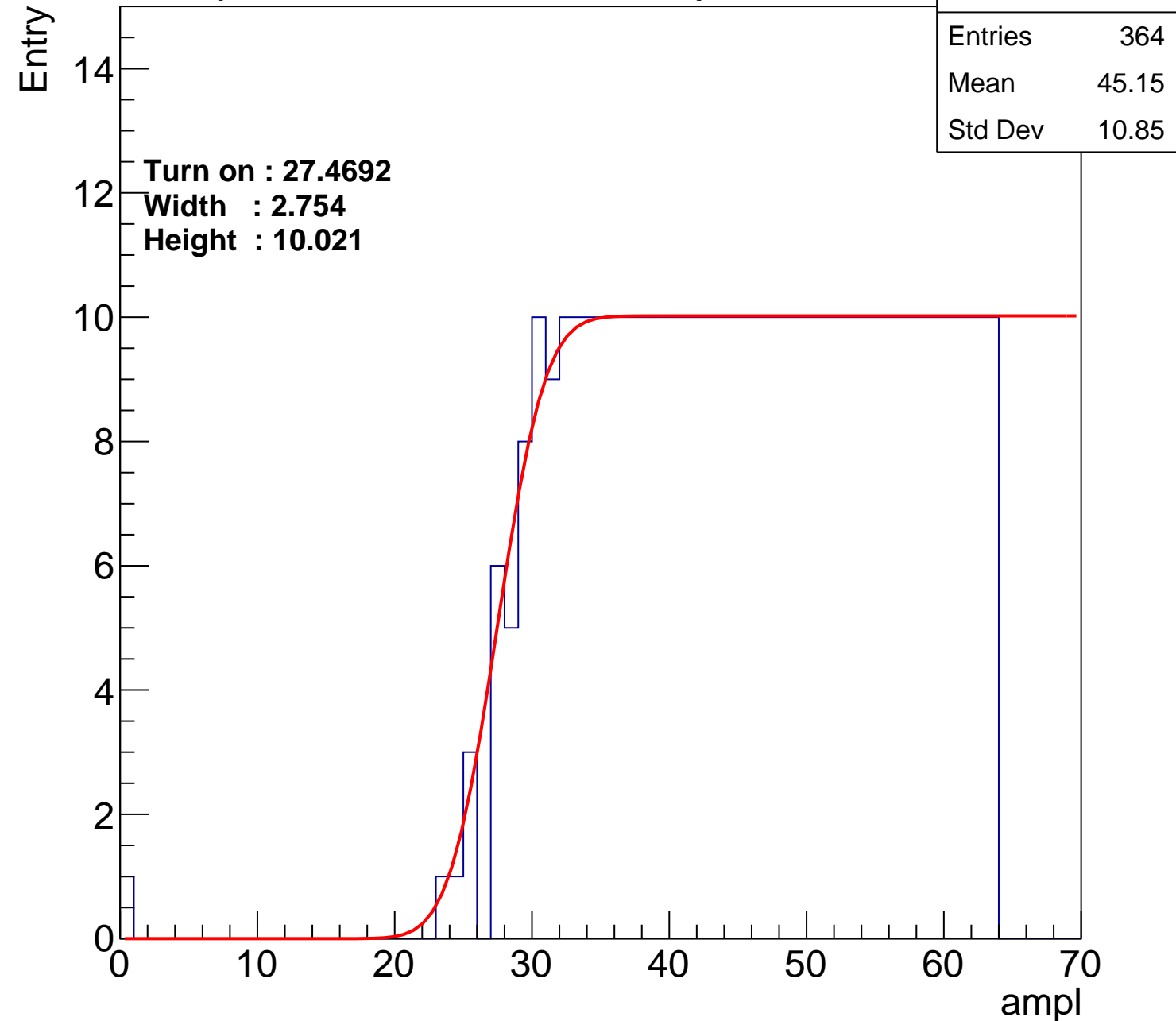
Width : 2.754

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch19

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.37 |
| Std Dev | 10.83 |

Turn on : 28.8436

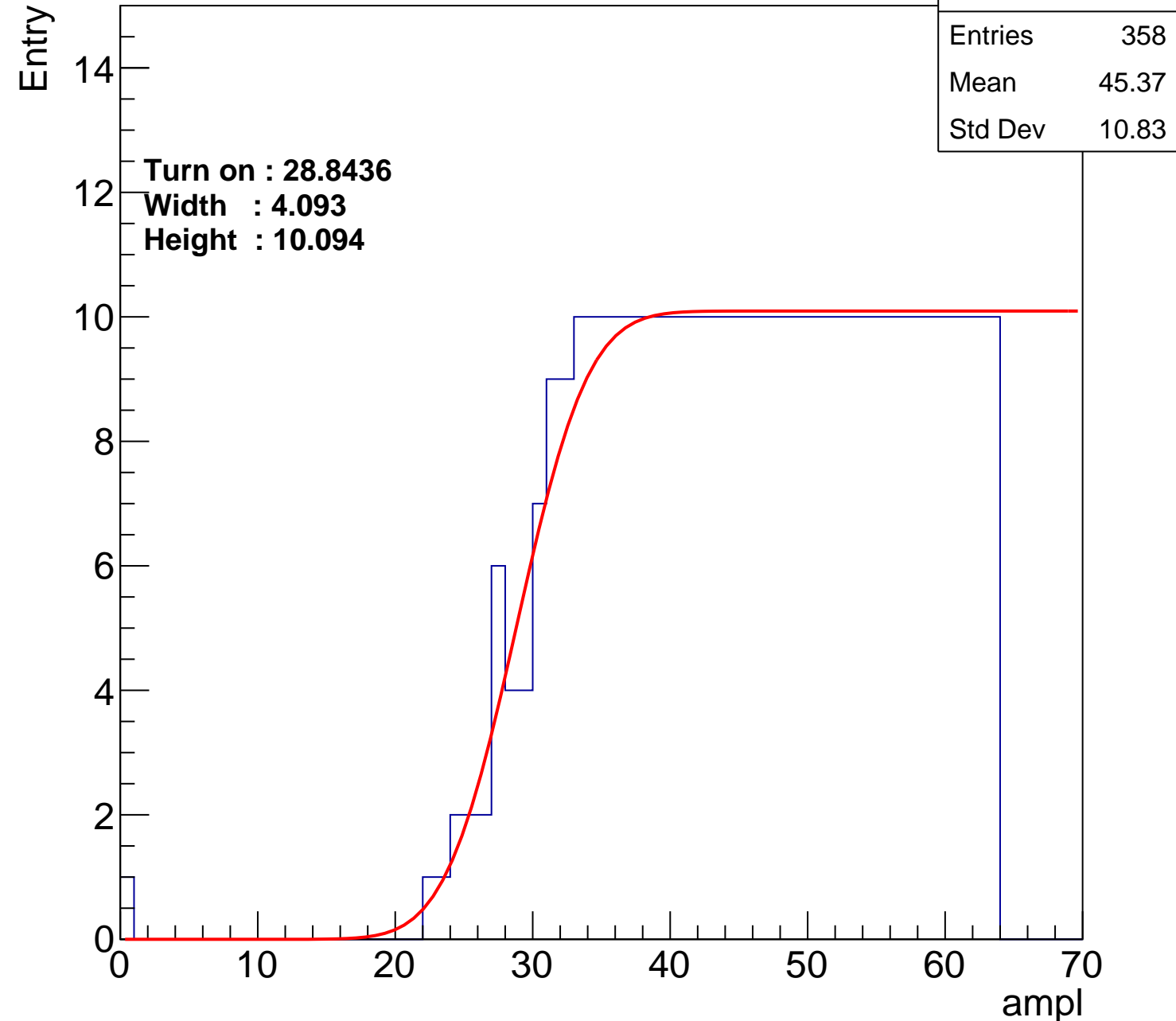
Width : 4.093

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch20

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.45 |
| Std Dev | 10.9 |

Turn on : 28.7557

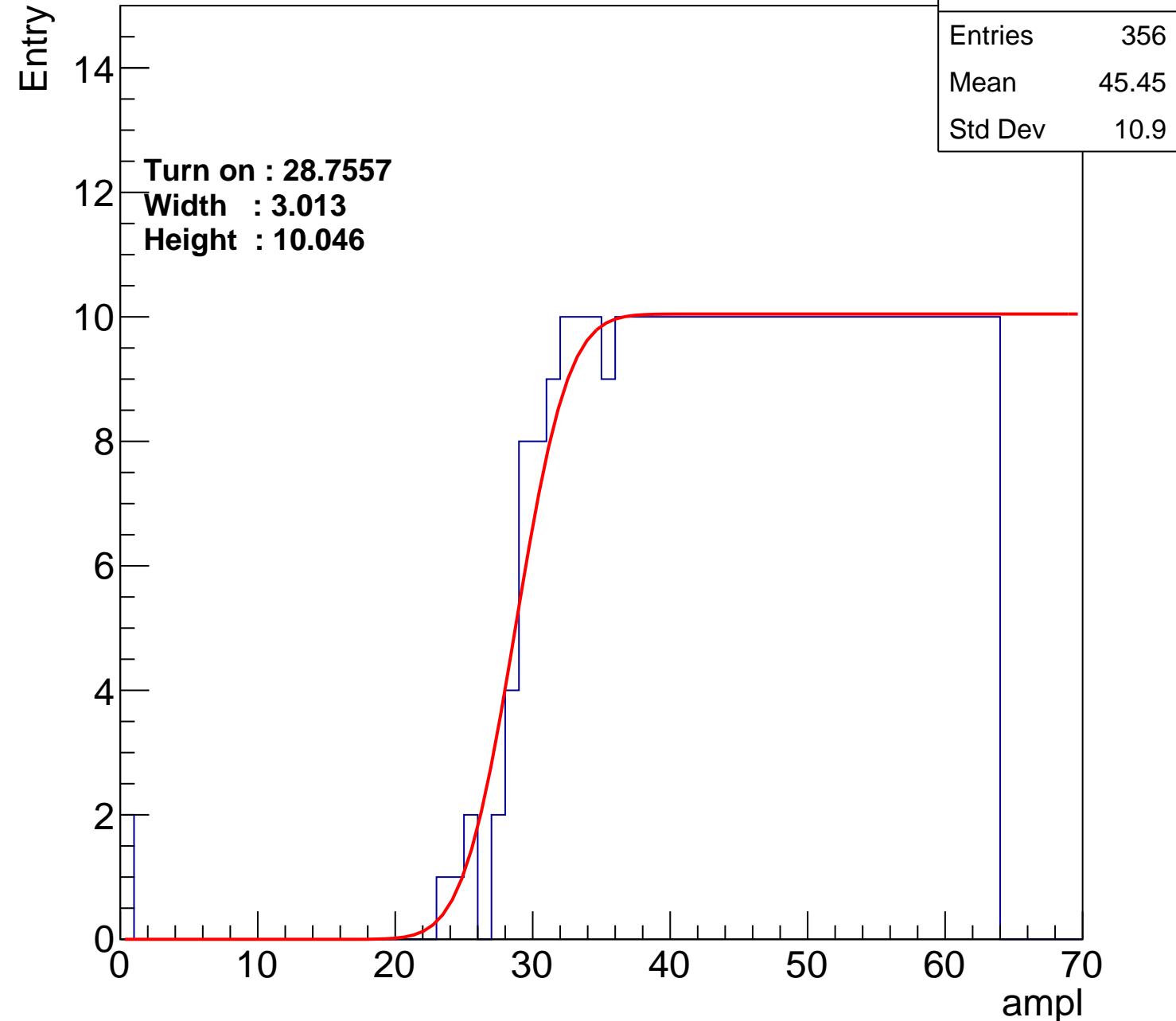
Width : 3.013

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch21

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 345 |
| Mean | 45.87 |
| Std Dev | 10.93 |

Turn on : 29.9076

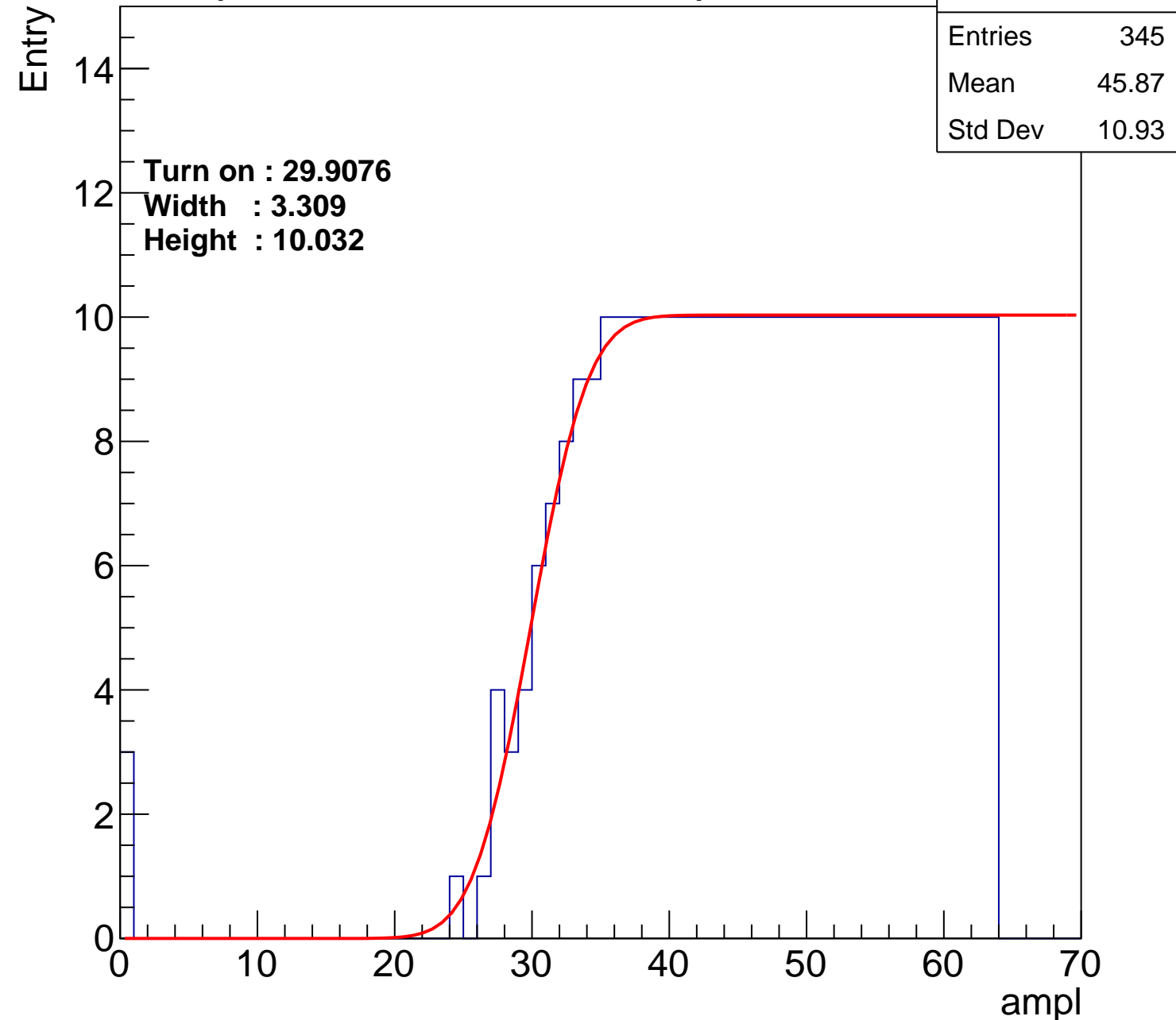
Width : 3.309

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch22

calib_packv5_042523_0143.root, FC#9, port A1

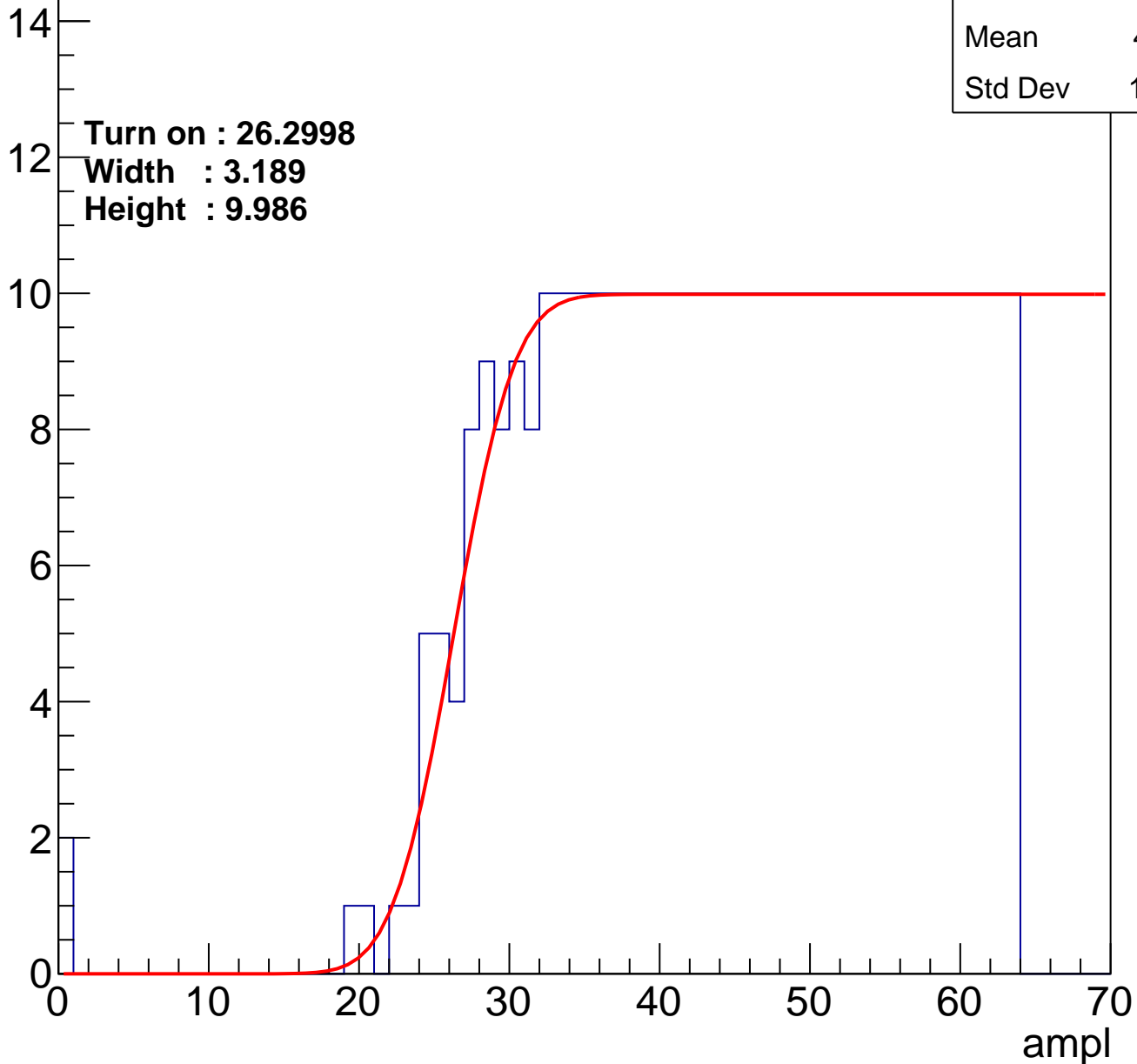
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.11 |
| Std Dev | 11.65 |

Turn on : 26.2998

Width : 3.189

Height : 9.986

Entry



B0L001S, U23-ch23

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.89 |
| Std Dev | 11.2 |

Turn on : 27.5267

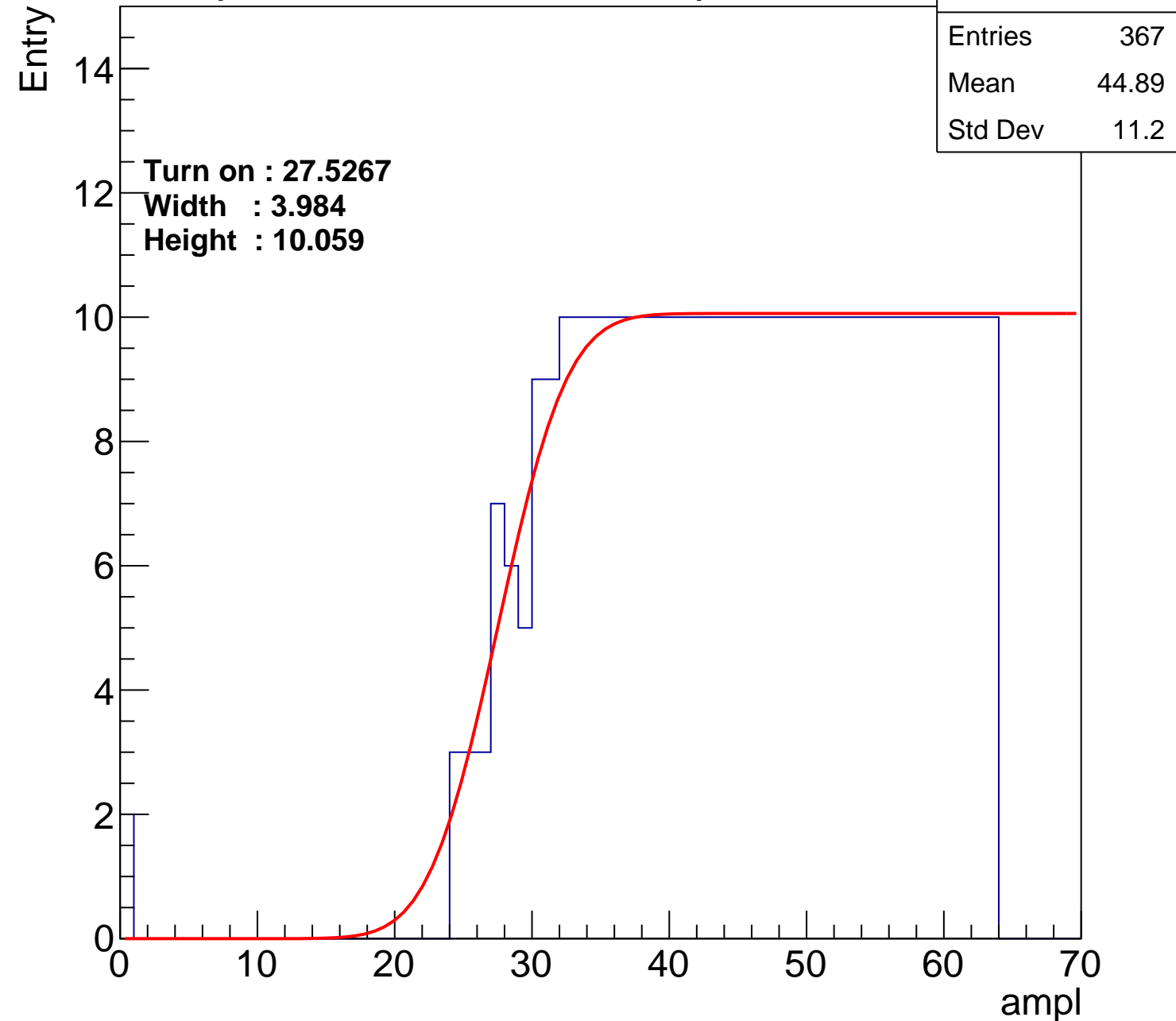
Width : 3.984

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch24

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.25 |
| Std Dev | 11.48 |

Turn on : 26.2789

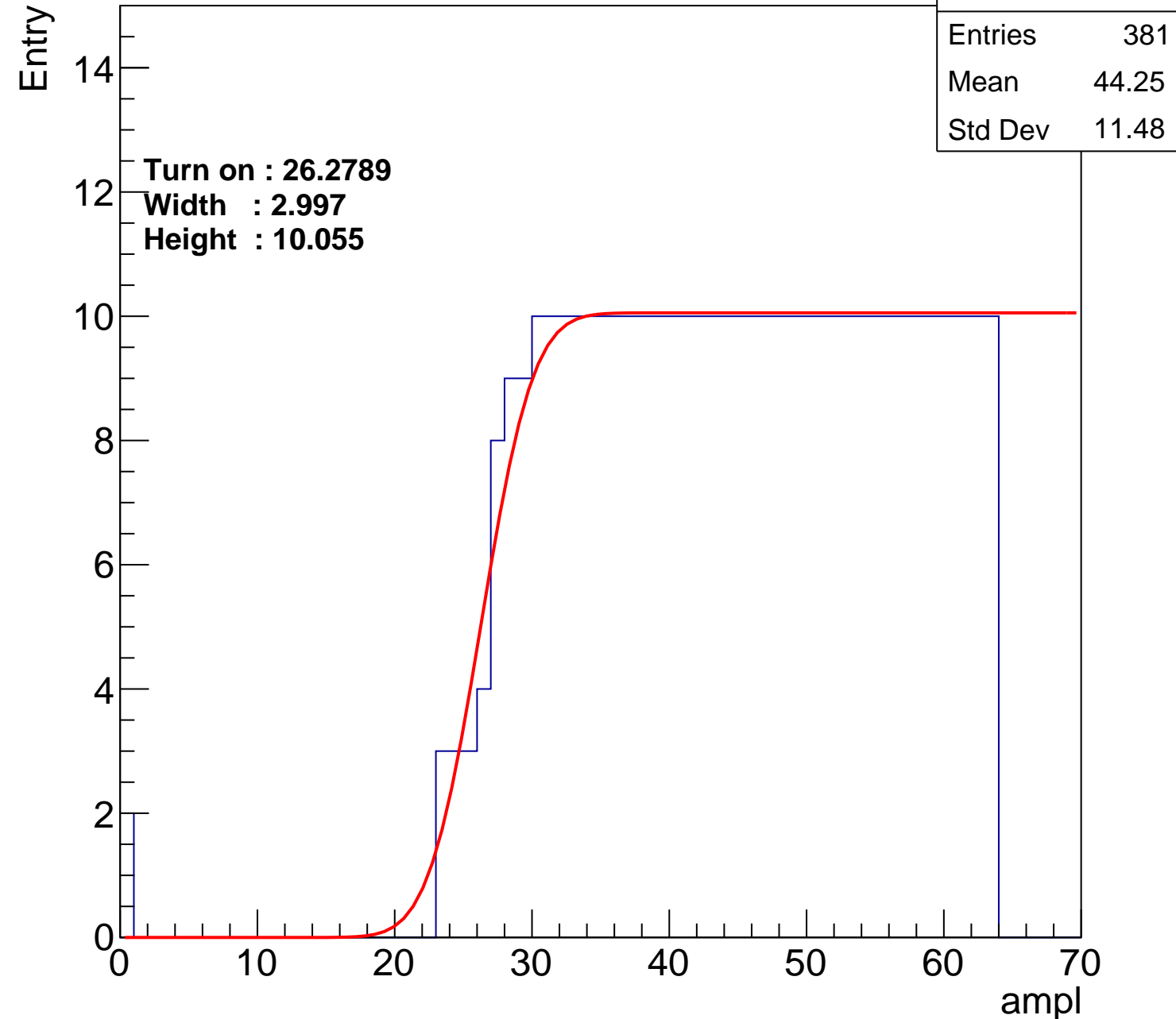
Width : 2.997

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch25

calib_packv5_042523_0143.root, FC#9, port A1

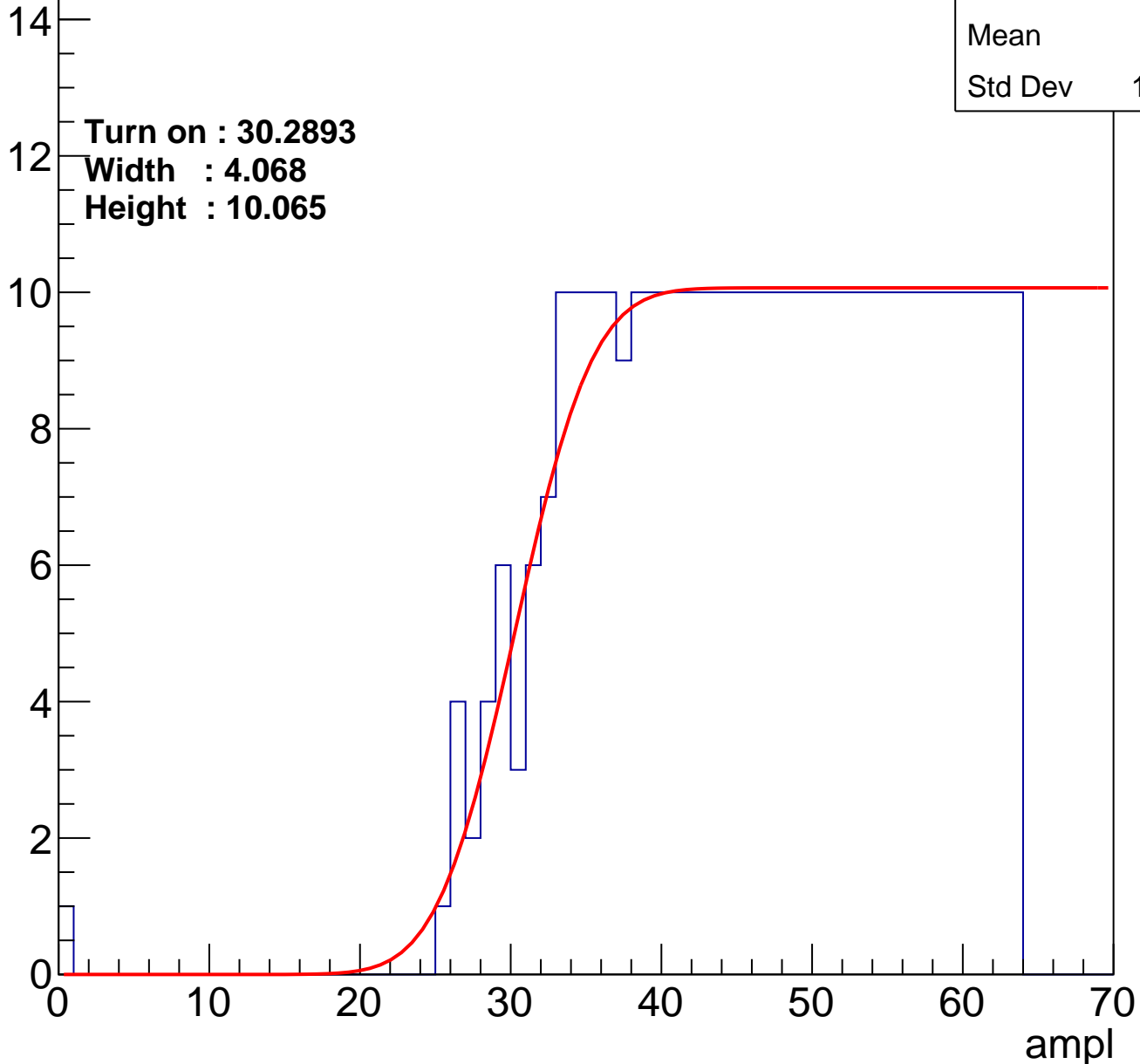
| | |
|---------|-------|
| Entries | 343 |
| Mean | 46.1 |
| Std Dev | 10.44 |

Turn on : 30.2893

Width : 4.068

Height : 10.065

Entry



B0L001S, U23-ch26

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 344 |
| Mean | 46.02 |
| Std Dev | 10.63 |

Turn on : 29.9791

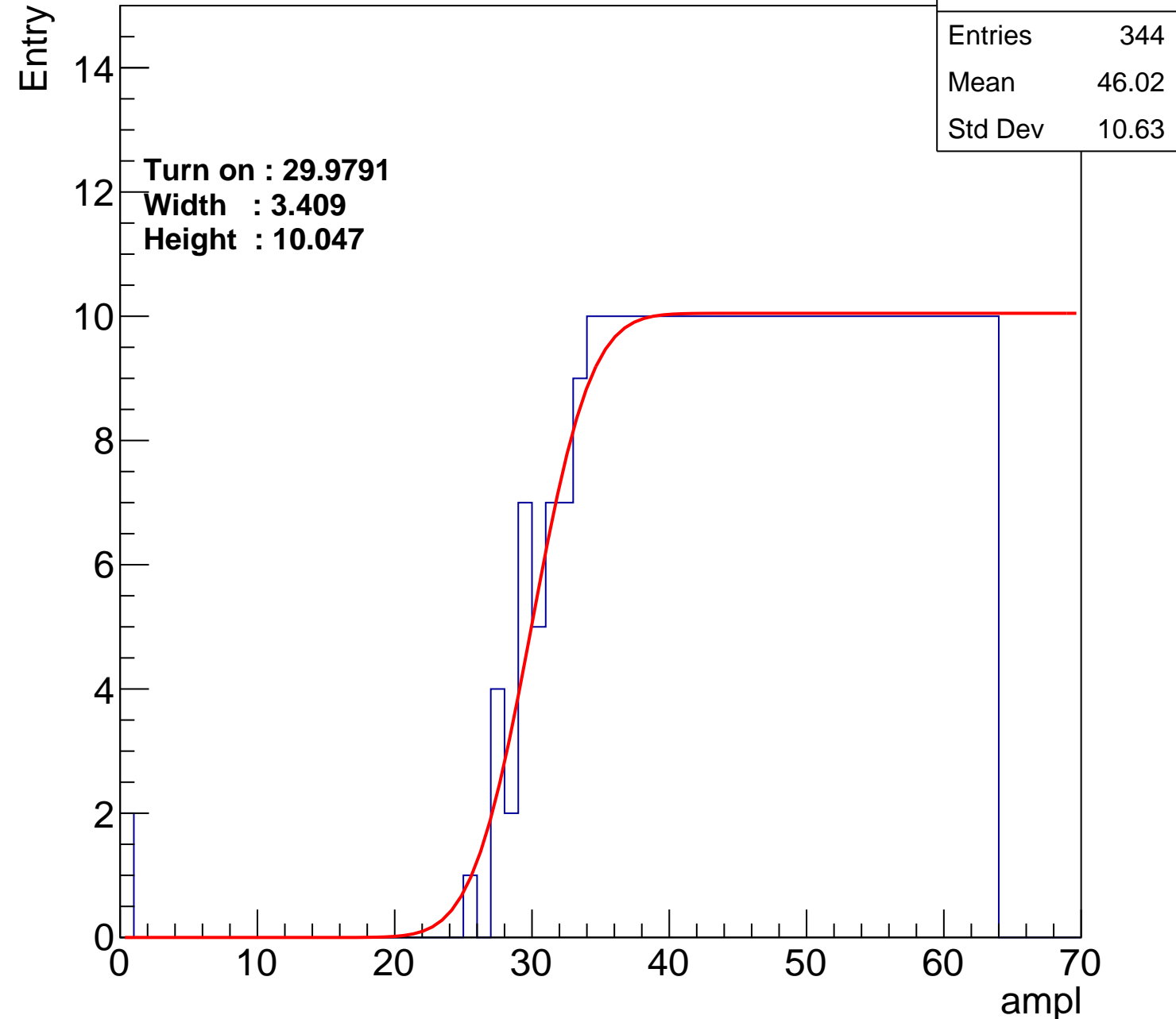
Width : 3.409

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch27

calib_packv5_042523_0143.root, FC#9, port A1

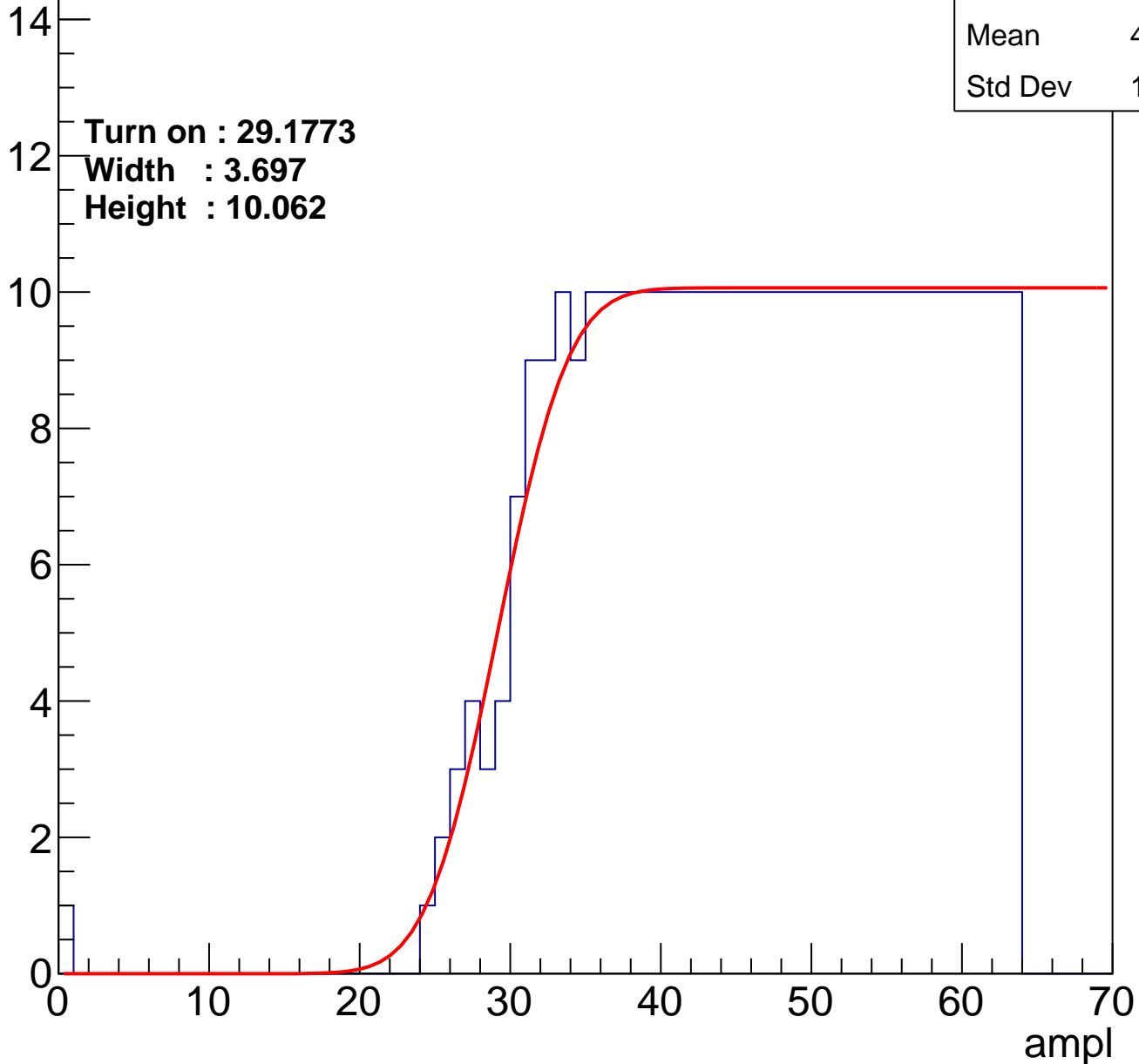
| | |
|---------|-------|
| Entries | 352 |
| Mean | 45.69 |
| Std Dev | 10.62 |

Turn on : 29.1773

Width : 3.697

Height : 10.062

Entry



B0L001S, U23-ch28

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 348 |
| Mean | 45.71 |
| Std Dev | 11 |

Turn on : 29.4572

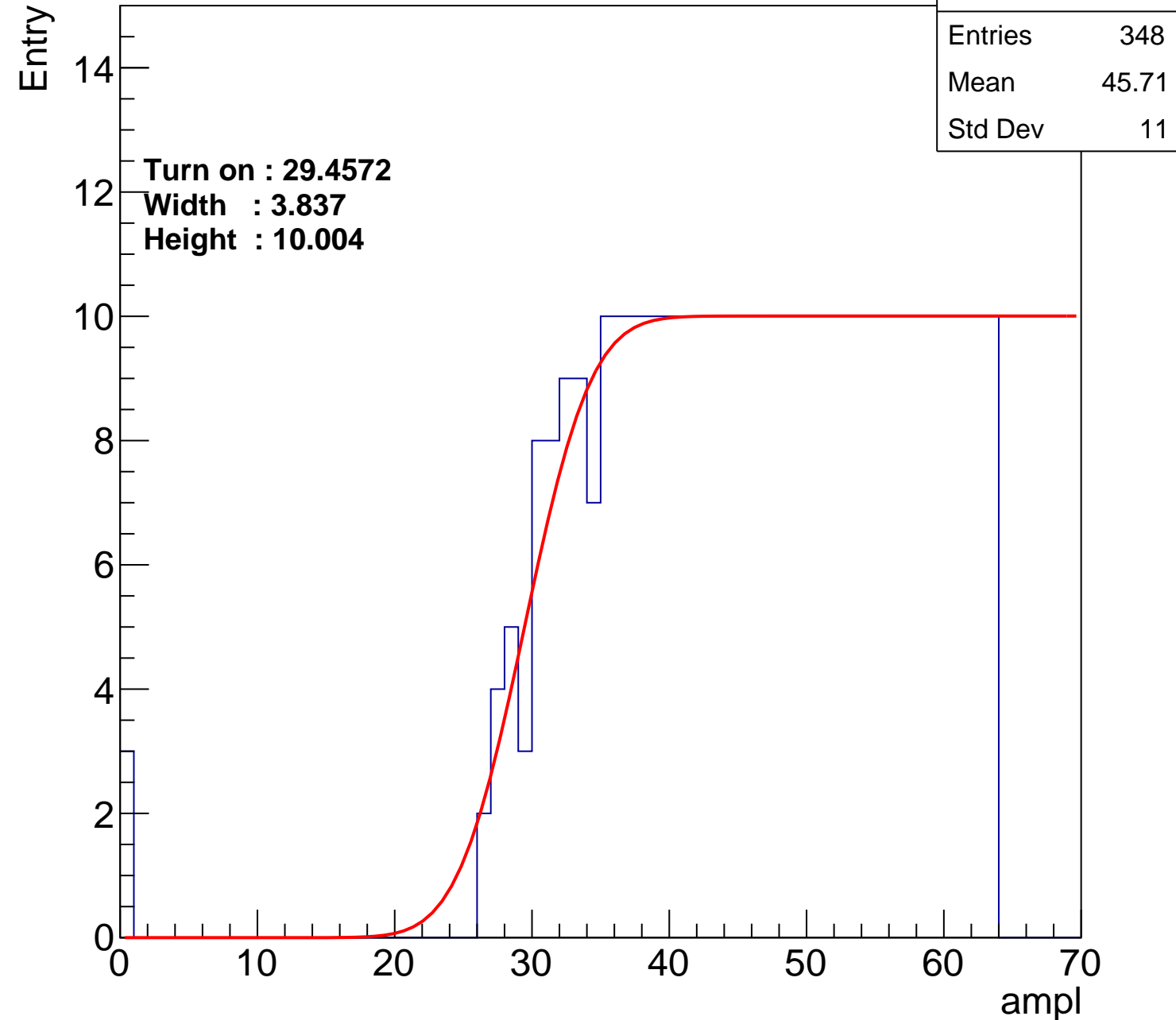
Width : 3.837

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch29

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.52 |
| Std Dev | 11.59 |

Turn on : 27.8765

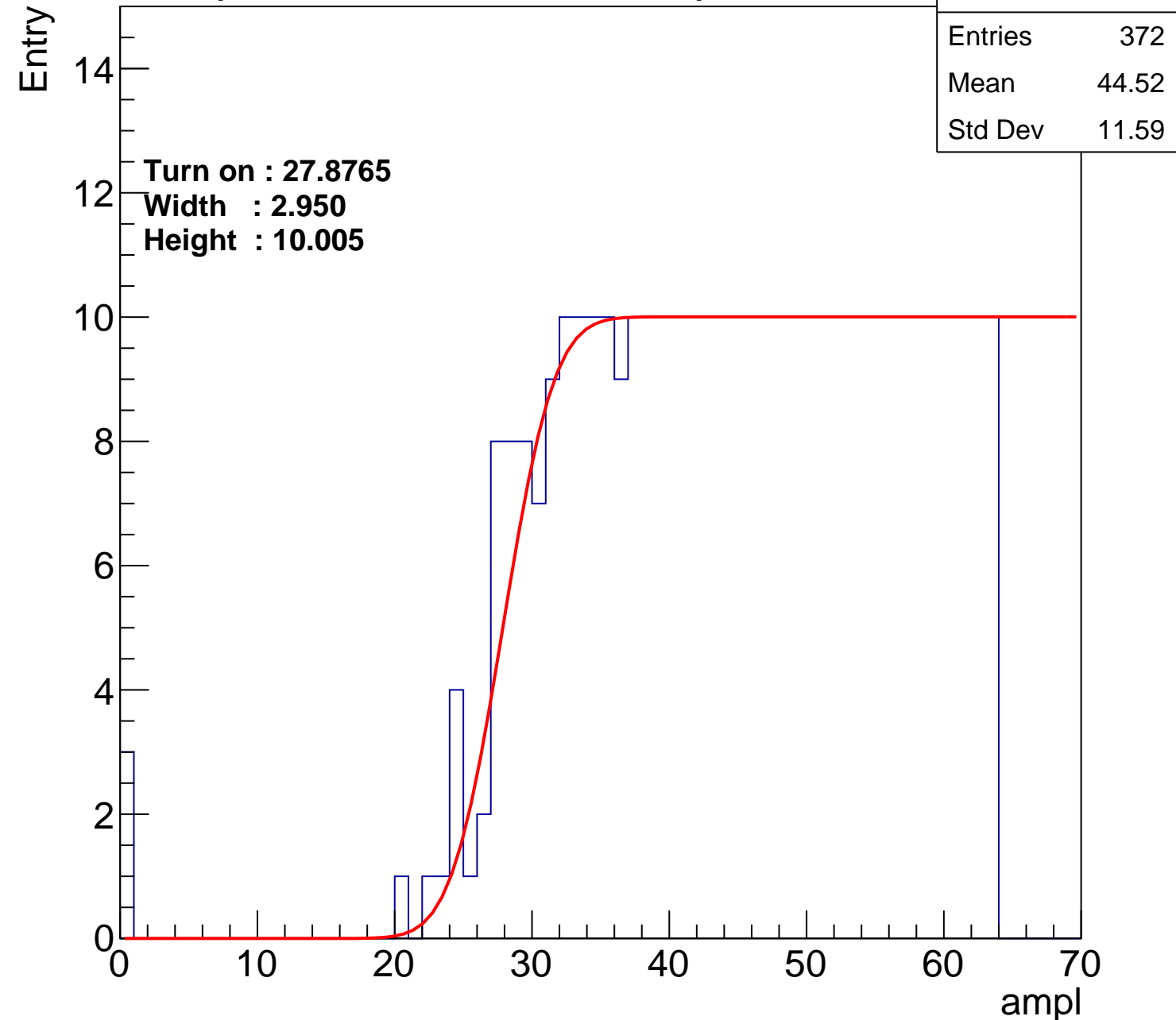
Width : 2.950

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch30

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 352 |
| Mean | 45.58 |
| Std Dev | 10.89 |

Turn on : 29.4822

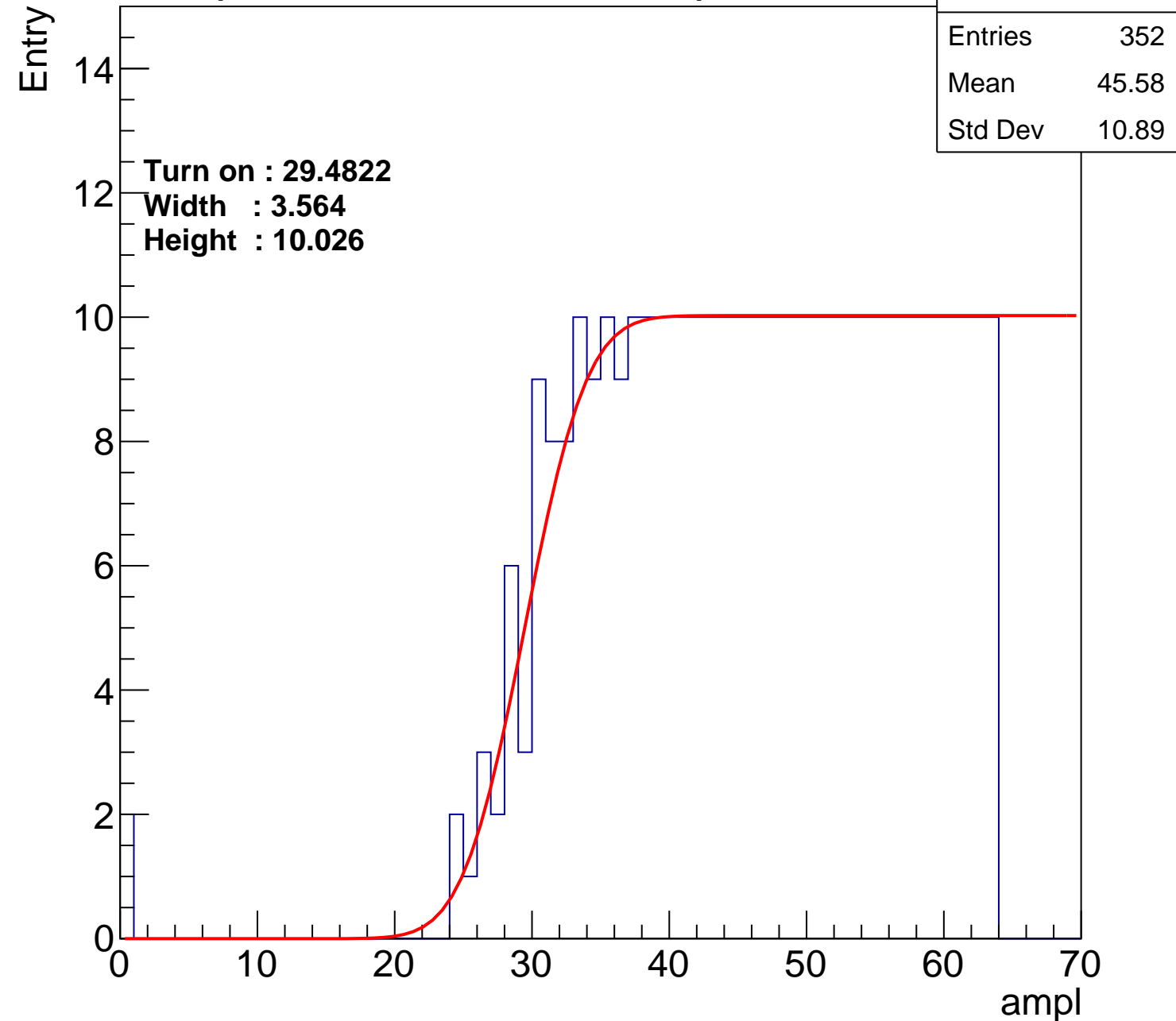
Width : 3.564

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch31

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.63 |
| Std Dev | 11.49 |

Turn on : 27.8692

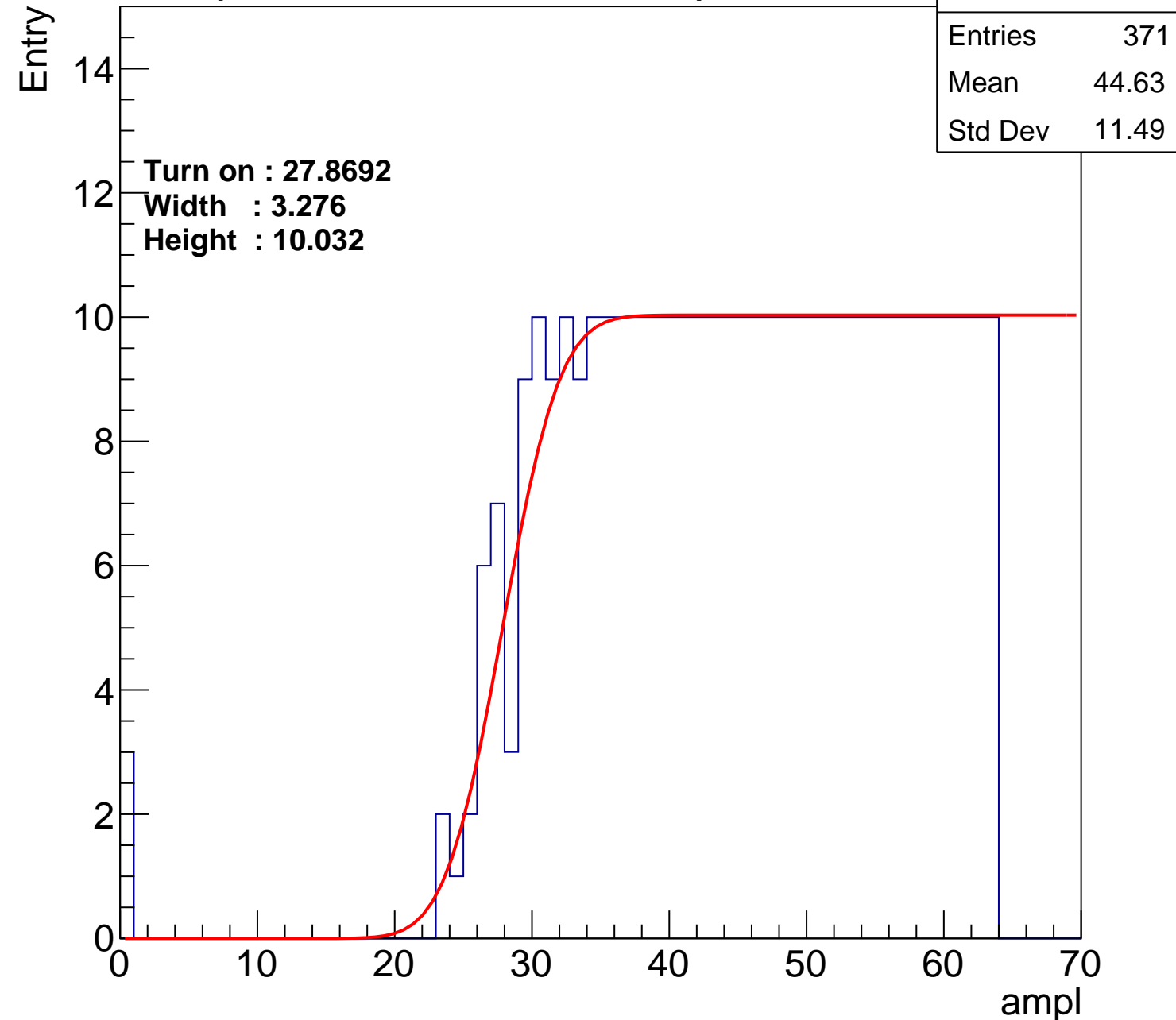
Width : 3.276

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch32

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.07 |
| Std Dev | 10.93 |

Turn on : 27.7472

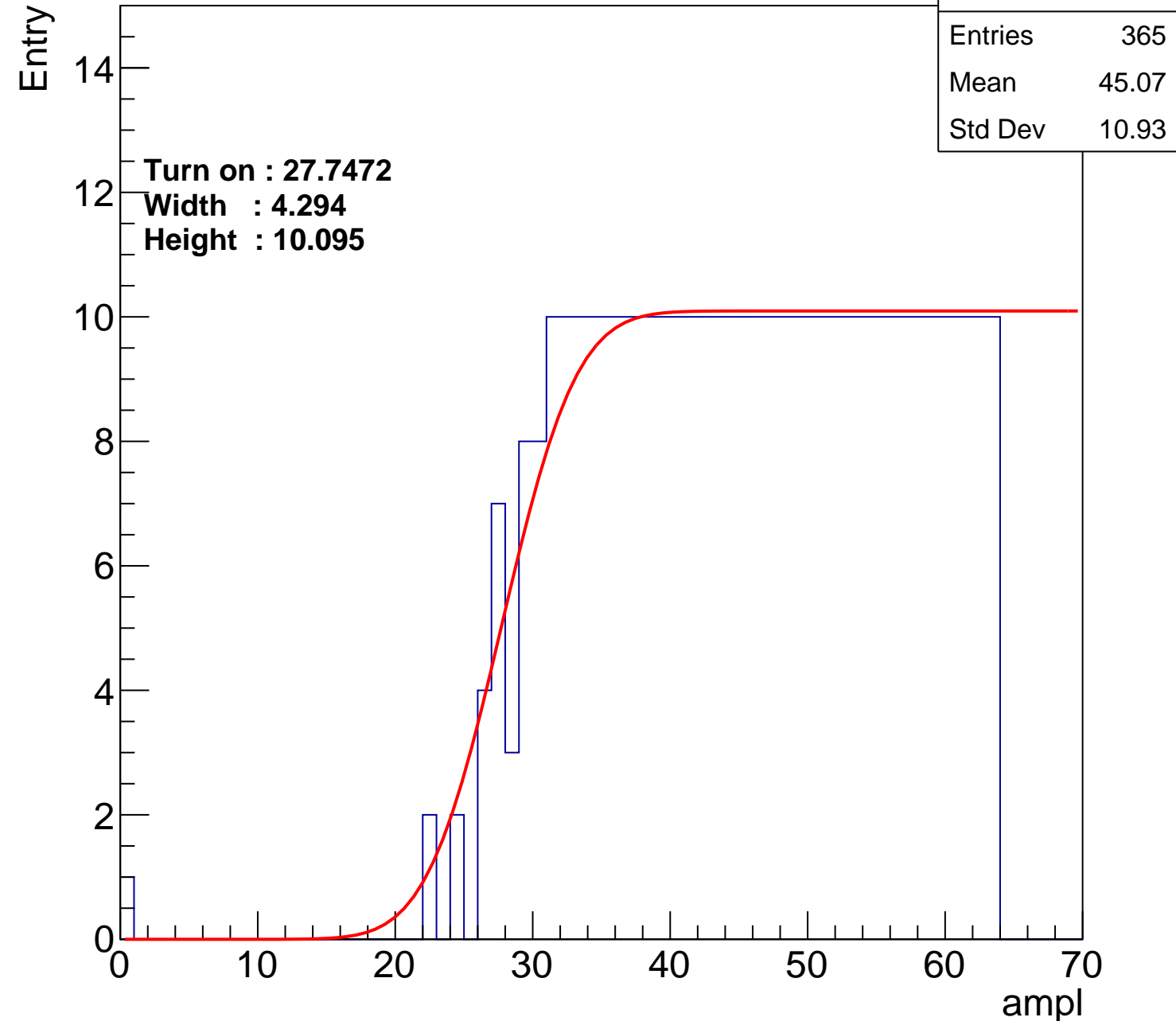
Width : 4.294

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch33

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.03 |
| Std Dev | 11.61 |

Turn on : 28.3504

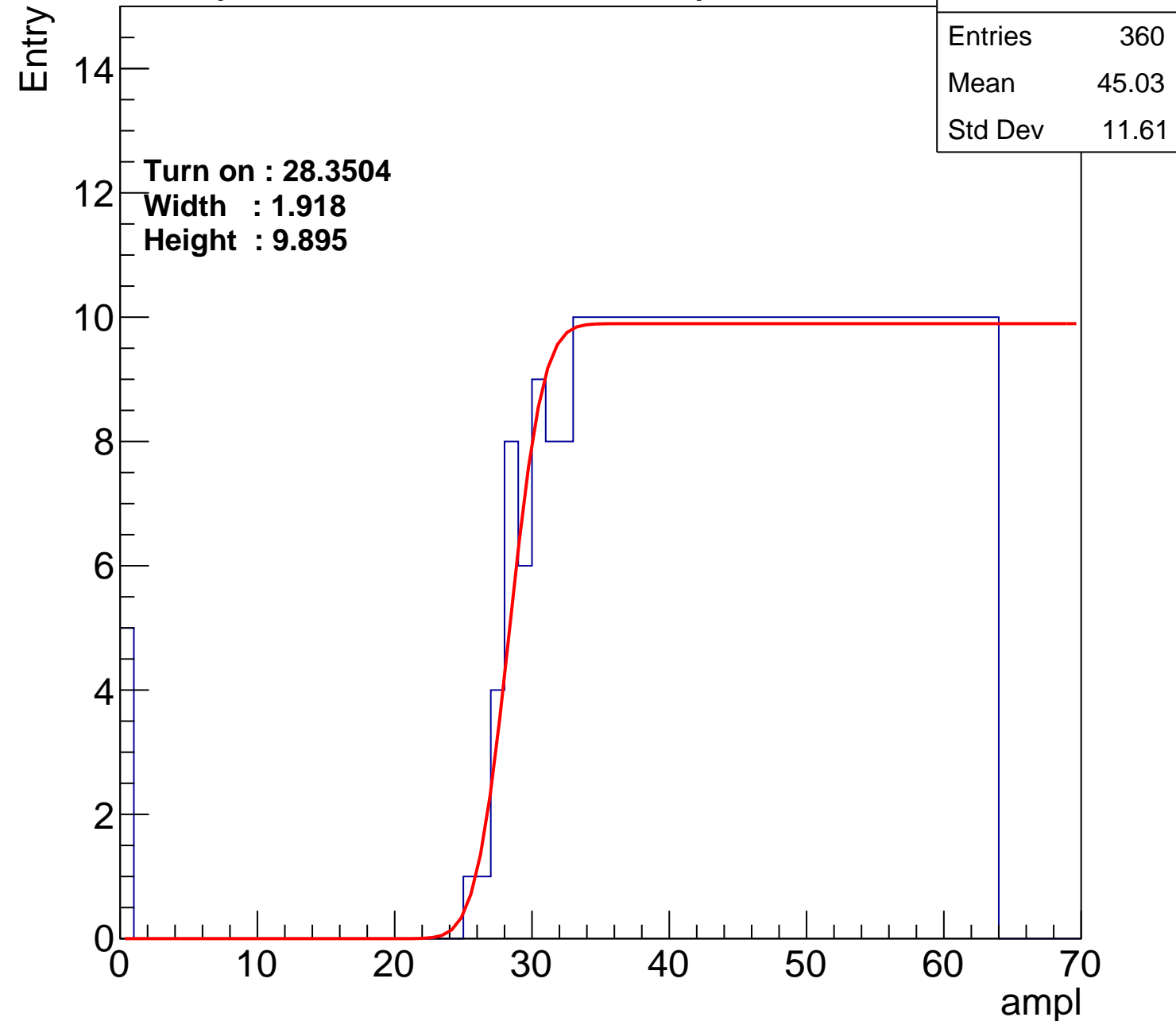
Width : 1.918

Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch34

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.65 |
| Std Dev | 10.58 |

Turn on : 28.4060

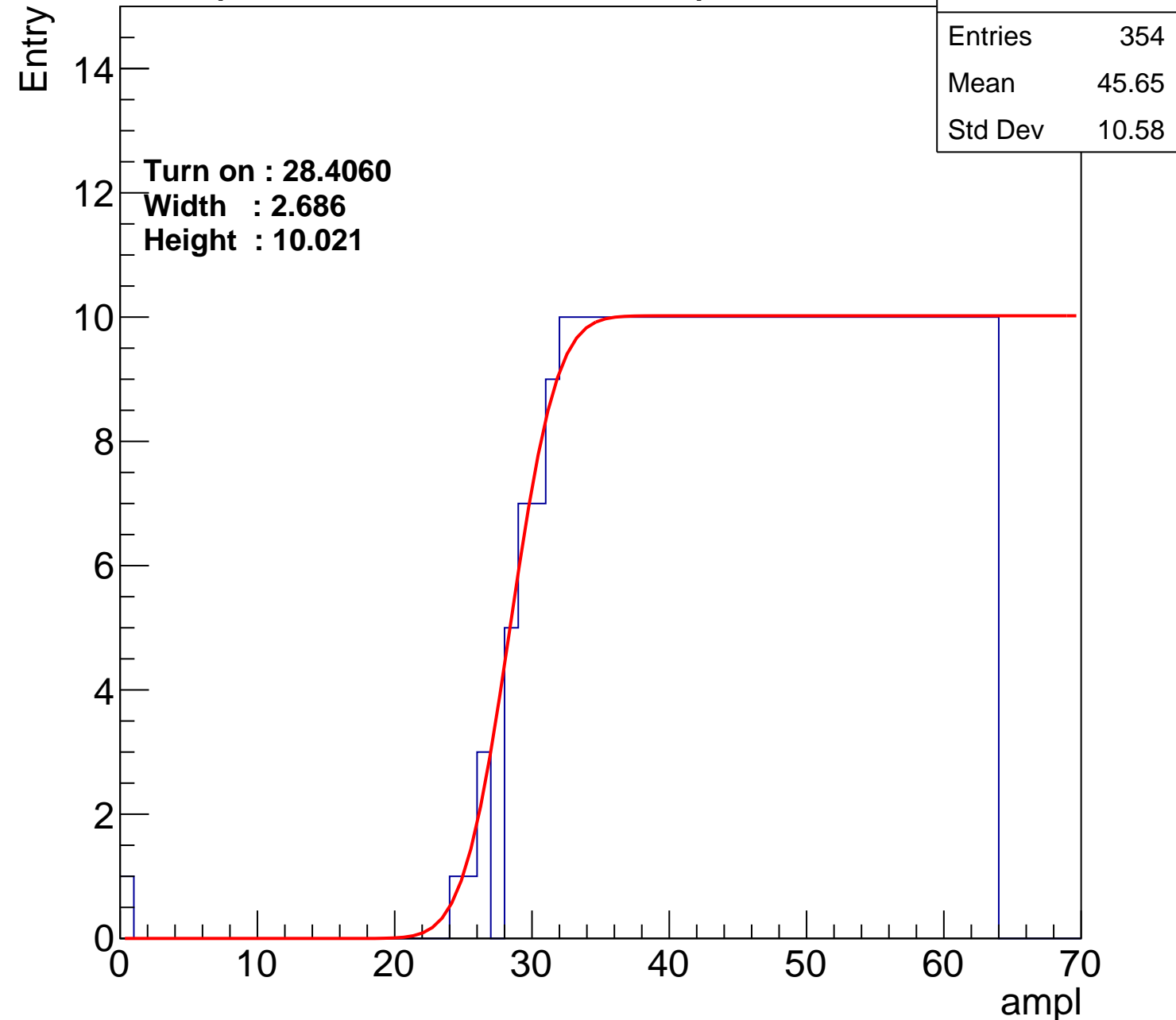
Width : 2.686

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl

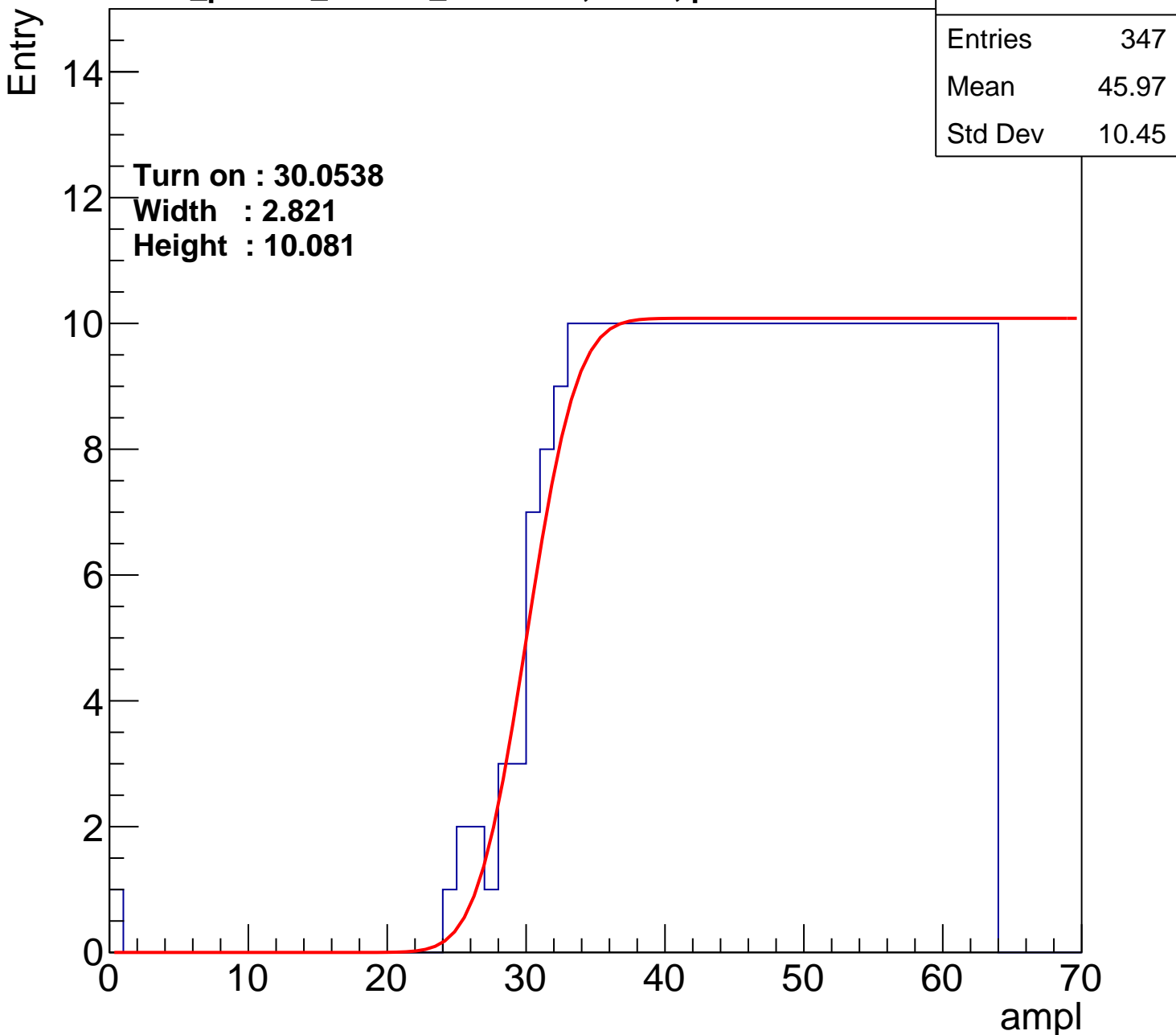


calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 347 |
| Mean | 45.97 |
| Std Dev | 10.45 |

Height : 10.081



B0L001S, U23-ch36

calib_packv5_042523_0143.root, FC#9, port A1

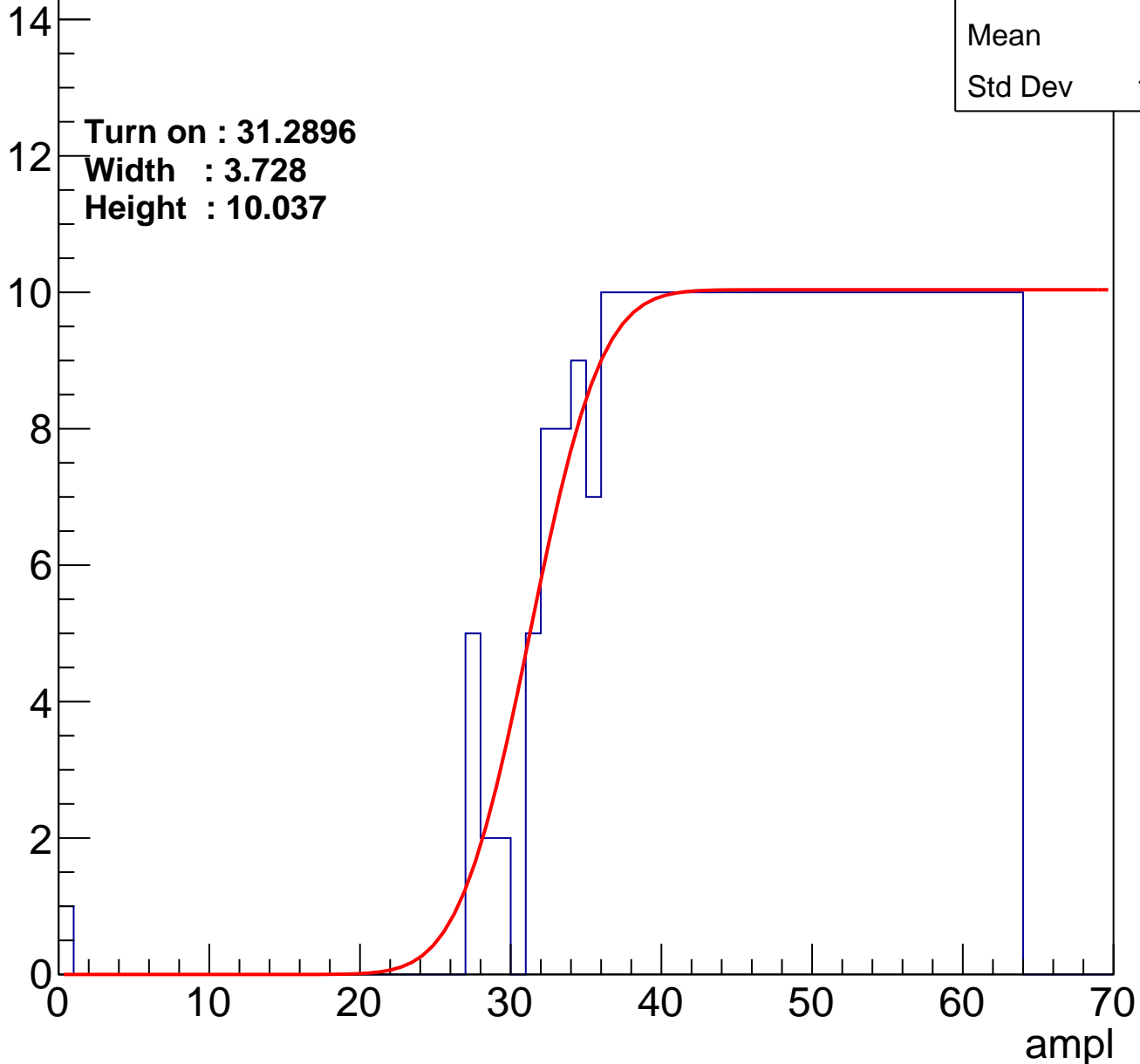
| | |
|---------|-------|
| Entries | 327 |
| Mean | 46.9 |
| Std Dev | 10.01 |

Turn on : 31.2896

Width : 3.728

Height : 10.037

Entry



B0L001S, U23-ch37

calib_packv5_042523_0143.root, FC#9, port A1

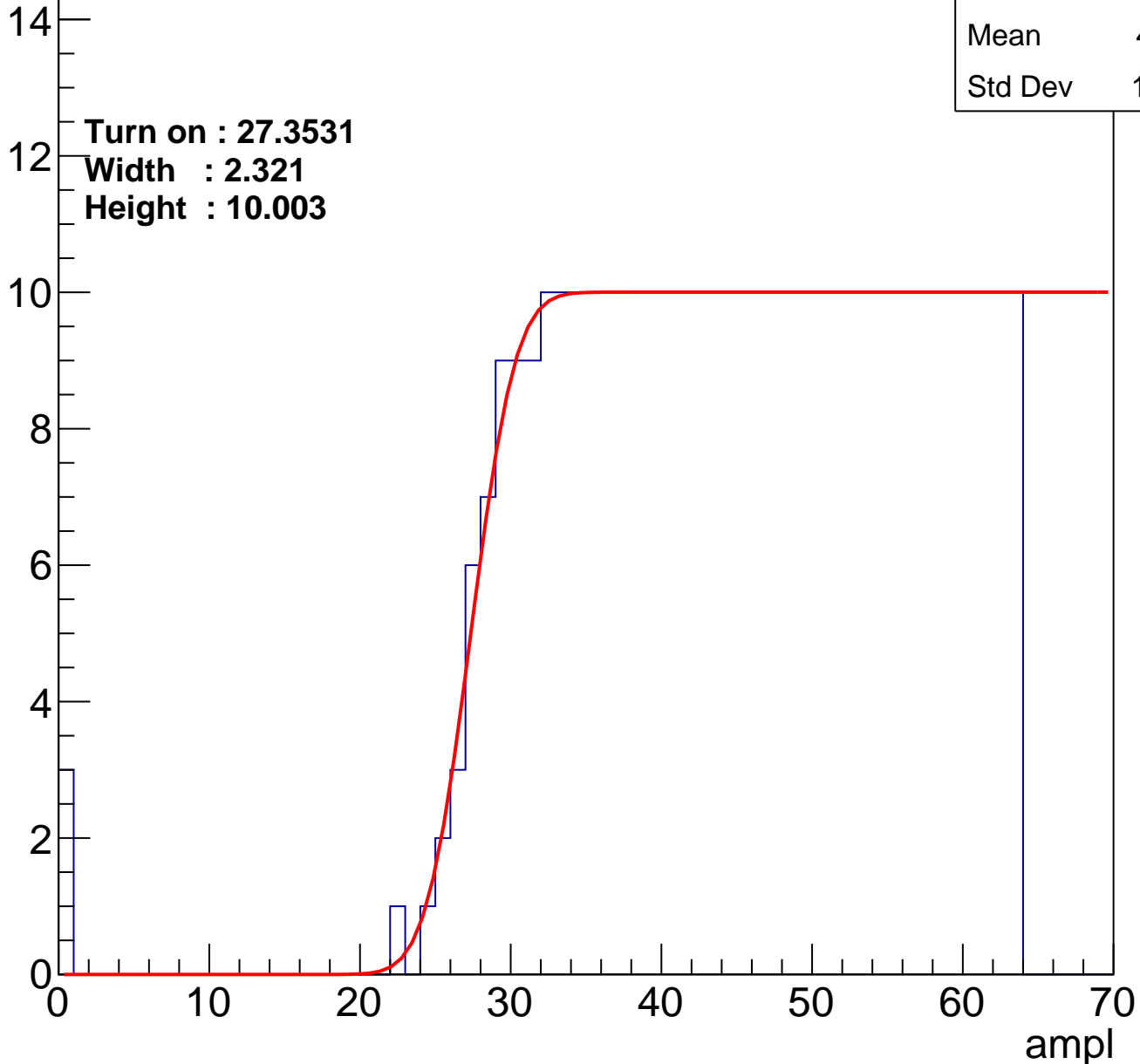
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.71 |
| Std Dev | 11.42 |

Turn on : 27.3531

Width : 2.321

Height : 10.003

Entry



B0L001S, U23-ch38

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.19 |
| Std Dev | 11.05 |

Turn on : 28.1589

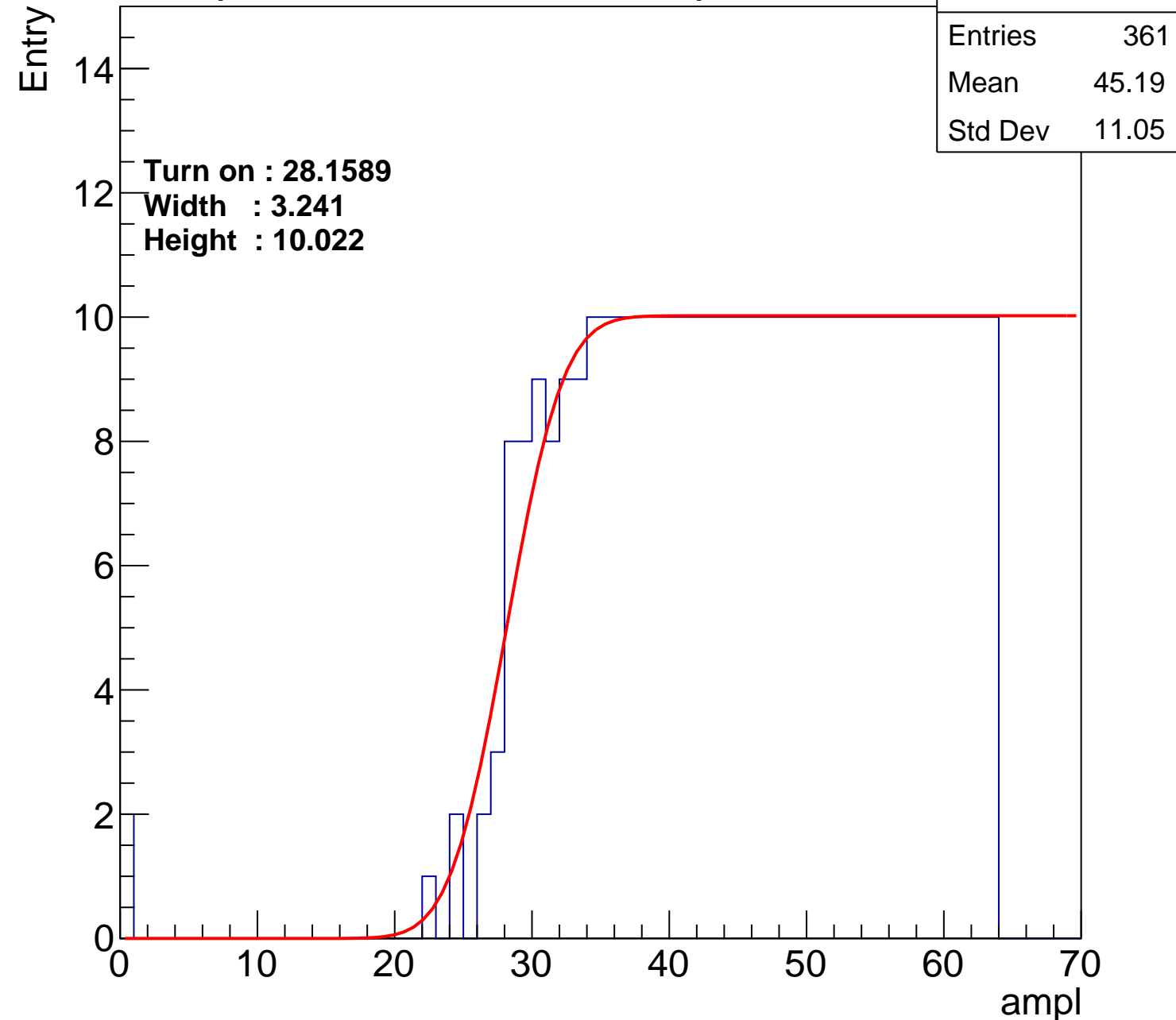
Width : 3.241

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch39

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.35 |
| Std Dev | 10.94 |

Turn on : 28.3478

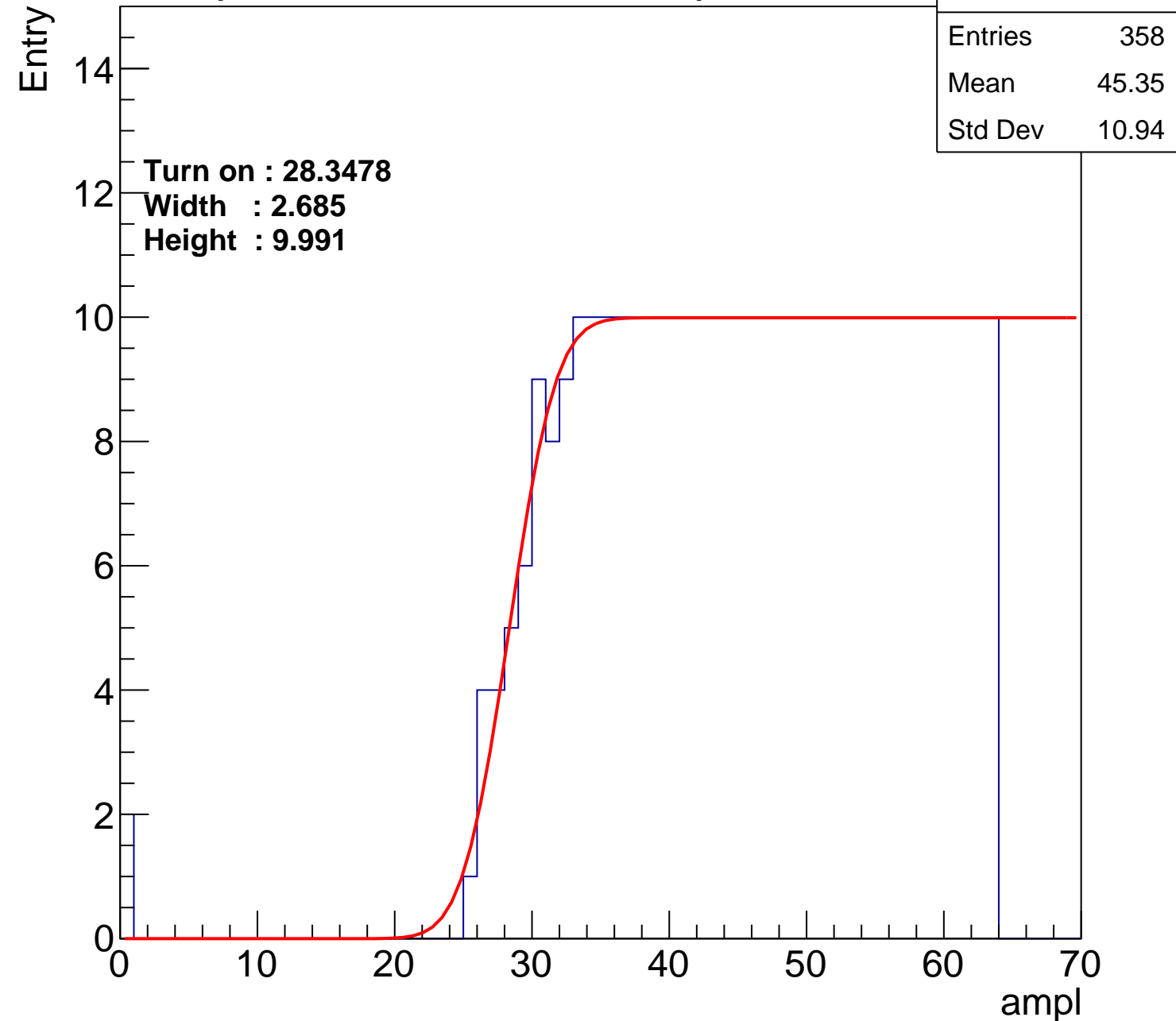
Width : 2.685

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch40

calib_packv5_042523_0143.root, FC#9, port A1

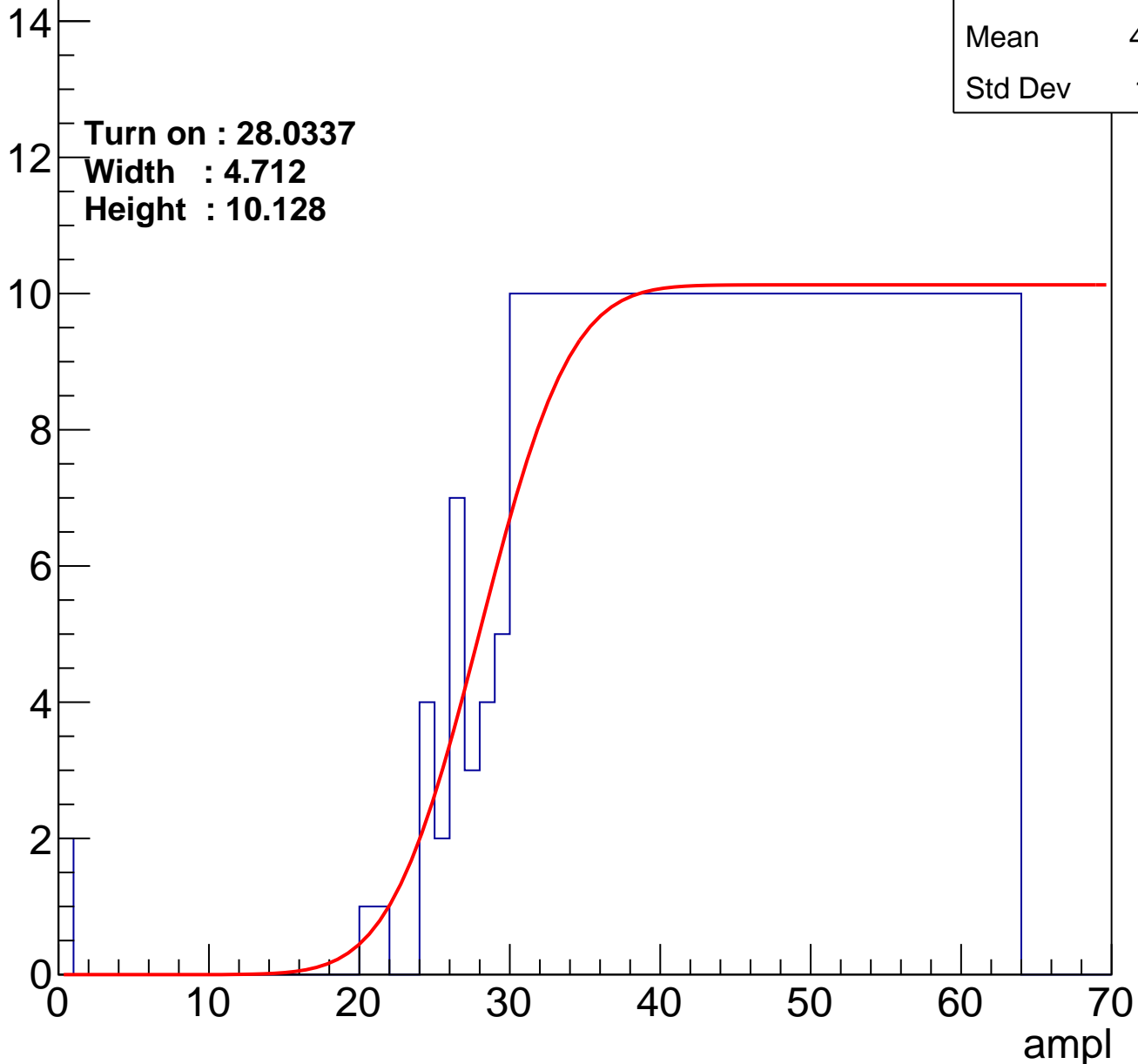
| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.76 |
| Std Dev | 11.31 |

Turn on : 28.0337

Width : 4.712

Height : 10.128

Entry



B0L001S, U23-ch41

calib_packv5_042523_0143.root, FC#9, port A1

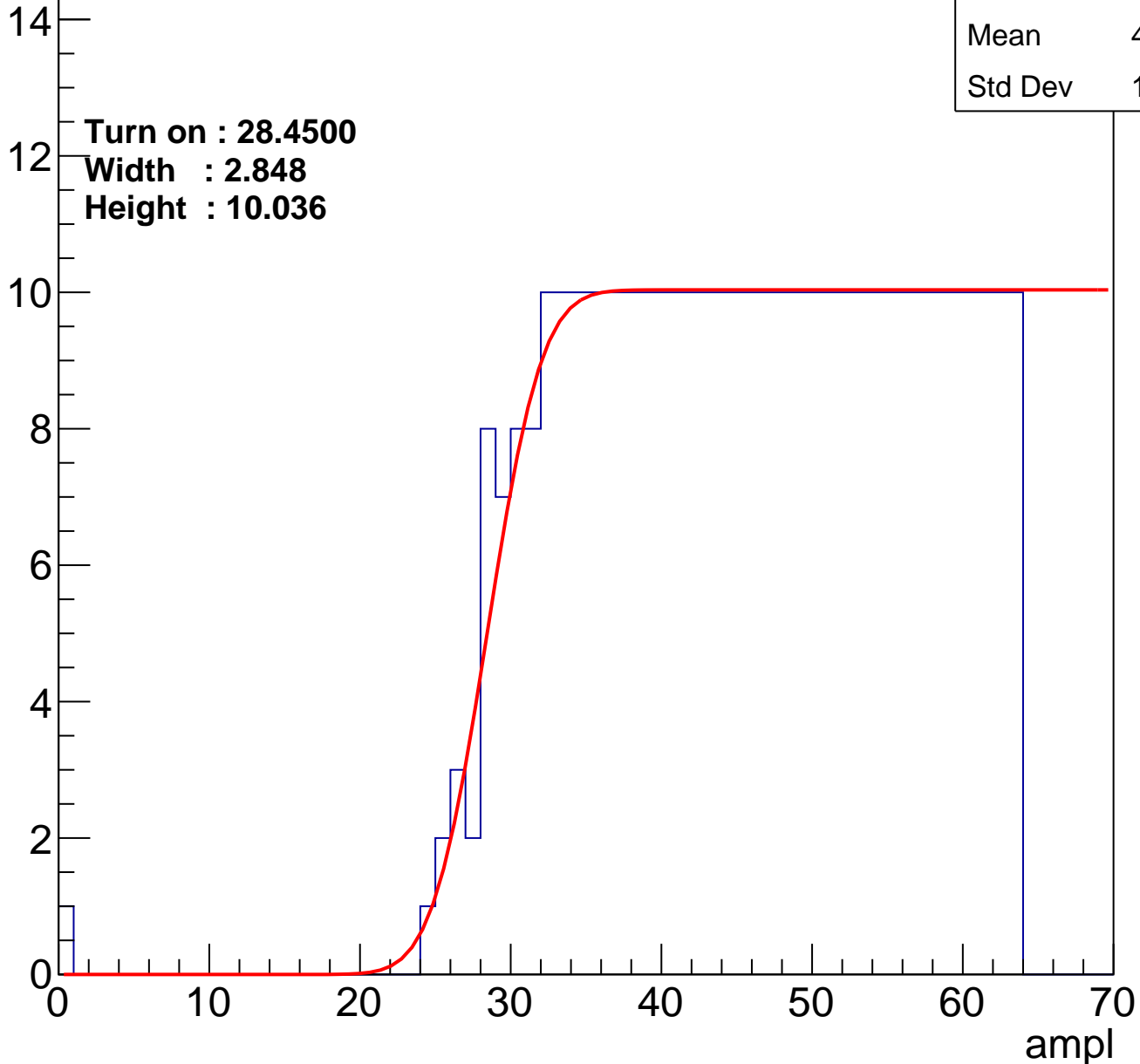
| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.34 |
| Std Dev | 10.76 |

Turn on : 28.4500

Width : 2.848

Height : 10.036

Entry



B0L001S, U23-ch42

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.25 |
| Std Dev | 10.85 |

Turn on : 28.2926

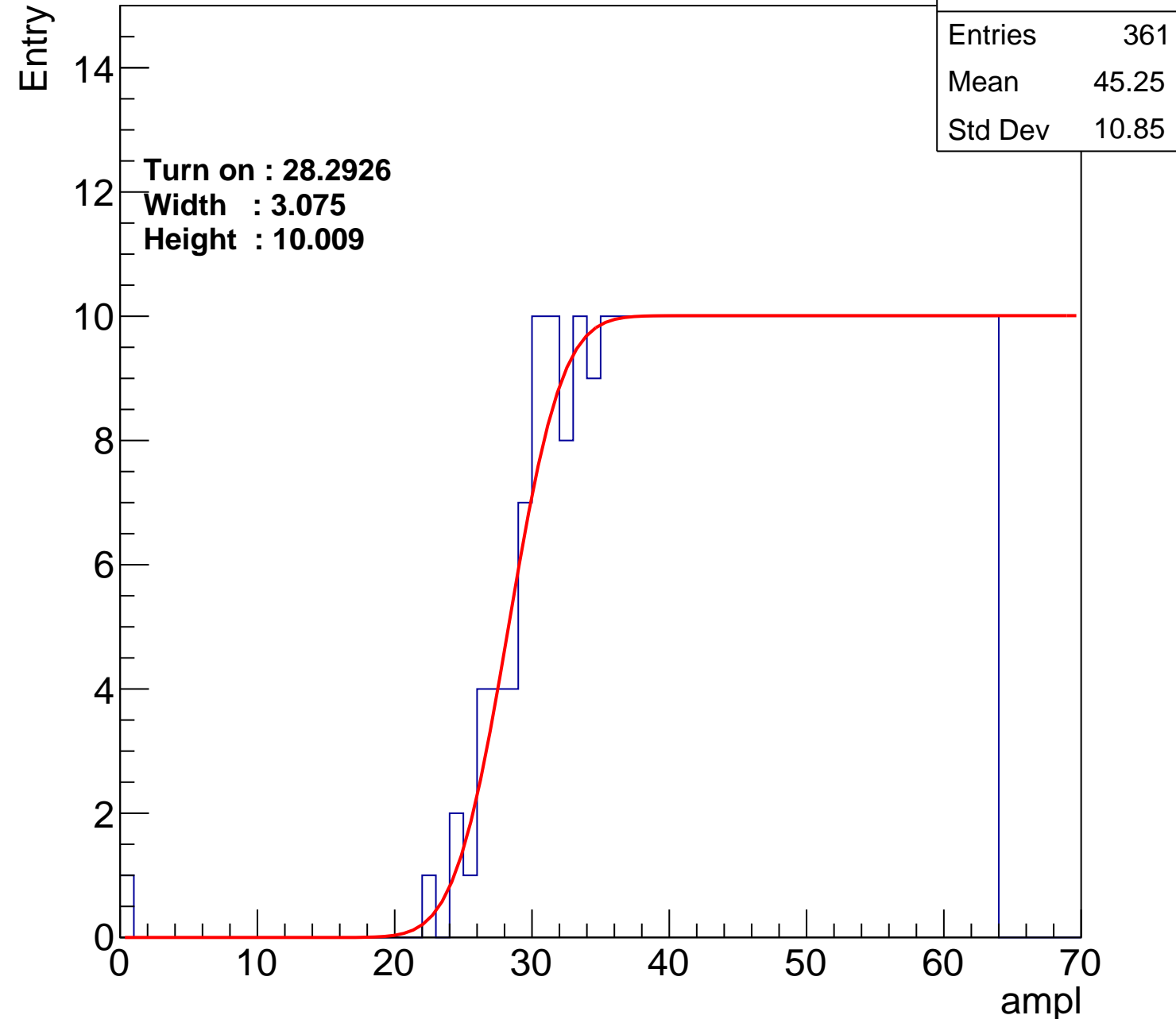
Width : 3.075

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch43

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 344 |
| Mean | 45.93 |
| Std Dev | 10.87 |

Turn on : 29.8149

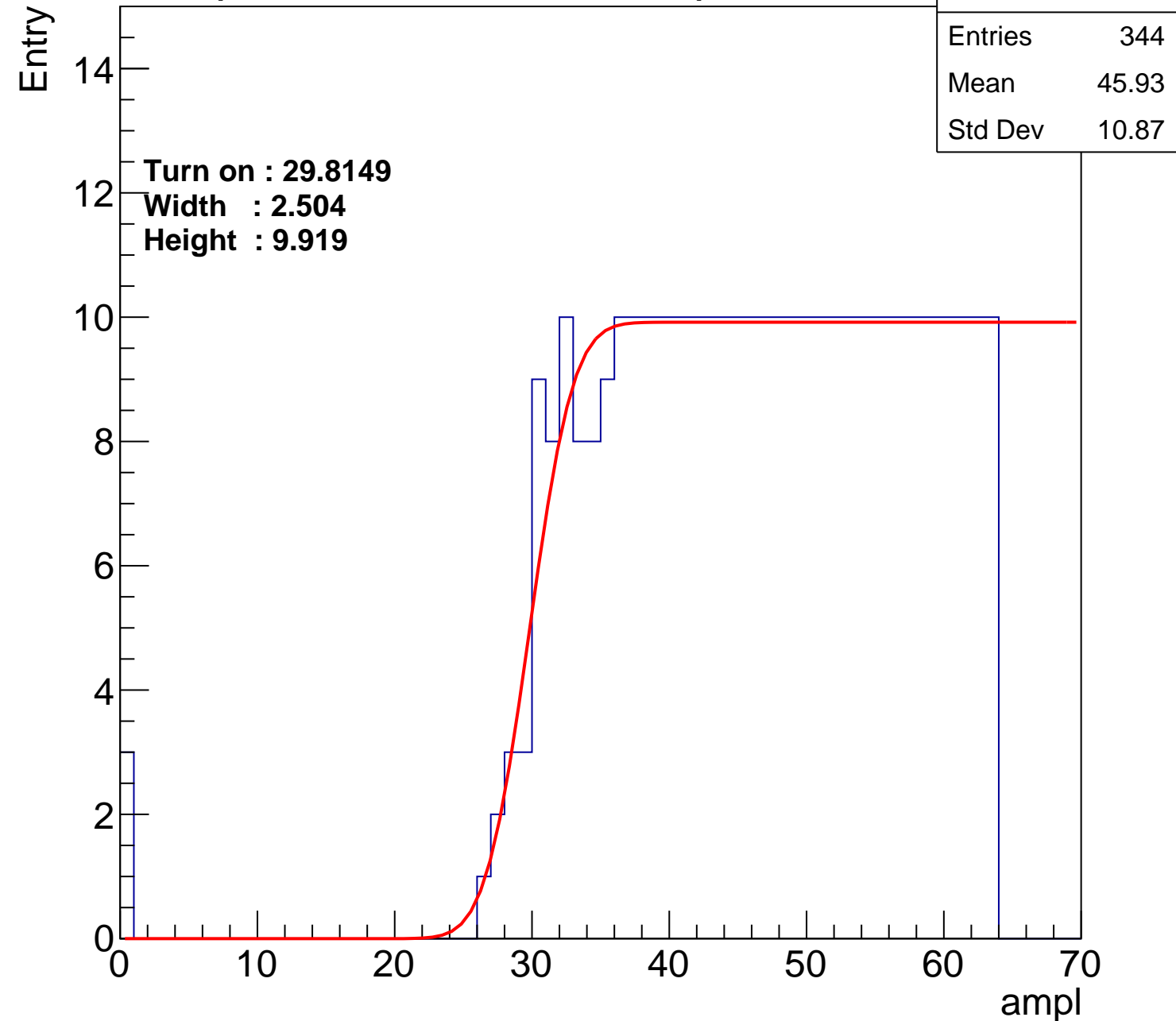
Width : 2.504

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch44

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.75 |
| Std Dev | 11.41 |

Turn on : 27.4638

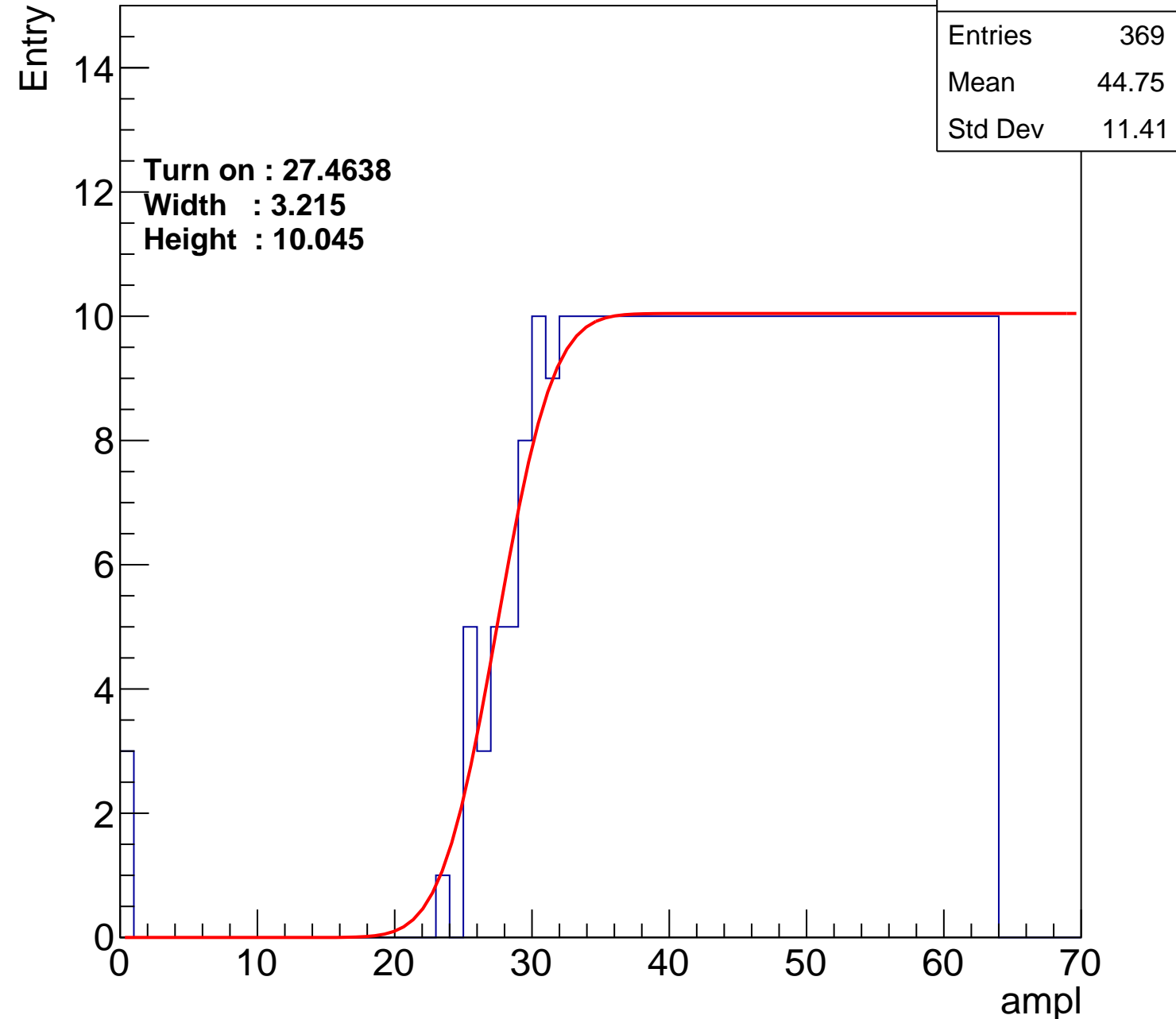
Width : 3.215

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch45

calib_packv5_042523_0143.root, FC#9, port A1

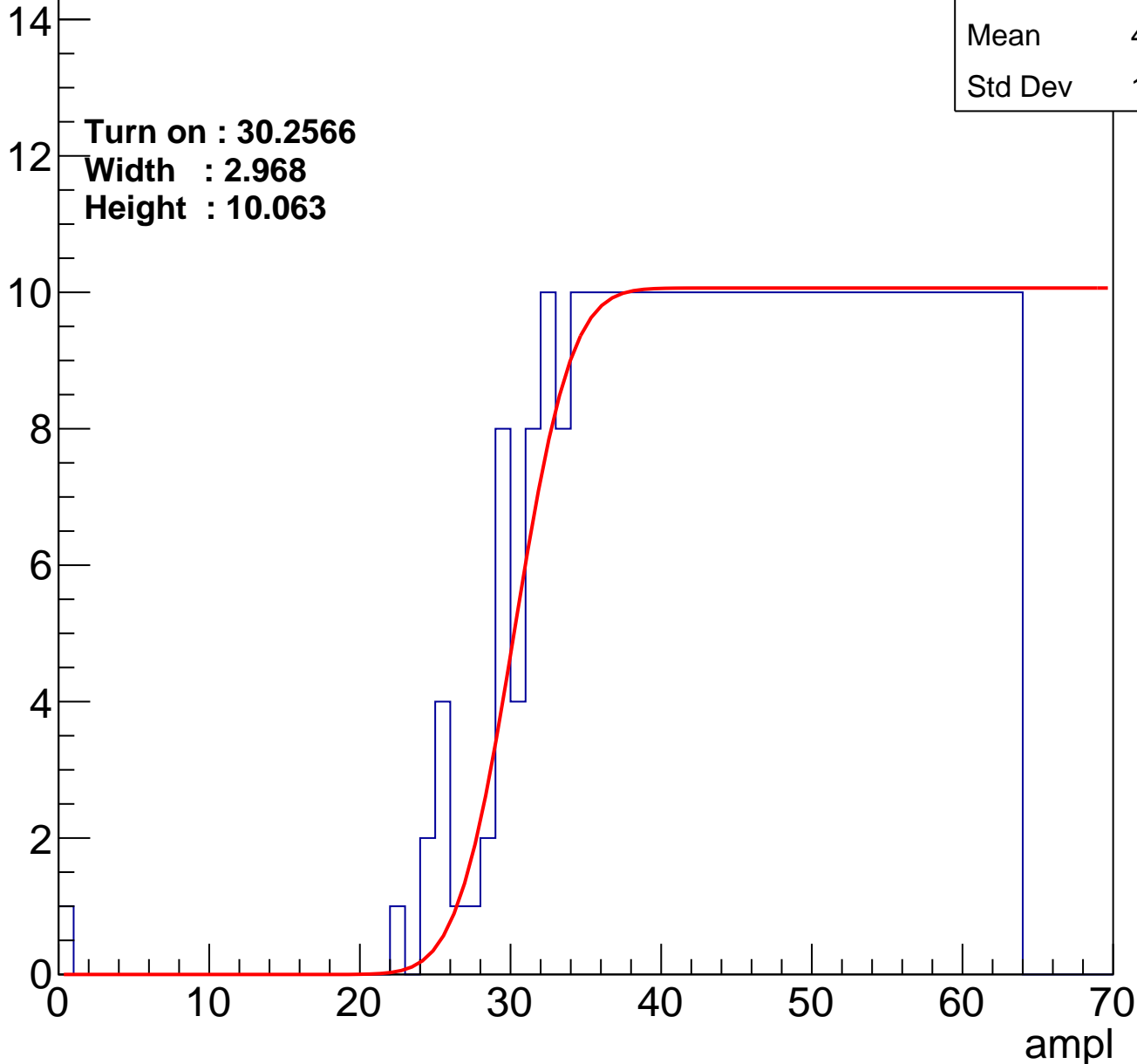
| | |
|---------|-------|
| Entries | 350 |
| Mean | 45.75 |
| Std Dev | 10.64 |

Turn on : 30.2566

Width : 2.968

Height : 10.063

Entry



B0L001S, U23-ch46

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.22 |
| Std Dev | 10.99 |

Turn on : 28.4461

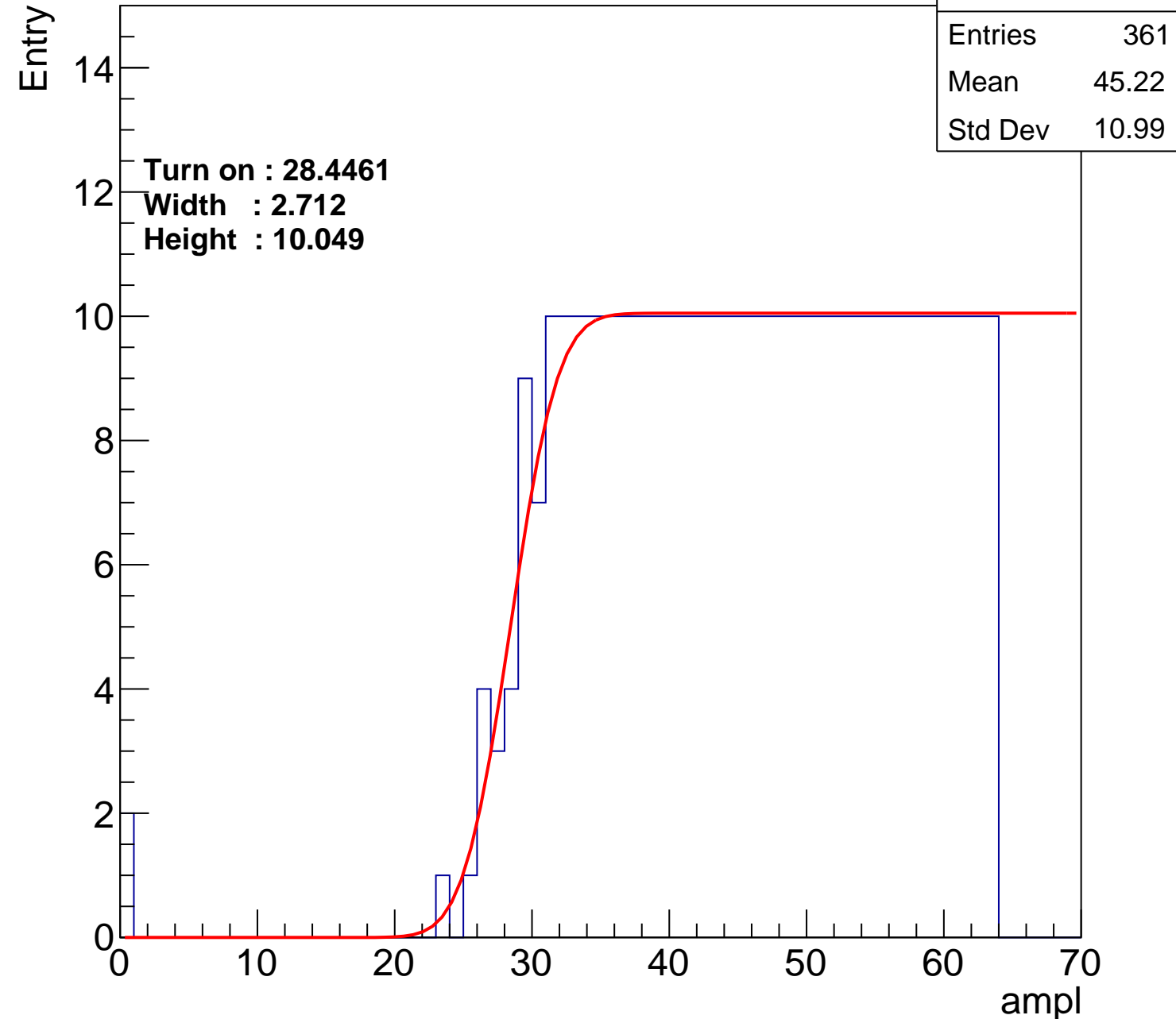
Width : 2.712

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch47

calib_packv5_042523_0143.root, FC#9, port A1

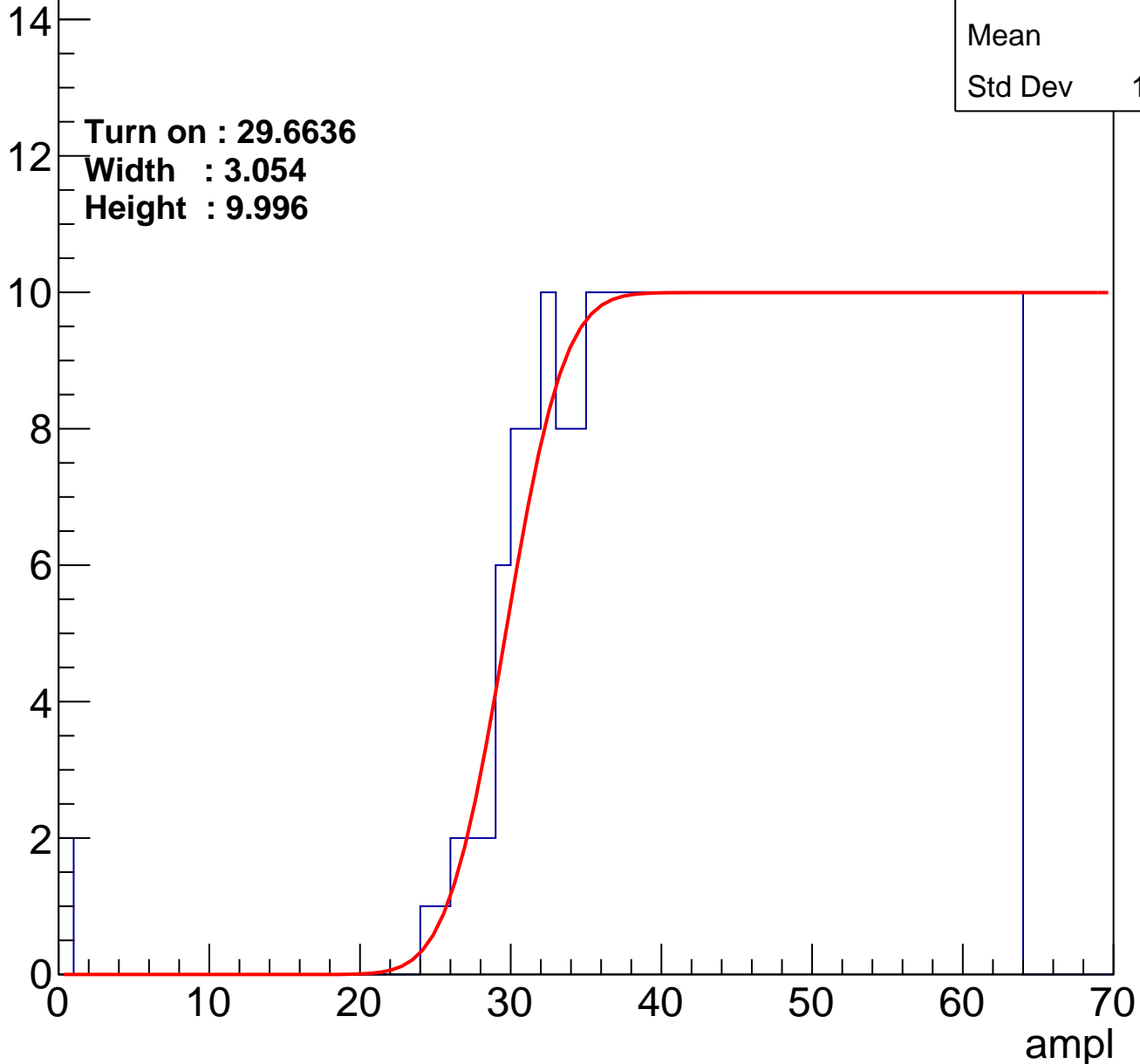
| | |
|---------|-------|
| Entries | 348 |
| Mean | 45.8 |
| Std Dev | 10.76 |

Turn on : 29.6636

Width : 3.054

Height : 9.996

Entry



B0L001S, U23-ch48

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.76 |
| Std Dev | 11.24 |

Turn on : 26.7941

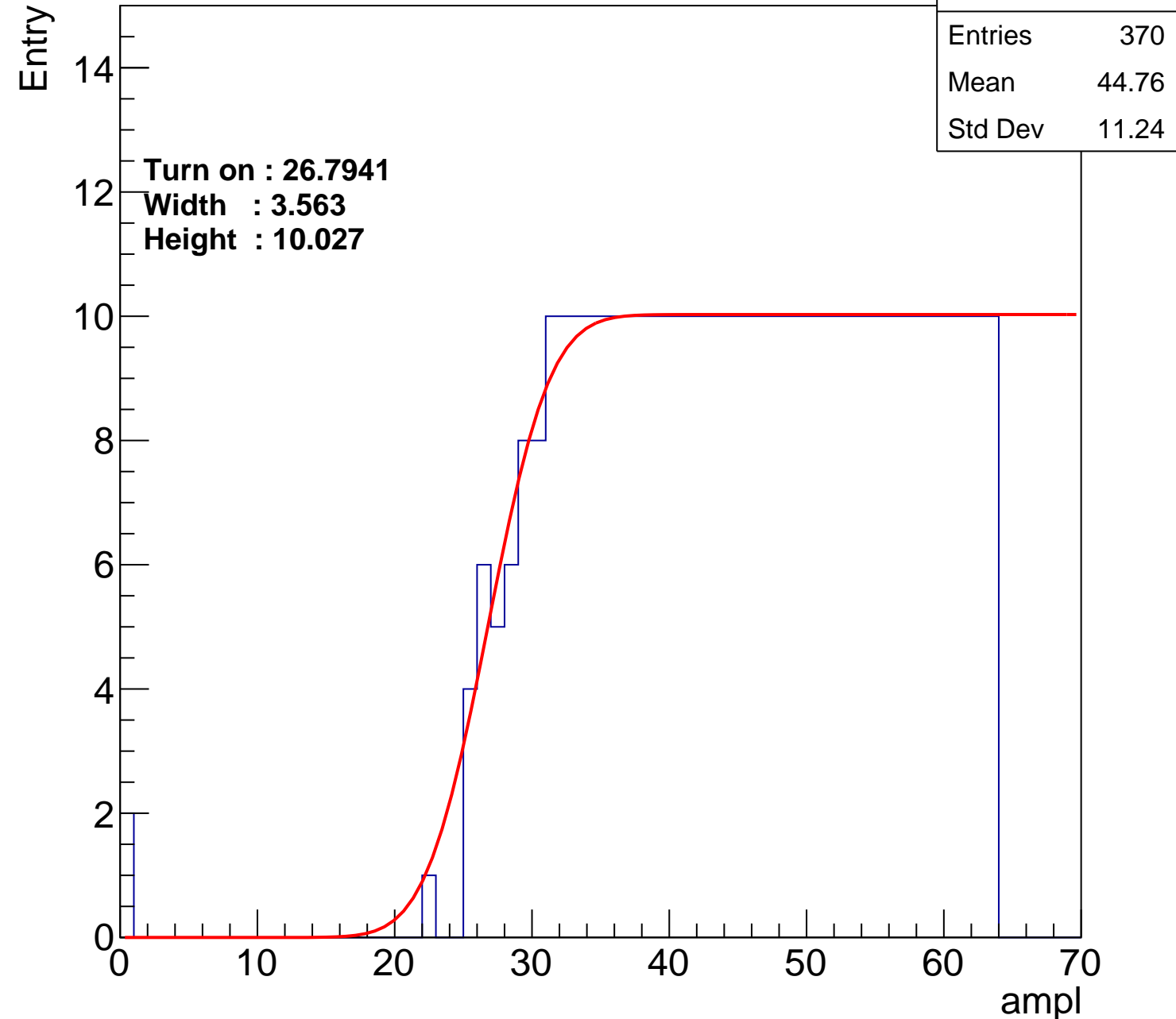
Width : 3.563

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch49

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.51 |
| Std Dev | 11.71 |

Turn on : 27.4856

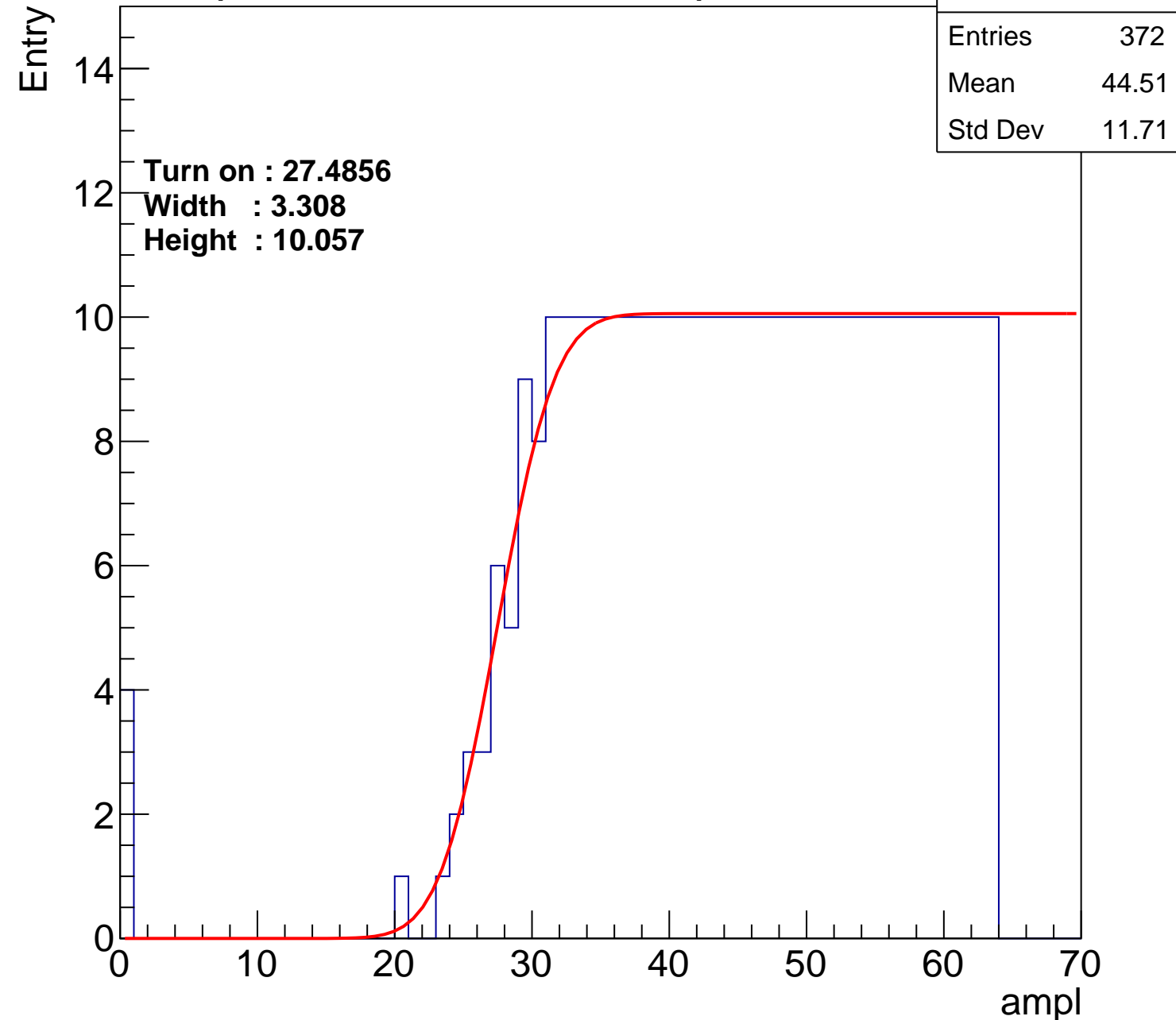
Width : 3.308

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch50

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.04 |
| Std Dev | 10.93 |

Turn on : 27.6580

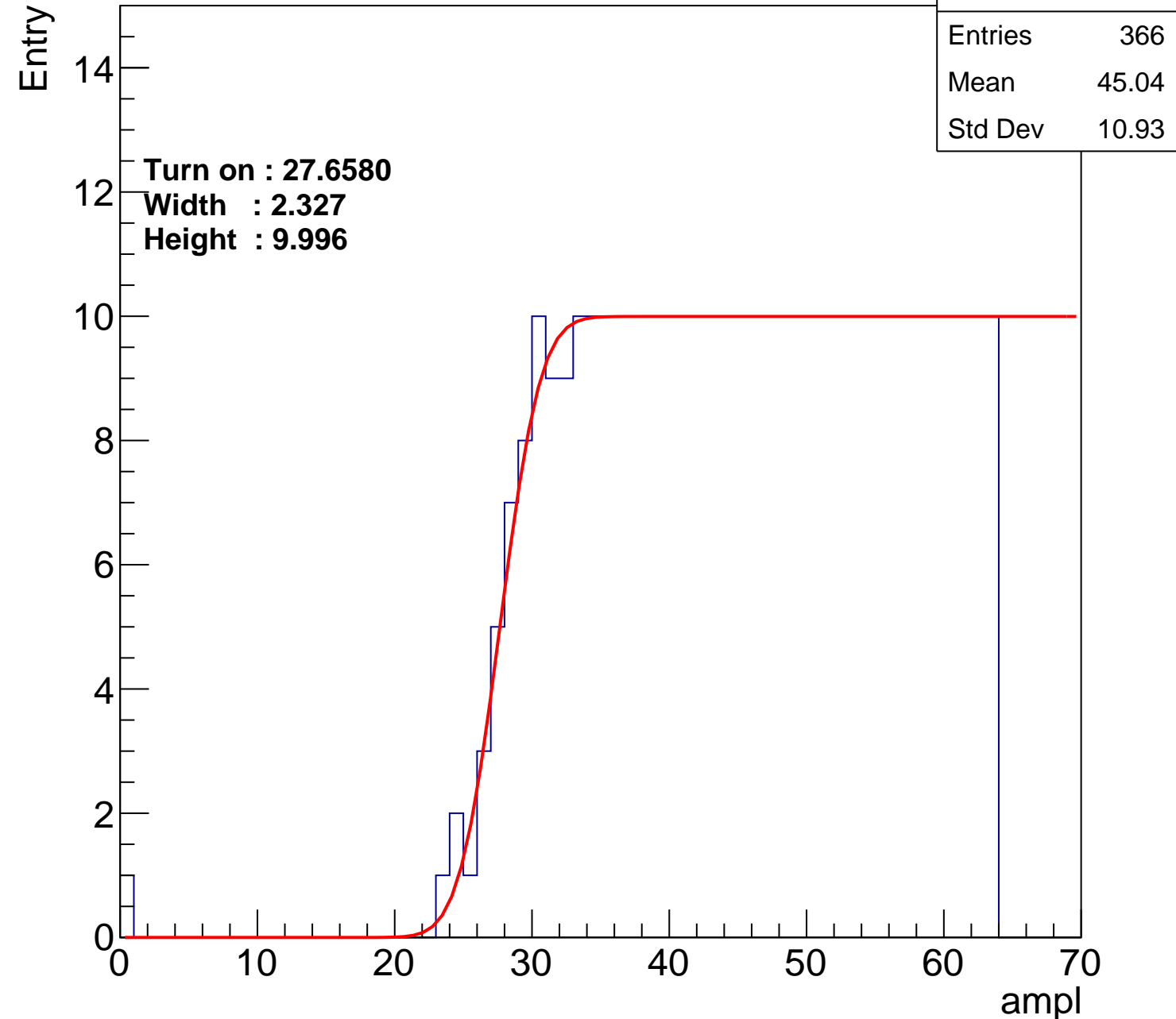
Width : 2.327

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch51

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 348 |
| Mean | 45.69 |
| Std Dev | 11.14 |

Turn on : 29.3775

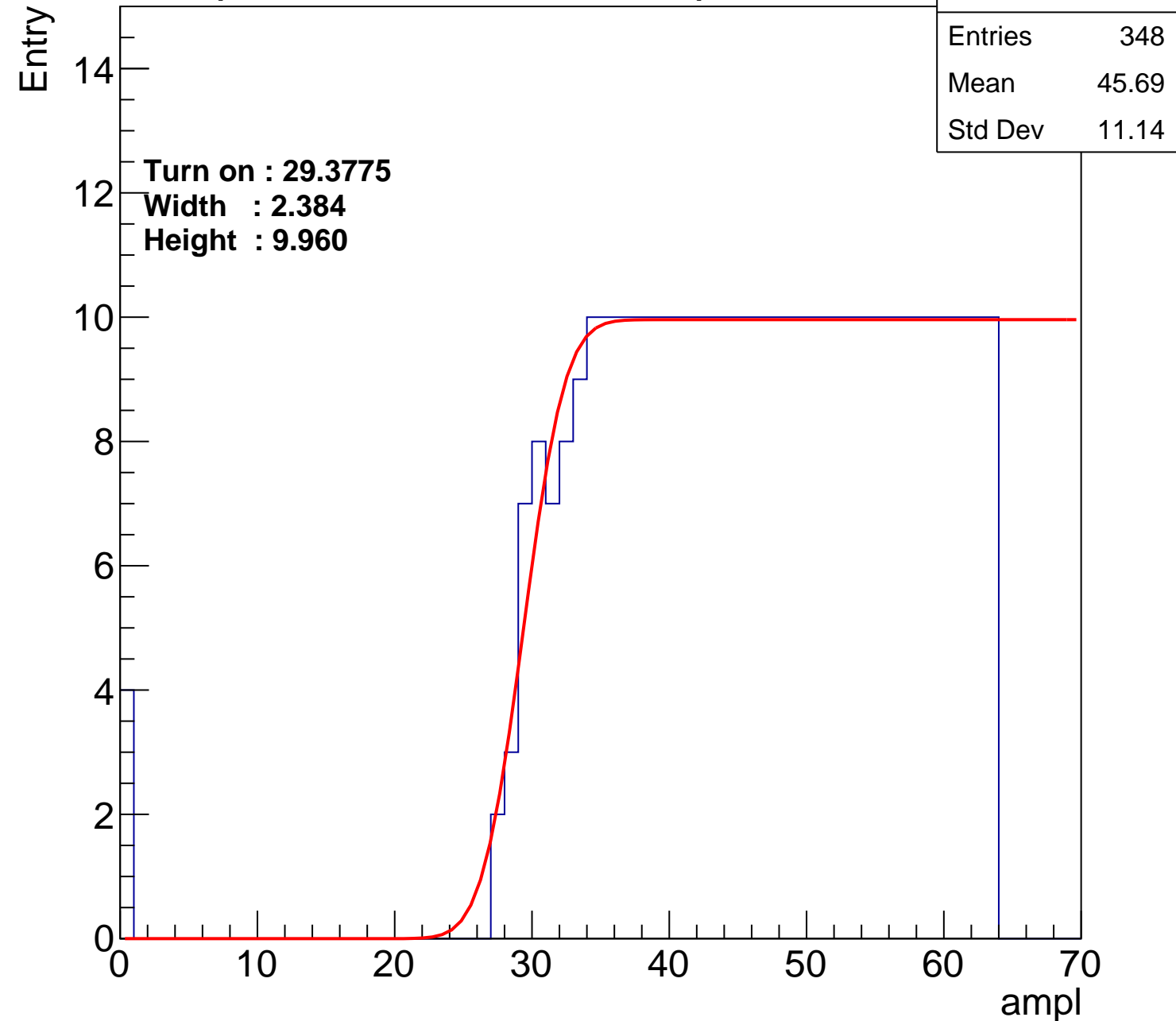
Width : 2.384

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch52

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.41 |
| Std Dev | 11.31 |

Turn on : 26.6333

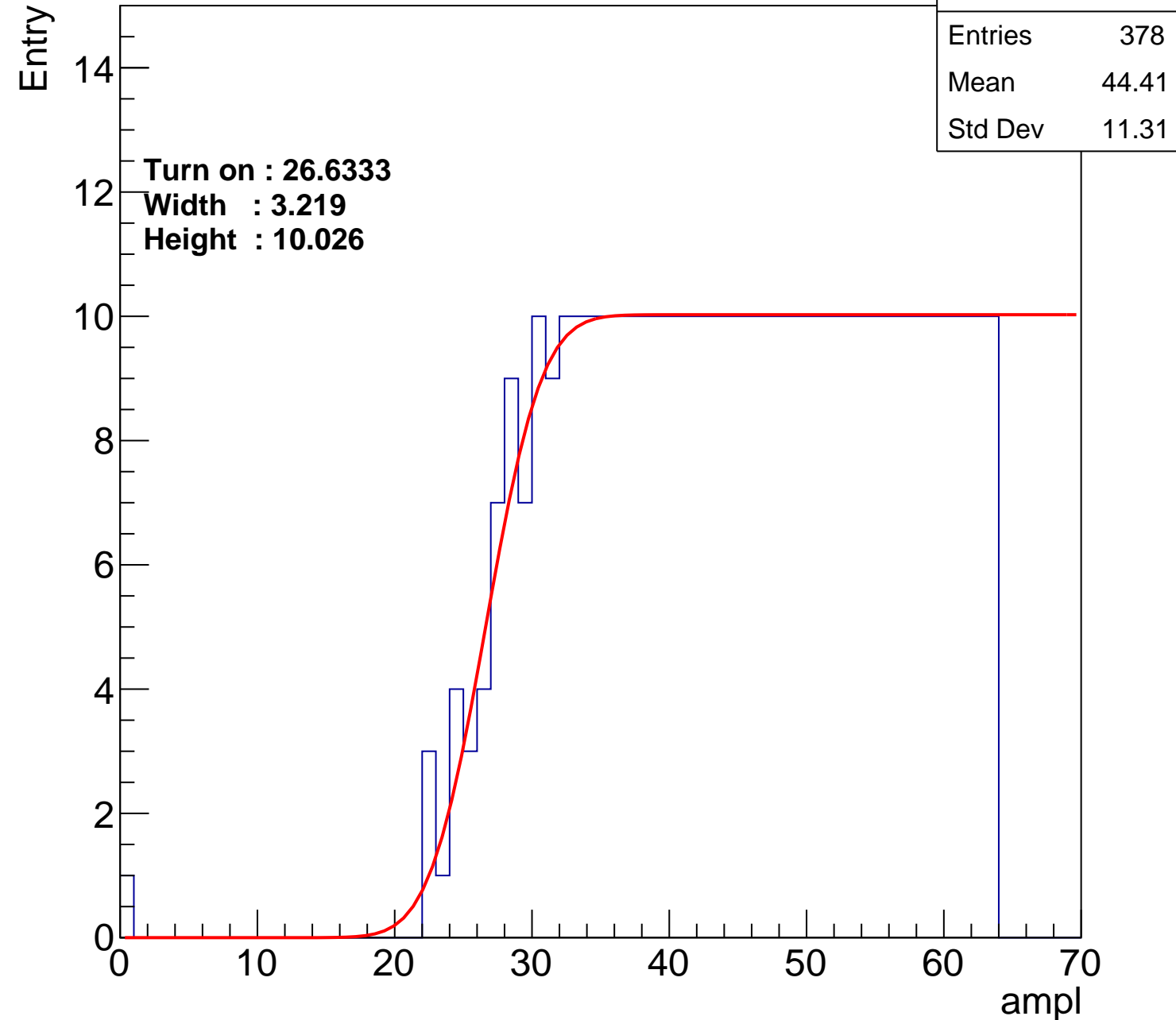
Width : 3.219

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch53

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.24 |
| Std Dev | 11.17 |

Turn on : 28.8753

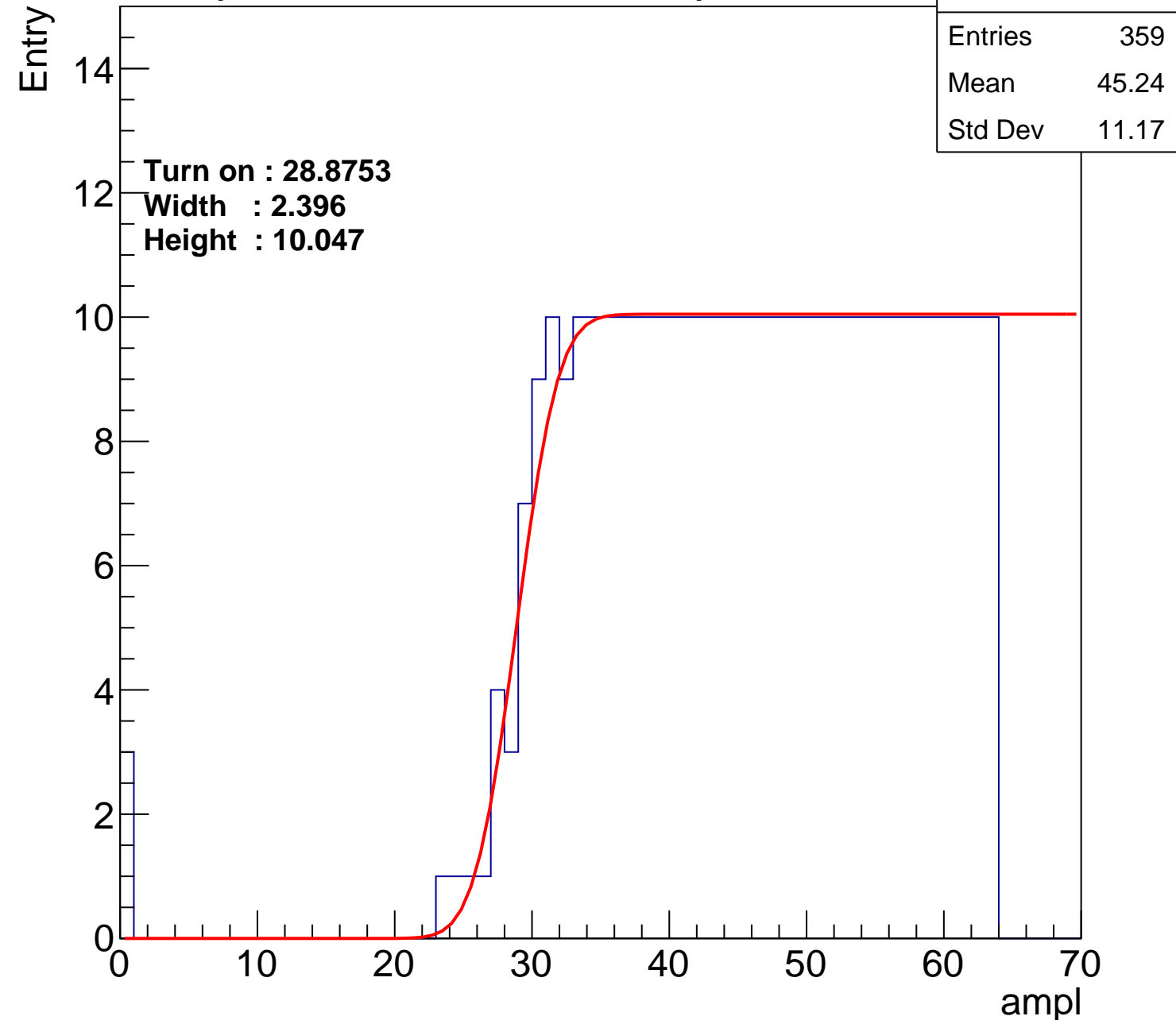
Width : 2.396

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch54

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 44.99 |
| Std Dev | 11.53 |

Turn on : 28.3364

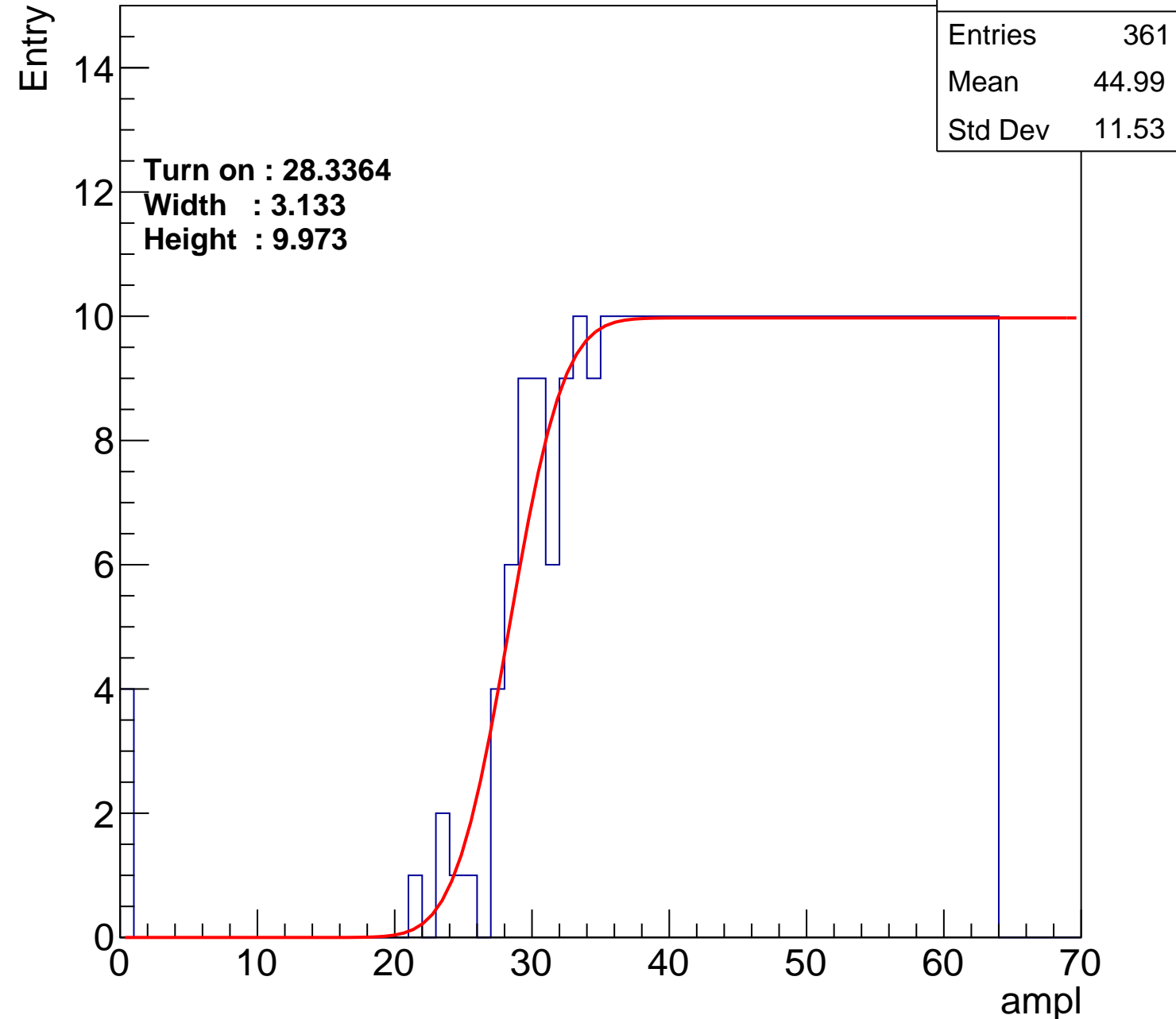
Width : 3.133

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch55

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.18 |
| Std Dev | 11.84 |

Turn on : 26.3463

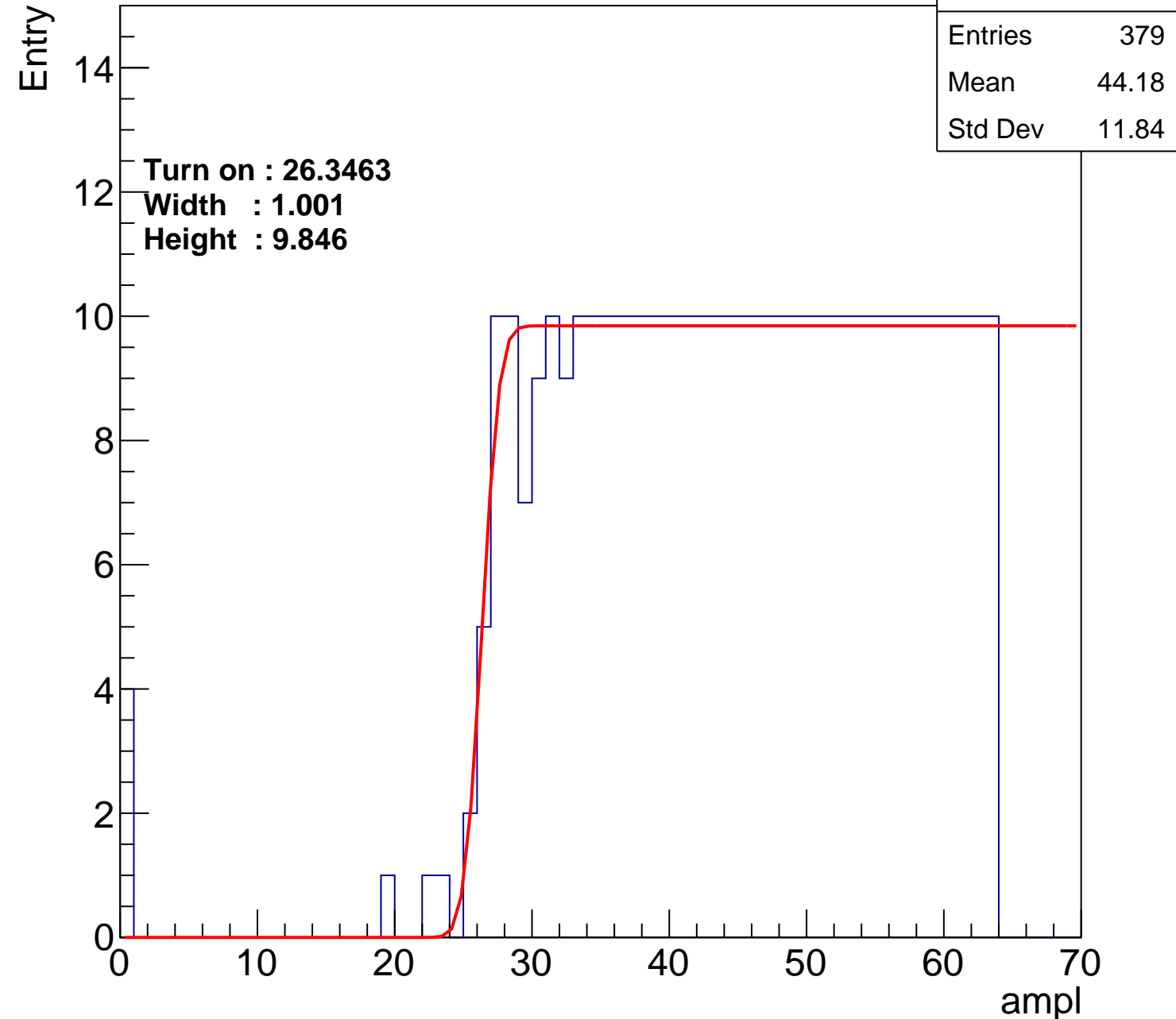
Width : 1.001

Height : 9.846

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch56

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.49 |
| Std Dev | 10.67 |

Turn on : 27.9202

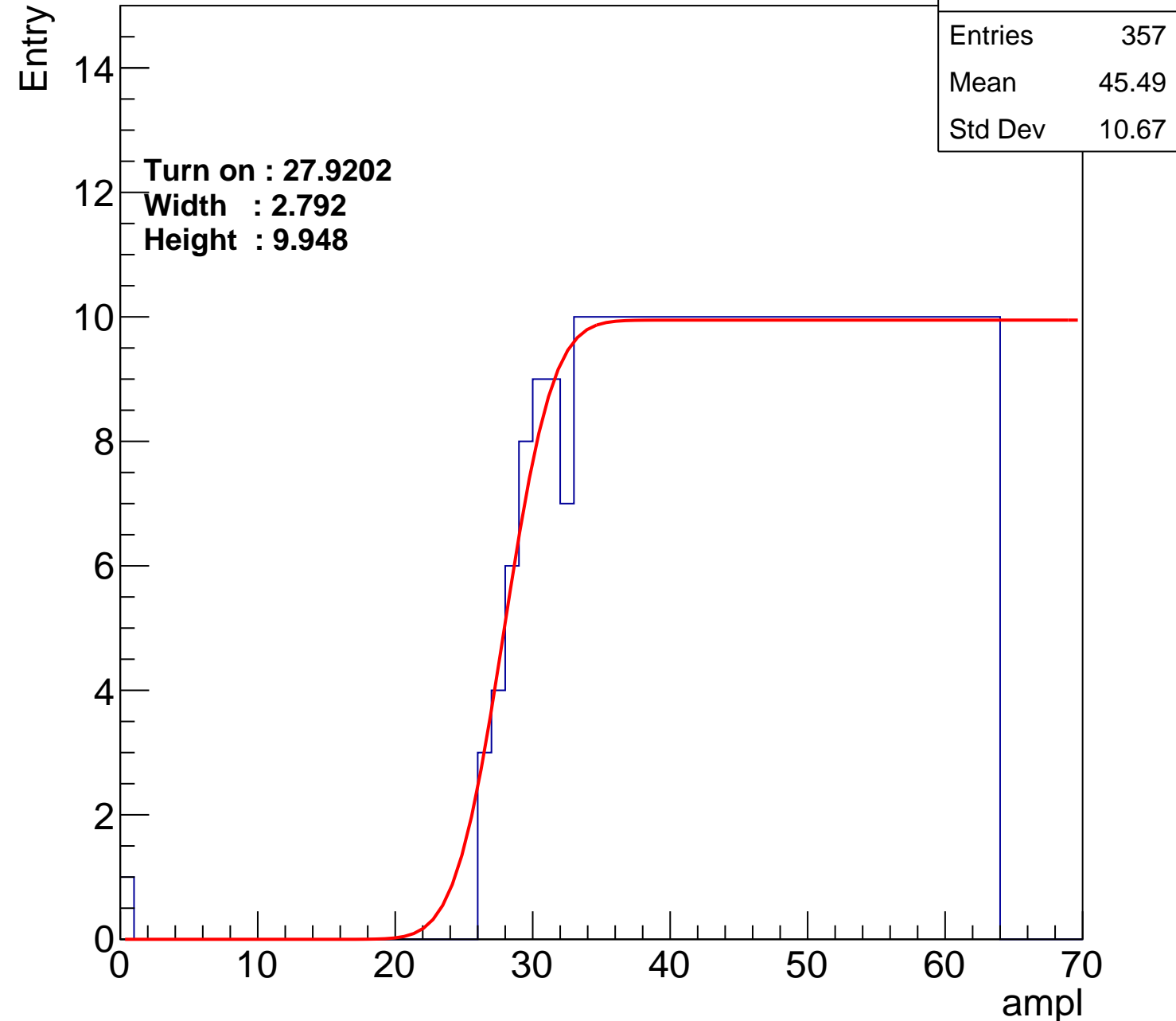
Width : 2.792

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch57

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 352 |
| Mean | 45.65 |
| Std Dev | 10.79 |

Turn on : 29.3849

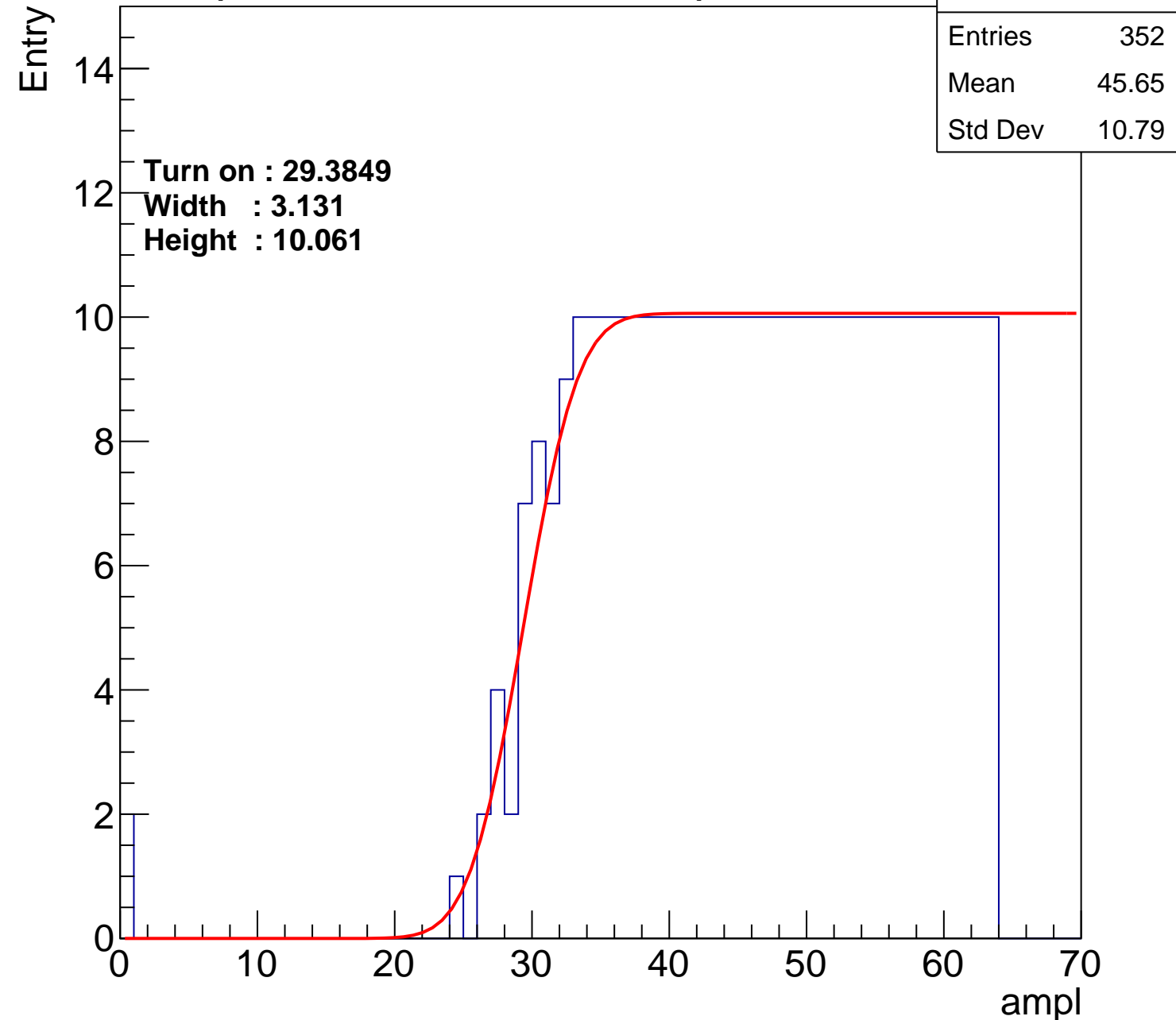
Width : 3.131

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch58

calib_packv5_042523_0143.root, FC#9, port A1

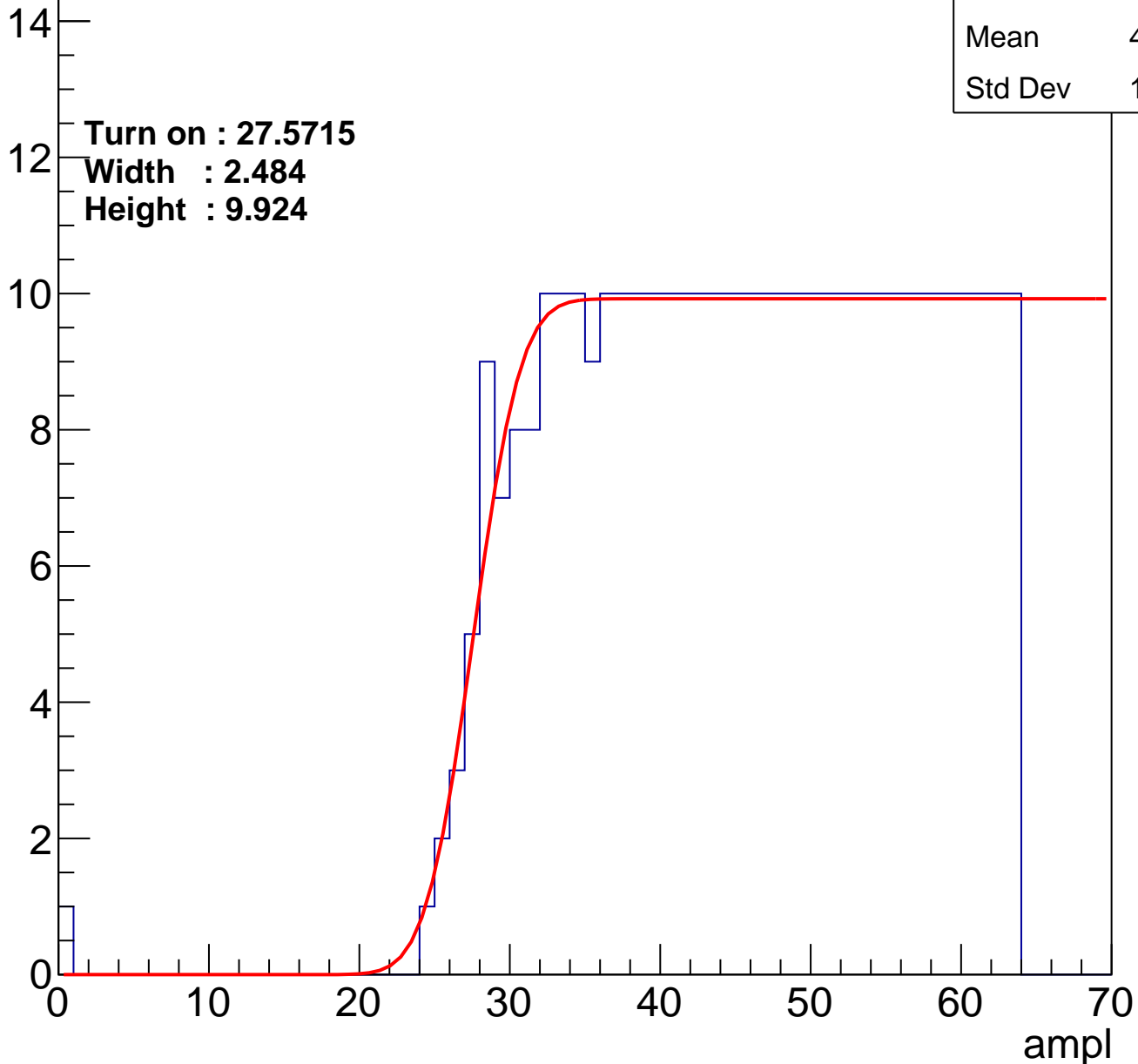
| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.17 |
| Std Dev | 10.87 |

Turn on : 27.5715

Width : 2.484

Height : 9.924

Entry



B0L001S, U23-ch59

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.42 |
| Std Dev | 10.9 |

Turn on : 28.6157

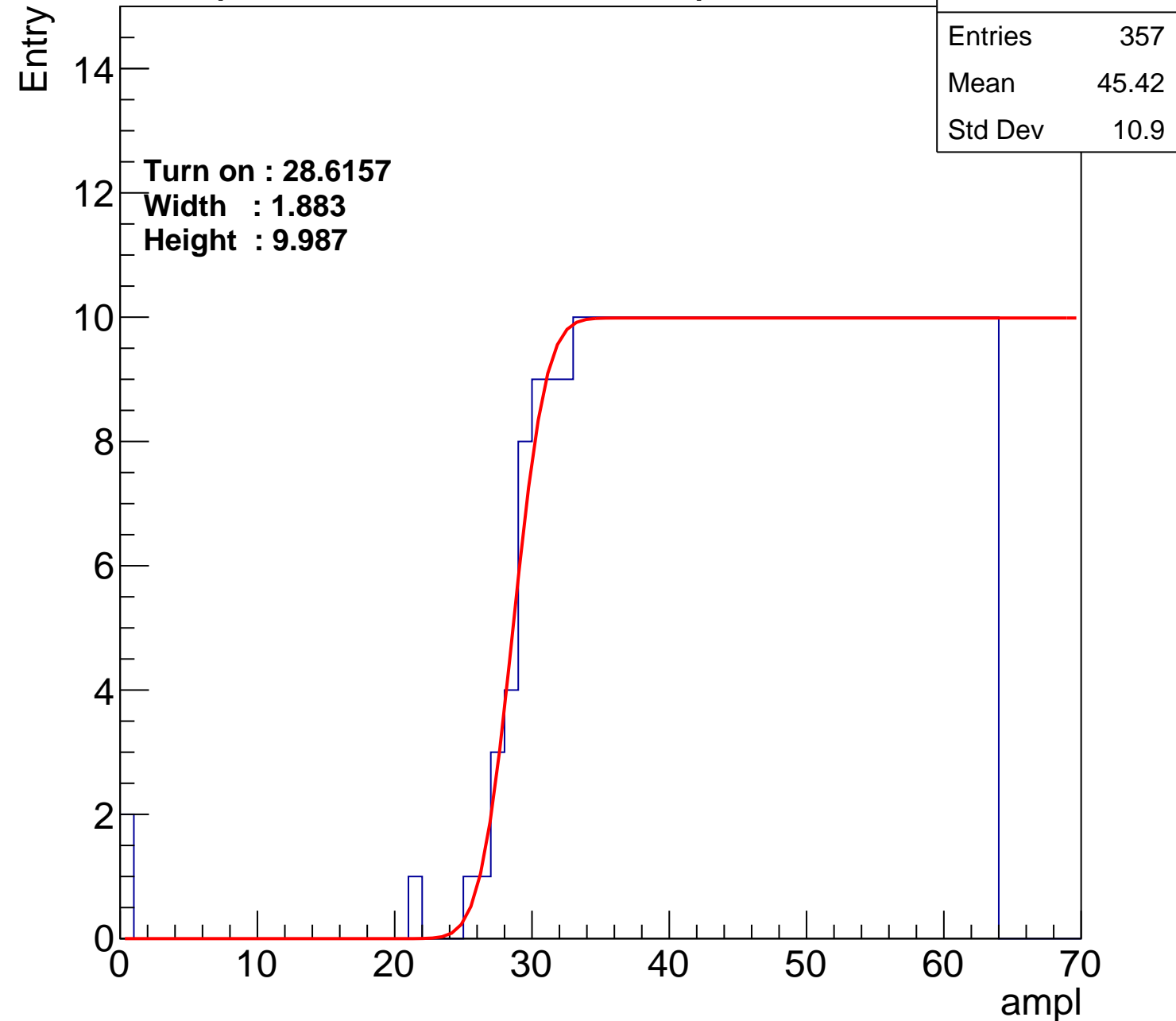
Width : 1.883

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch60

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.34 |
| Std Dev | 10.74 |

Turn on : 27.3916

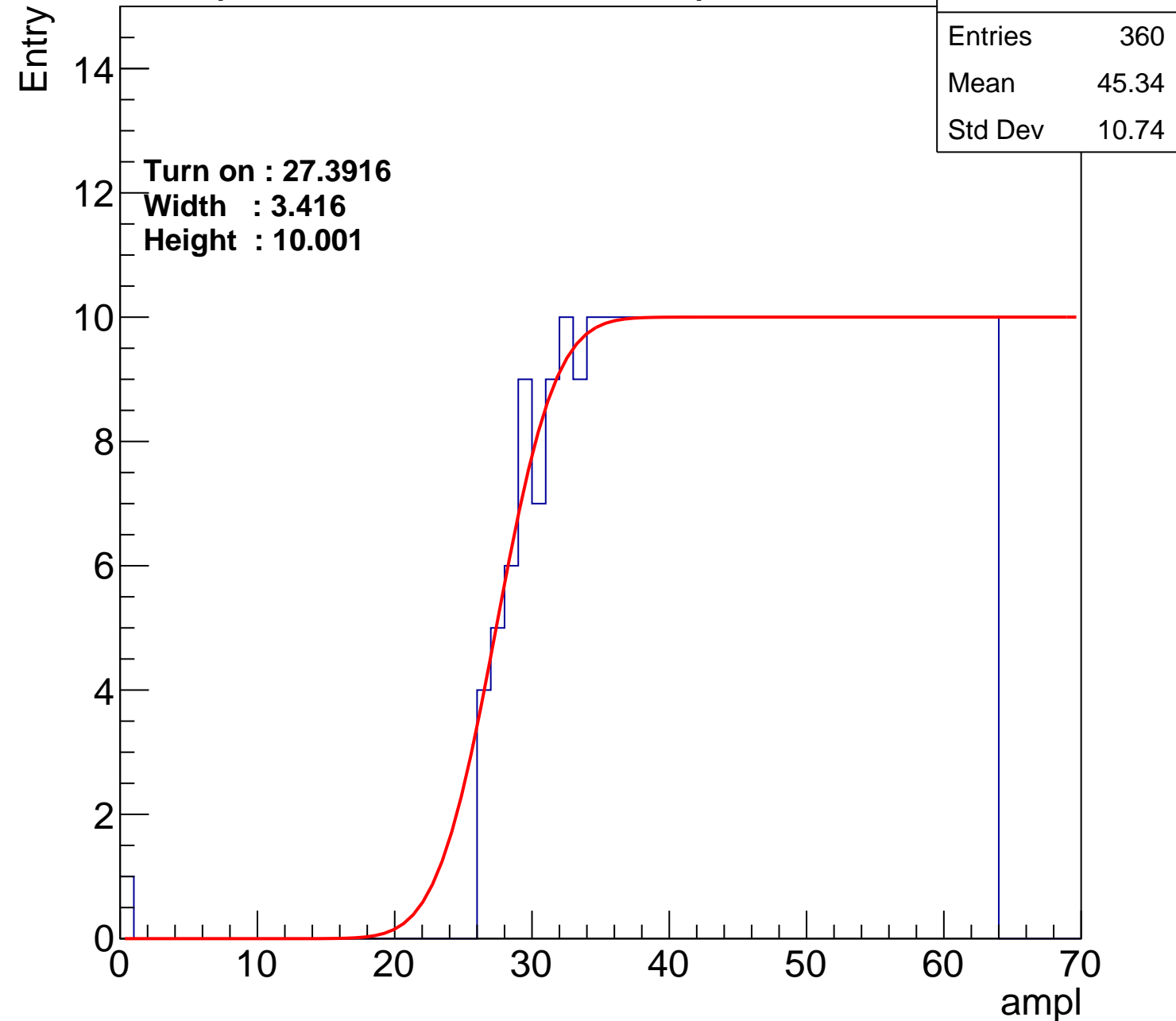
Width : 3.416

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch61

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 343 |
| Mean | 45.83 |
| Std Dev | 11.19 |

Turn on : 30.1435

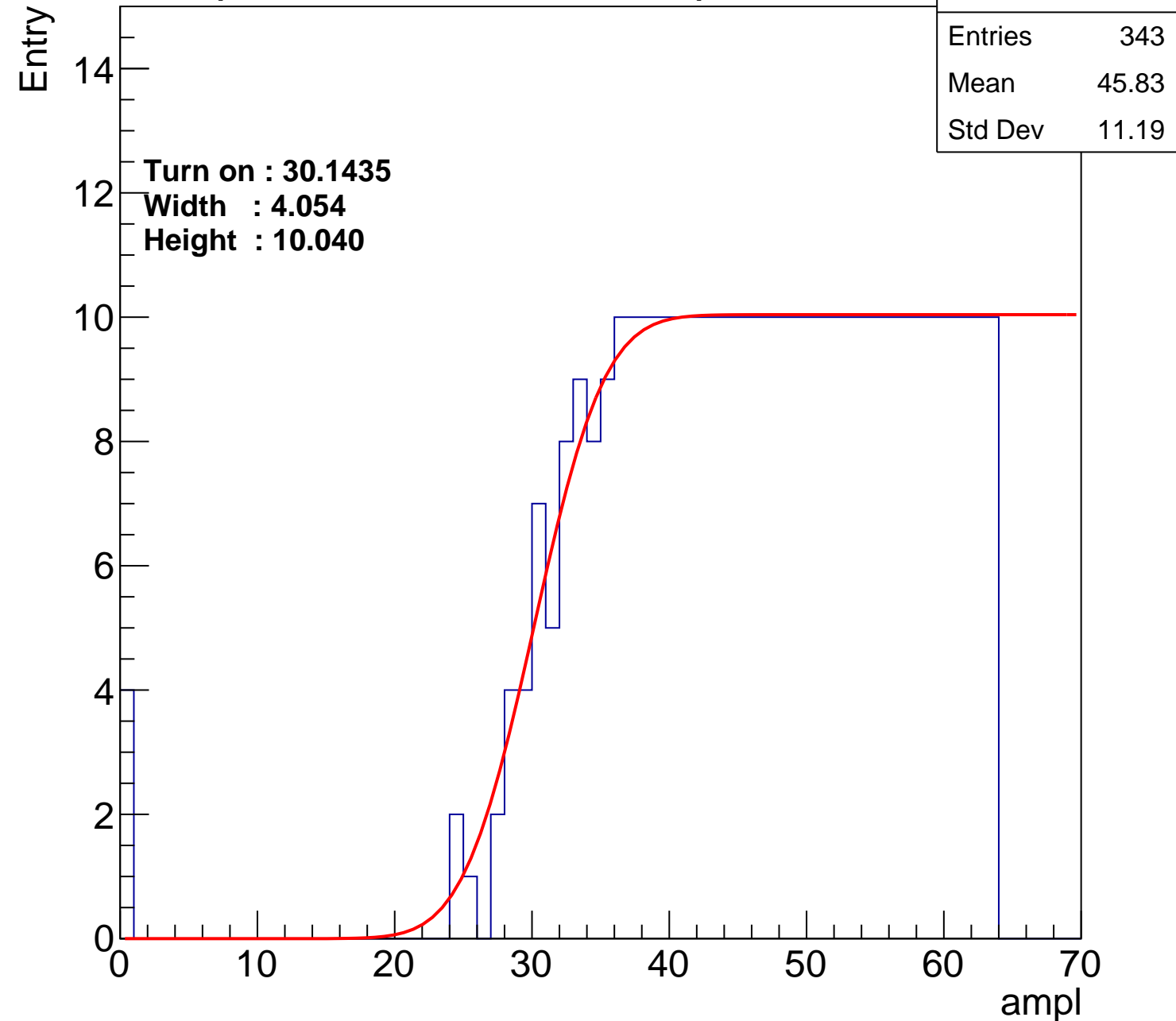
Width : 4.054

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch62

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.44 |
| Std Dev | 10.92 |

Turn on : 28.9470

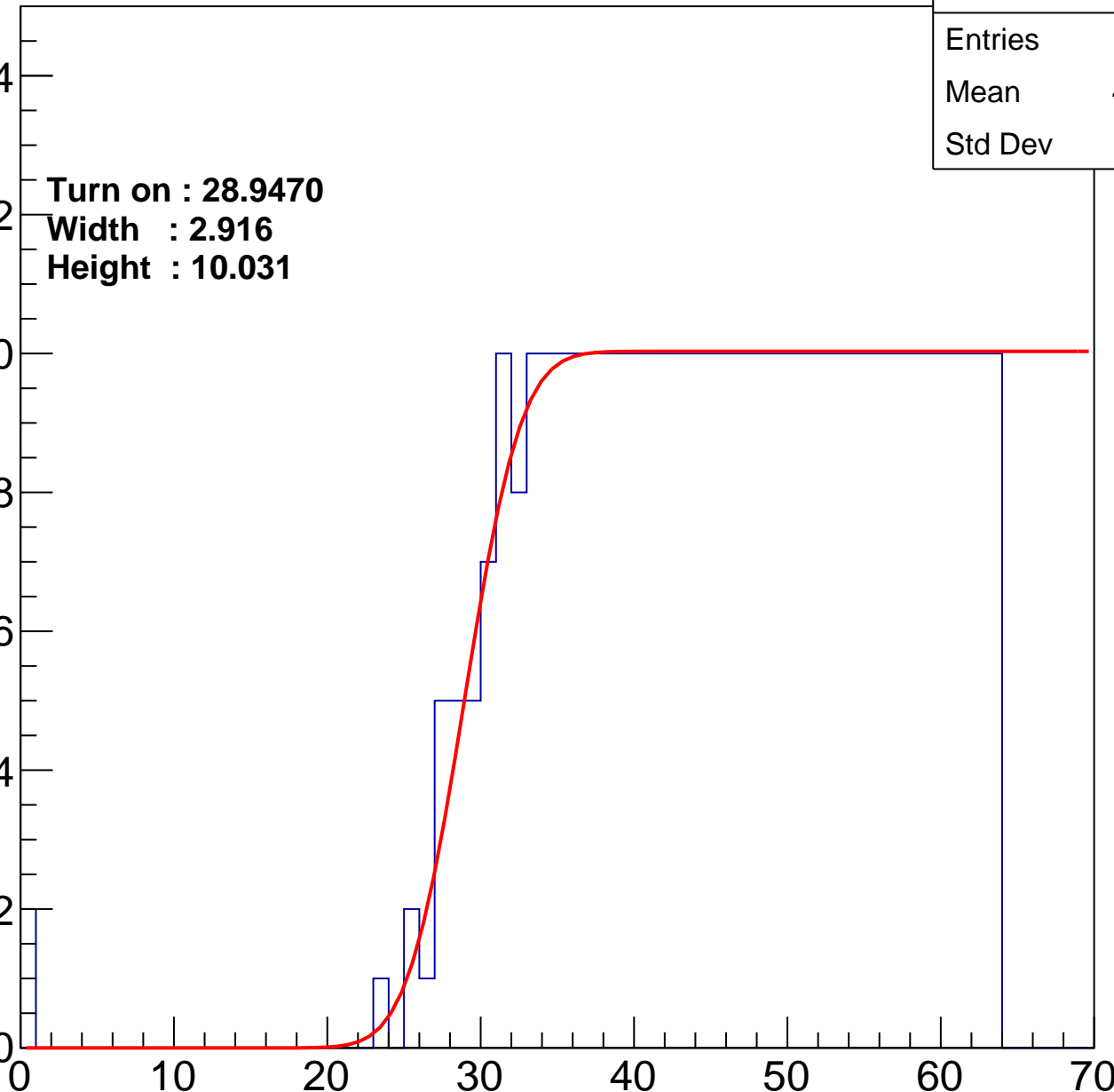
Width : 2.916

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 29.9069
Width : 3.302
Height : 10.037



B0L001S, U23-ch64

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.03 |
| Std Dev | 11.28 |

Turn on : 28.3462

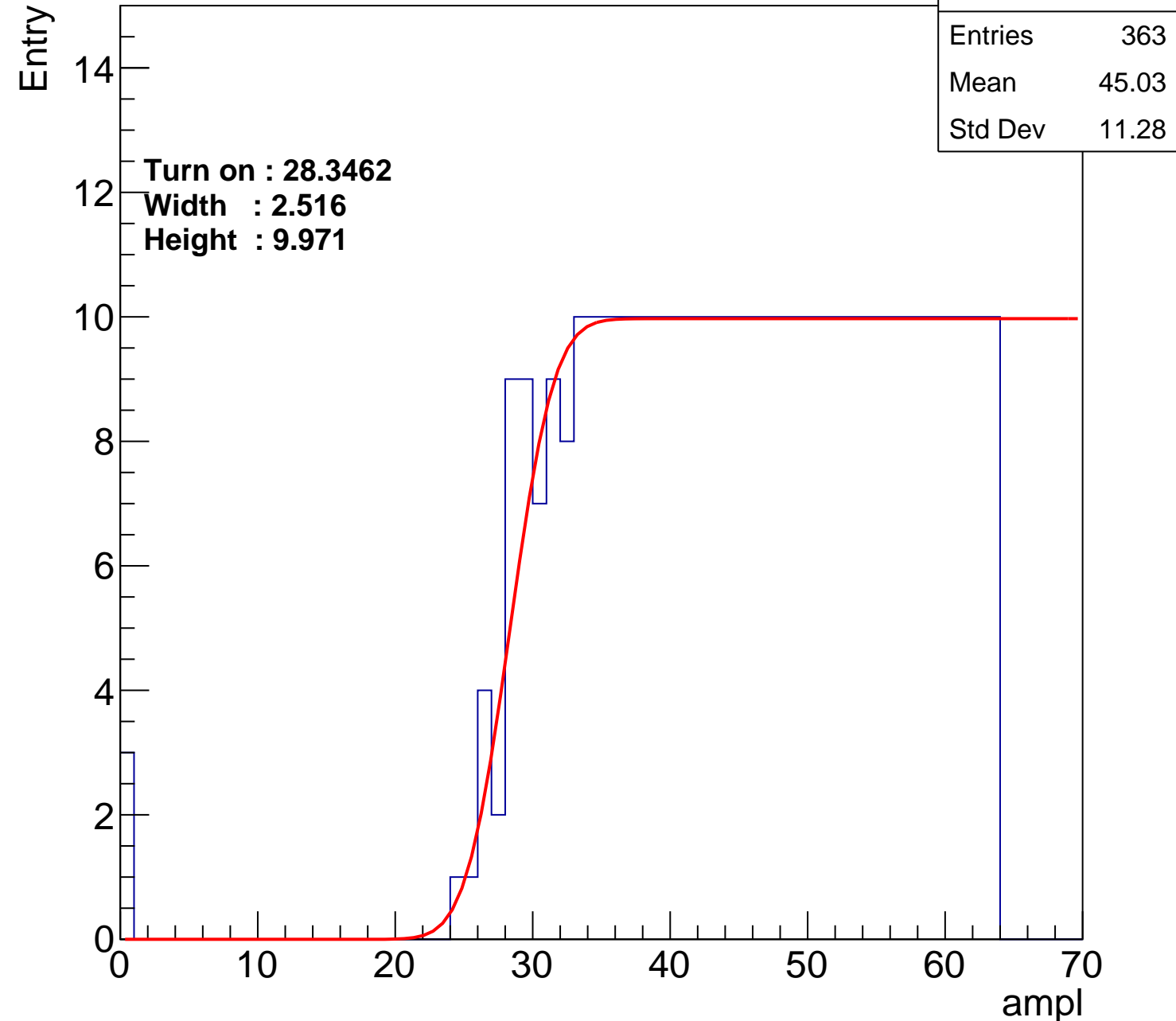
Width : 2.516

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch65

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 323 |
| Mean | 46.85 |
| Std Dev | 10.72 |

Turn on : 32.4993

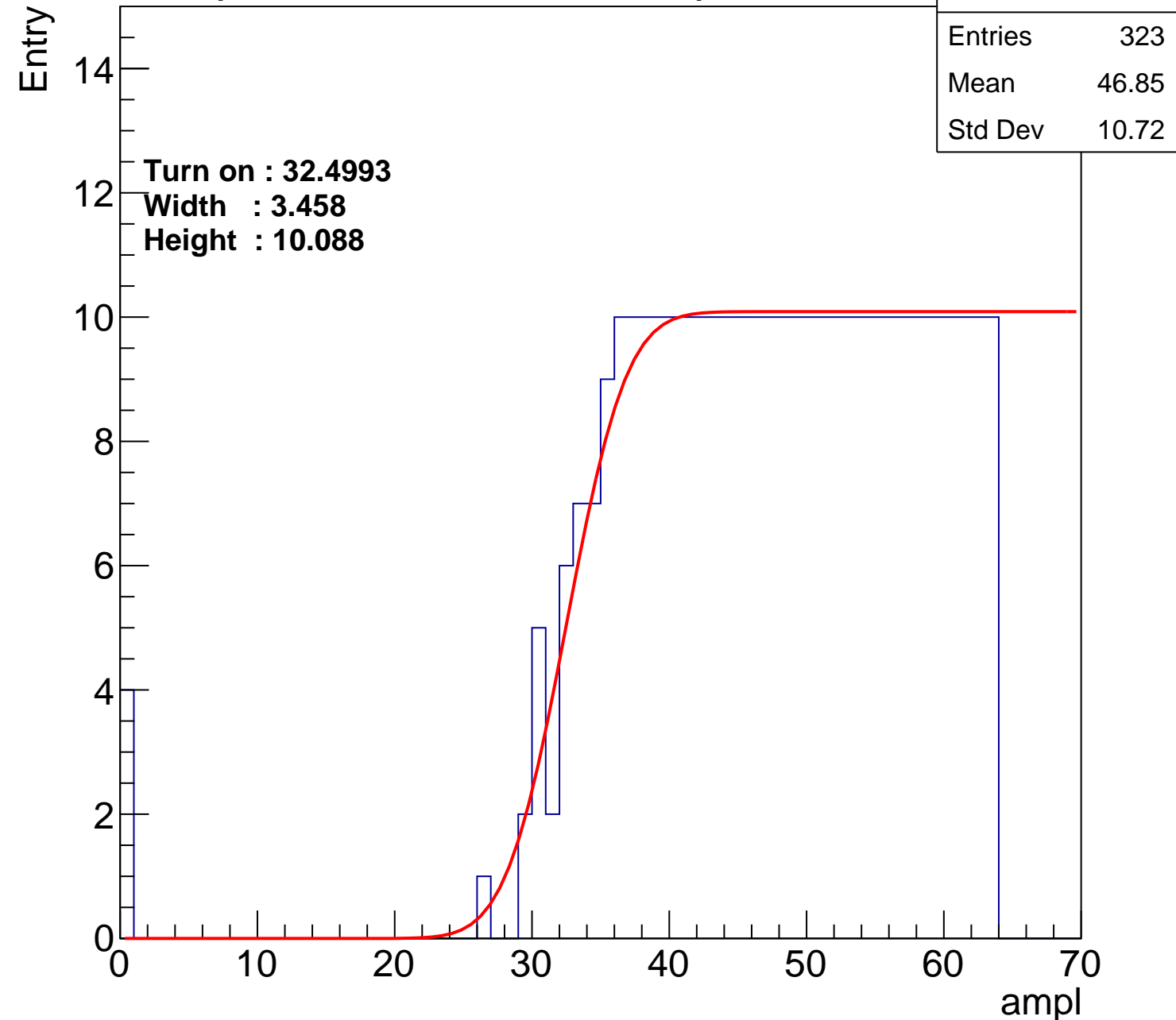
Width : 3.458

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 27.3249
Width : 2.661
Height : 9.954



B0L001S, U23-ch67

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 353 |
| Mean | 45.49 |
| Std Dev | 11.1 |

Turn on : 29.4997

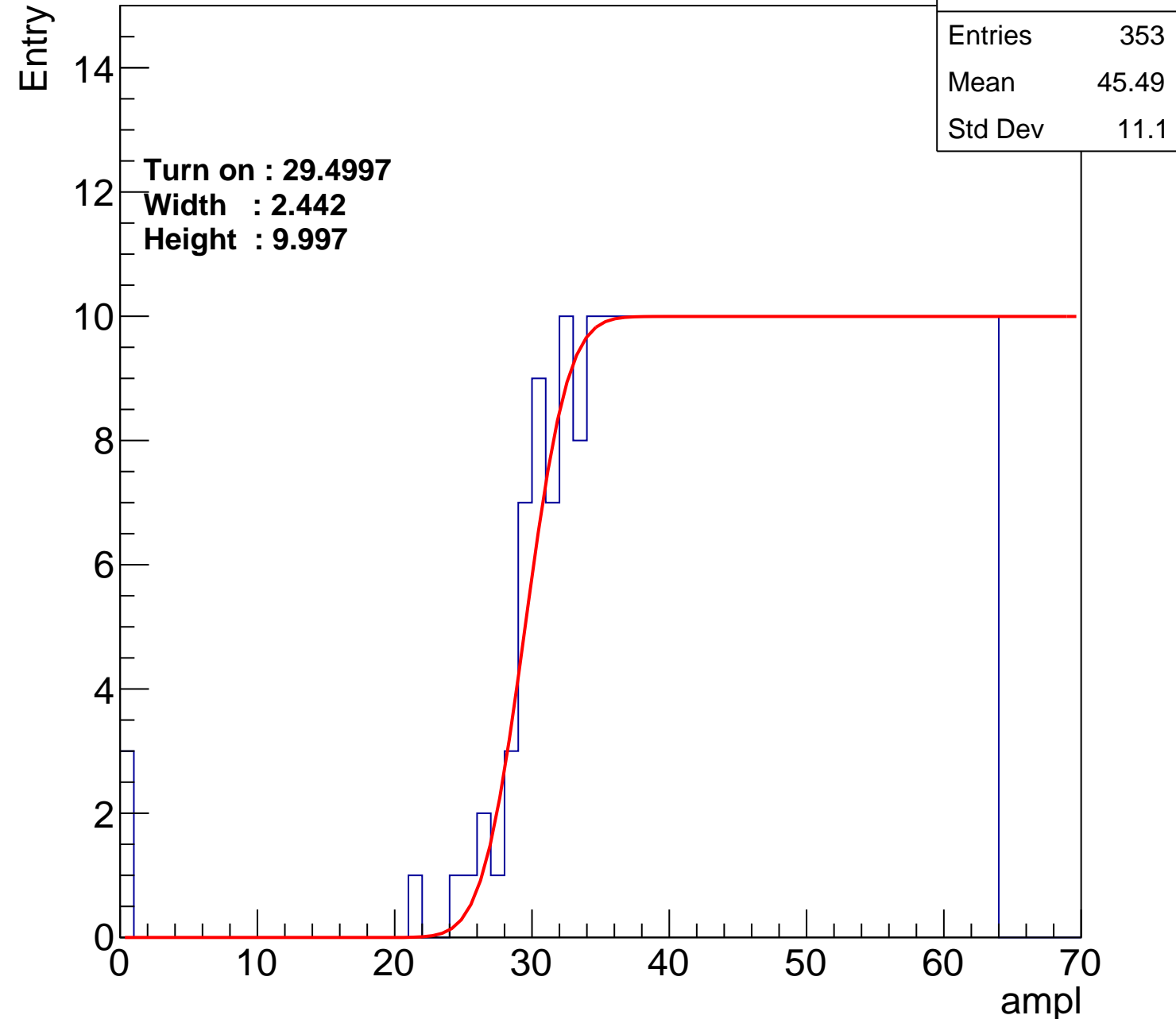
Width : 2.442

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch68

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|------|
| Entries | 343 |
| Mean | 45.9 |
| Std Dev | 11.1 |

Turn on : 30.7389

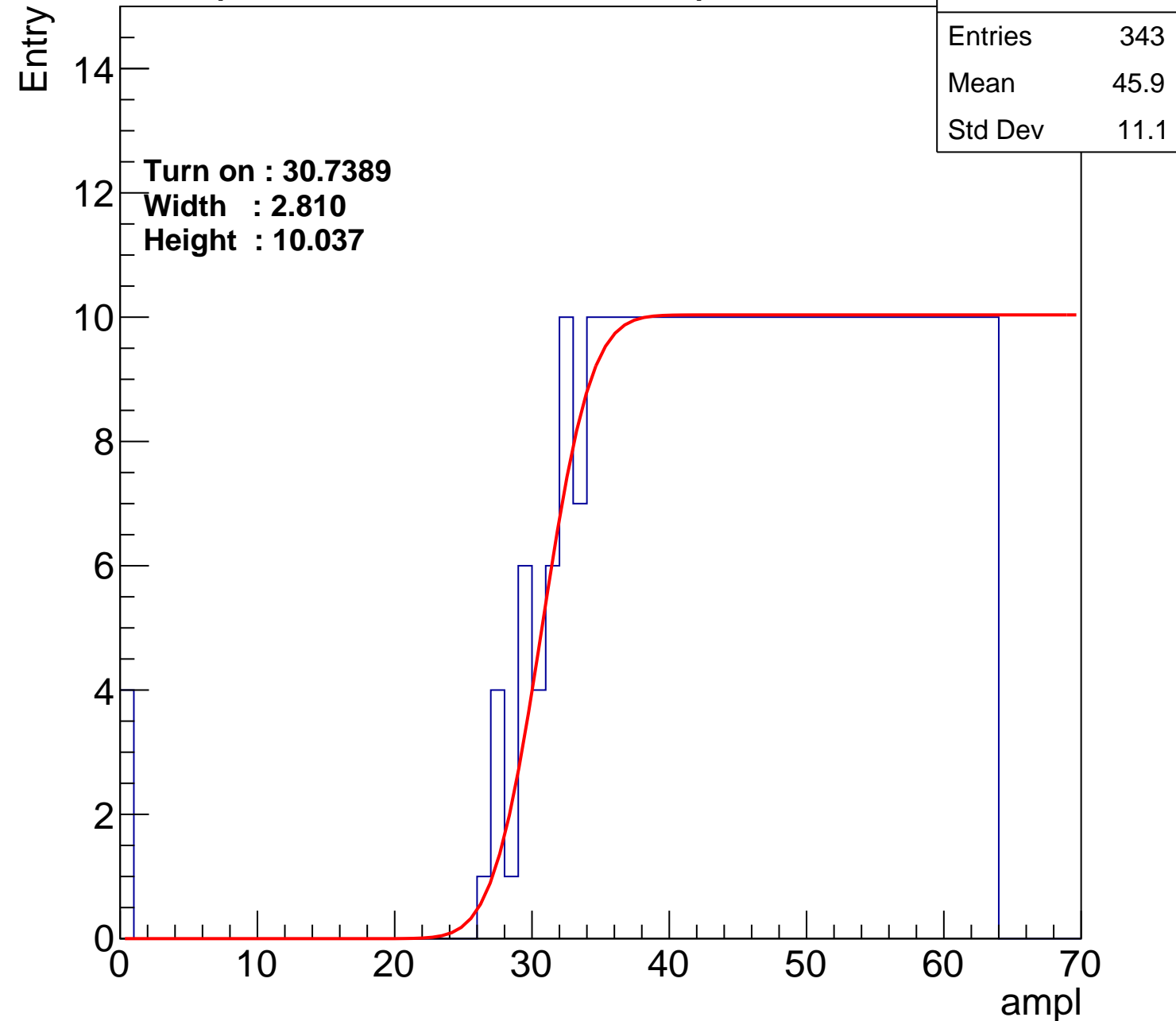
Width : 2.810

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch69

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.21 |
| Std Dev | 10.84 |

Turn on : 27.1670

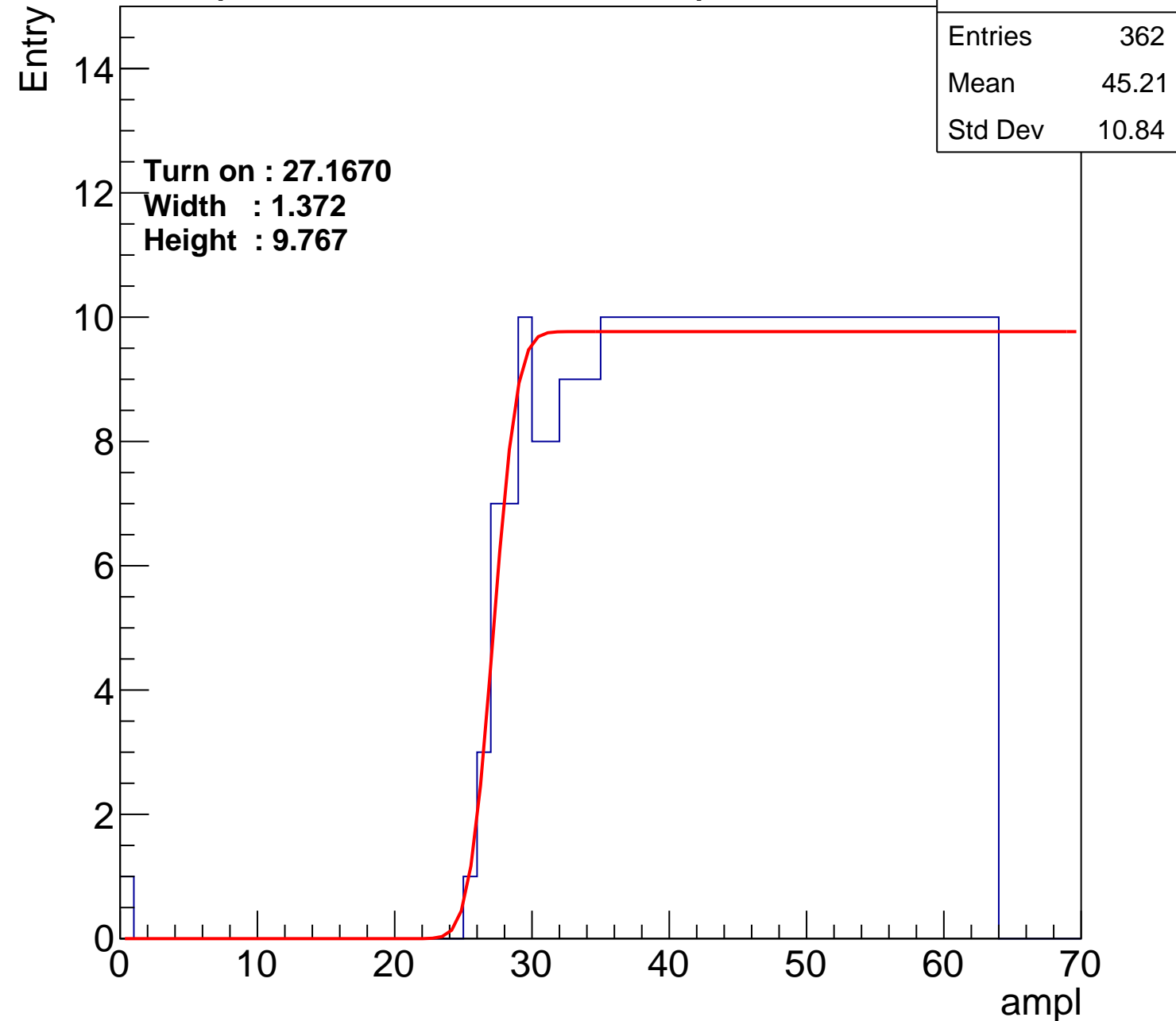
Width : 1.372

Height : 9.767

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch70

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.44 |
| Std Dev | 10.91 |

Turn on : 29.0441

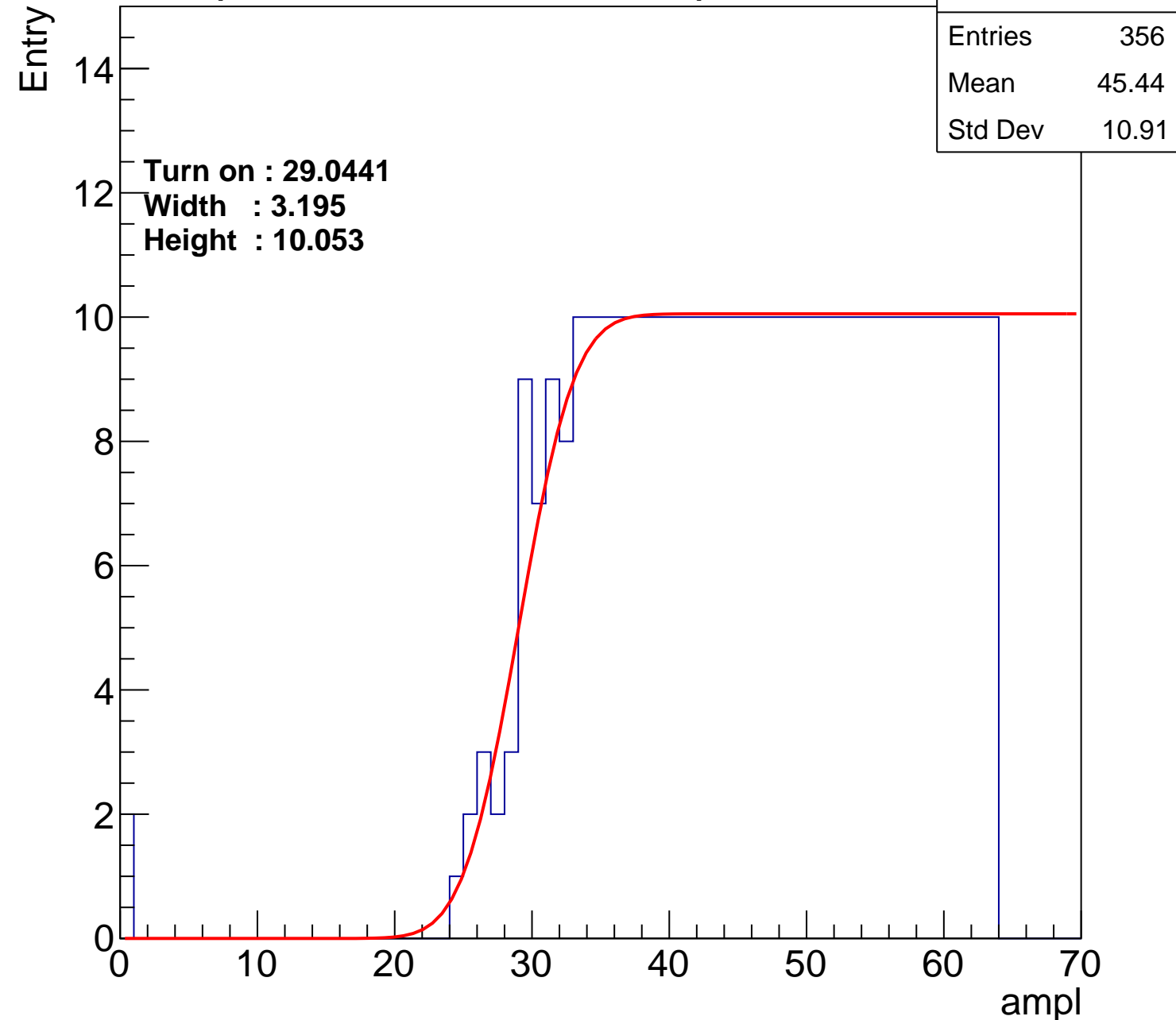
Width : 3.195

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch71

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.35 |
| Std Dev | 11.1 |

Turn on : 28.8977

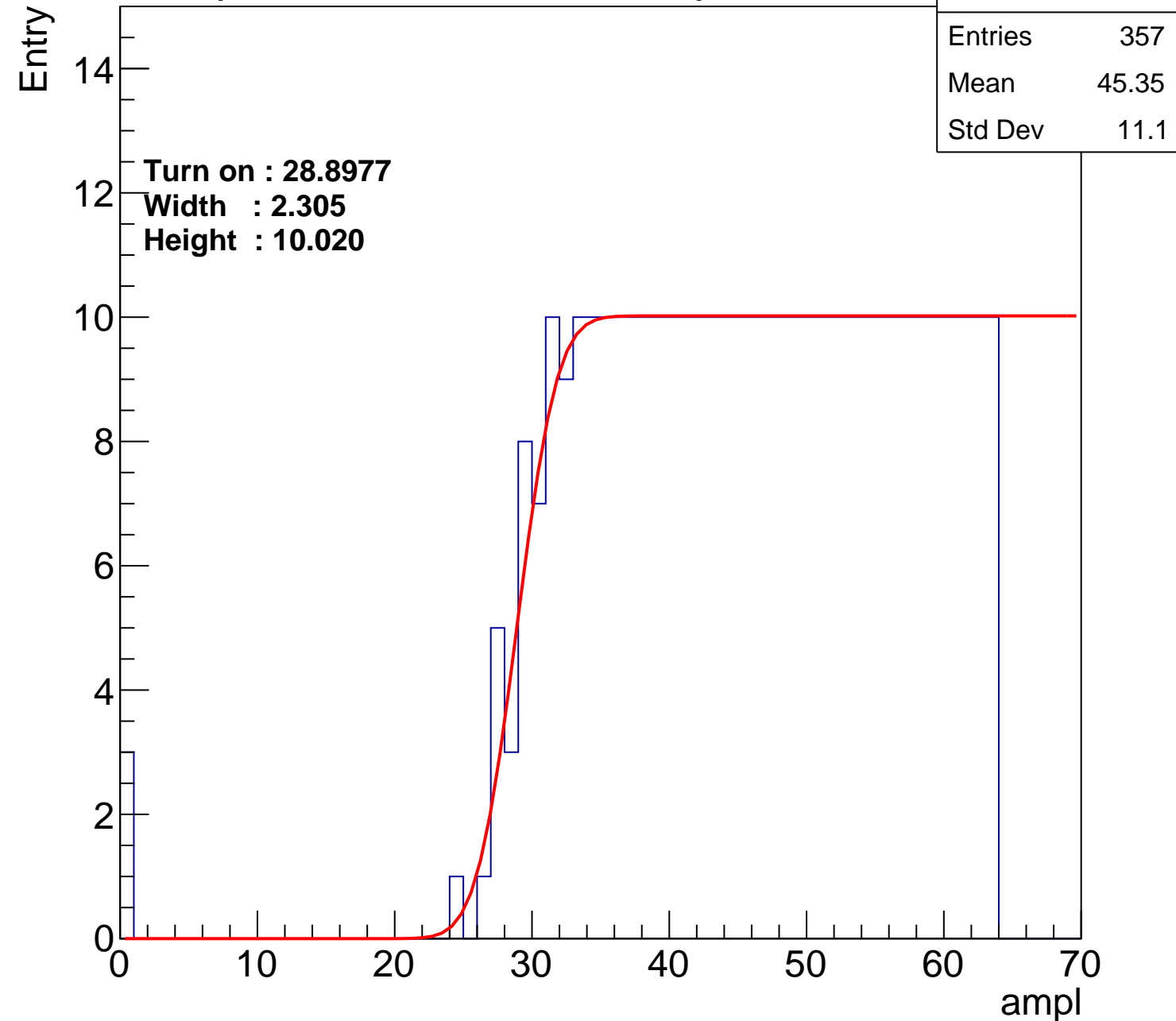
Width : 2.305

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch72

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.61 |
| Std Dev | 12.15 |

Turn on : 28.0603

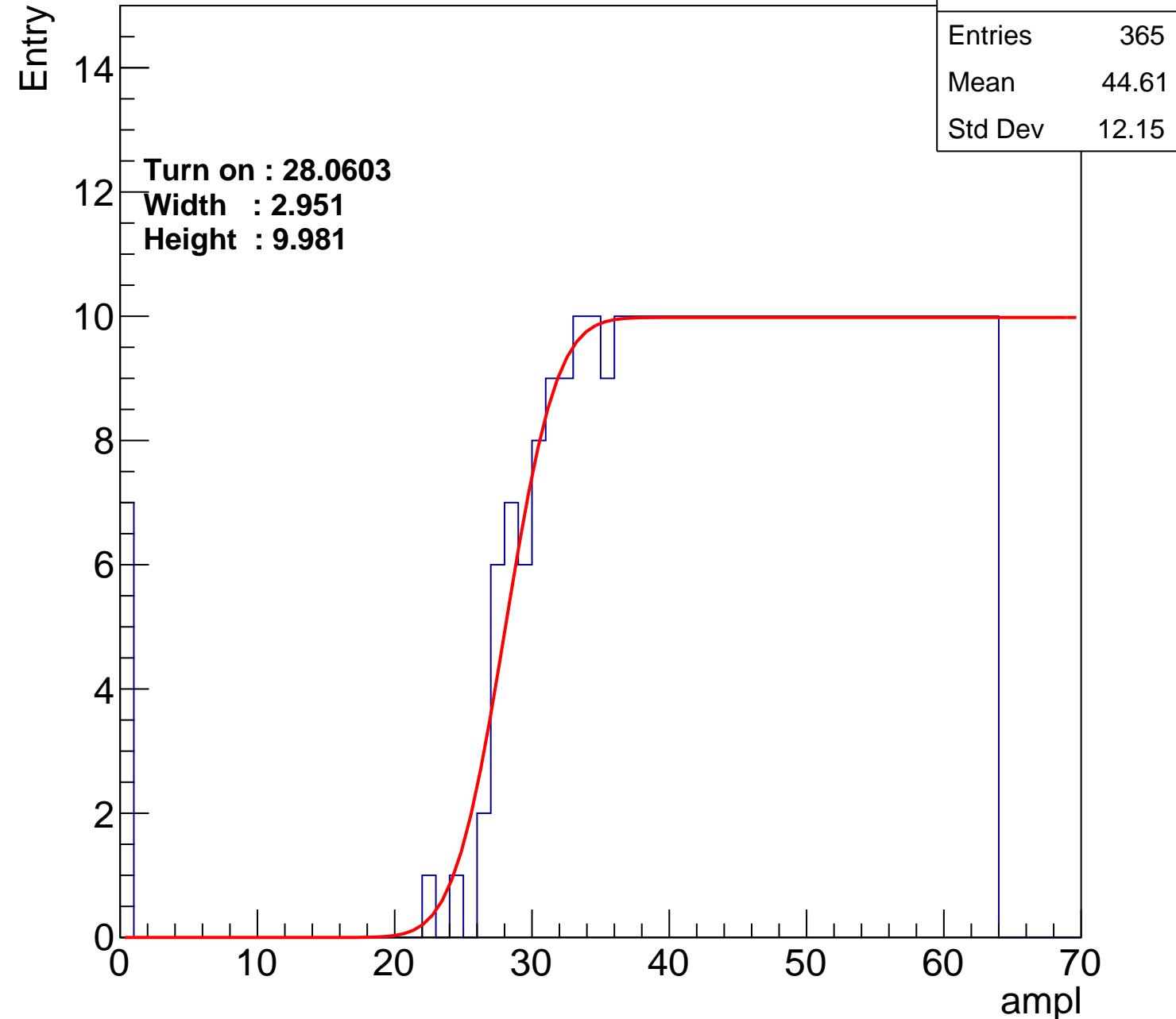
Width : 2.951

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch73

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.71 |
| Std Dev | 11.46 |

Turn on : 27.2719

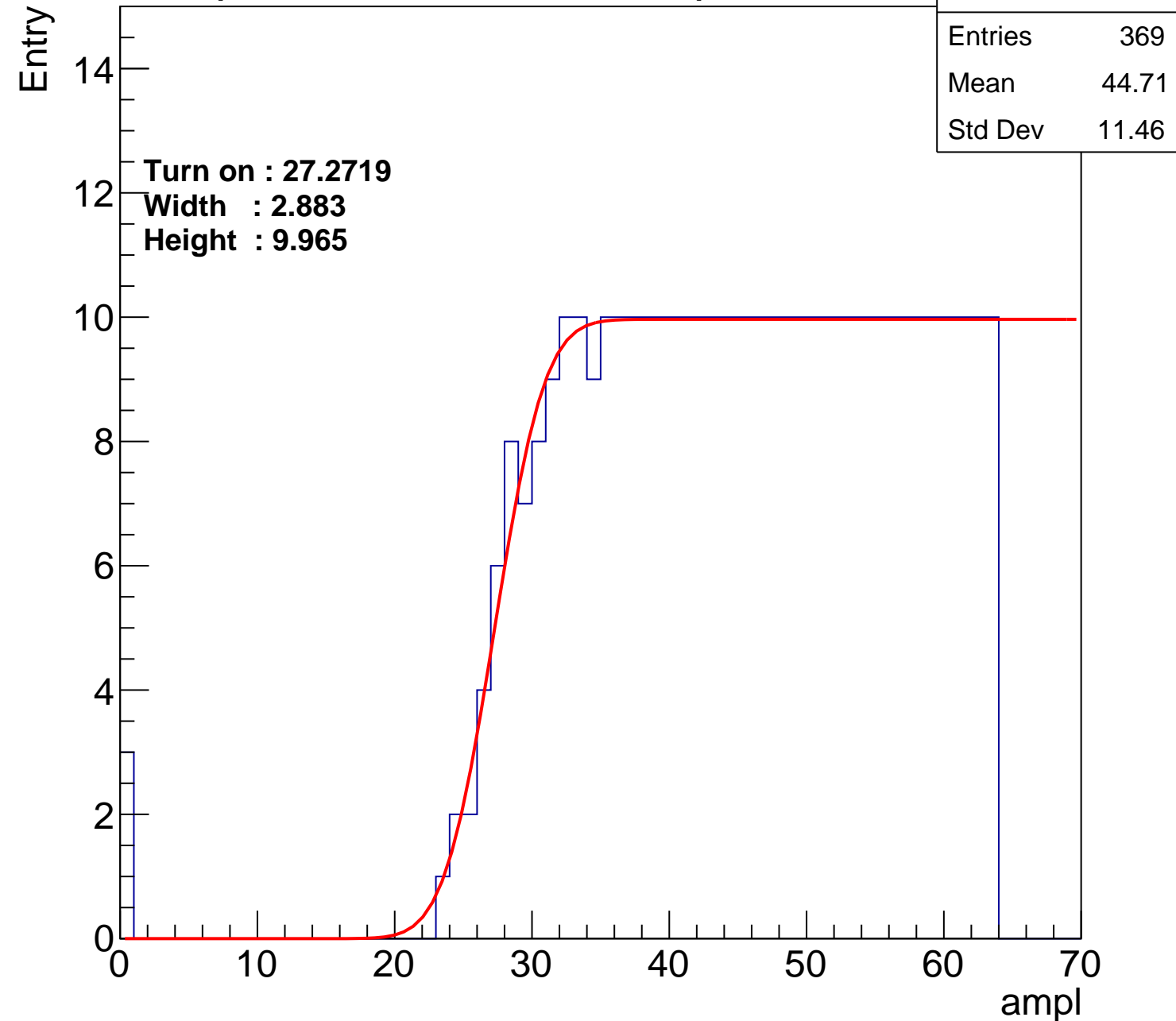
Width : 2.883

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch74

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.22 |
| Std Dev | 11.69 |

Turn on : 26.2264

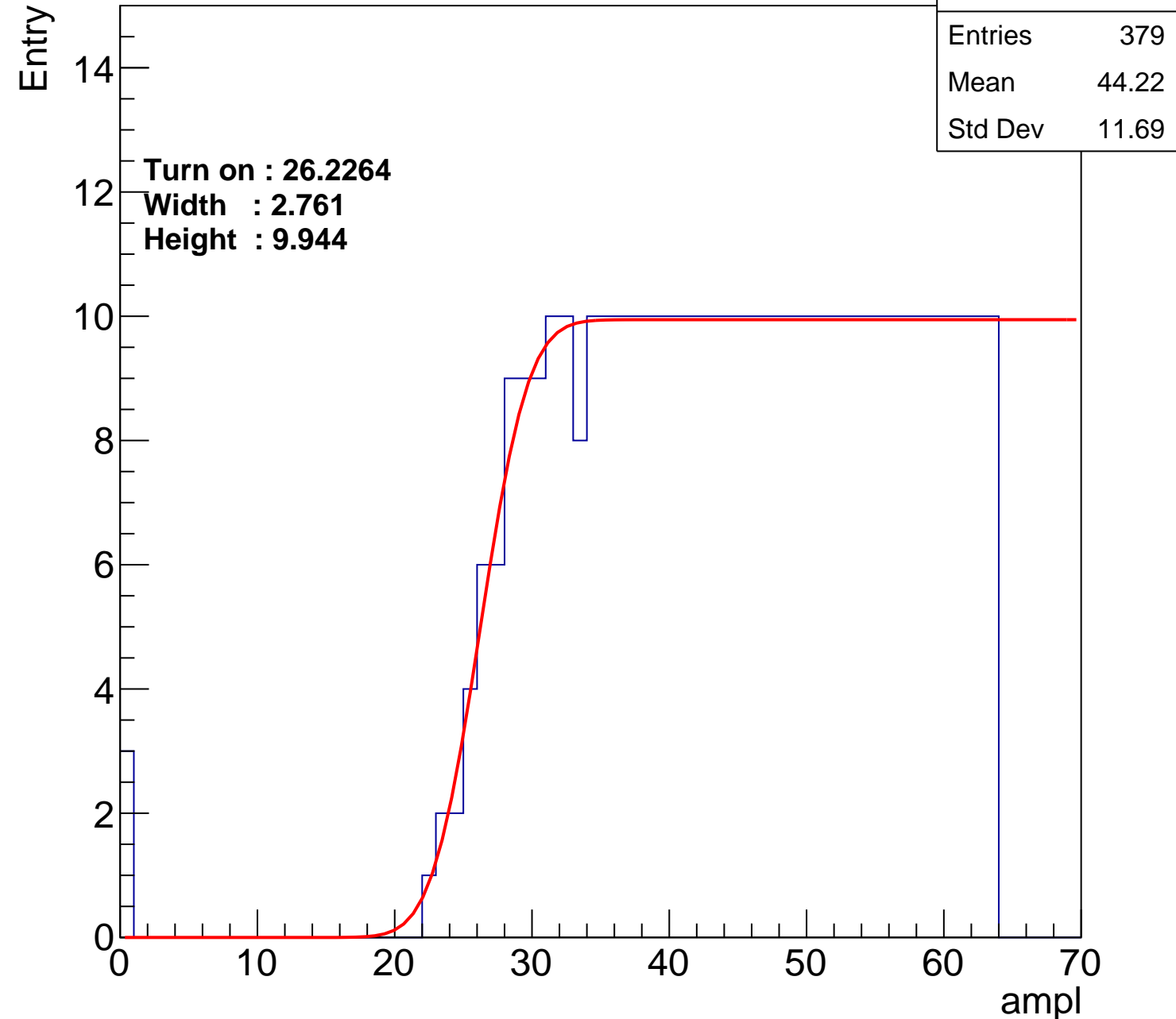
Width : 2.761

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch75

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 342 |
| Mean | 46.06 |
| Std Dev | 10.67 |

Turn on : 30.4092

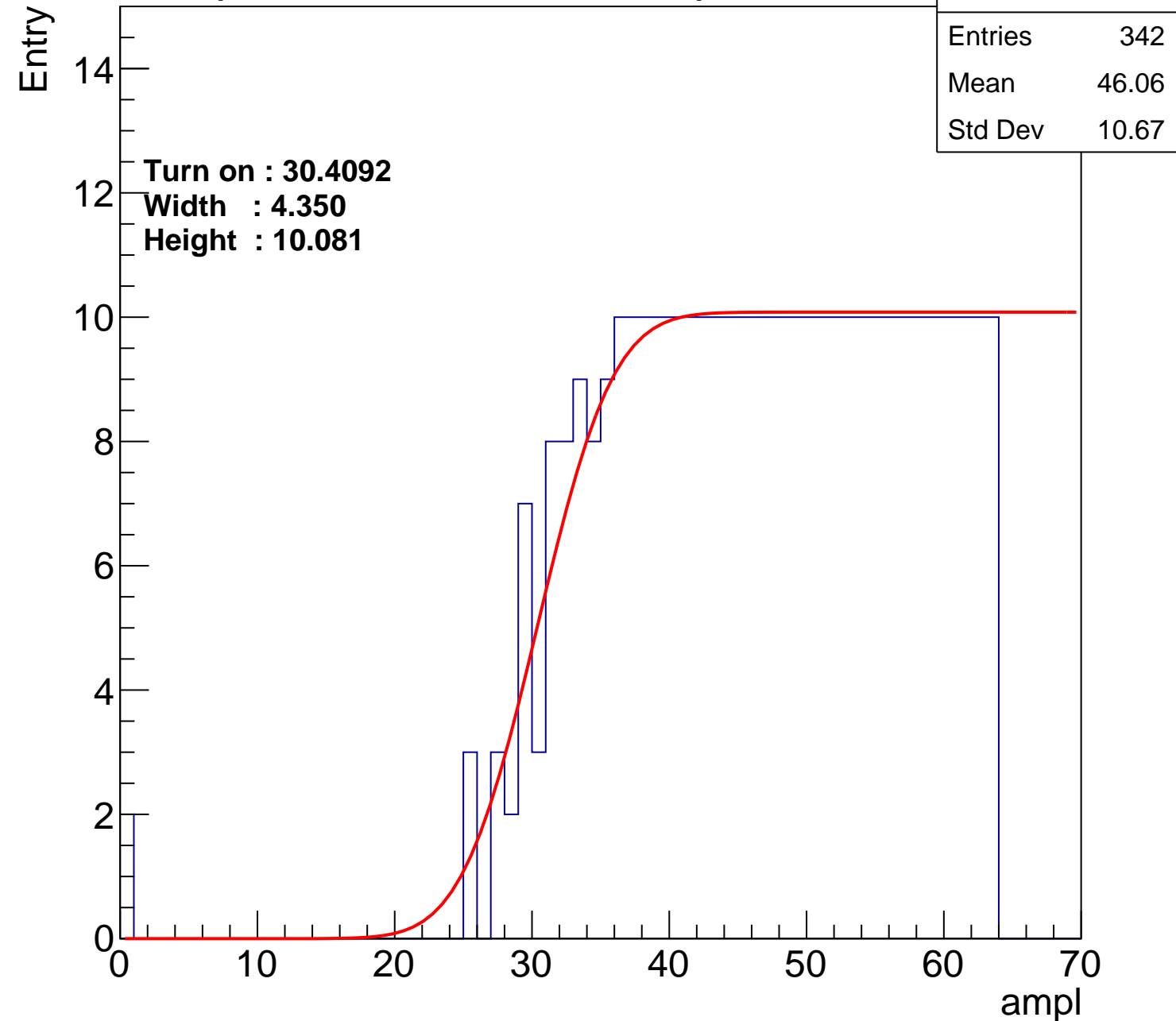
Width : 4.350

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch76

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.07 |
| Std Dev | 10.99 |

Turn on : 28.4568

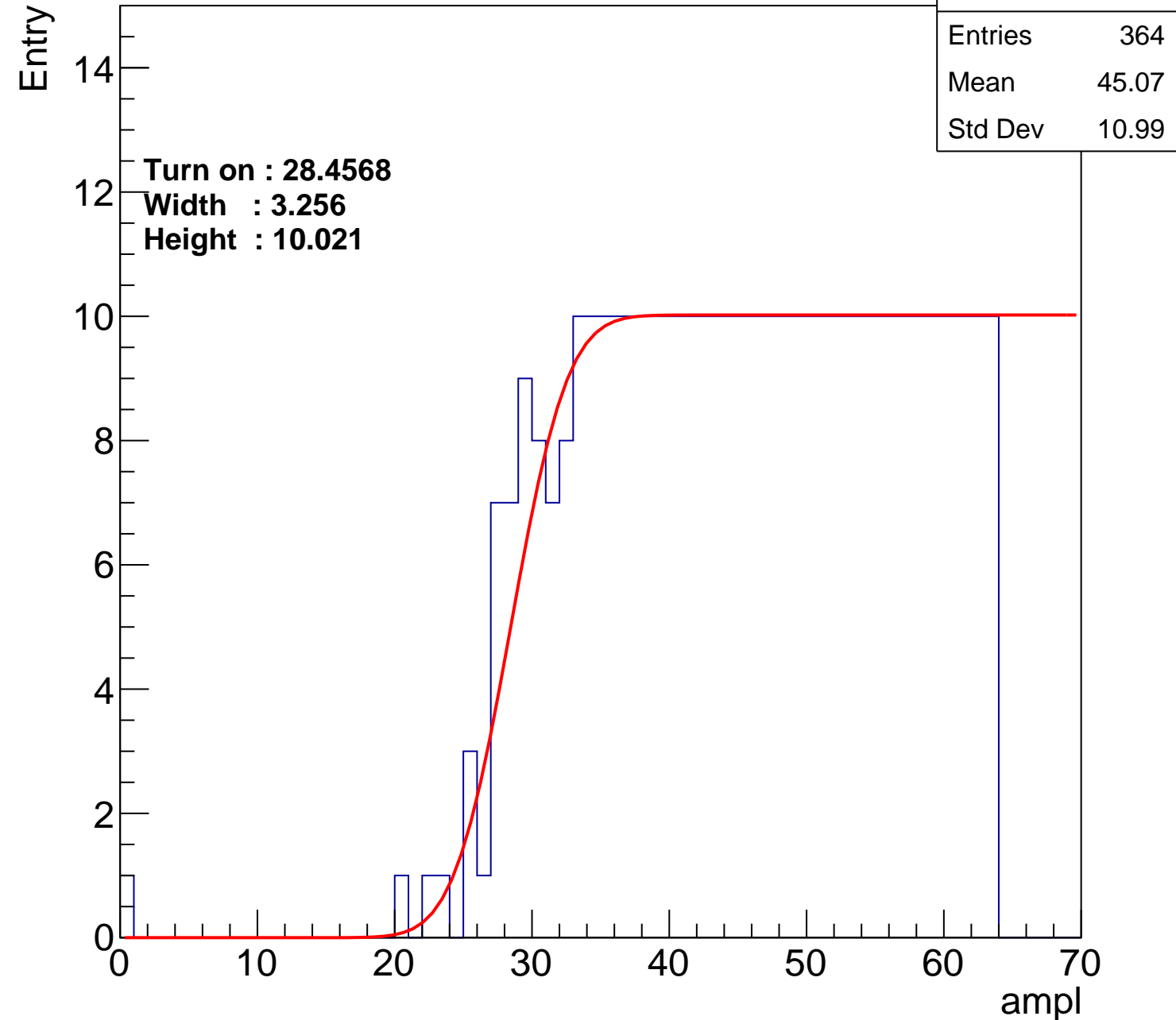
Width : 3.256

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch77

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.7 |
| Std Dev | 11.62 |

Turn on : 28.3817

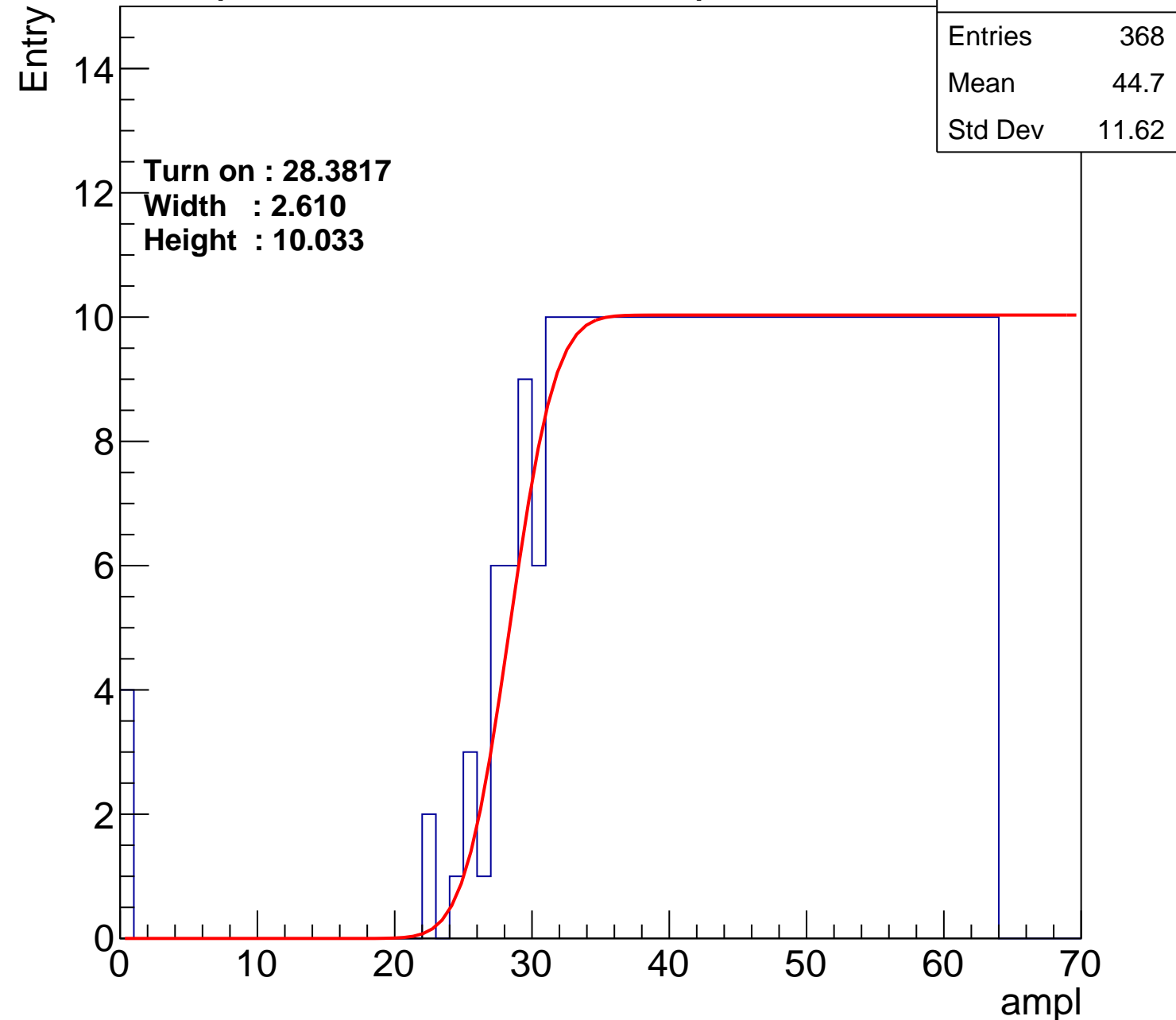
Width : 2.610

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch78

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.55 |
| Std Dev | 11.38 |

Turn on : 26.8126

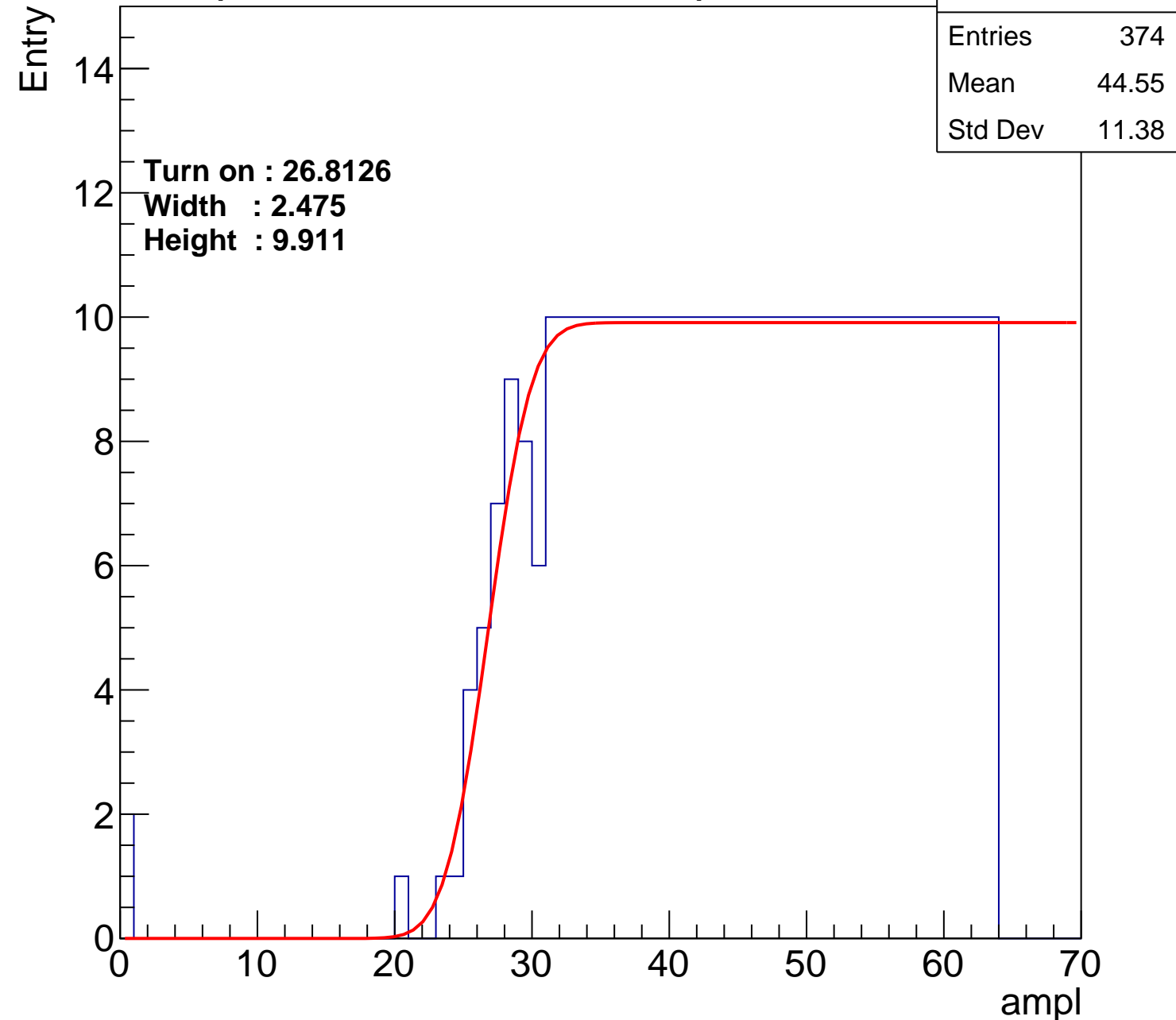
Width : 2.475

Height : 9.911

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch79

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.37 |
| Std Dev | 11.64 |

Turn on : 27.1405

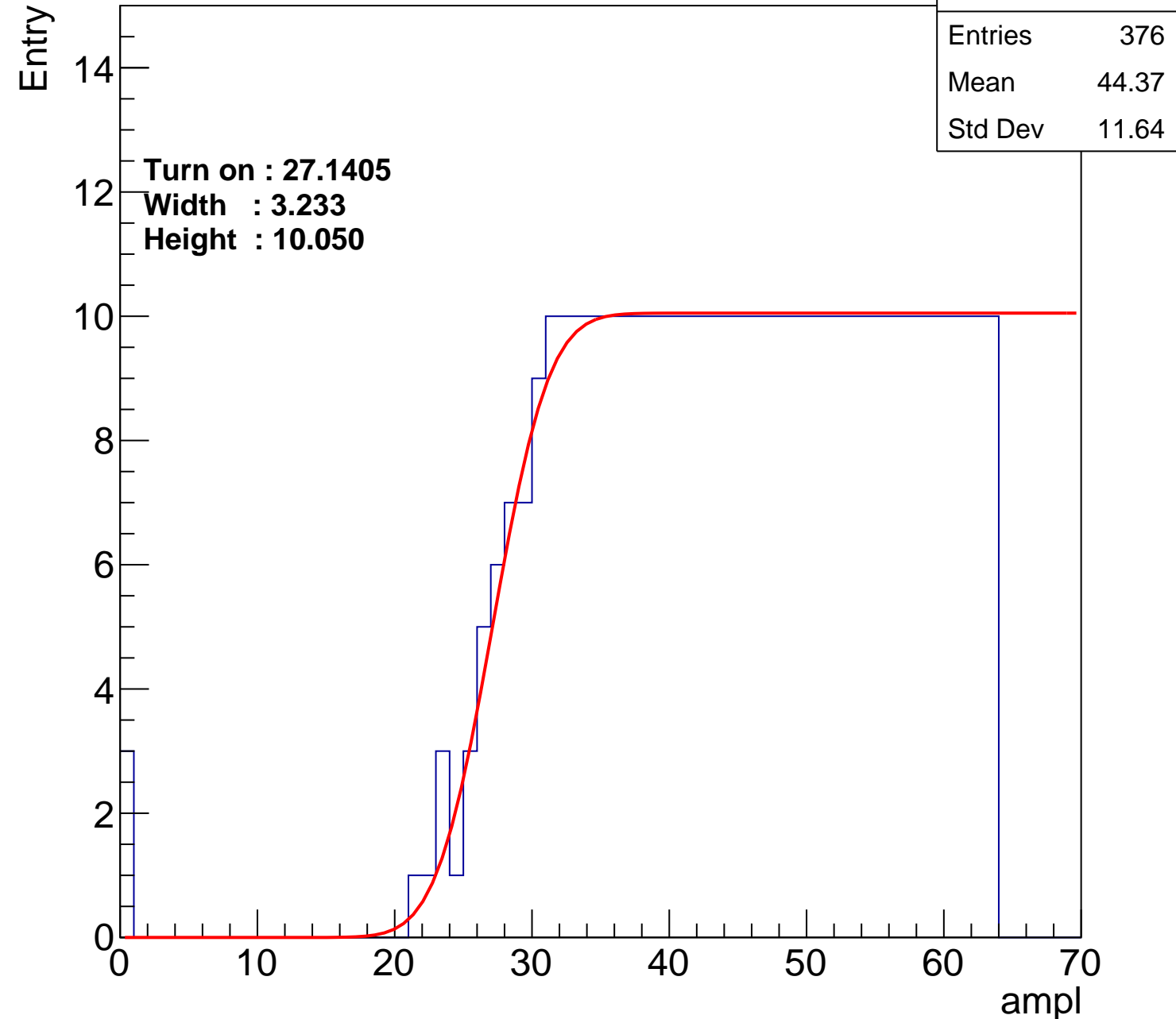
Width : 3.233

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch80

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.51 |
| Std Dev | 11.71 |

Turn on : 27.4032

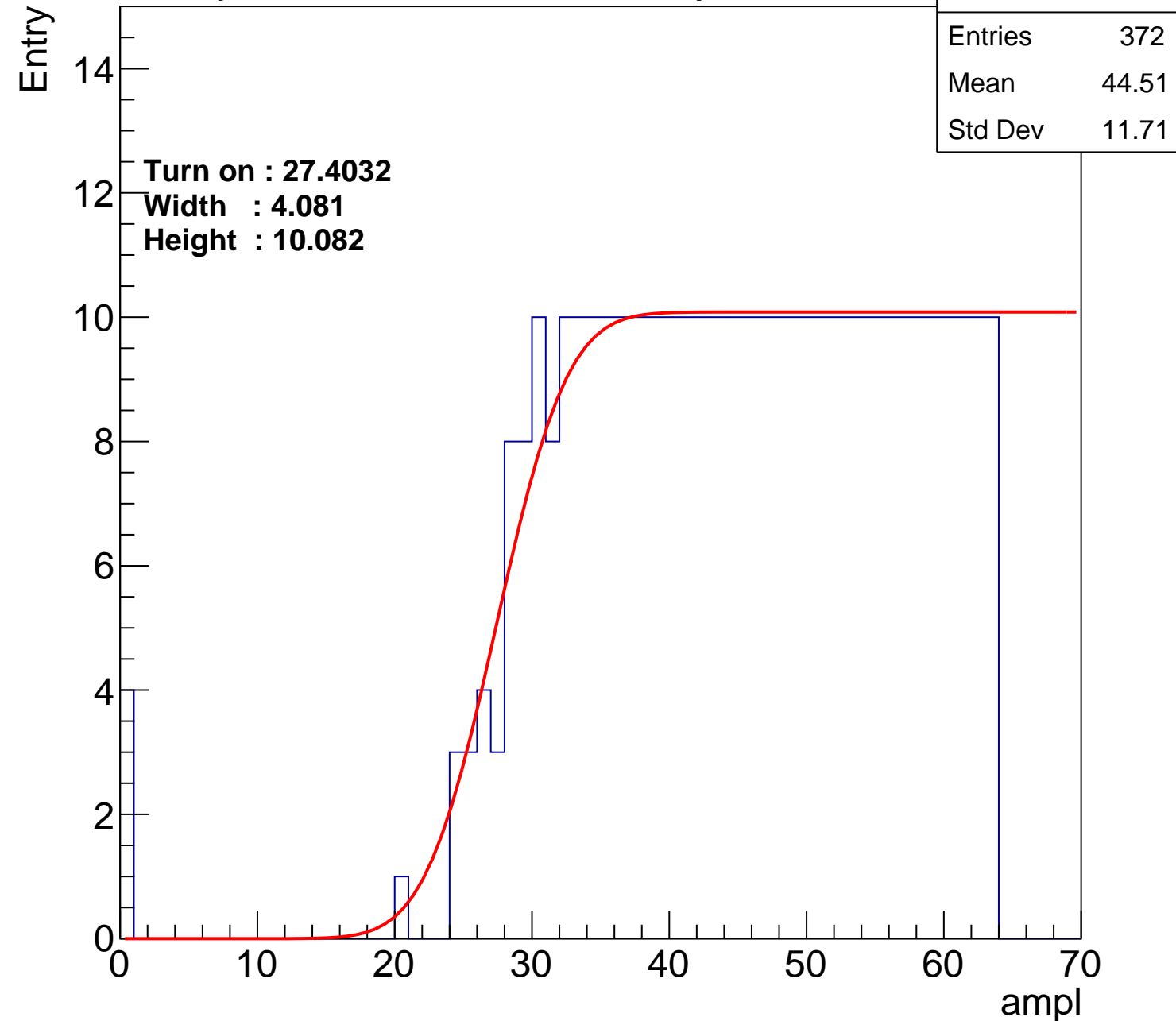
Width : 4.081

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch81

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.37 |
| Std Dev | 10.92 |

Turn on : 28.4277

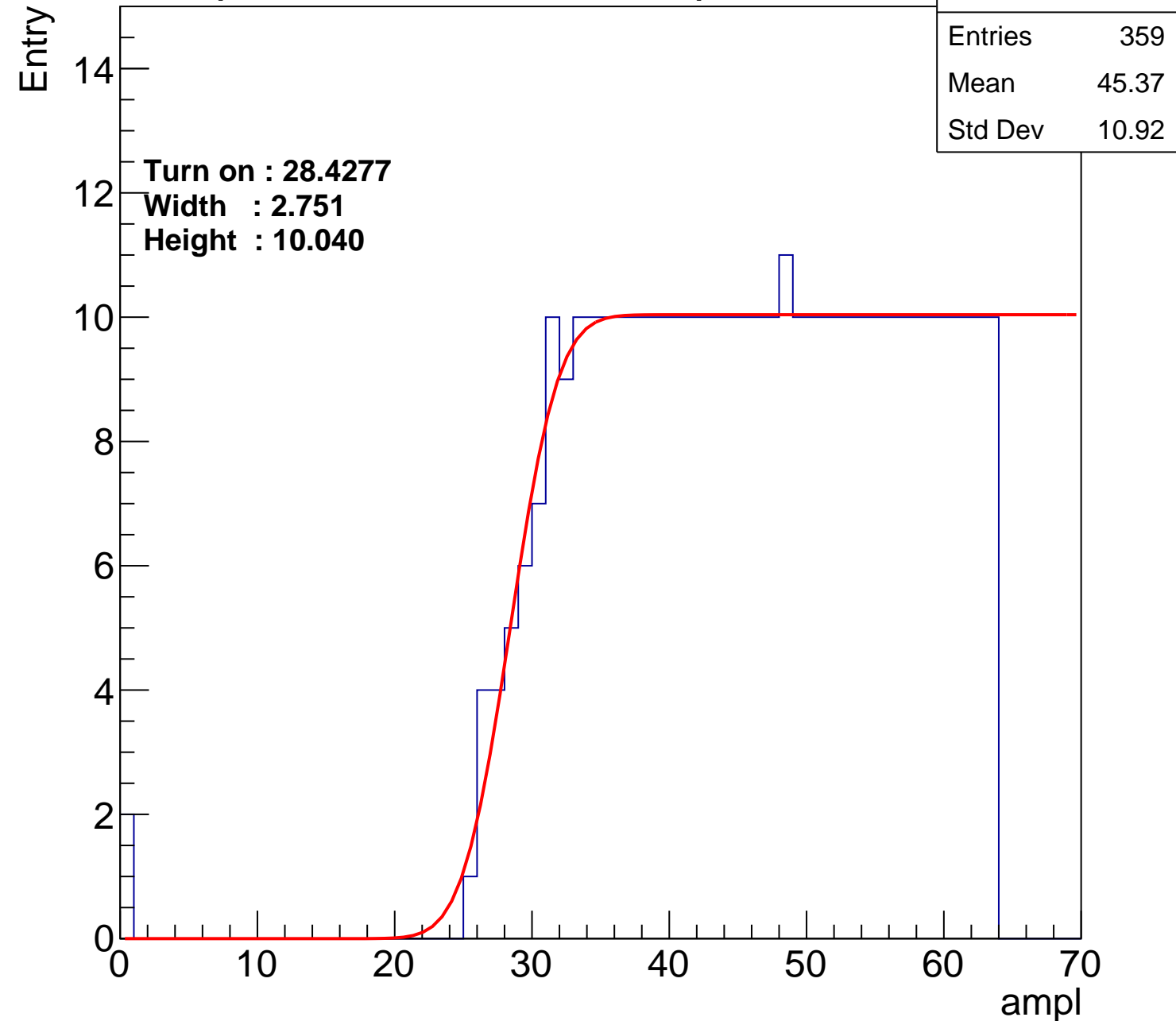
Width : 2.751

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch82

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.26 |
| Std Dev | 11.19 |

Turn on : 29.0637

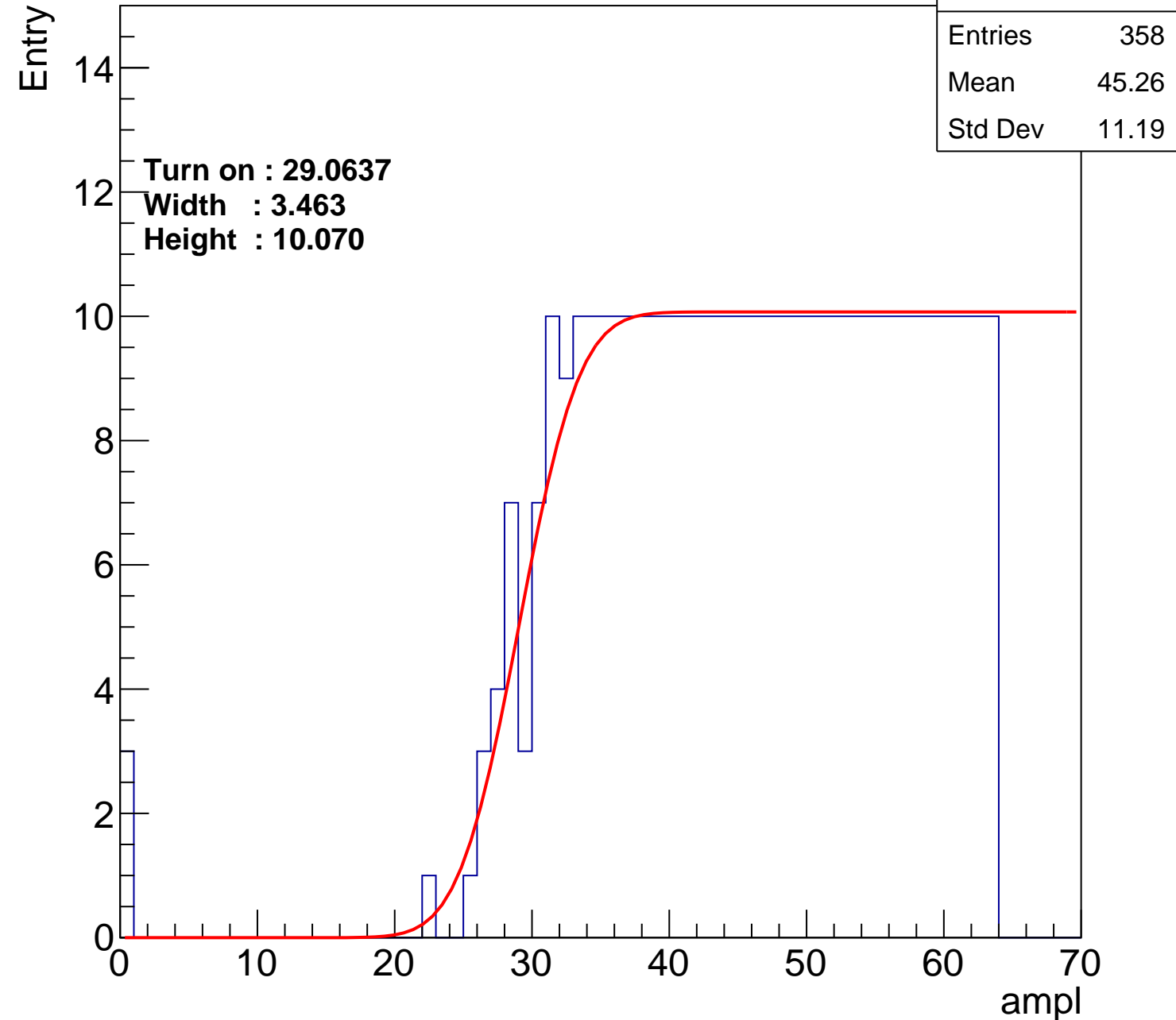
Width : 3.463

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch83

calib_packv5_042523_0143.root, FC#9, port A1

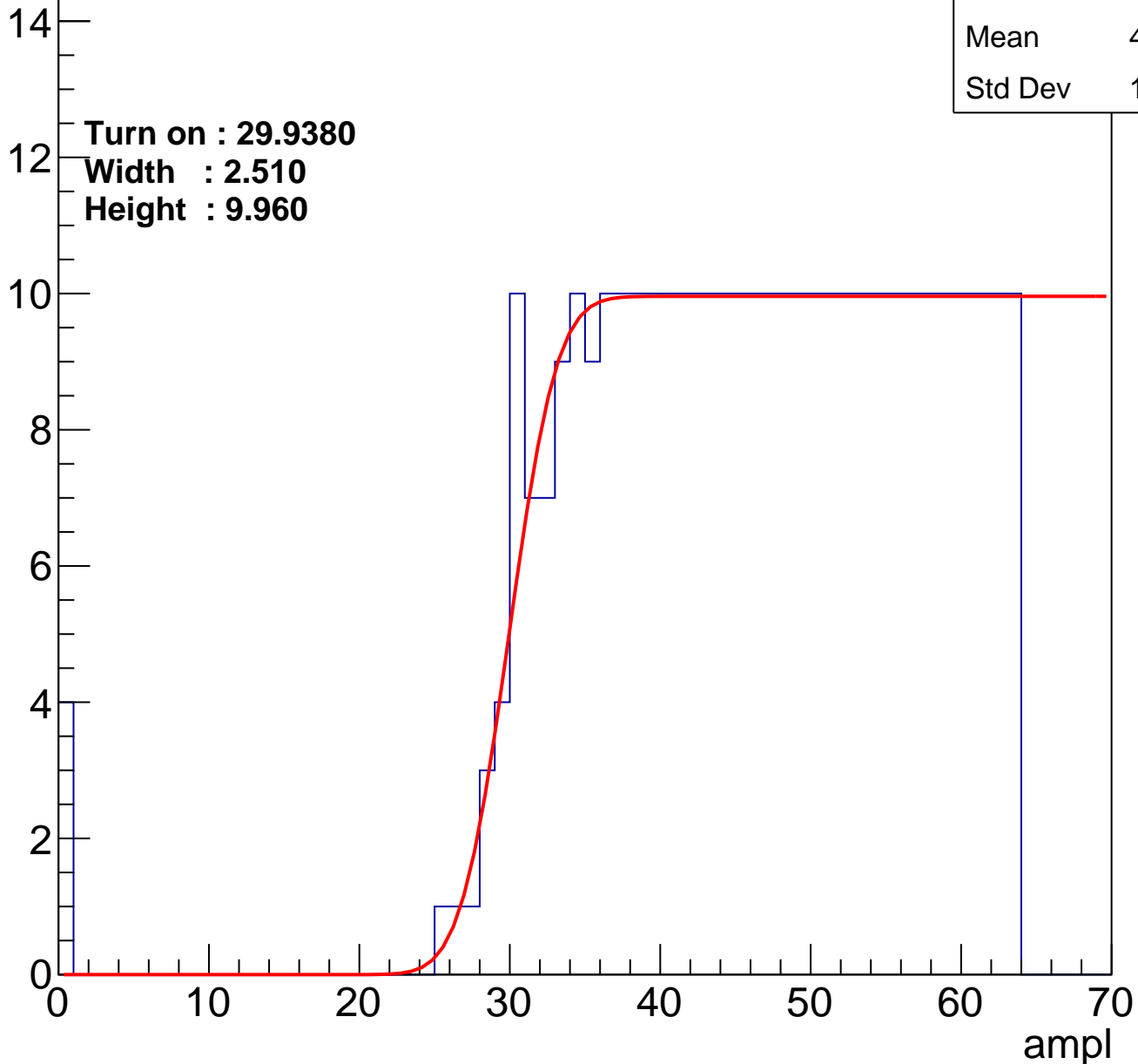
| | |
|---------|-------|
| Entries | 346 |
| Mean | 45.75 |
| Std Dev | 11.15 |

Turn on : 29.9380

Width : 2.510

Height : 9.960

Entry



B0L001S, U23-ch84

calib_packv5_042523_0143.root, FC#9, port A1

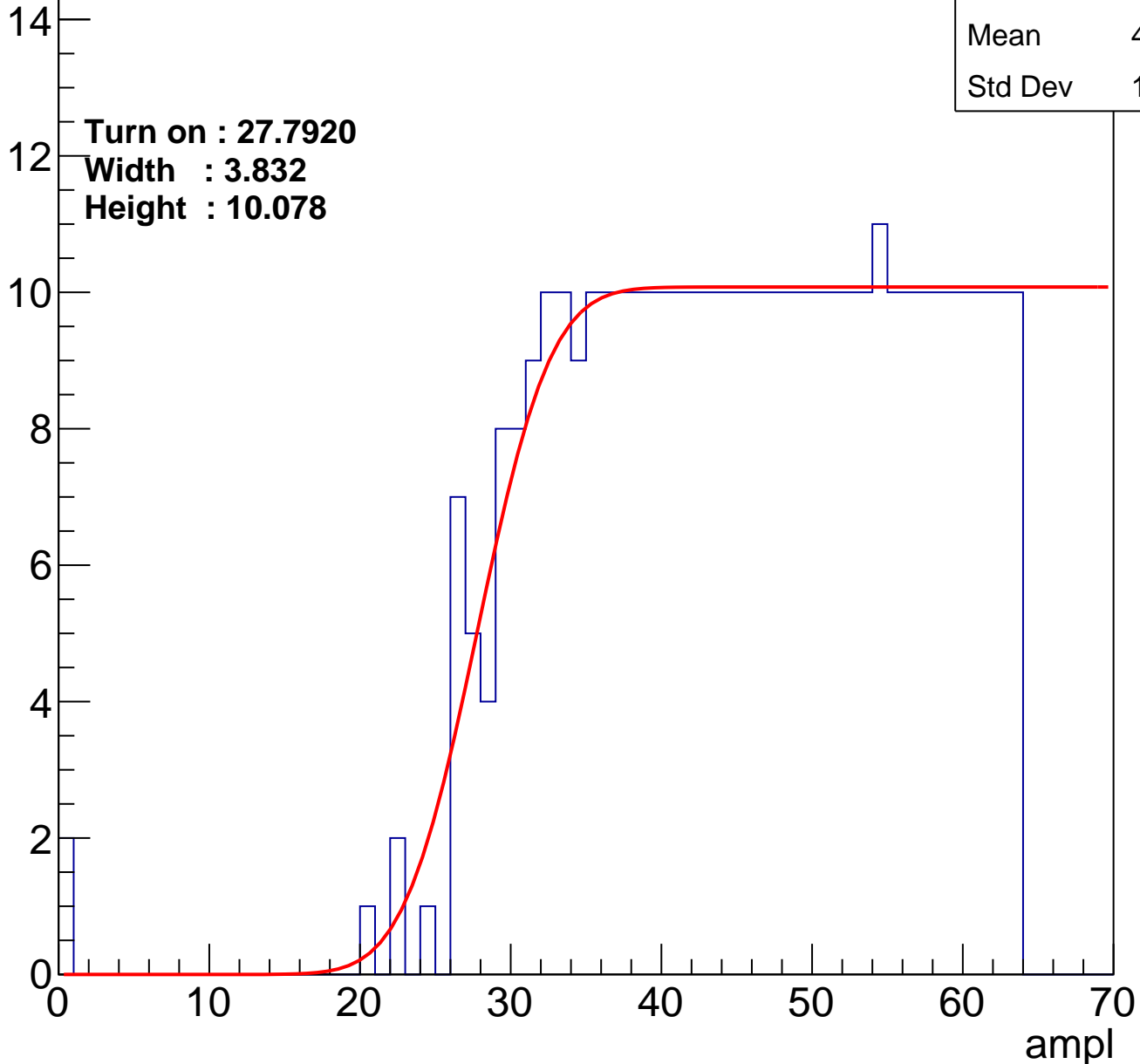
| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.93 |
| Std Dev | 11.24 |

Turn on : 27.7920

Width : 3.832

Height : 10.078

Entry



B0L001S, U23-ch85

calib_packv5_042523_0143.root, FC#9, port A1

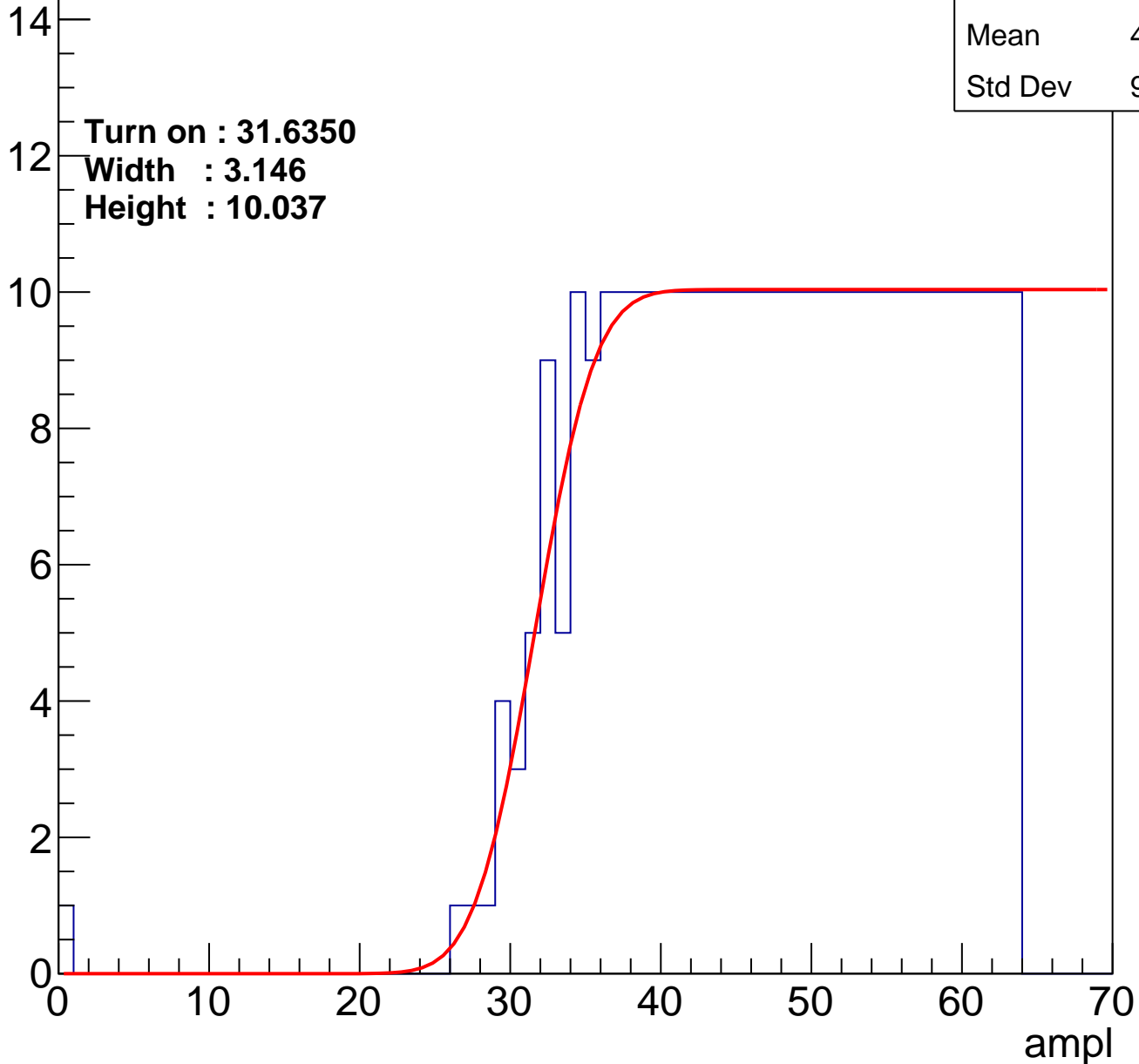
| | |
|---------|-------|
| Entries | 329 |
| Mean | 46.84 |
| Std Dev | 9.993 |

Turn on : 31.6350

Width : 3.146

Height : 10.037

Entry



B0L001S, U23-ch86

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.57 |
| Std Dev | 11.83 |

Turn on : 27.7014

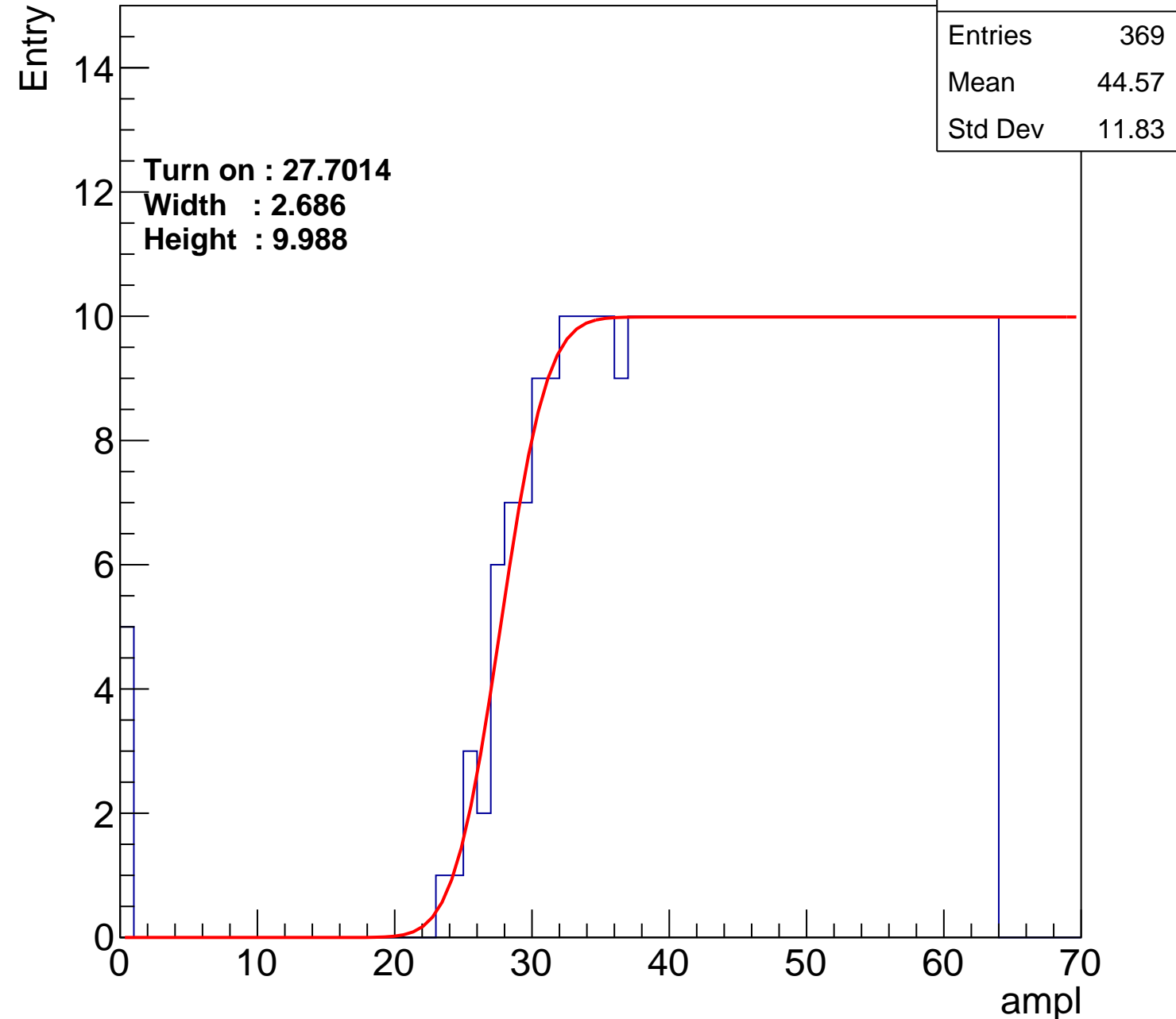
Width : 2.686

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch87

calib_packv5_042523_0143.root, FC#9, port A1

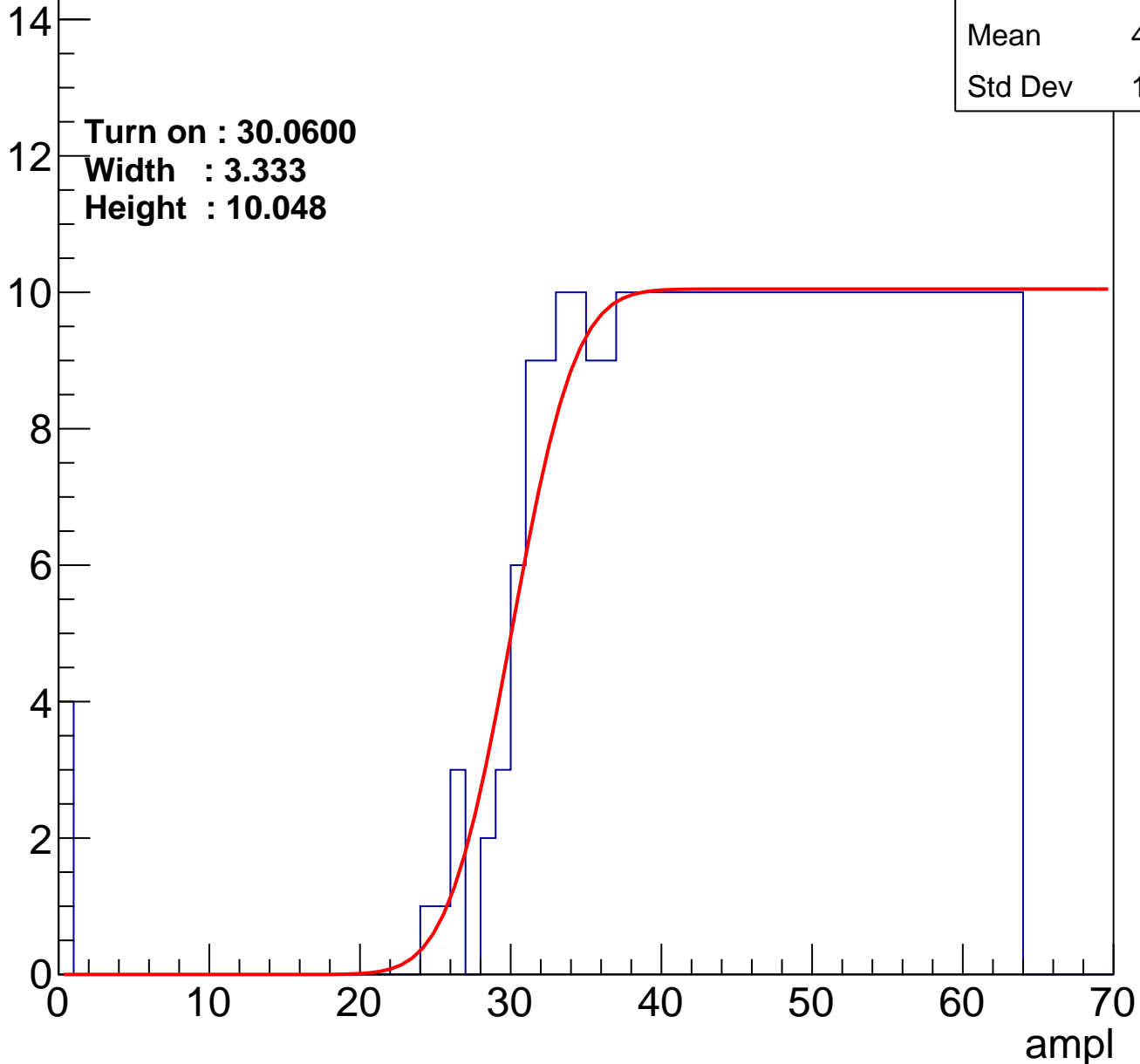
| | |
|---------|-------|
| Entries | 346 |
| Mean | 45.74 |
| Std Dev | 11.18 |

Turn on : 30.0600

Width : 3.333

Height : 10.048

Entry



B0L001S, U23-ch88

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 340 |
| Mean | 46.3 |
| Std Dev | 10.27 |

Turn on : 29.7982

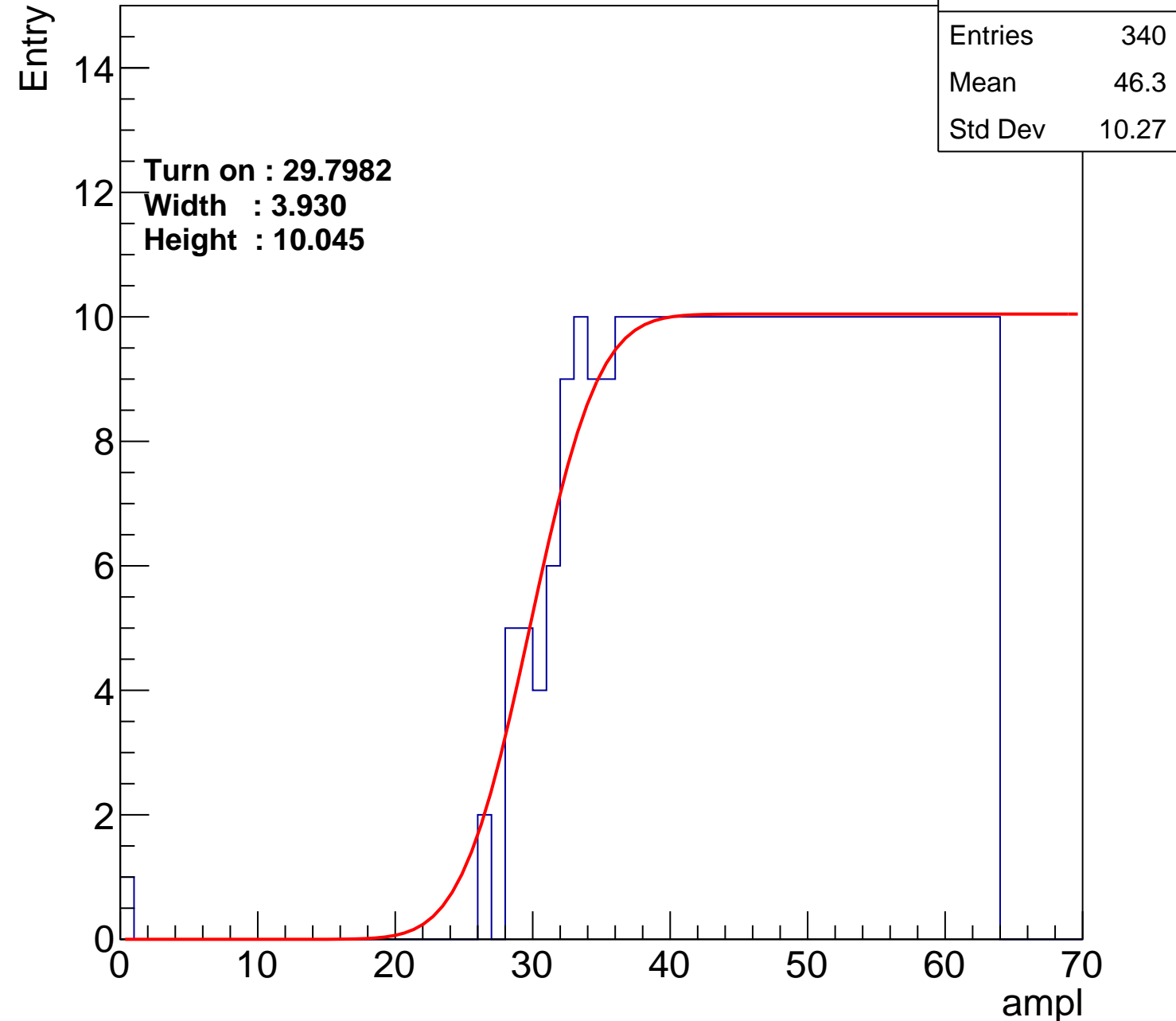
Width : 3.930

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 28.8902
Width : 3.431
Height : 10.077



B0L001S, U23-ch90

calib_packv5_042523_0143.root, FC#9, port A1

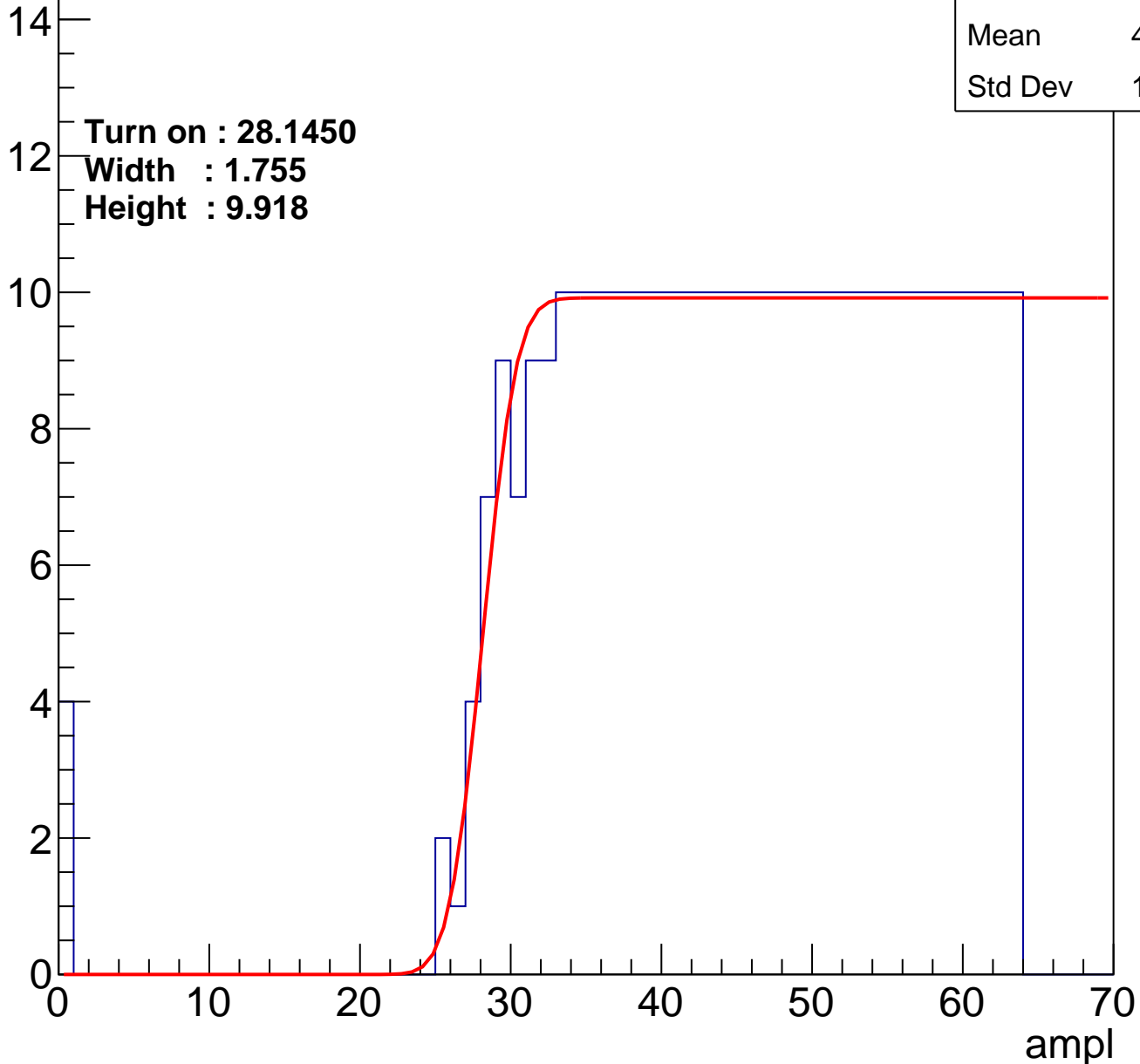
| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.02 |
| Std Dev | 11.43 |

Turn on : 28.1450

Width : 1.755

Height : 9.918

Entry



B0L001S, U23-ch91

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 348 |
| Mean | 45.42 |
| Std Dev | 11.86 |

Turn on : 30.1751

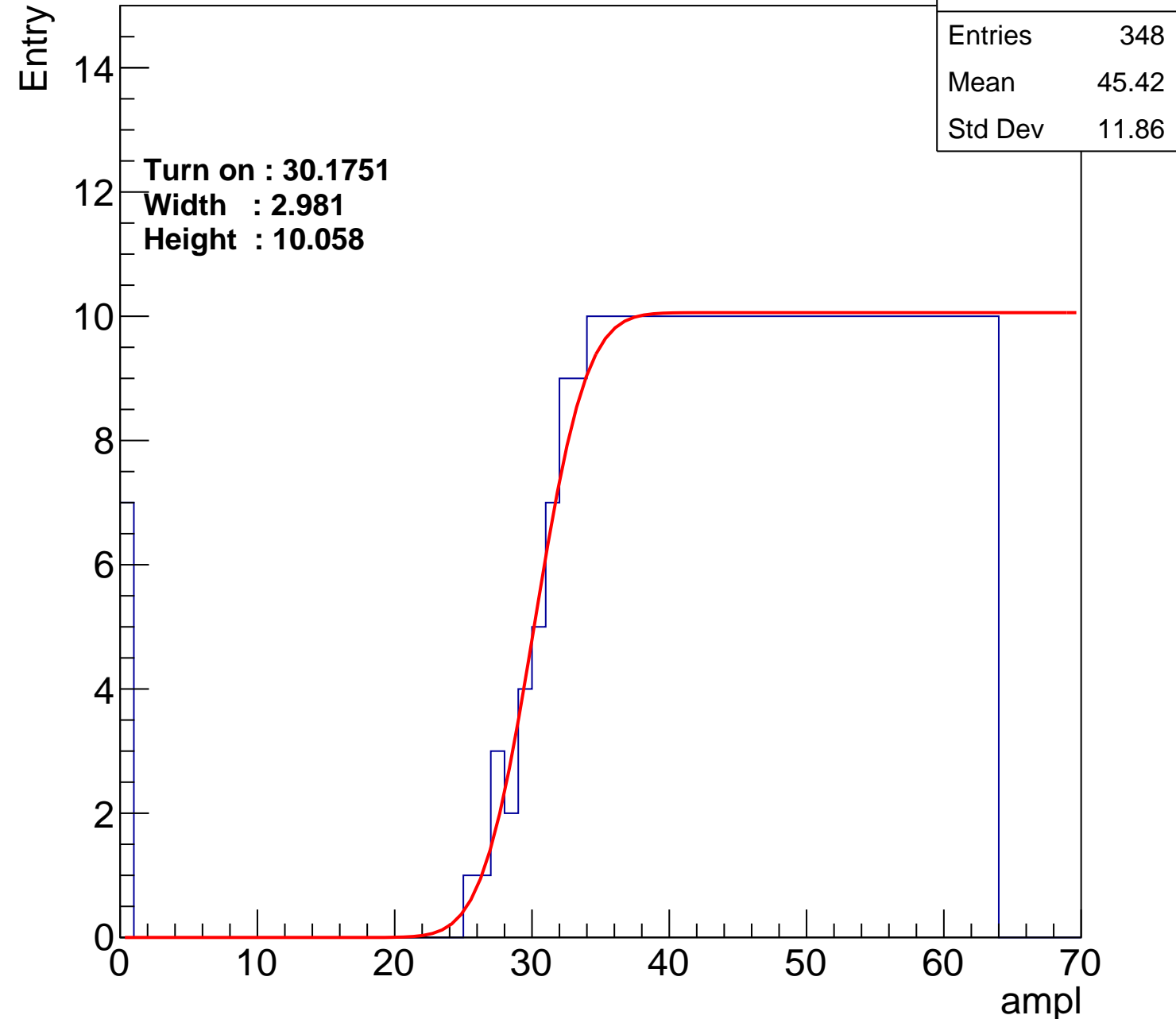
Width : 2.981

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch92

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.08 |
| Std Dev | 11.89 |

Turn on : 26.1837

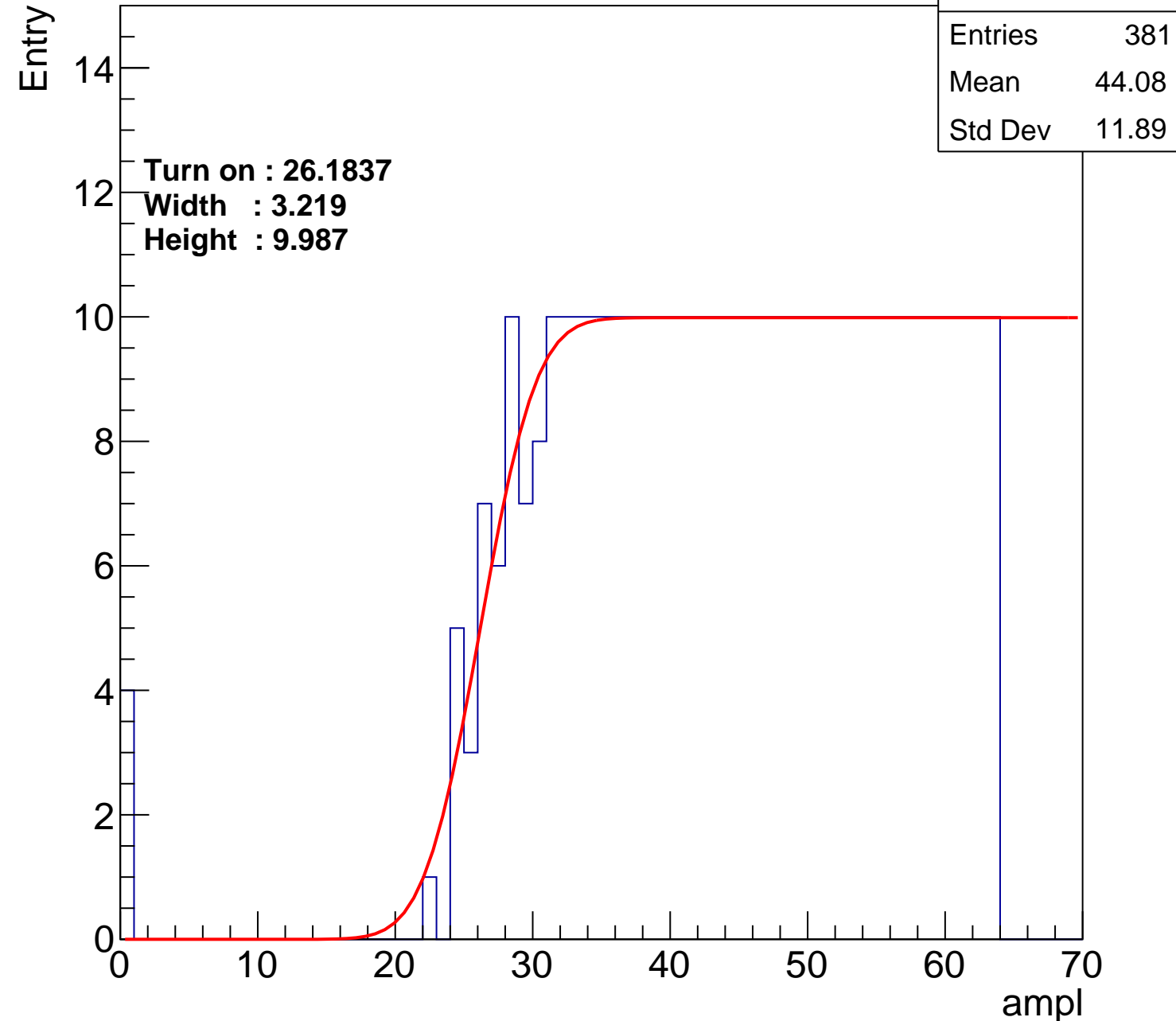
Width : 3.219

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch93

calib_packv5_042523_0143.root, FC#9, port A1

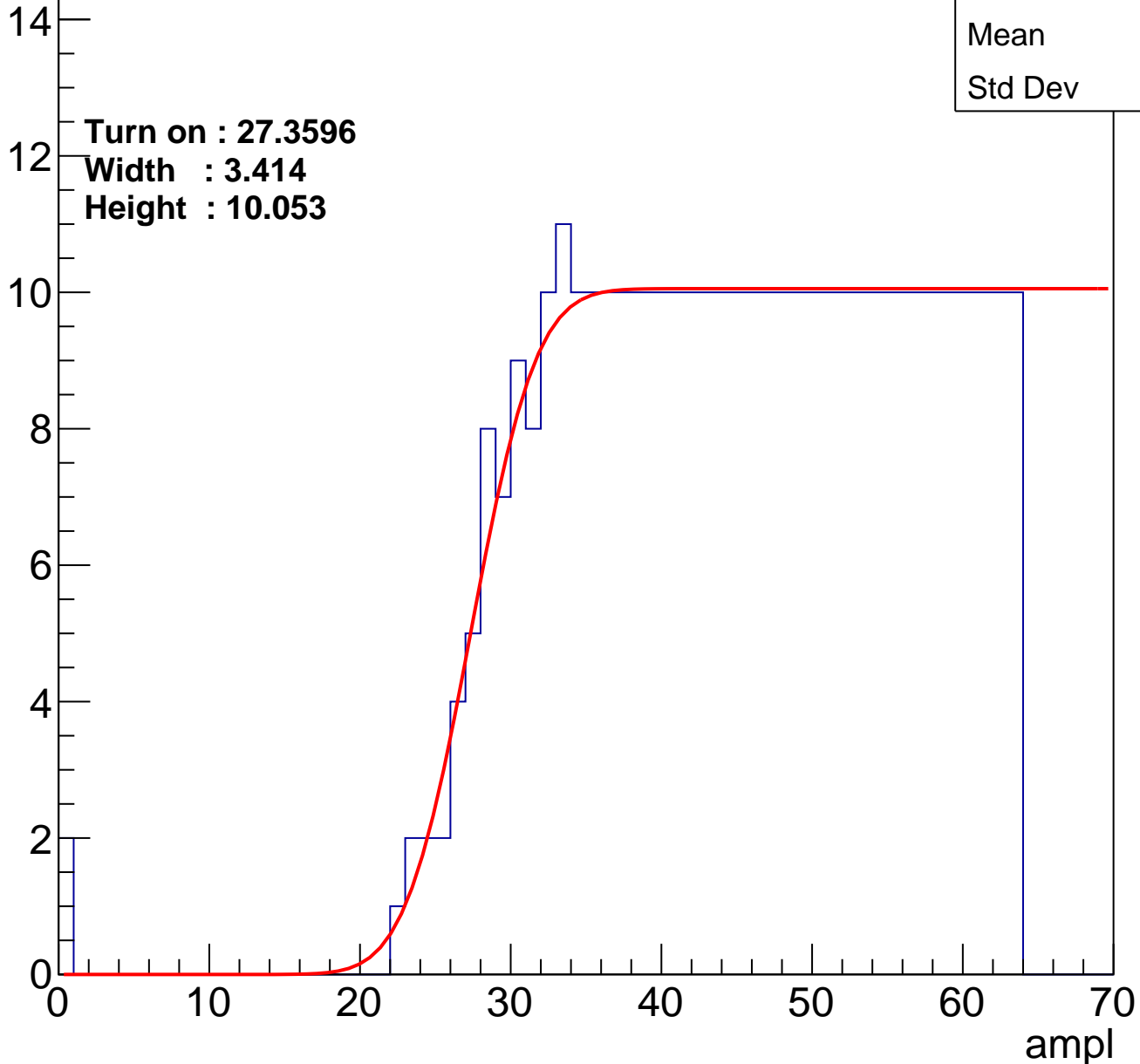
| | |
|---------|------|
| Entries | 371 |
| Mean | 44.7 |
| Std Dev | 11.3 |

Turn on : 27.3596

Width : 3.414

Height : 10.053

Entry



B0L001S, U23-ch94

calib_packv5_042523_0143.root, FC#9, port A1

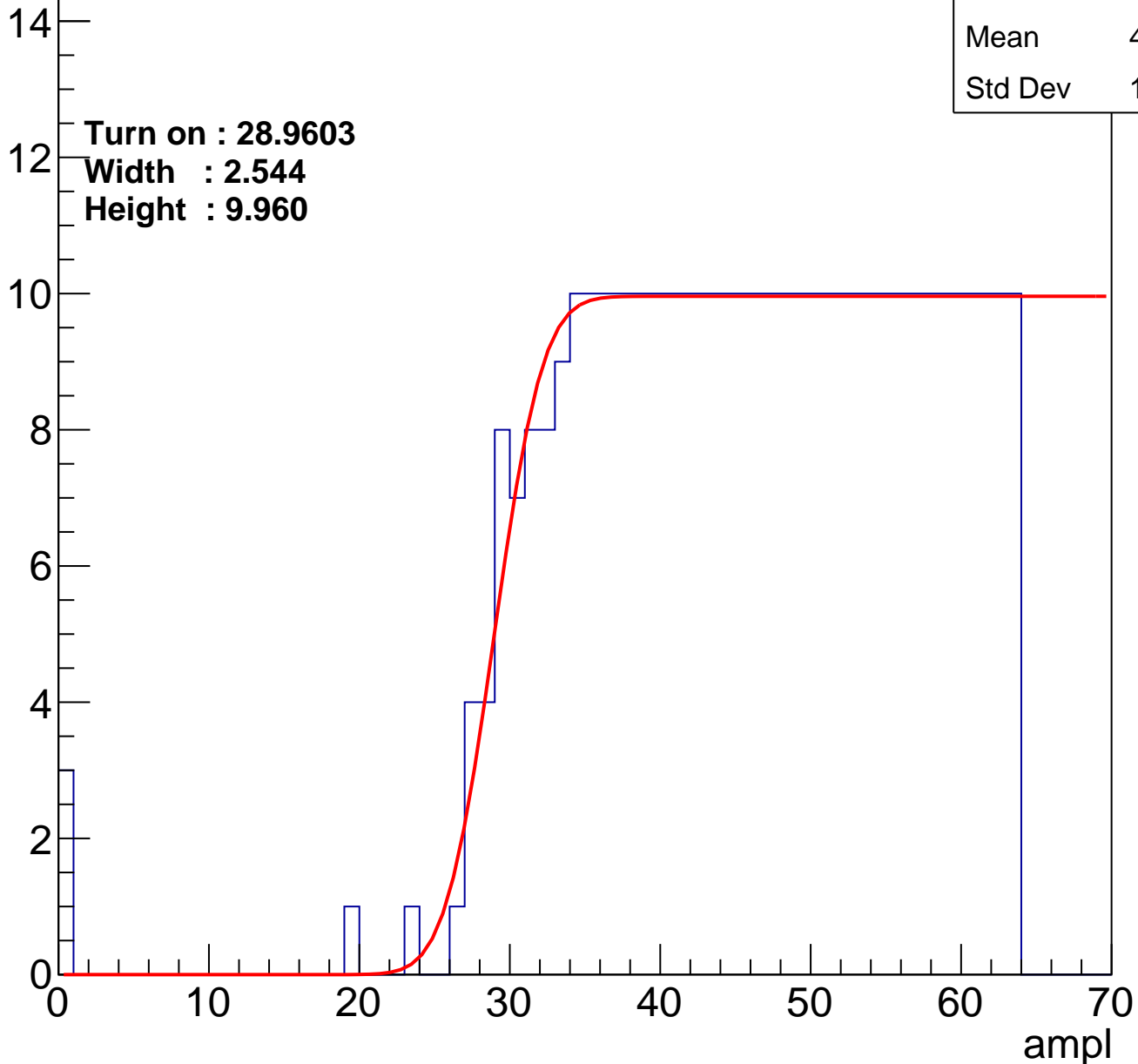
| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.43 |
| Std Dev | 11.15 |

Turn on : 28.9603

Width : 2.544

Height : 9.960

Entry



calib_packv5_042523_0143.root, FC#9, port A1

calib_packv5_042523_0143.root, FC#9, port A1

Turn on : 26.9240
Width : 2.788
Height : 10.029



B0L001S, U23-ch96

calib_packv5_042523_0143.root, FC#9, port A1

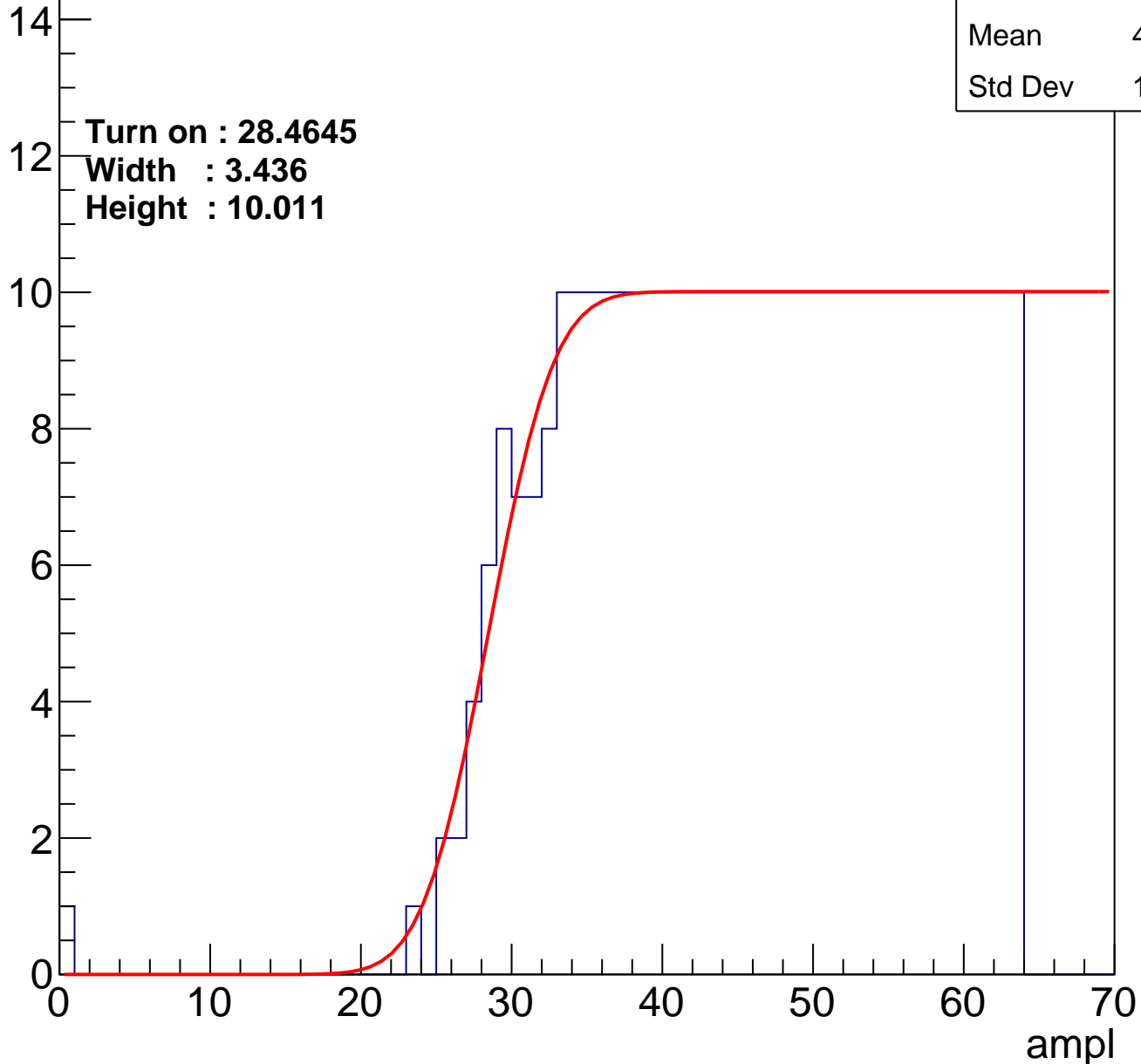
| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.49 |
| Std Dev | 10.72 |

Turn on : 28.4645

Width : 3.436

Height : 10.011

Entry



B0L001S, U23-ch97

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.98 |
| Std Dev | 11.66 |

Turn on : 26.2485

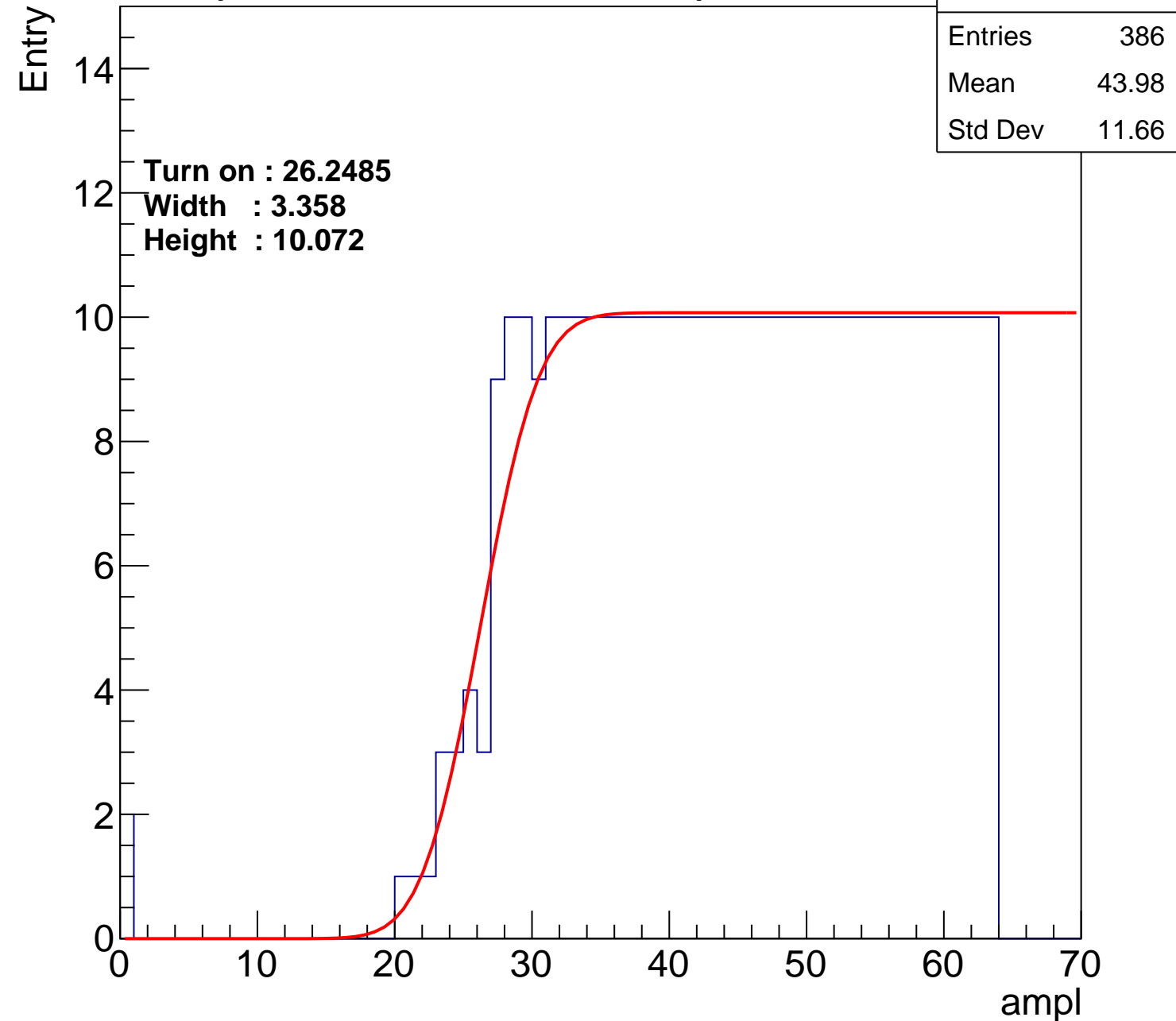
Width : 3.358

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch98

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.09 |
| Std Dev | 11.3 |

Turn on : 28.6794

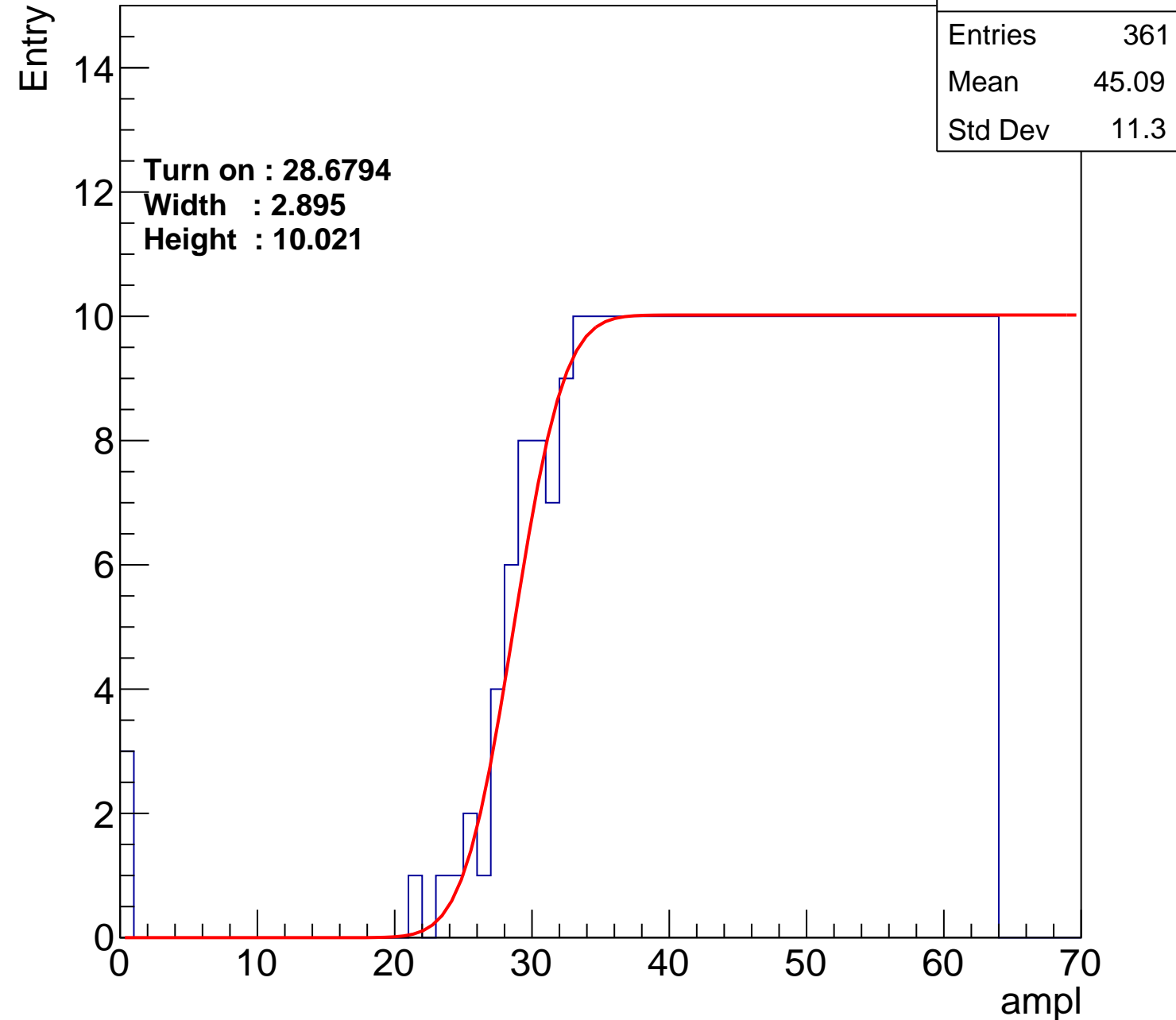
Width : 2.895

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch99

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.38 |
| Std Dev | 11.84 |

Turn on : 27.2188

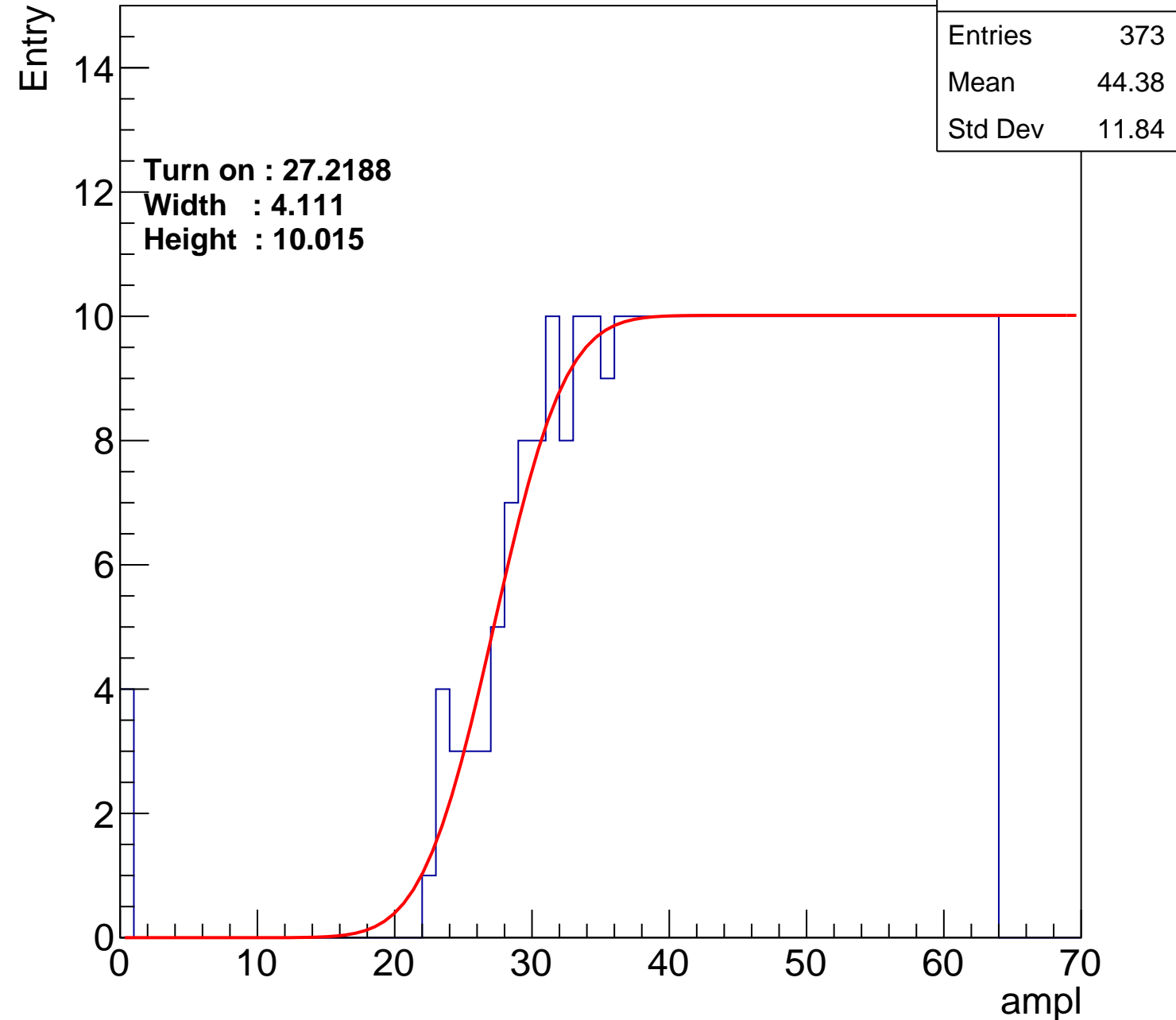
Width : 4.111

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch100

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.59 |
| Std Dev | 11.73 |

Turn on : 27.4820

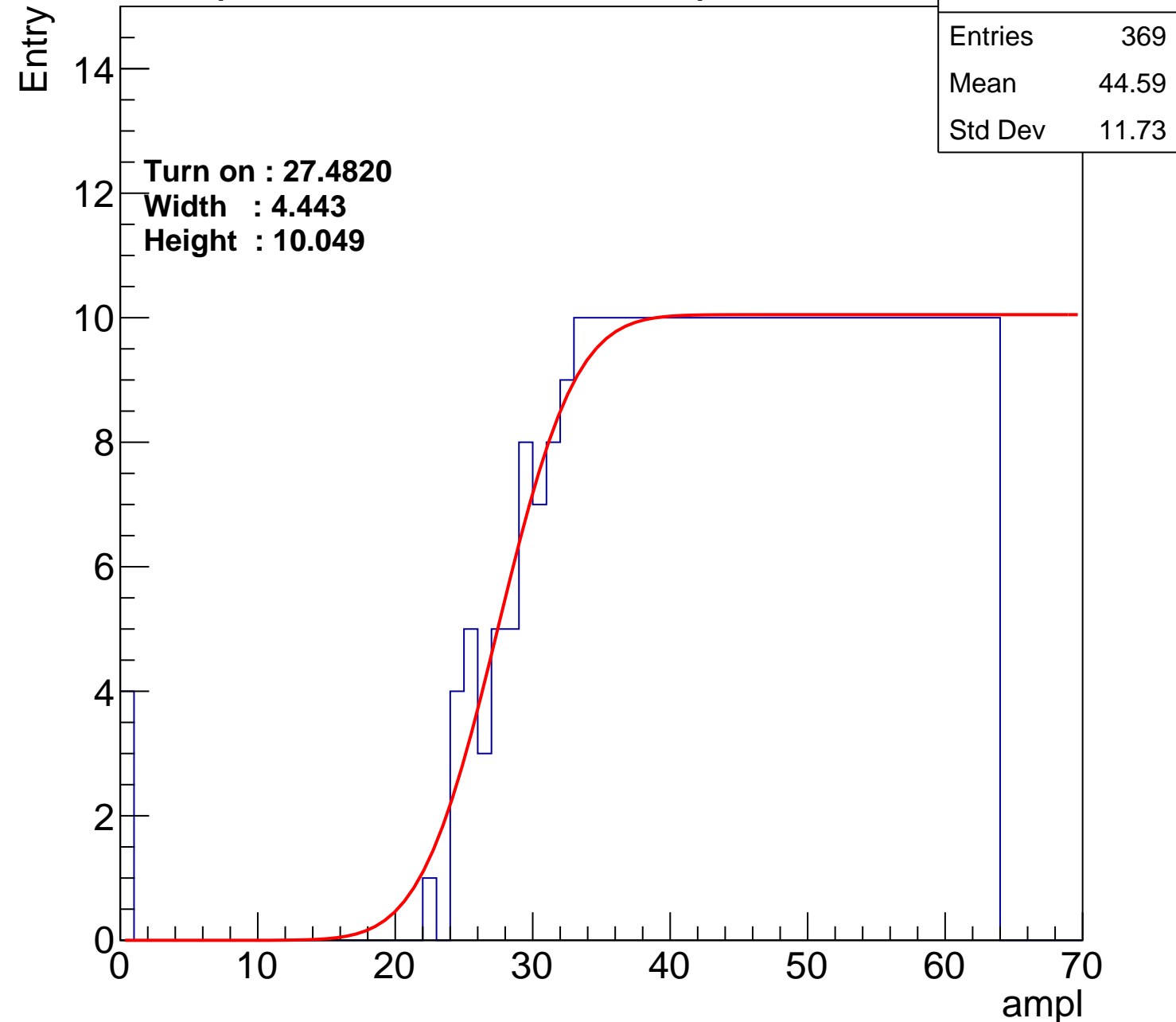
Width : 4.443

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch101

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.65 |
| Std Dev | 11.32 |

Turn on : 26.9337

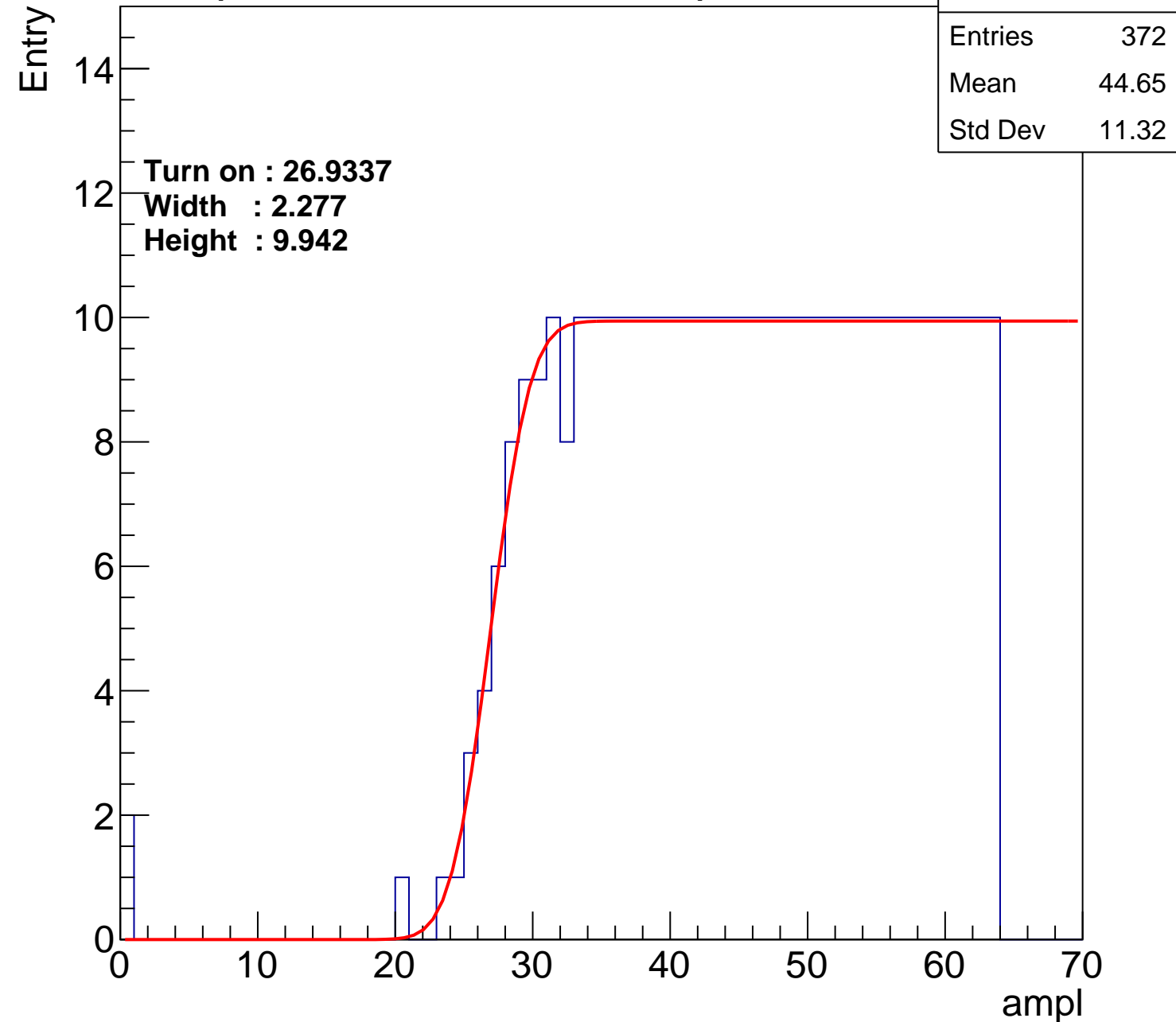
Width : 2.277

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch102

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.34 |
| Std Dev | 10.82 |

Turn on : 29.3851

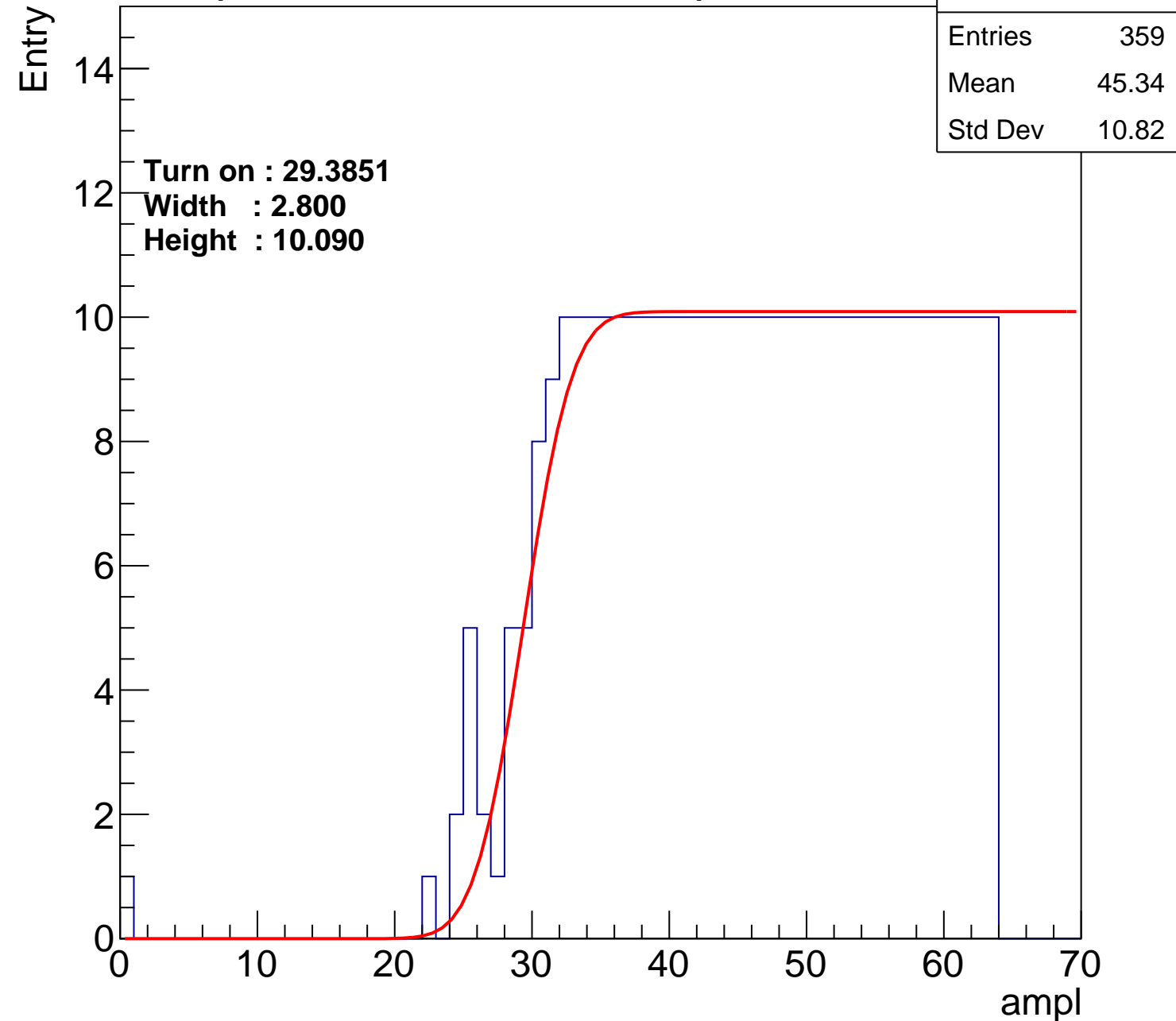
Width : 2.800

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch103

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 344 |
| Mean | 45.82 |
| Std Dev | 11.16 |

Turn on : 29.3942

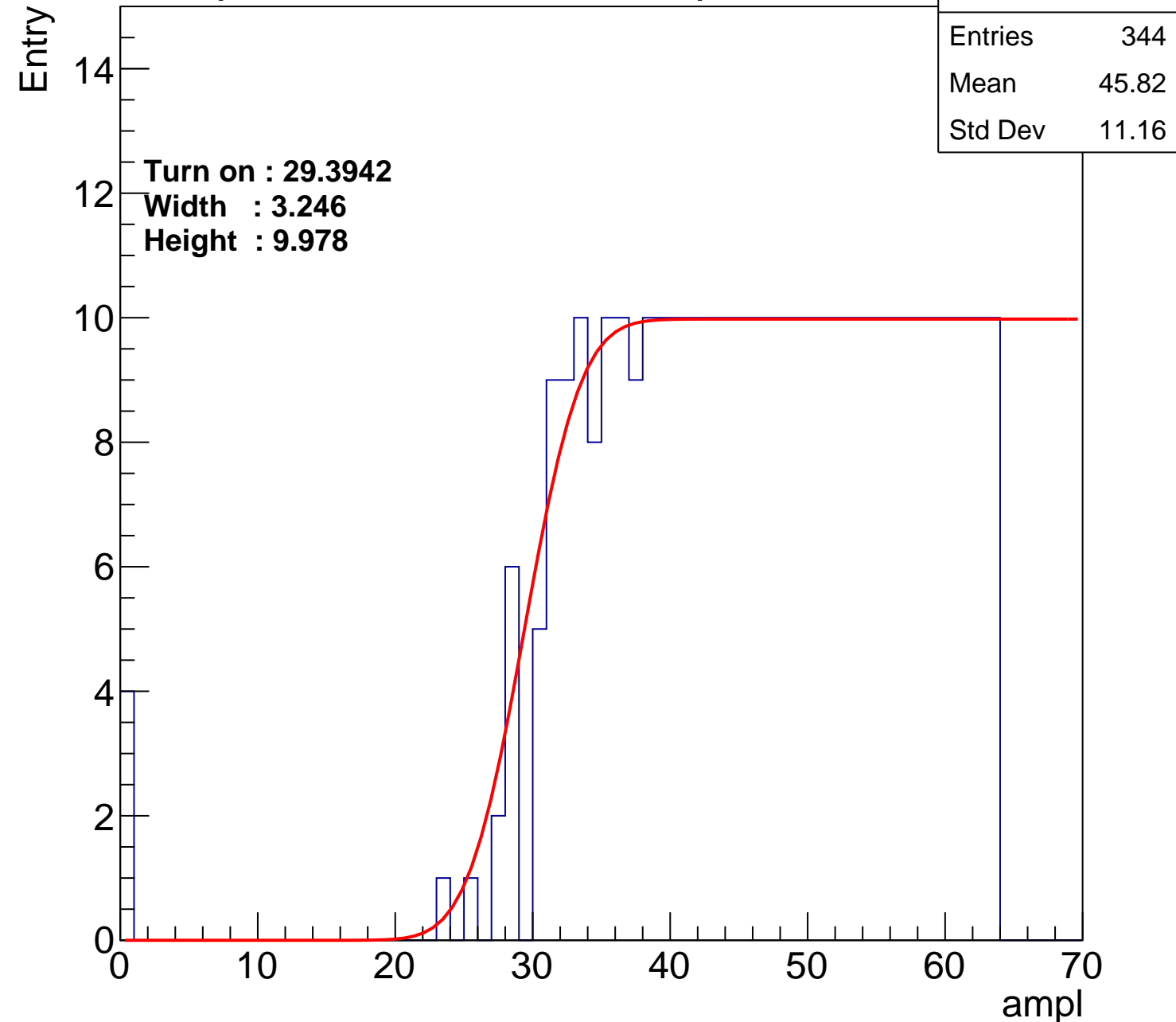
Width : 3.246

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch104

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-----|
| Entries | 358 |
|---------|-----|

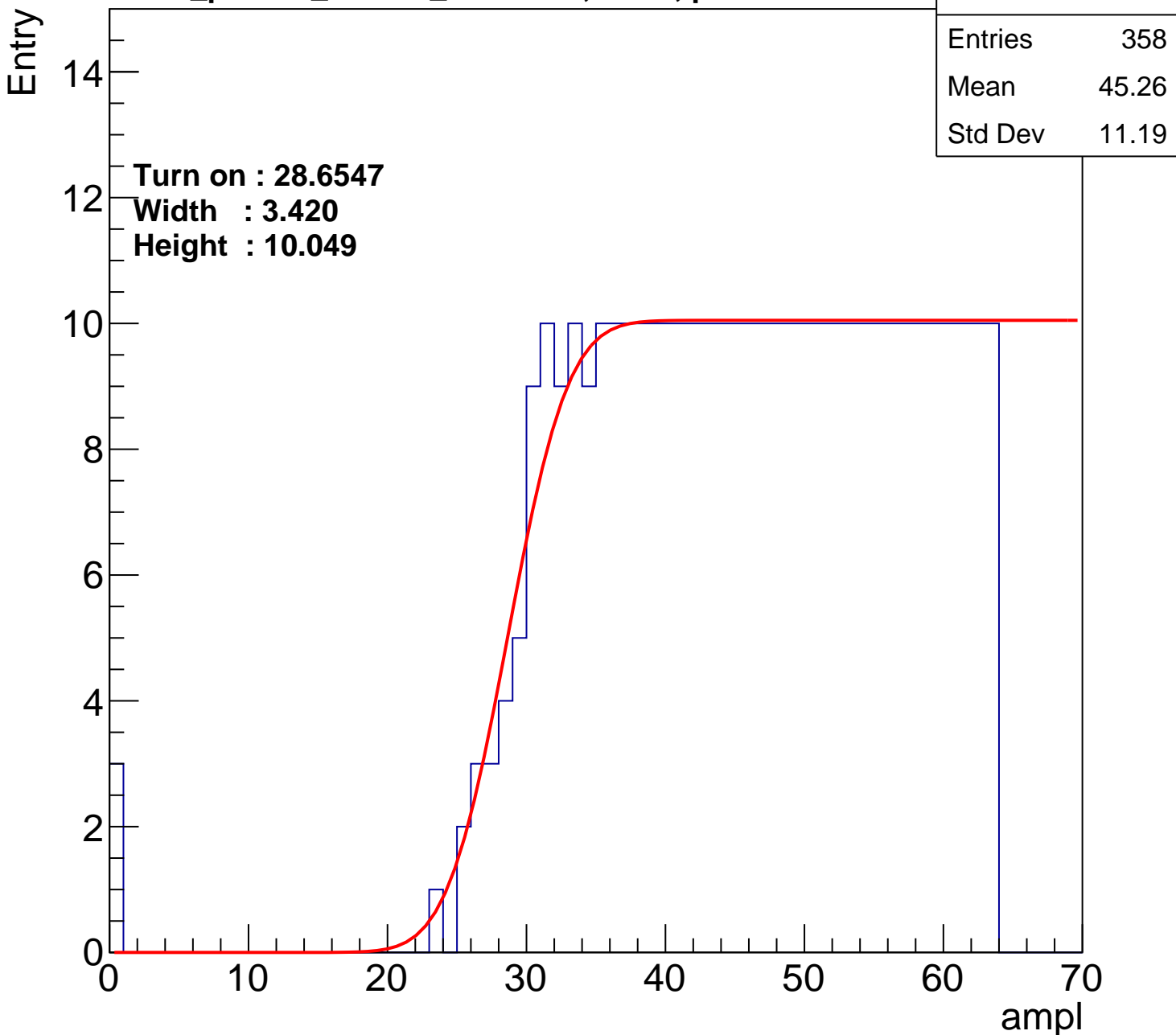
| | |
|------|-------|
| Mean | 45.26 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 11.19 |
|---------|-------|

Turn on : 28.6547

Width : 3.420

Height : 10.049



B0L001S, U23-ch105

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.53 |
| Std Dev | 10.88 |

Turn on : 29.2634

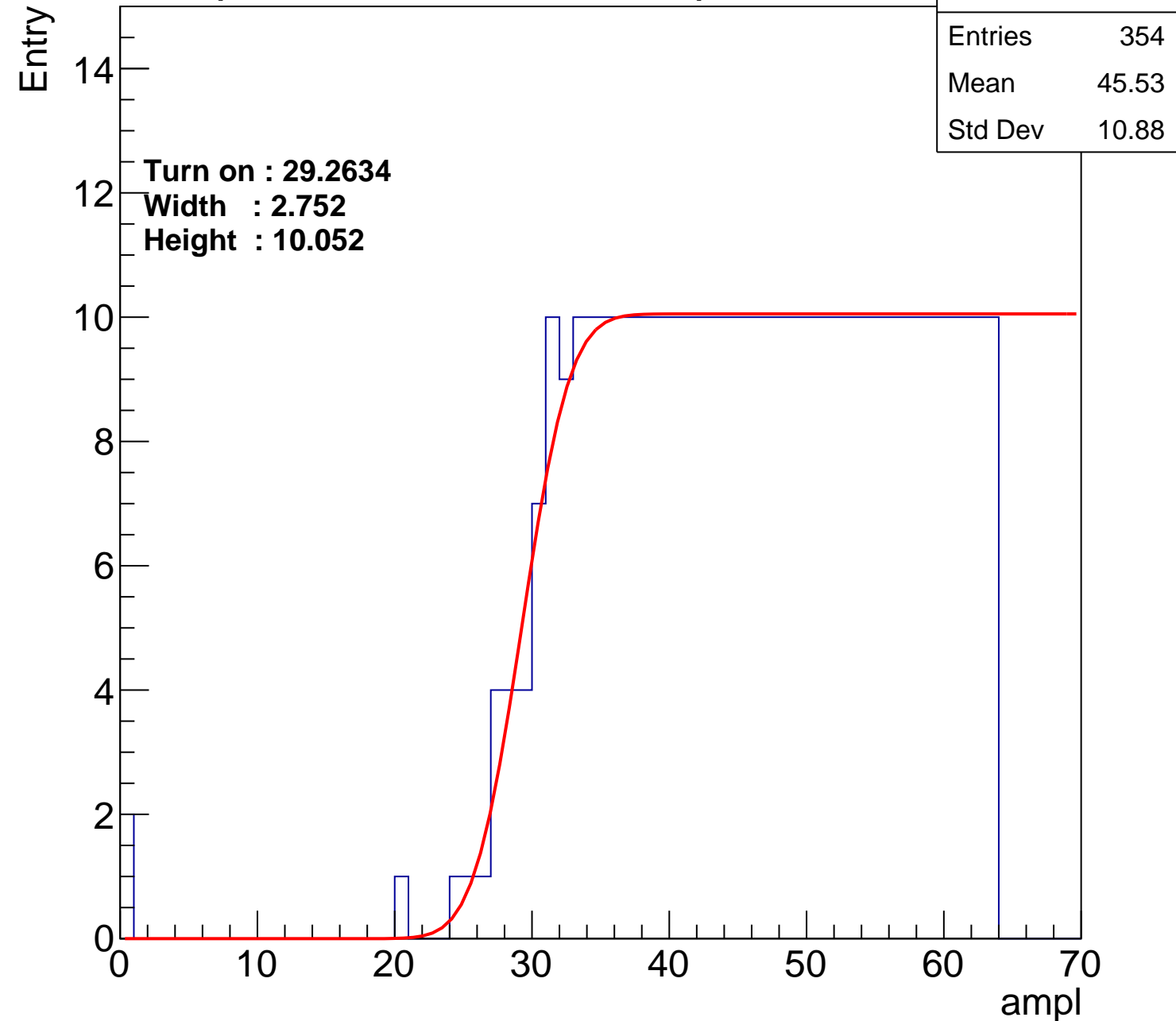
Width : 2.752

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch106

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.38 |
| Std Dev | 10.96 |

Turn on : 28.8014

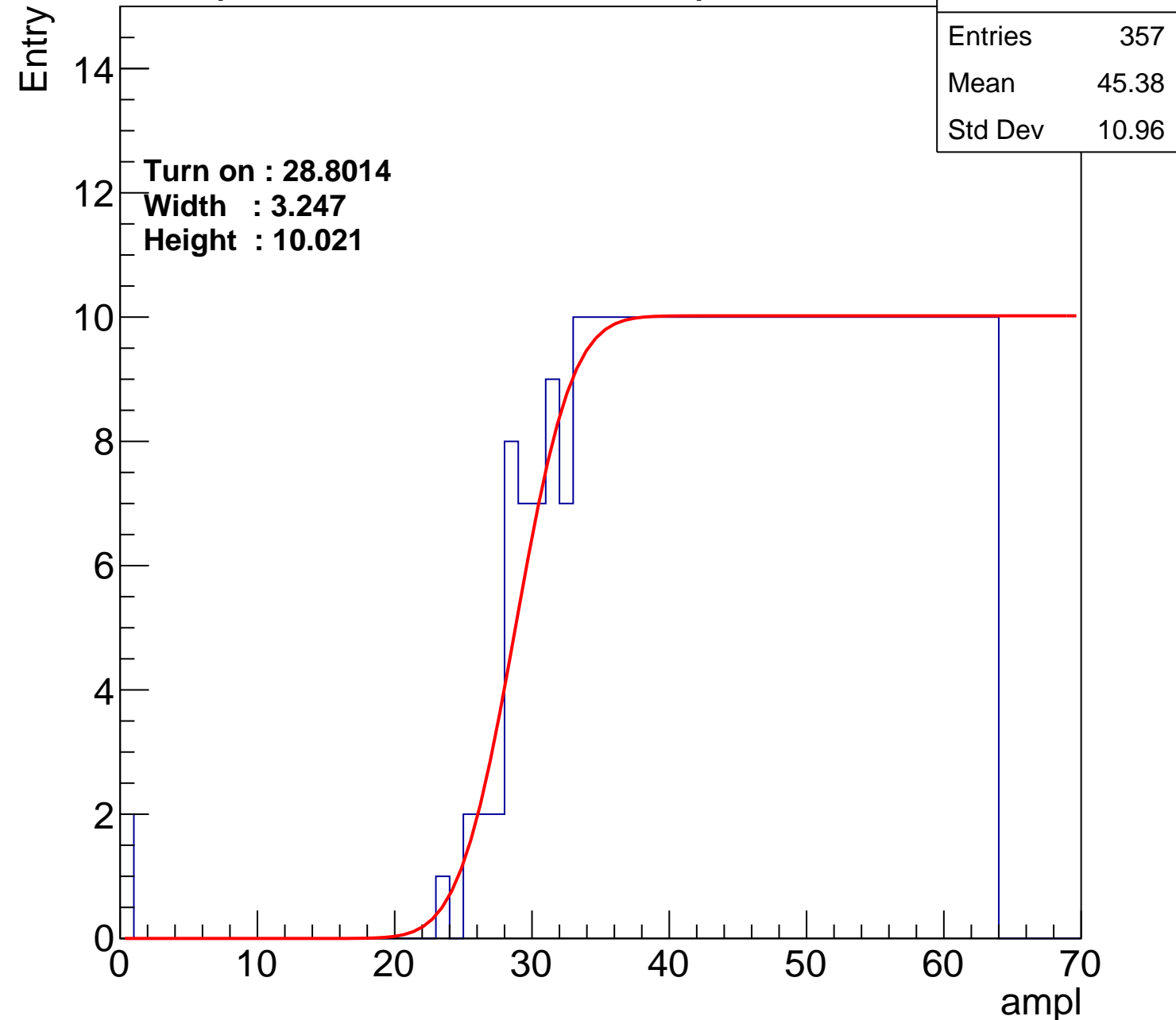
Width : 3.247

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch107

calib_packv5_042523_0143.root, FC#9, port A1

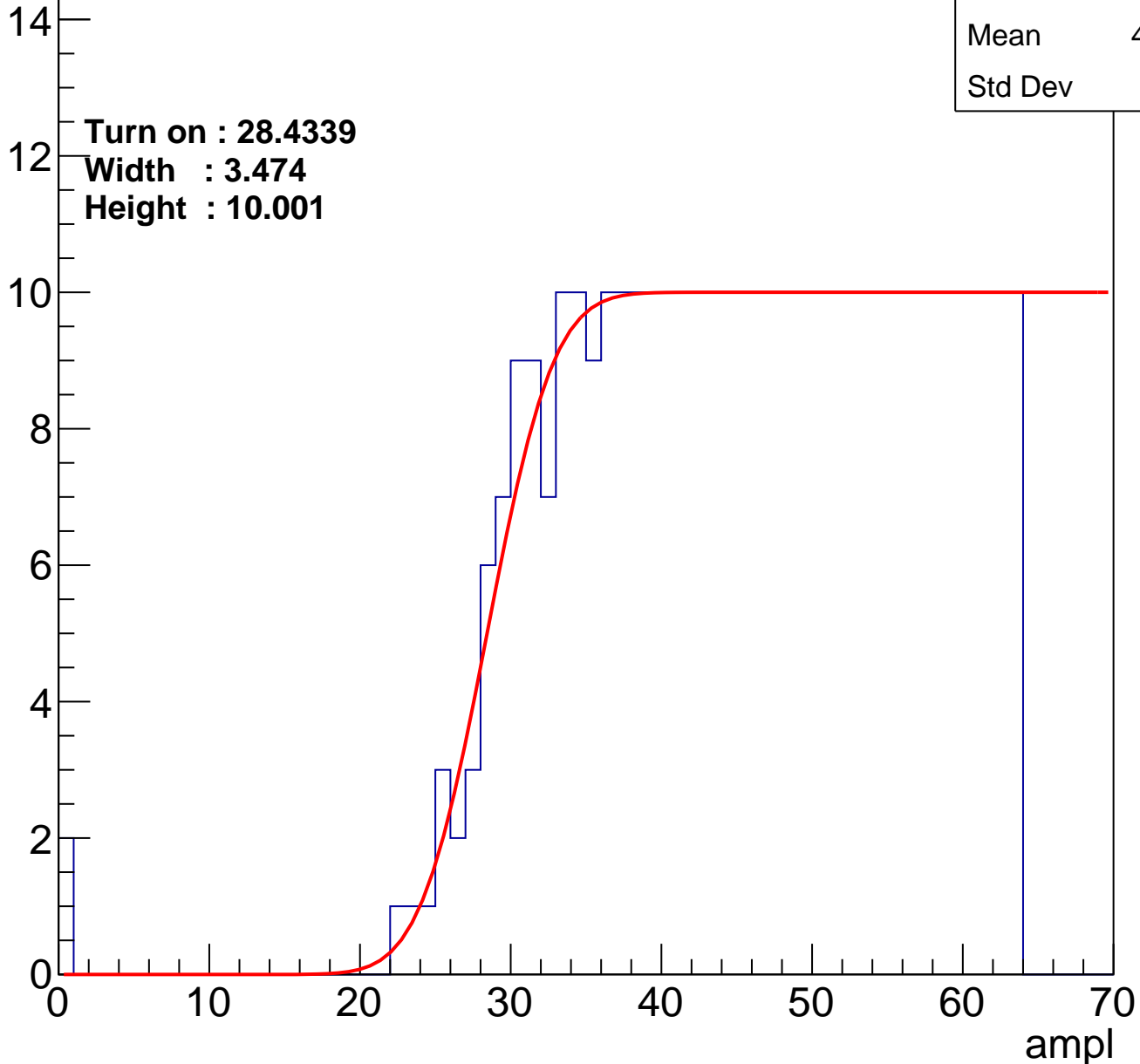
| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.18 |
| Std Dev | 11.1 |

Turn on : 28.4339

Width : 3.474

Height : 10.001

Entry



B0L001S, U23-ch108

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.34 |
| Std Dev | 10.95 |

Turn on : 28.3049

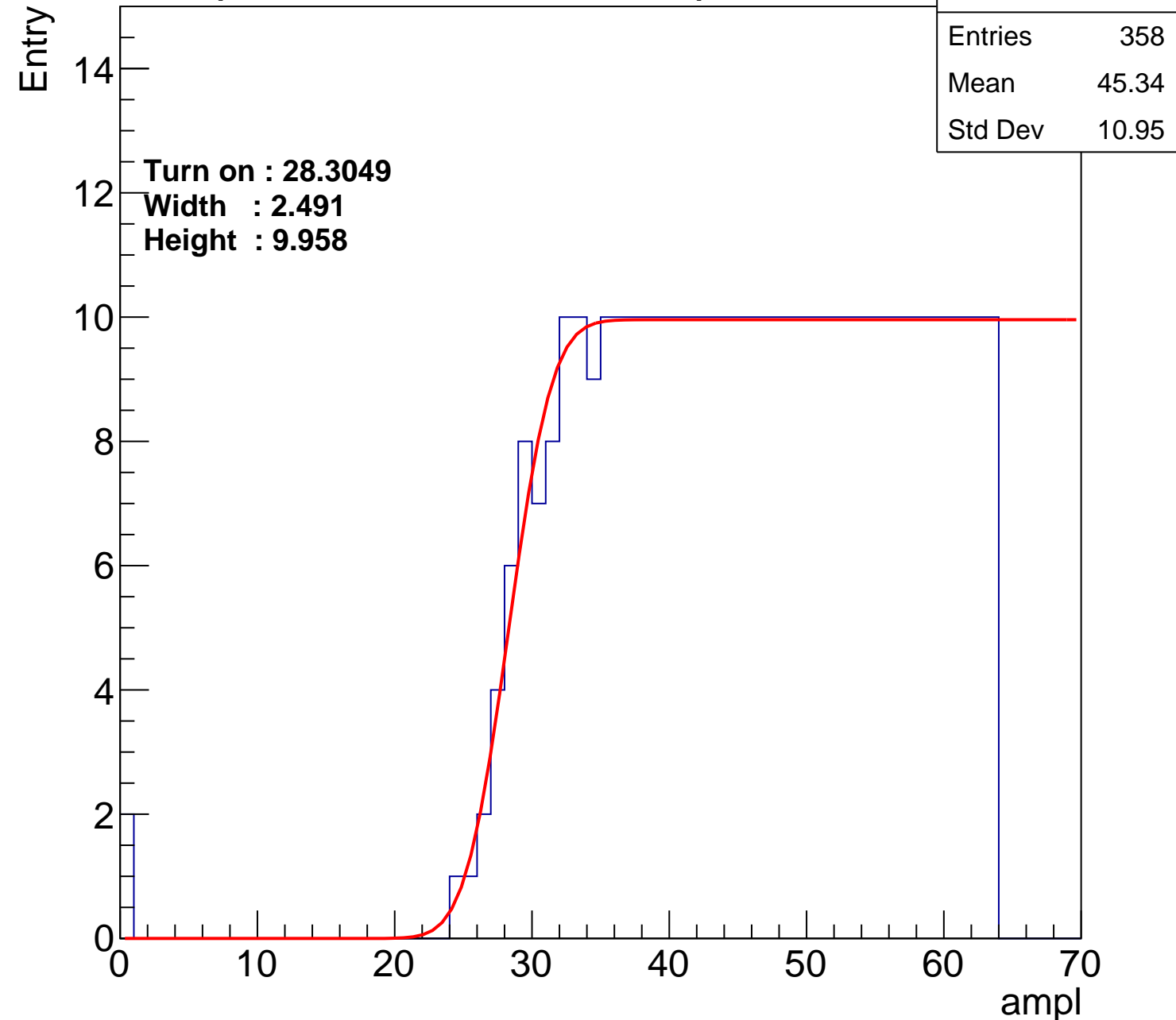
Width : 2.491

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch109

calib_packv5_042523_0143.root, FC#9, port A1

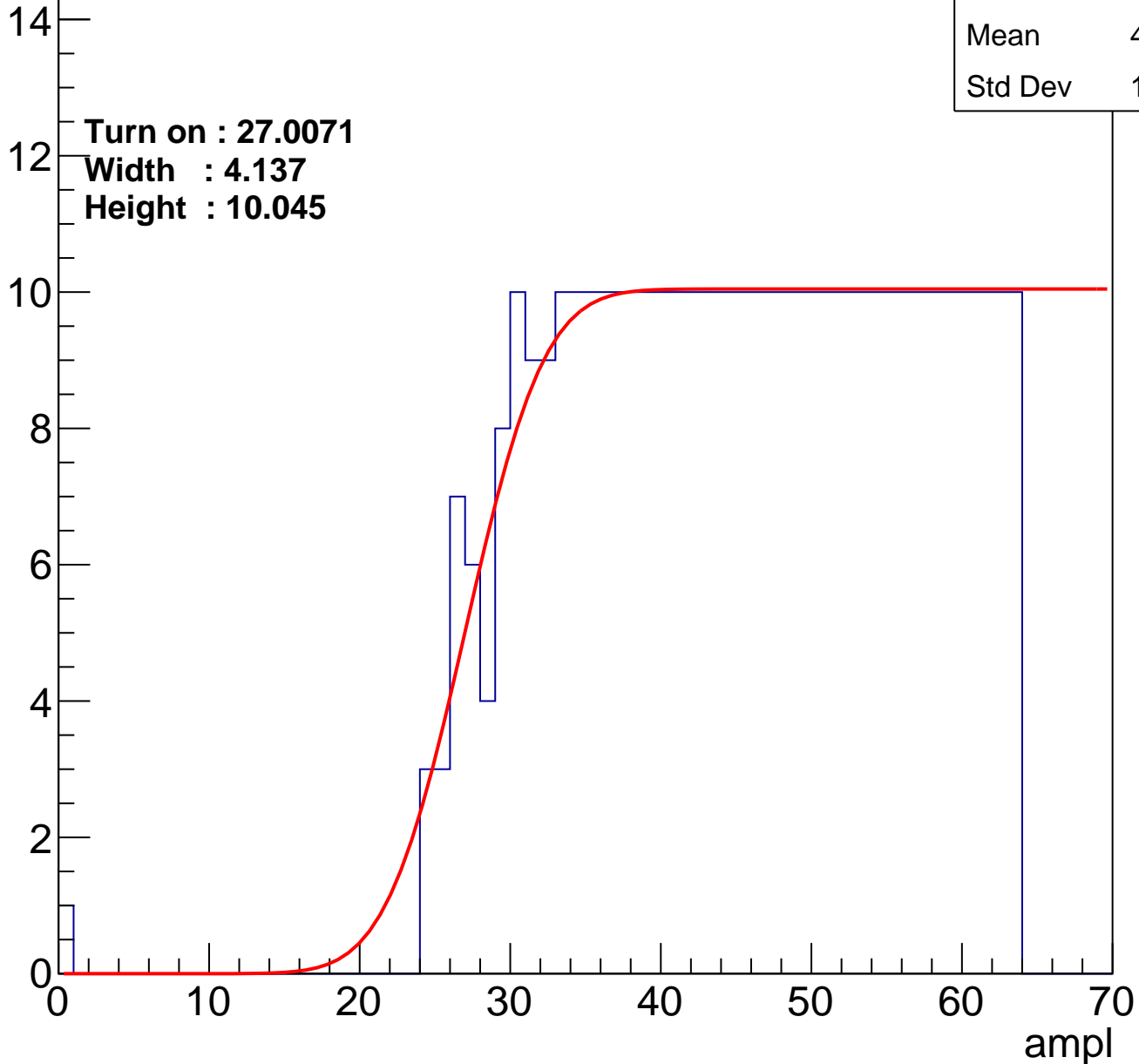
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.82 |
| Std Dev | 11.07 |

Turn on : 27.0071

Width : 4.137

Height : 10.045

Entry



B0L001S, U23-ch110

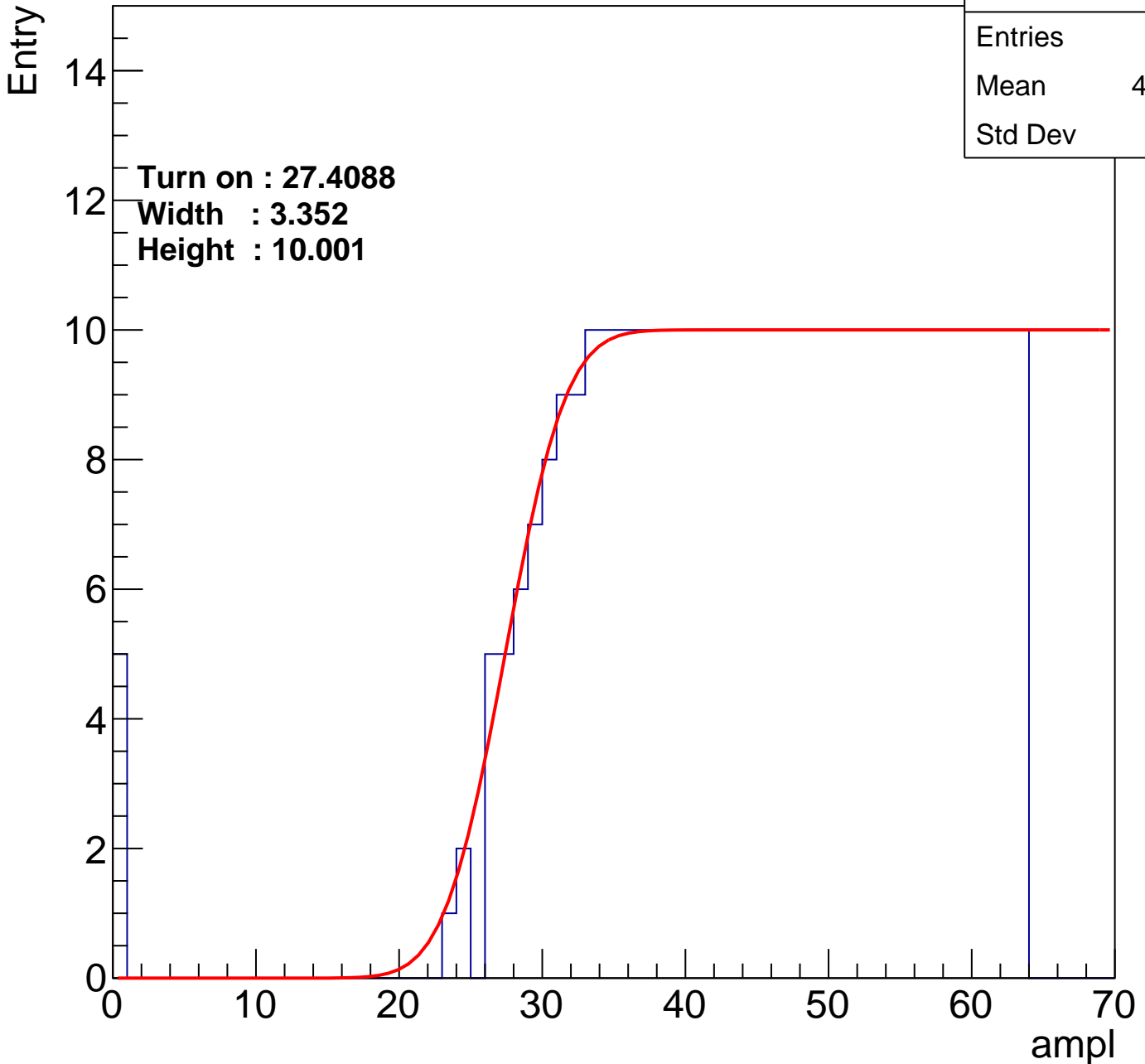
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.67 |
| Std Dev | 11.8 |

Turn on : 27.4088

Width : 3.352

Height : 10.001



B0L001S, U23-ch111

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.88 |
| Std Dev | 11.4 |

Turn on : 27.8106

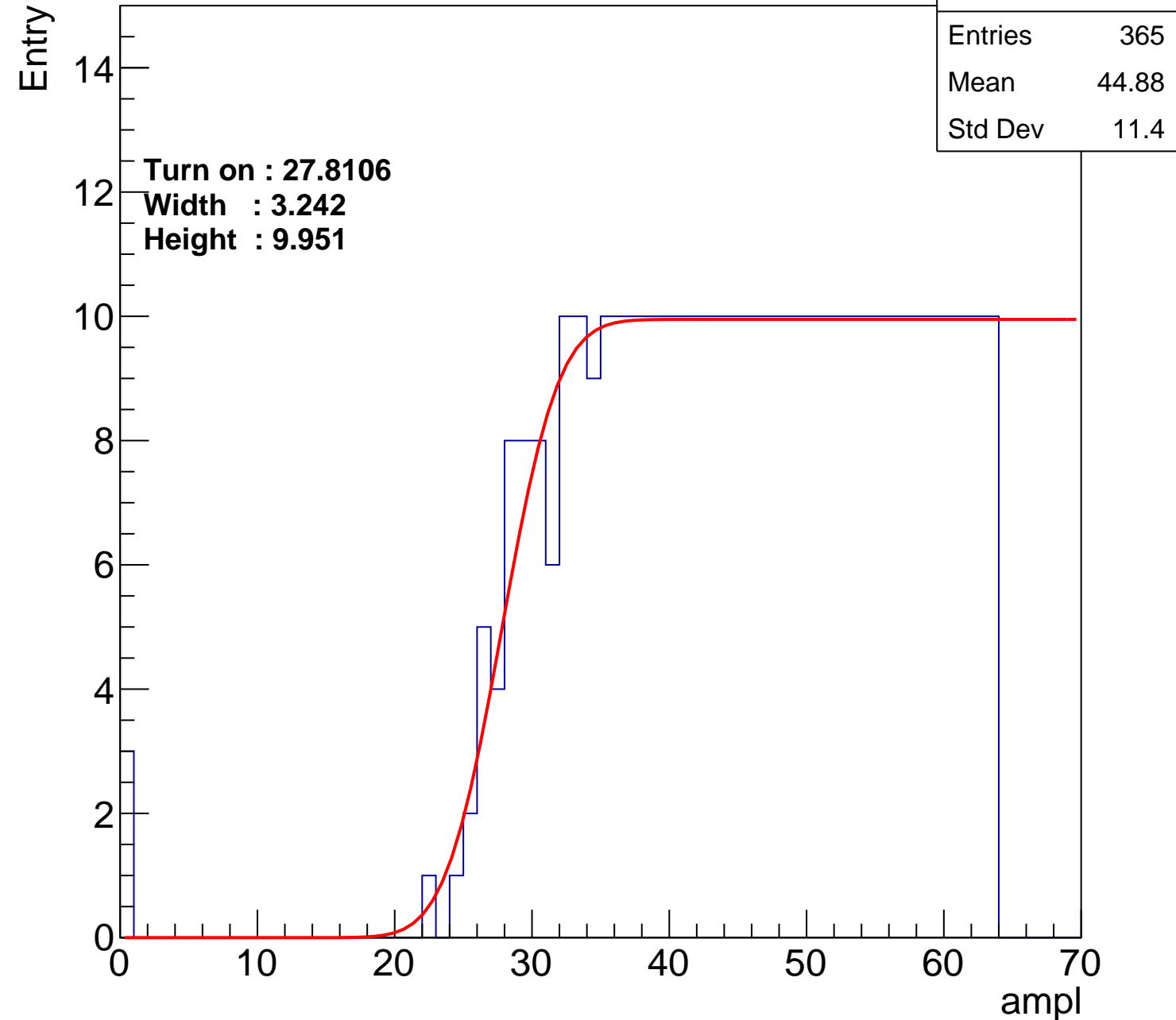
Width : 3.242

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch112

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.79 |
| Std Dev | 11.59 |

Turn on : 28.4751

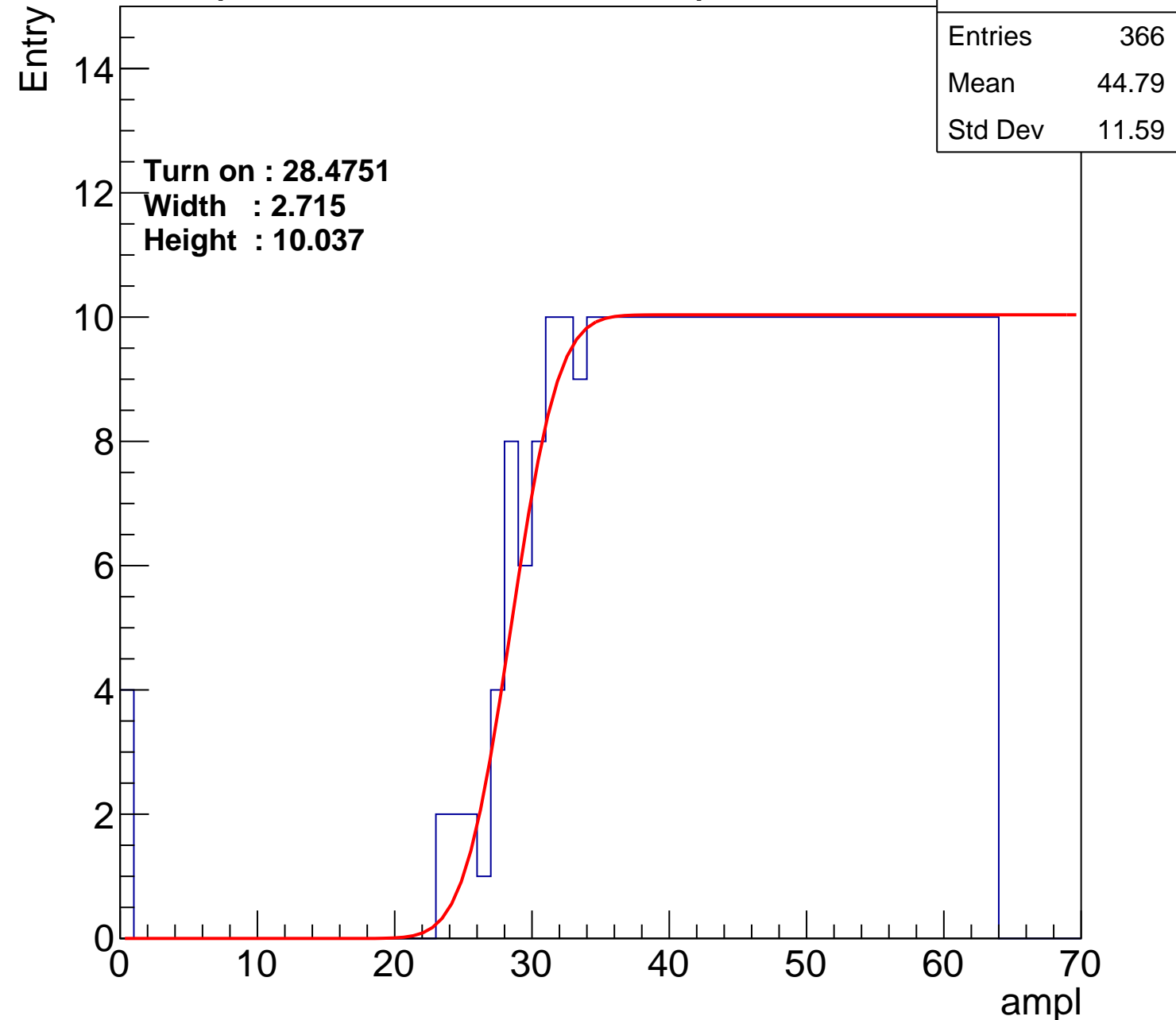
Width : 2.715

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch113

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.57 |
| Std Dev | 11.39 |

Turn on : 27.4163

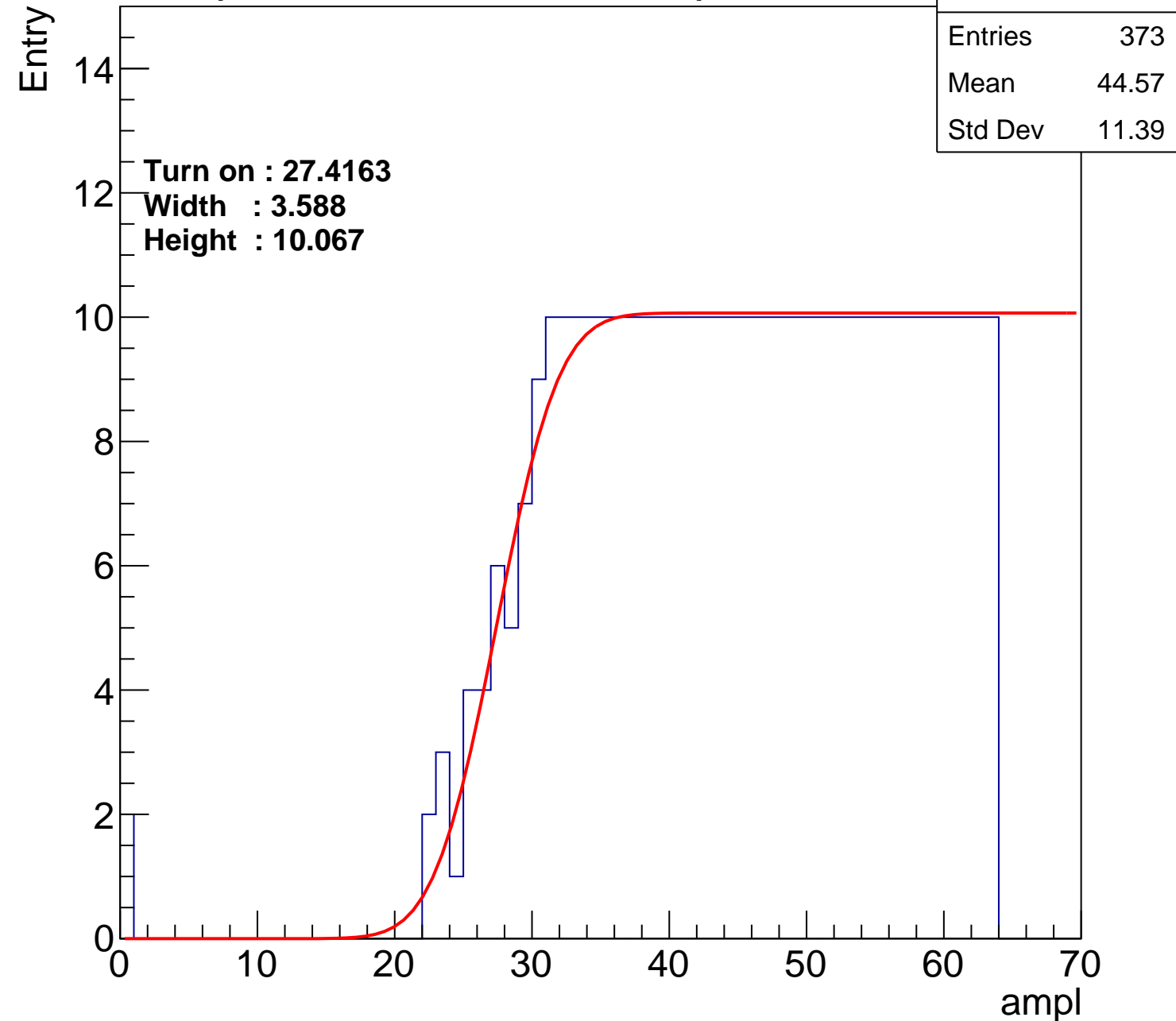
Width : 3.588

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch114

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 349 |
| Mean | 45.88 |
| Std Dev | 10.47 |

Turn on : 29.0293

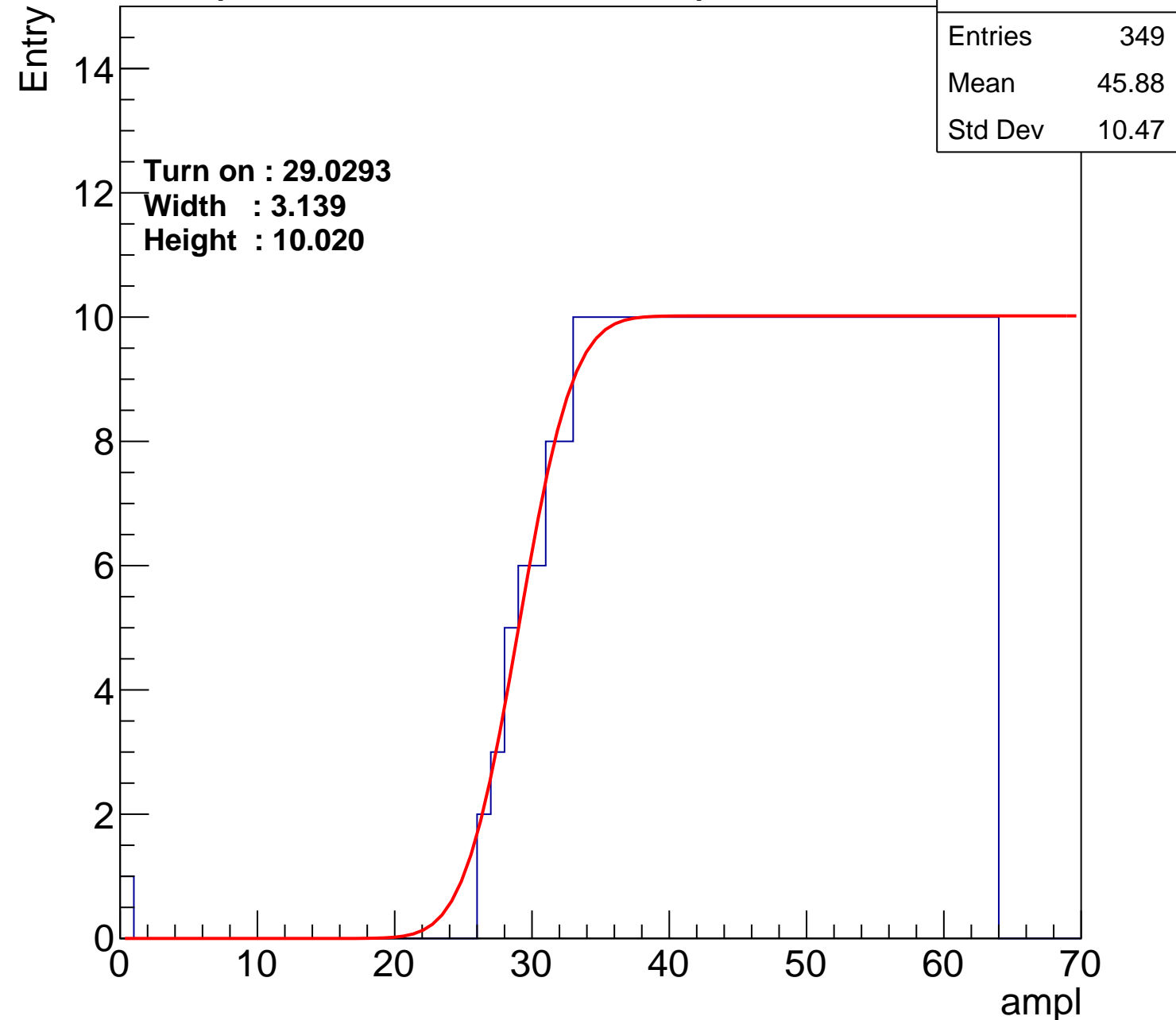
Width : 3.139

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch115

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 357 |
| Mean | 45.15 |
| Std Dev | 11.51 |

Turn on : 29.0527

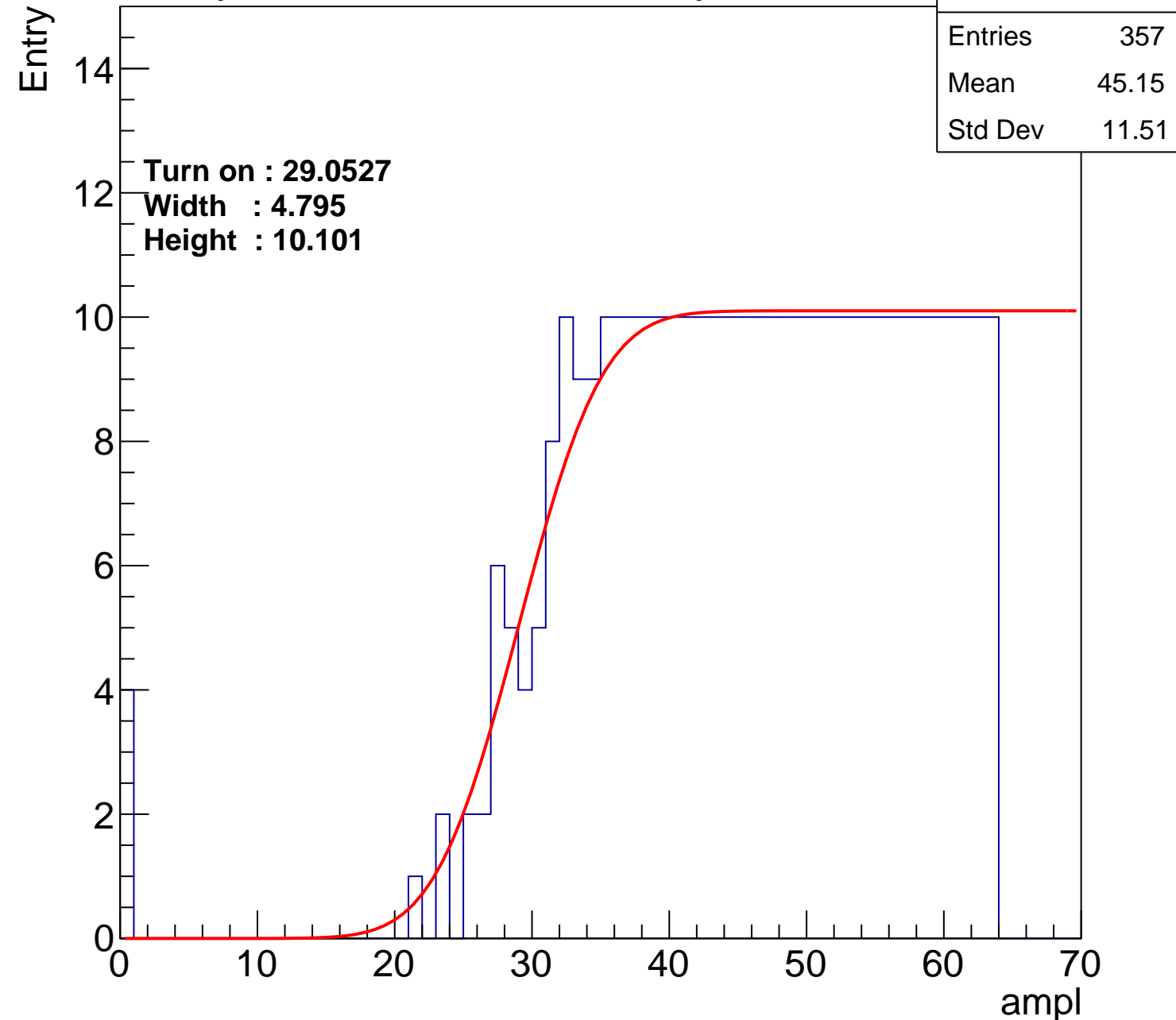
Width : 4.795

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch116

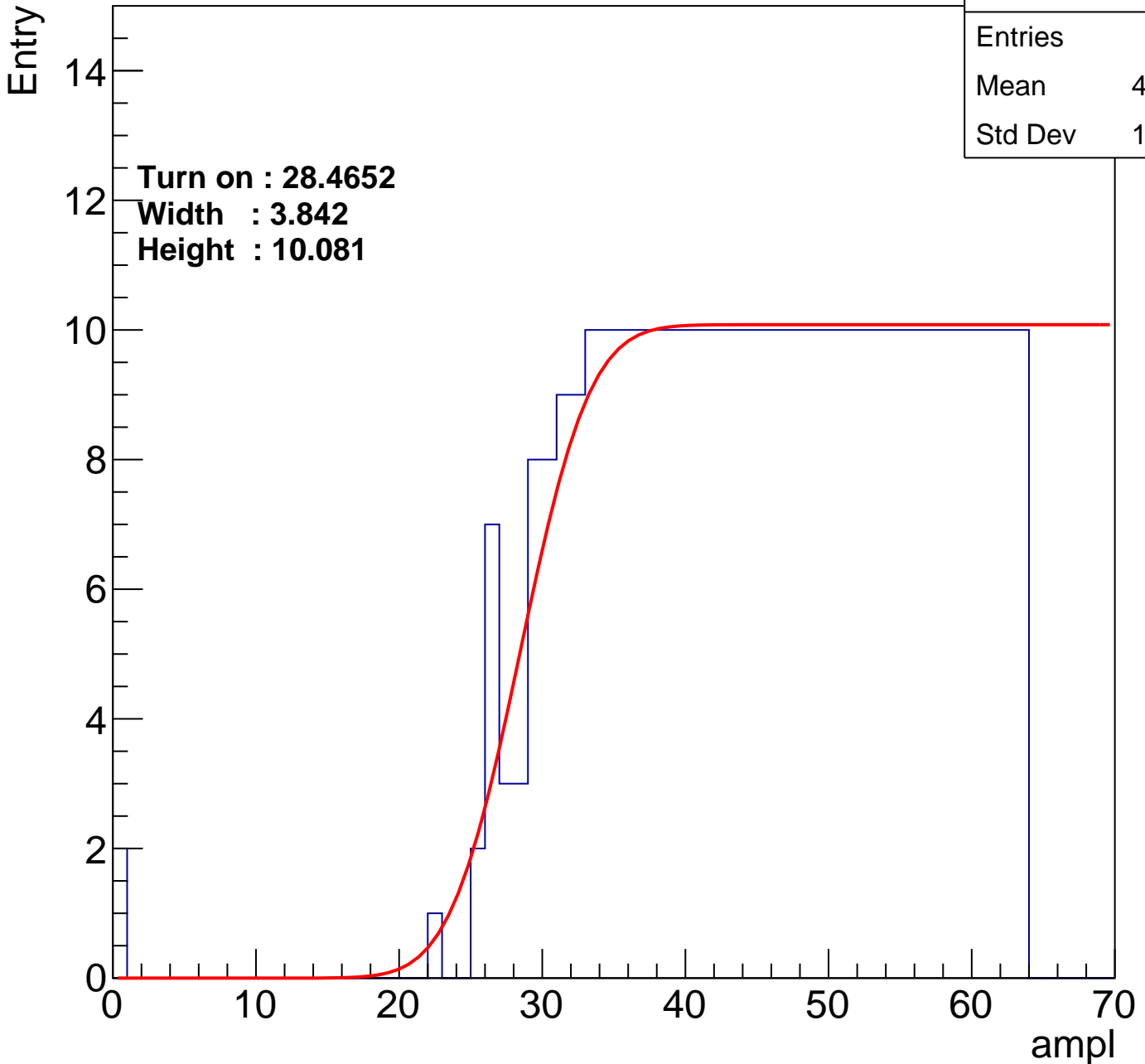
calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.13 |
| Std Dev | 11.08 |

Turn on : 28.4652

Width : 3.842

Height : 10.081



B0L001S, U23-ch117

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.99 |
| Std Dev | 11.15 |

Turn on : 27.8610

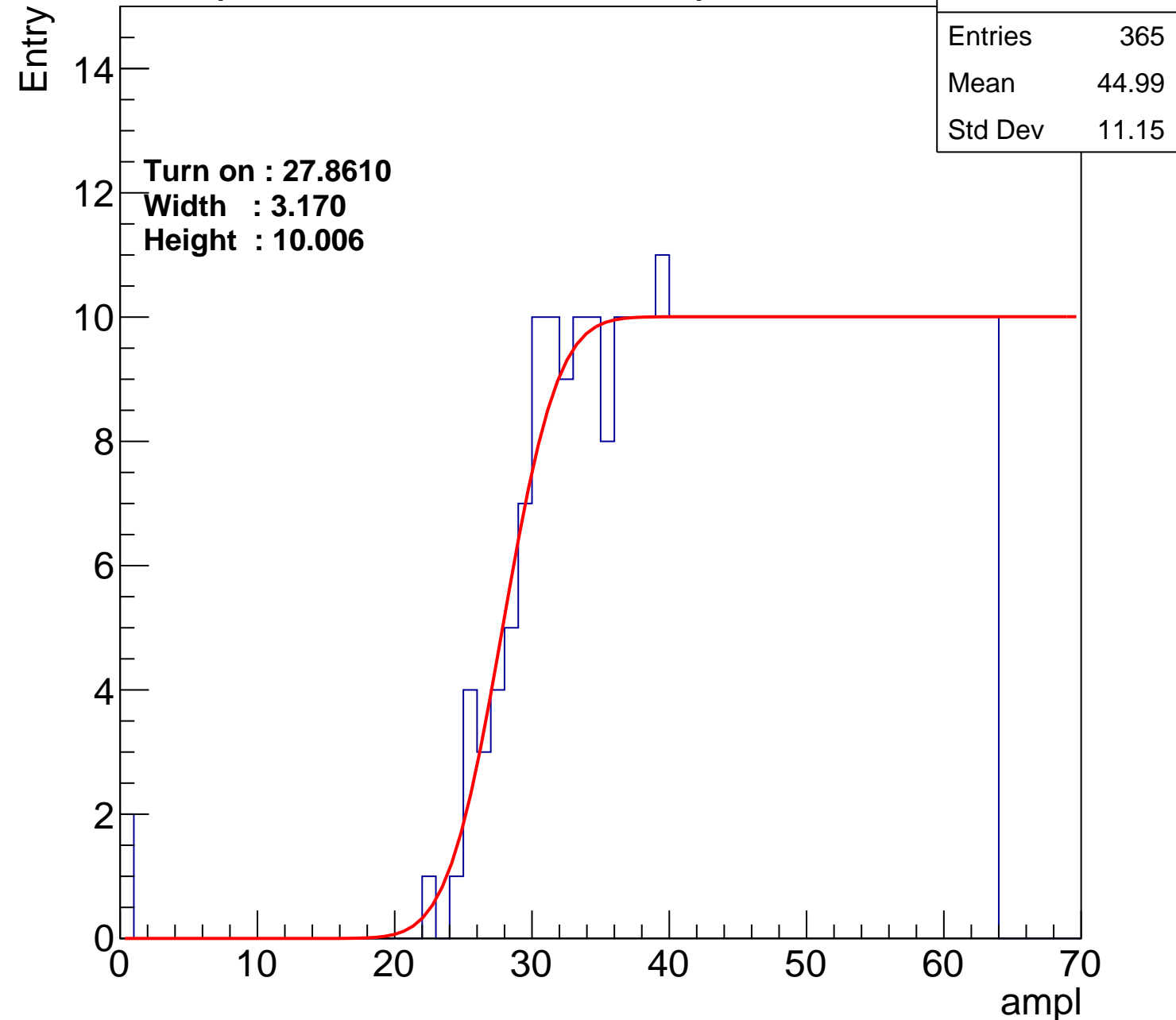
Width : 3.170

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch118

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 44.94 |
| Std Dev | 12.04 |

Turn on : 29.4361

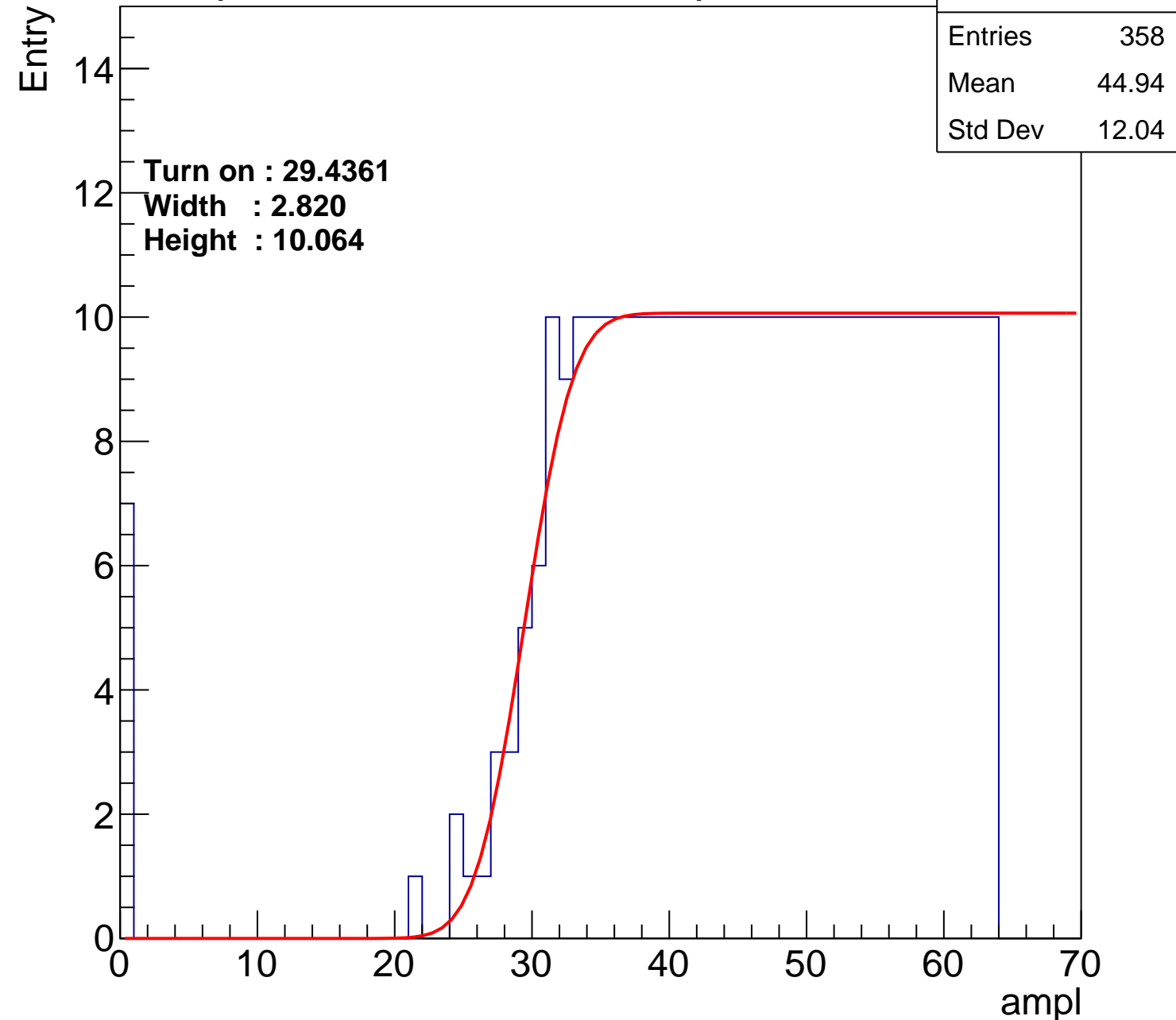
Width : 2.820

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch119

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 351 |
| Mean | 45.62 |
| Std Dev | 10.88 |

Turn on : 29.1412

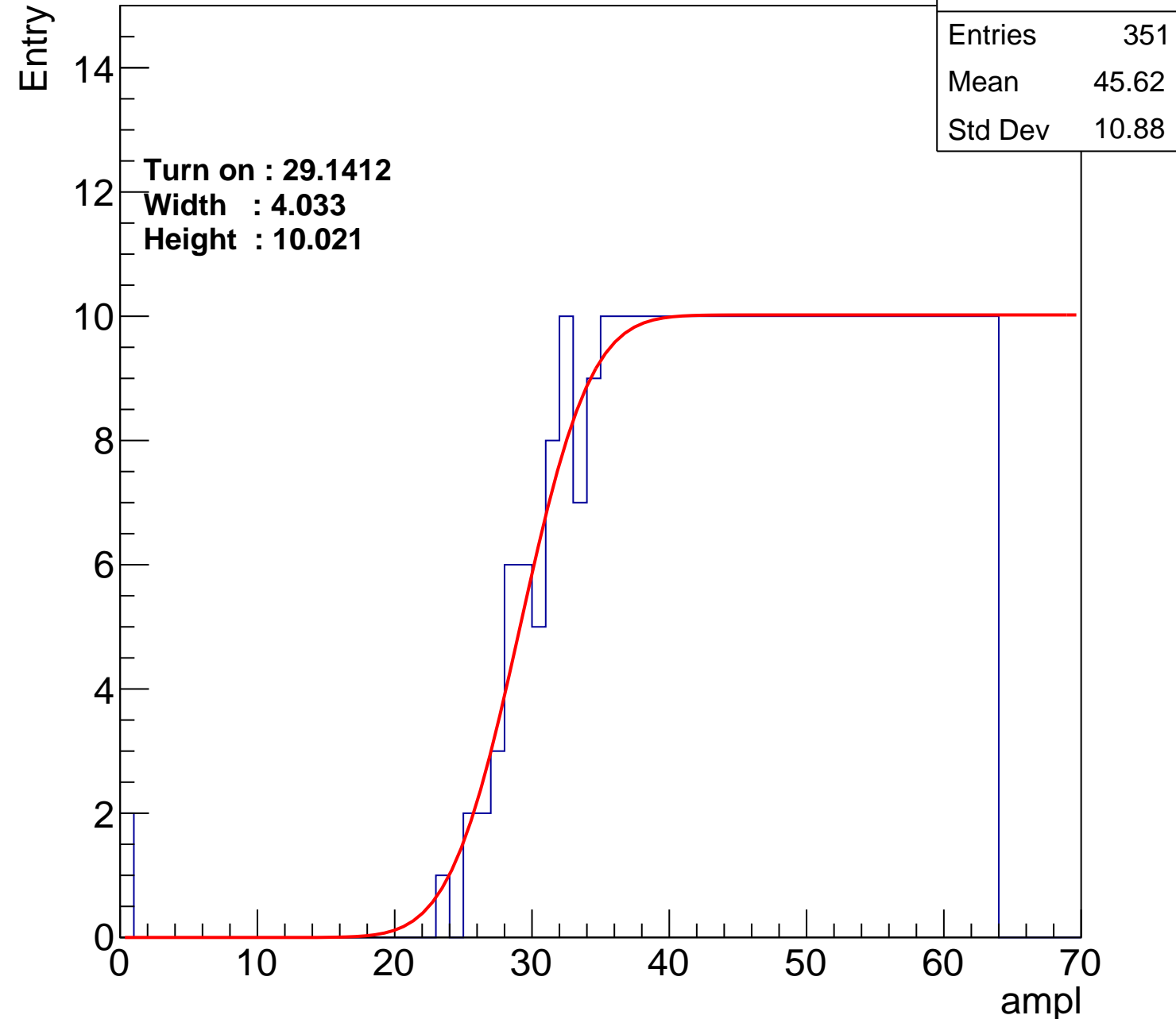
Width : 4.033

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch120

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.24 |
| Std Dev | 11.17 |

Turn on : 28.4306

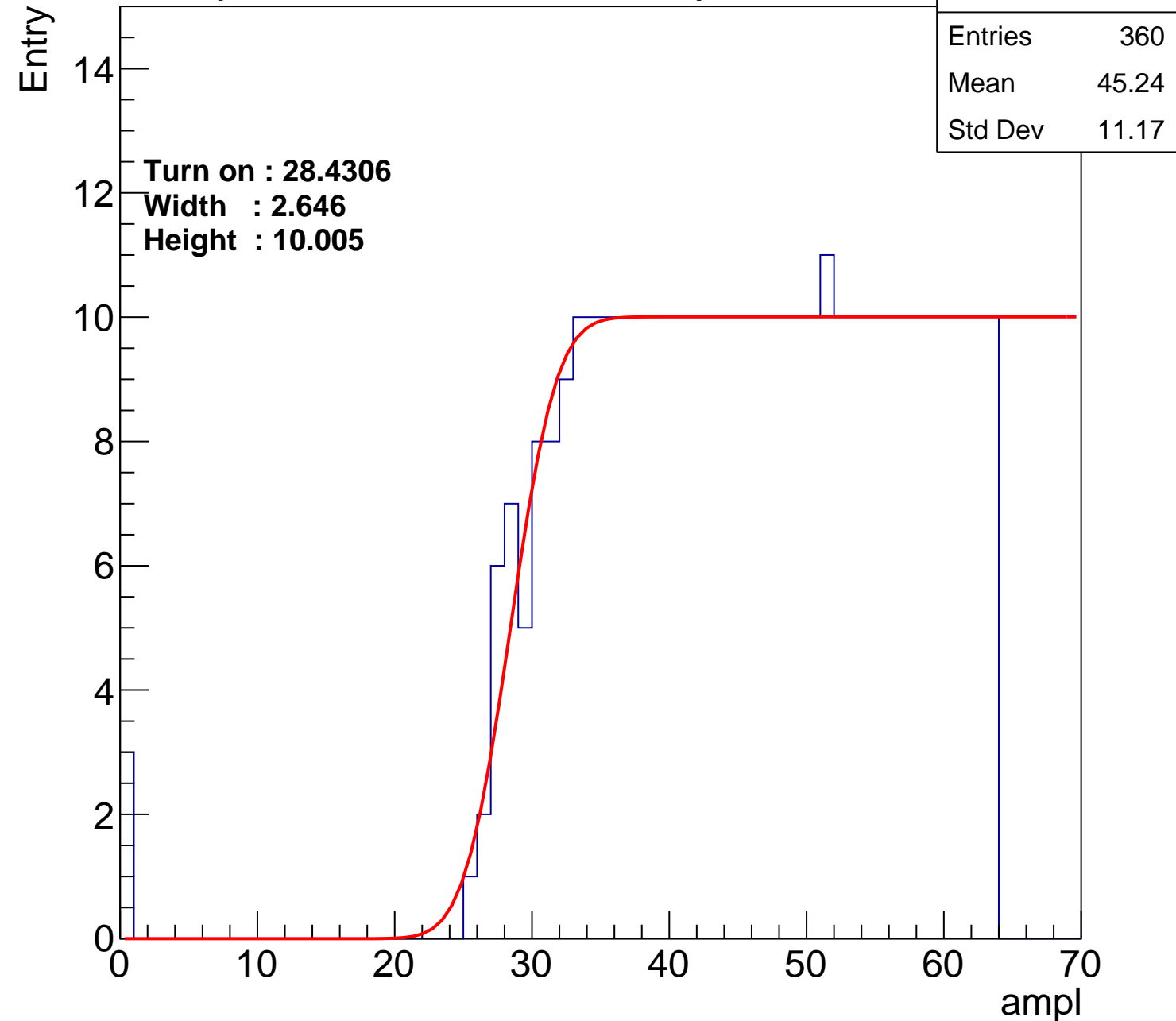
Width : 2.646

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch121

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.09 |
| Std Dev | 10.96 |

Turn on : 27.9249

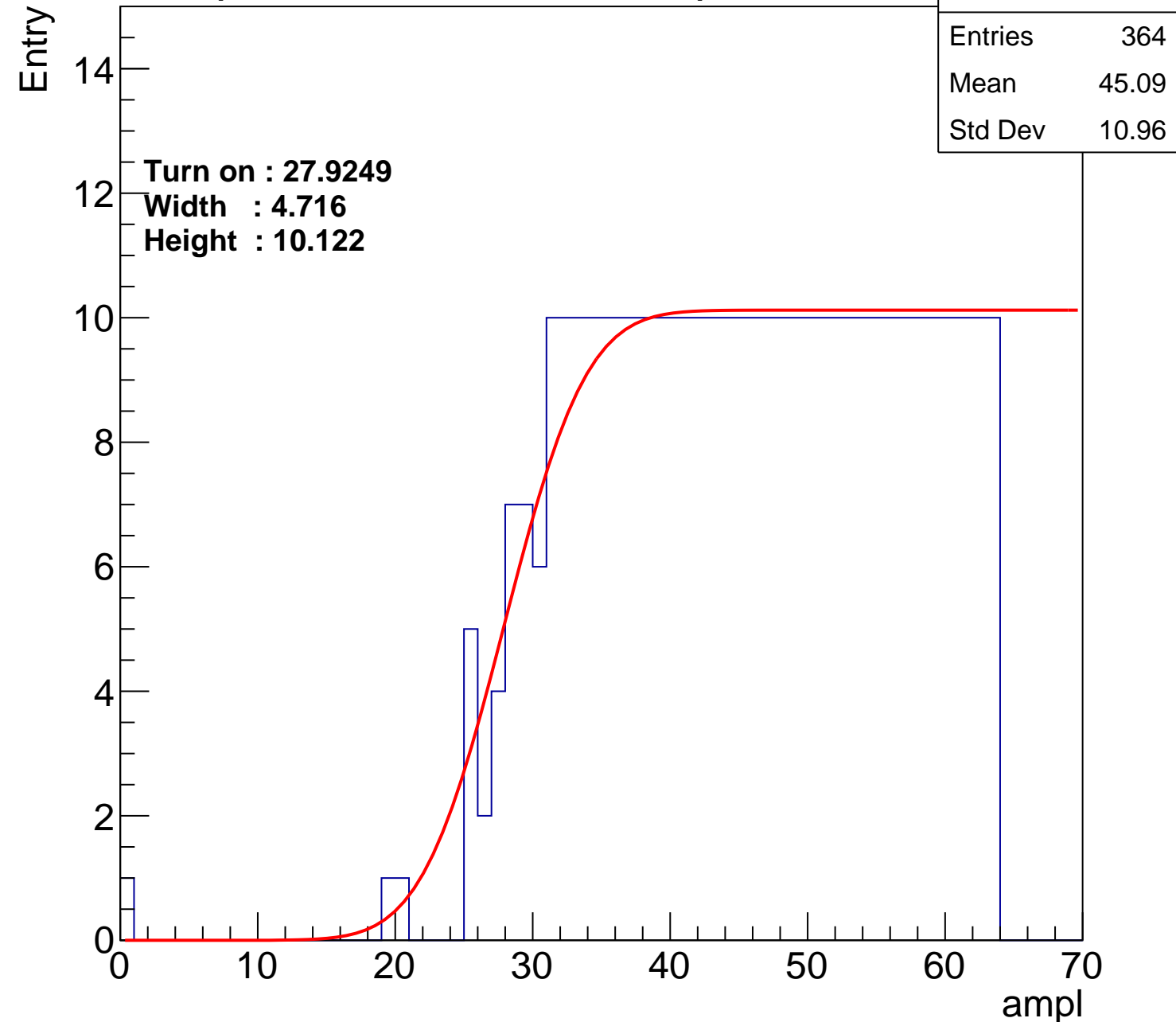
Width : 4.716

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch122

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.25 |
| Std Dev | 11.39 |

Turn on : 28.9339

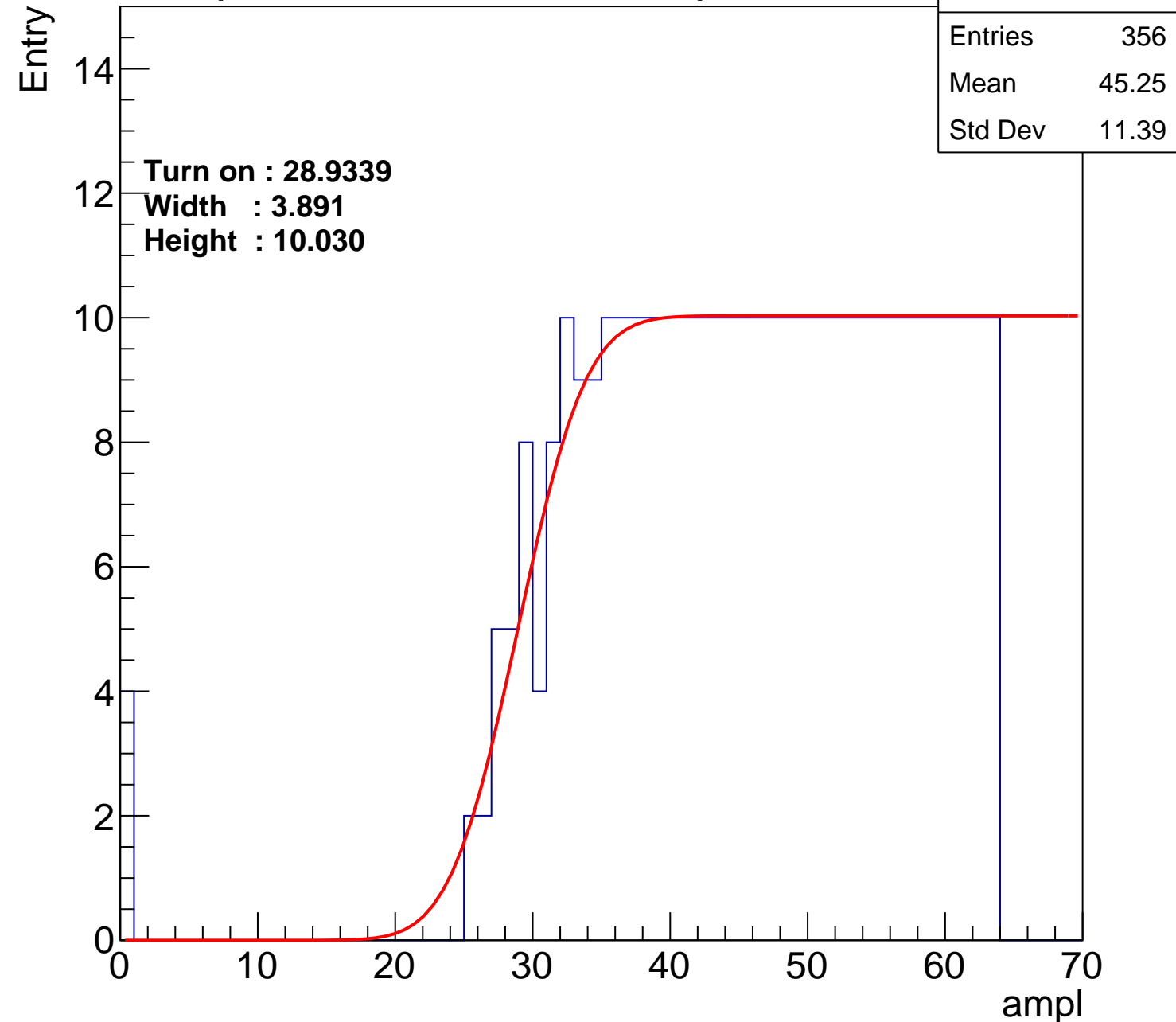
Width : 3.891

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch123

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.72 |
| Std Dev | 11.5 |

Turn on : 27.8024

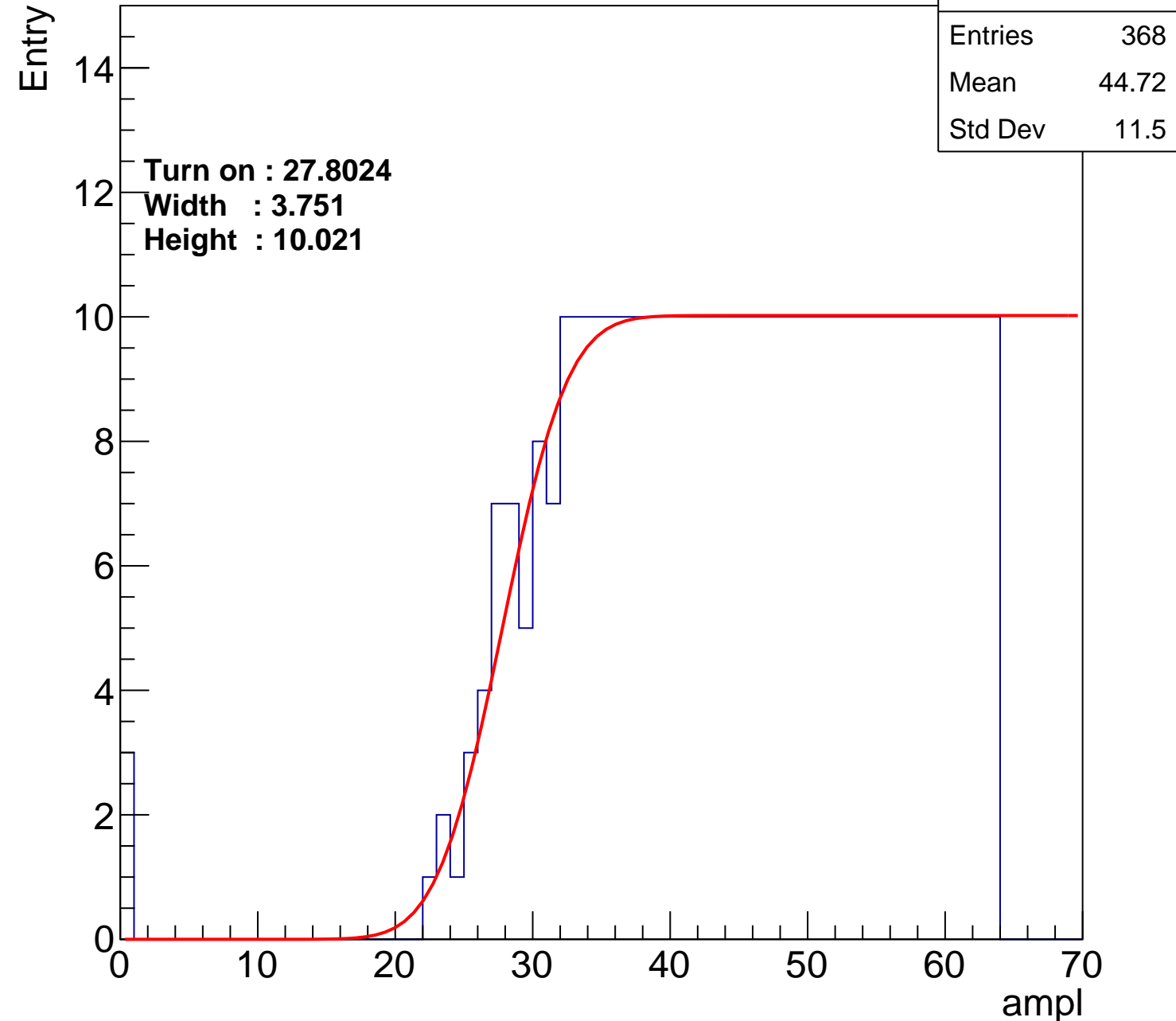
Width : 3.751

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch124

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.55 |
| Std Dev | 11.56 |

Turn on : 27.3926

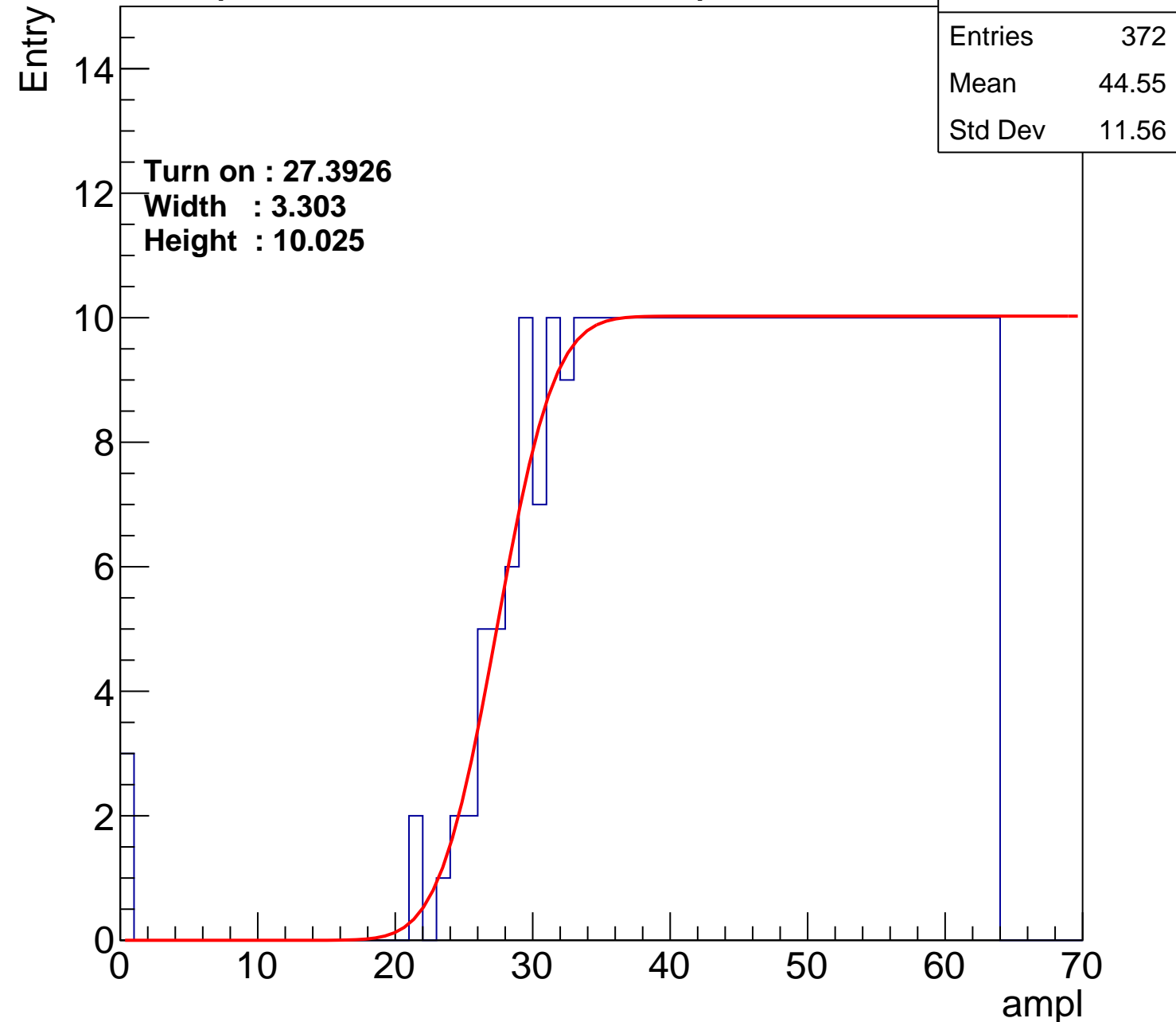
Width : 3.303

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch125

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 356 |
| Mean | 45.15 |
| Std Dev | 11.74 |

Turn on : 29.0388

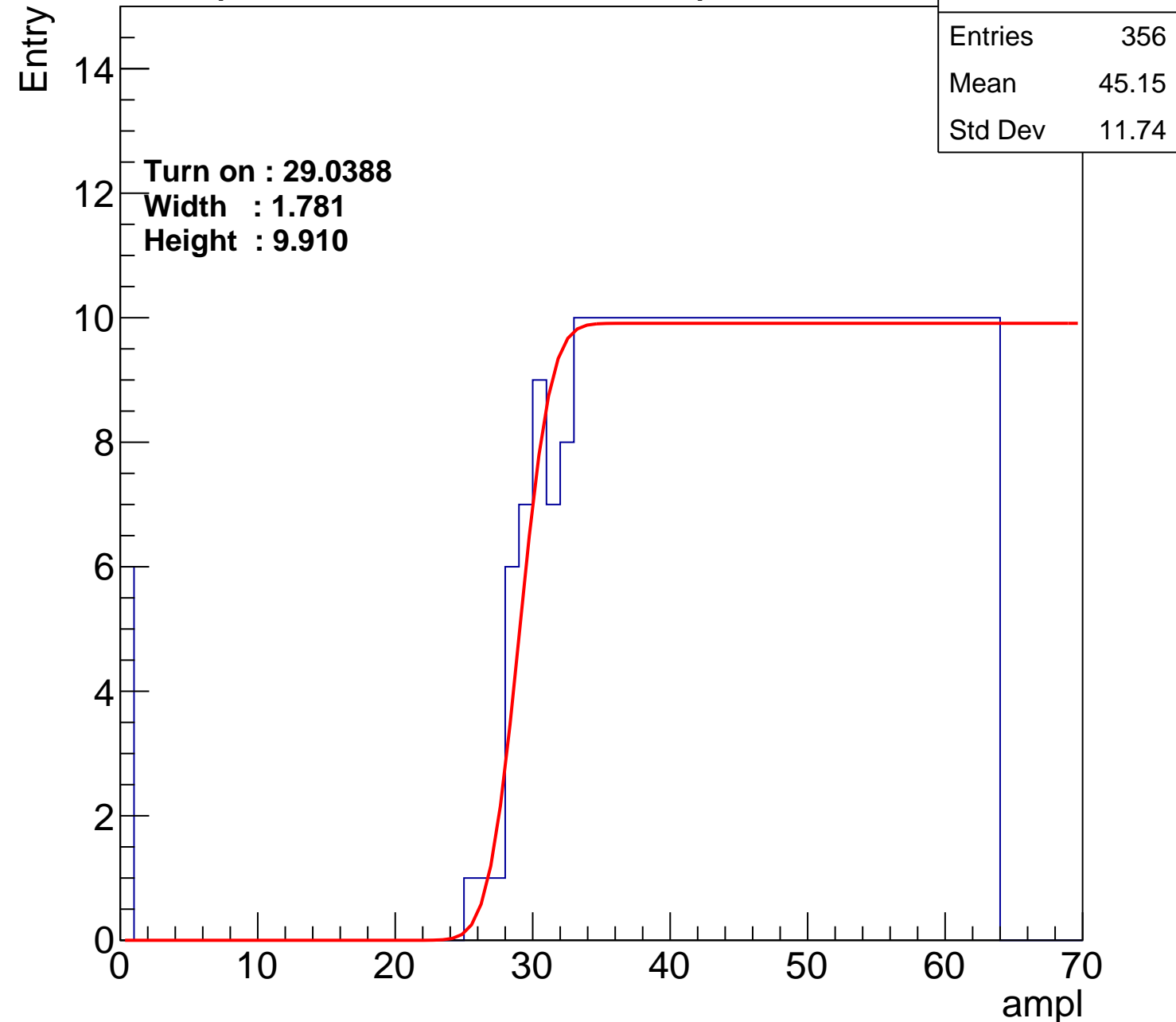
Width : 1.781

Height : 9.910

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch126

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 353 |
| Mean | 45.64 |
| Std Dev | 10.64 |

Turn on : 28.9841

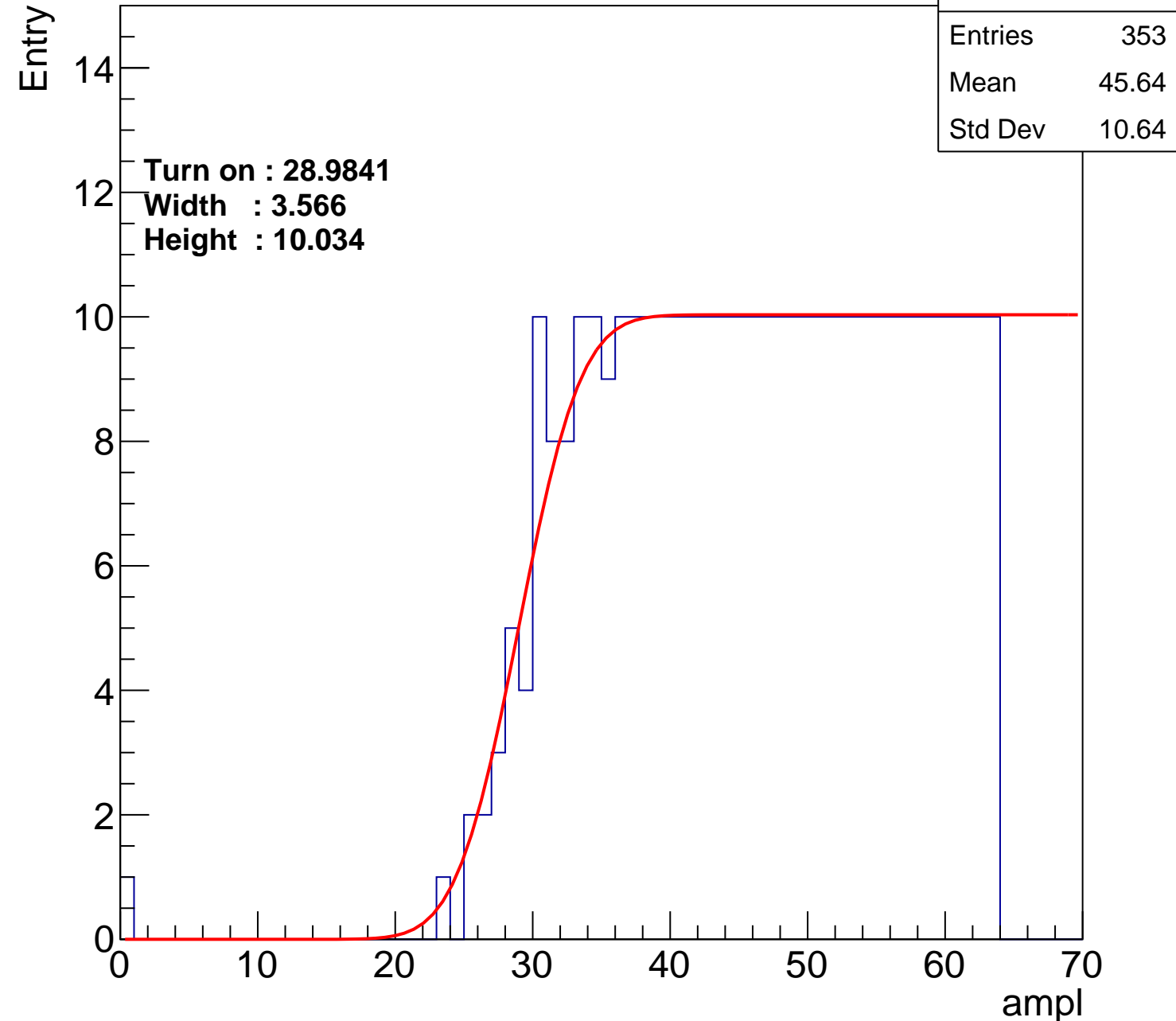
Width : 3.566

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch127

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.2 |
| Std Dev | 11.29 |

Turn on : 29.0434

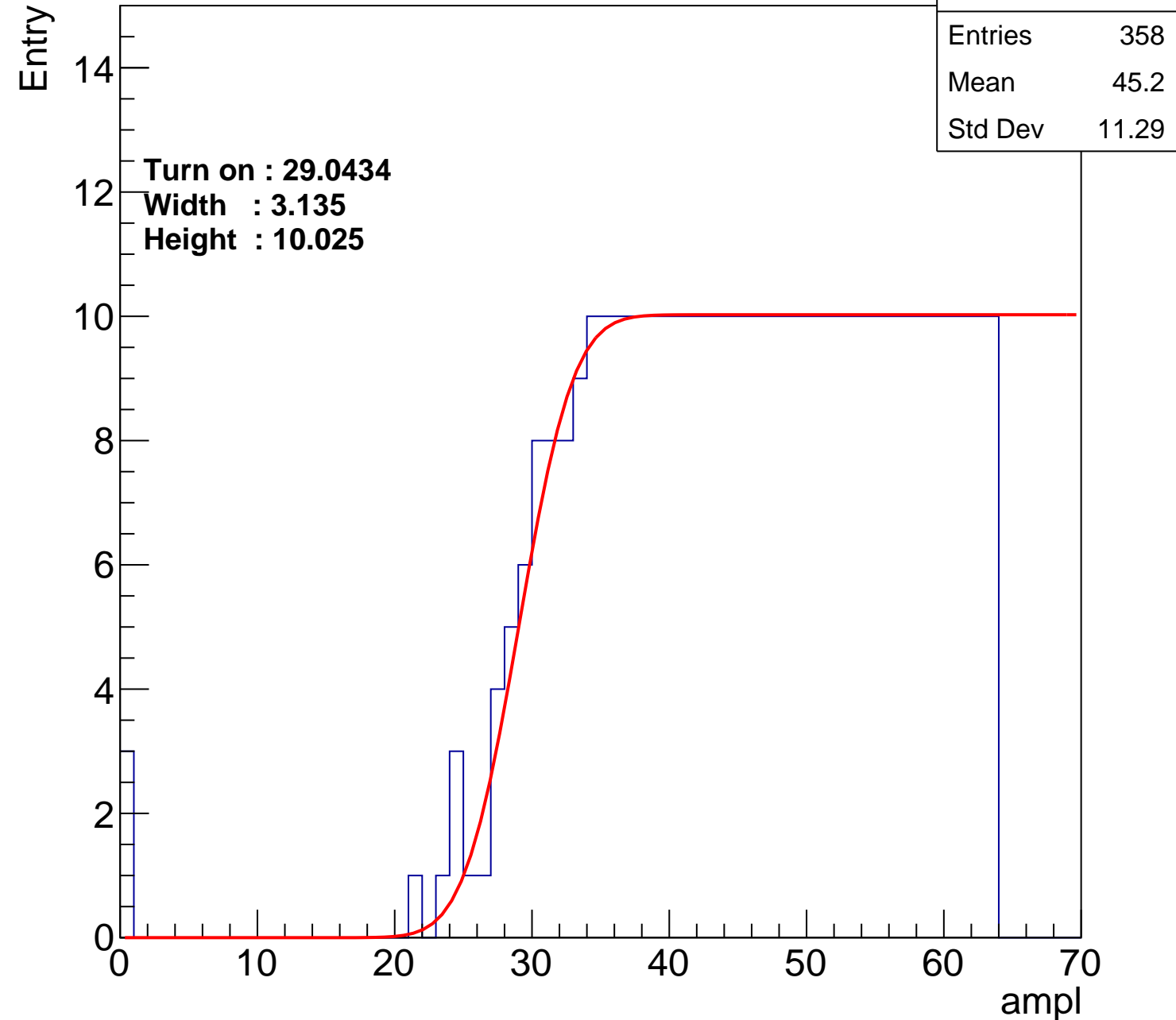
Width : 3.135

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L001S, U23-ch127

calib_packv5_042523_0143.root, FC#9, port A1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.2 |
| Std Dev | 11.29 |

Turn on : 29.0434

Width : 3.135

Height : 10.025

Entry

