

B0L000S, U11-ch0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.4865

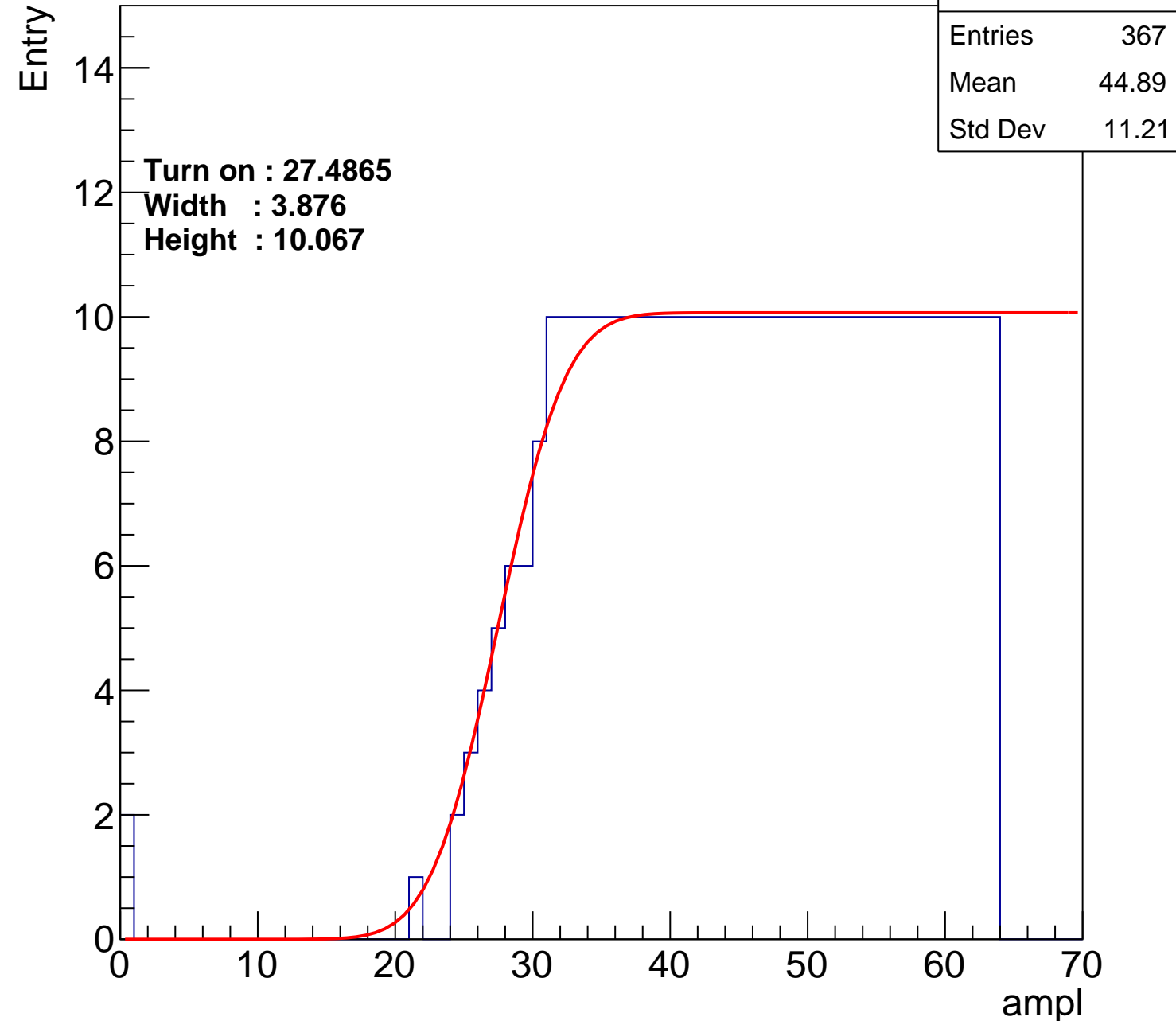
Width : 3.876

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
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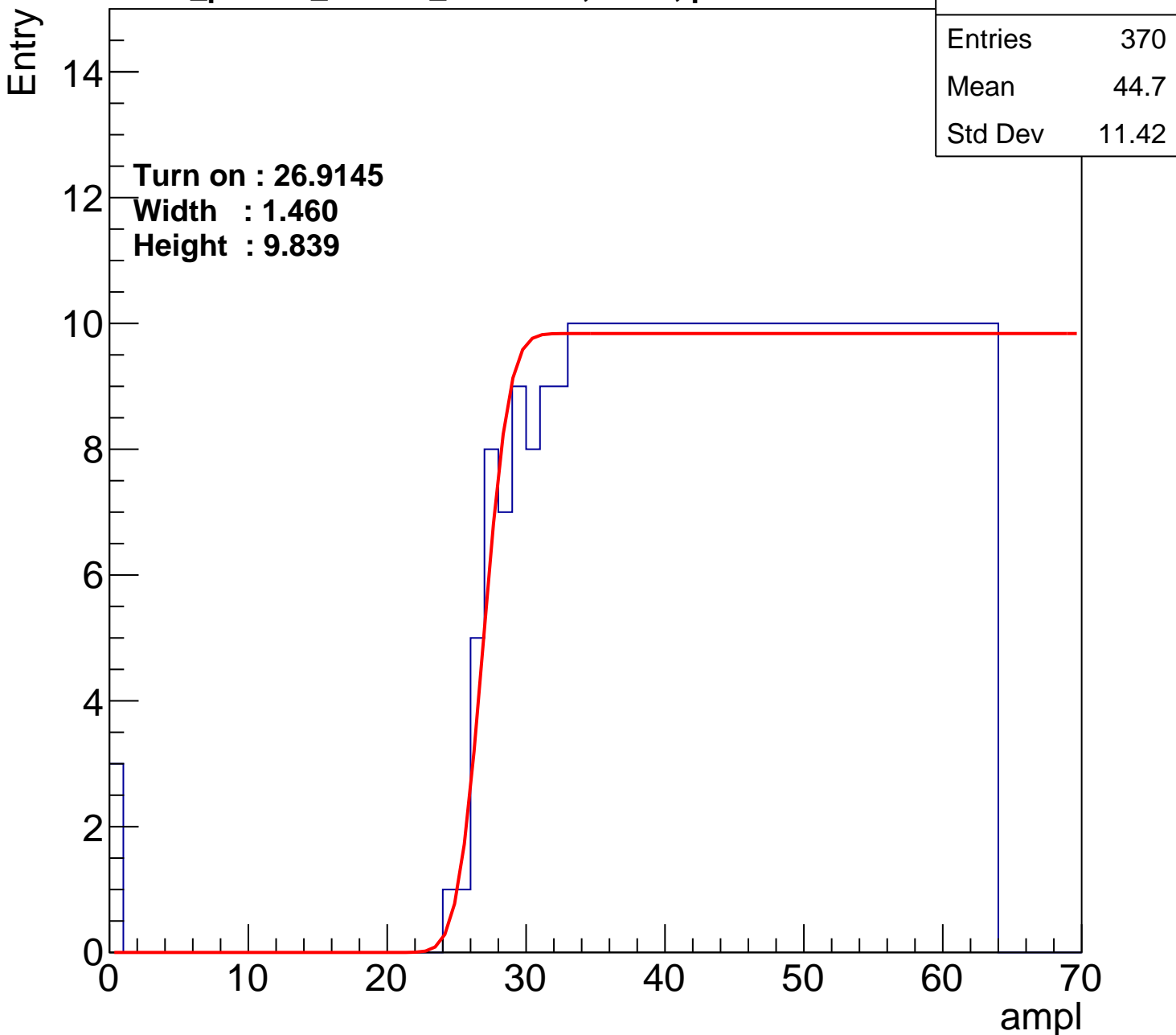
Mean	44.7
------	------

Std Dev	11.42
---------	-------

Turn on : 26.9145

Width : 1.460

Height : 9.839



B0L000S, U11-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.8
Std Dev	10.55

Turn on : 29.4285

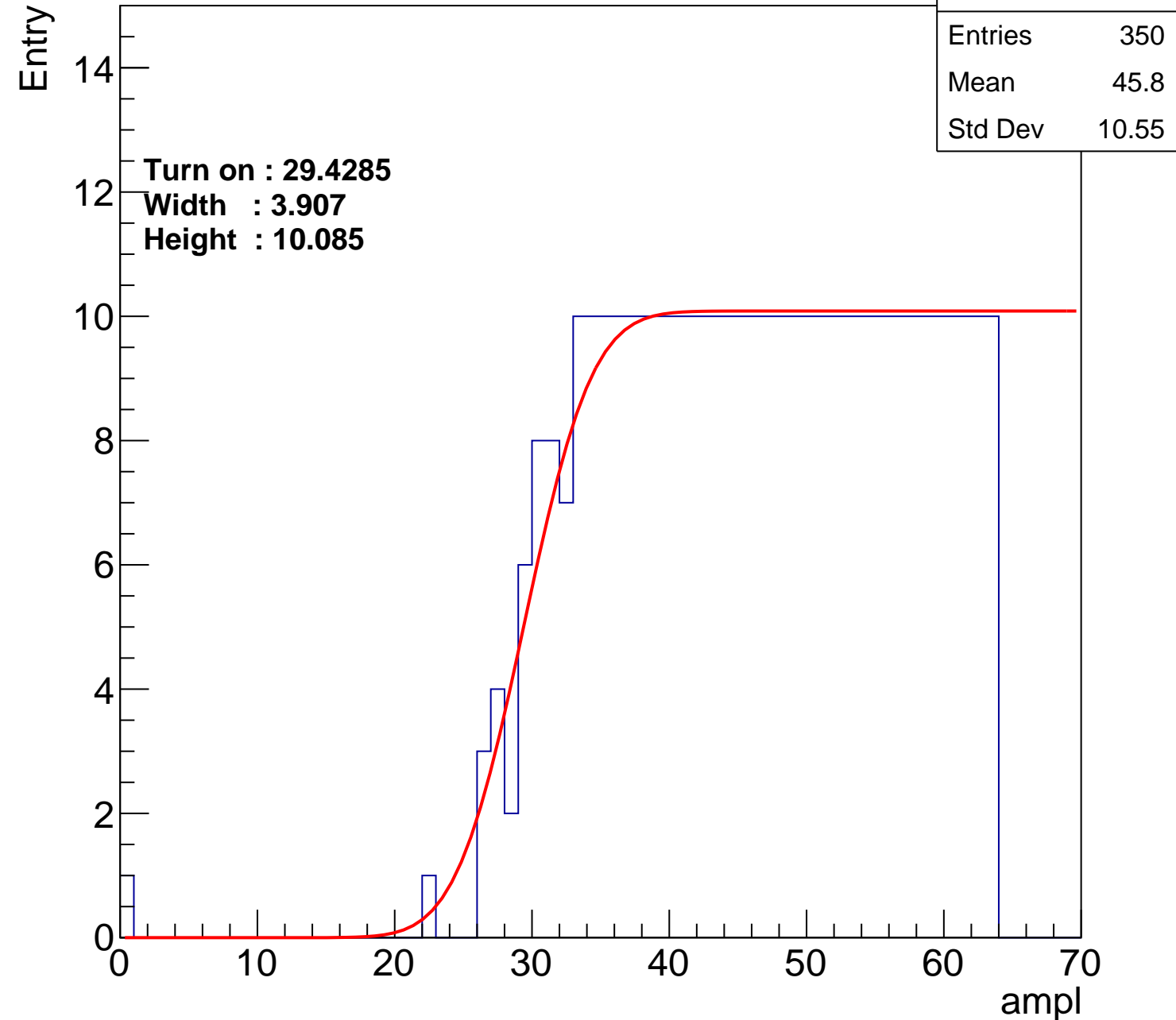
Width : 3.907

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.46
Std Dev	11.35

Turn on : 26.4295

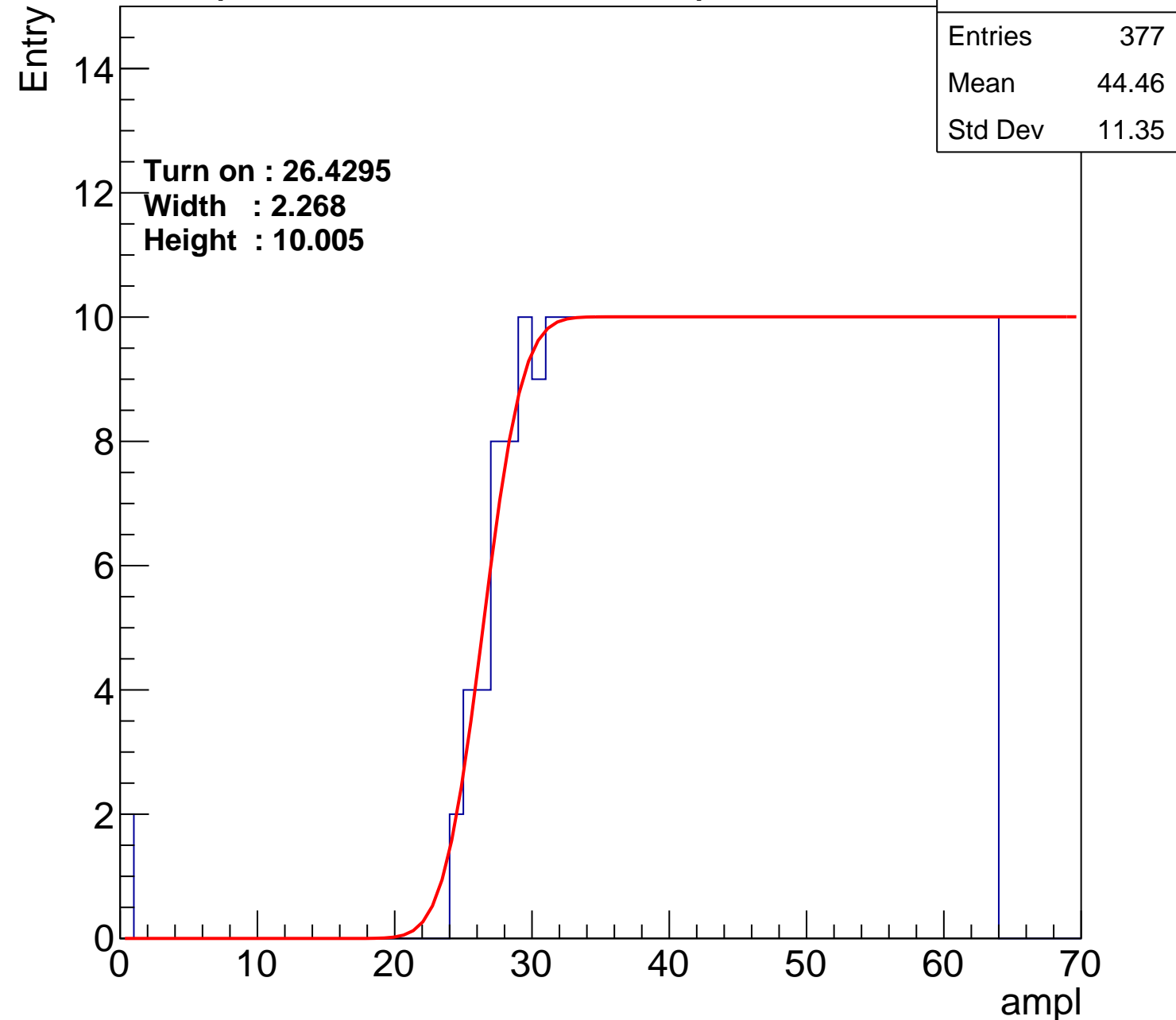
Width : 2.268

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch4

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.22
Std Dev	10.79

Turn on : 28.0440

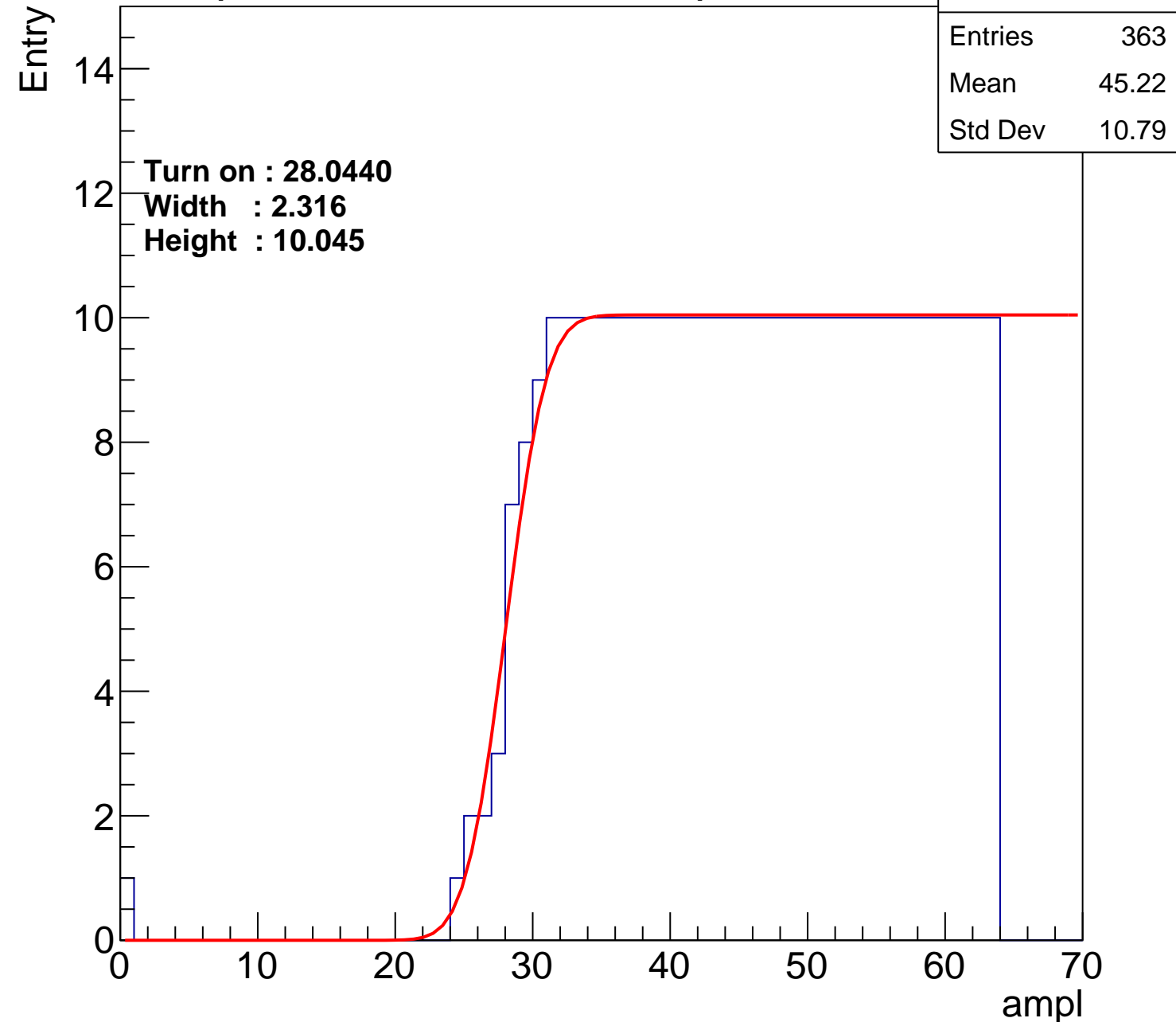
Width : 2.316

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.82
Std Dev	10.57

Turn on : 30.1288

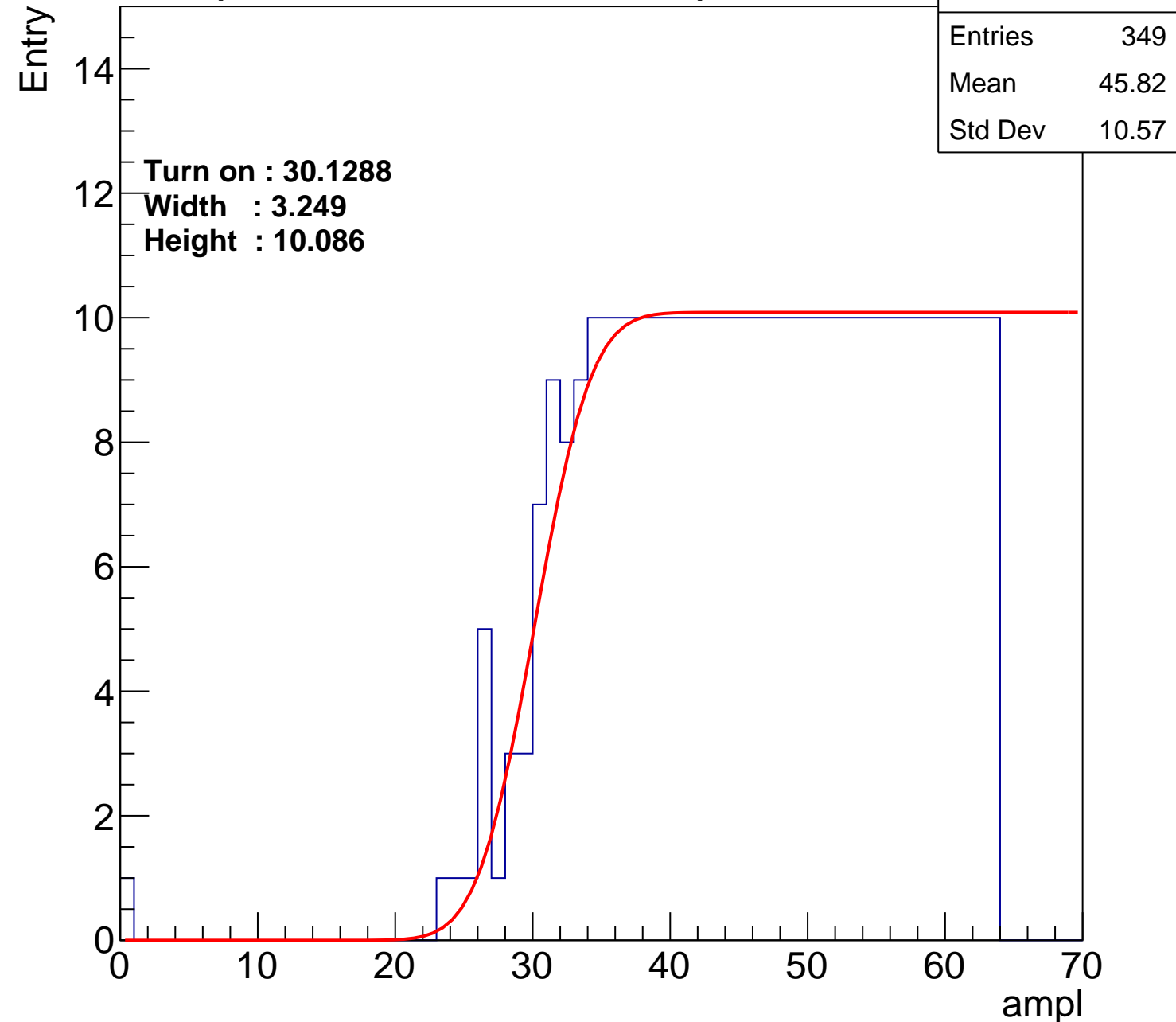
Width : 3.249

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.53
Std Dev	11.21

Turn on : 26.4787

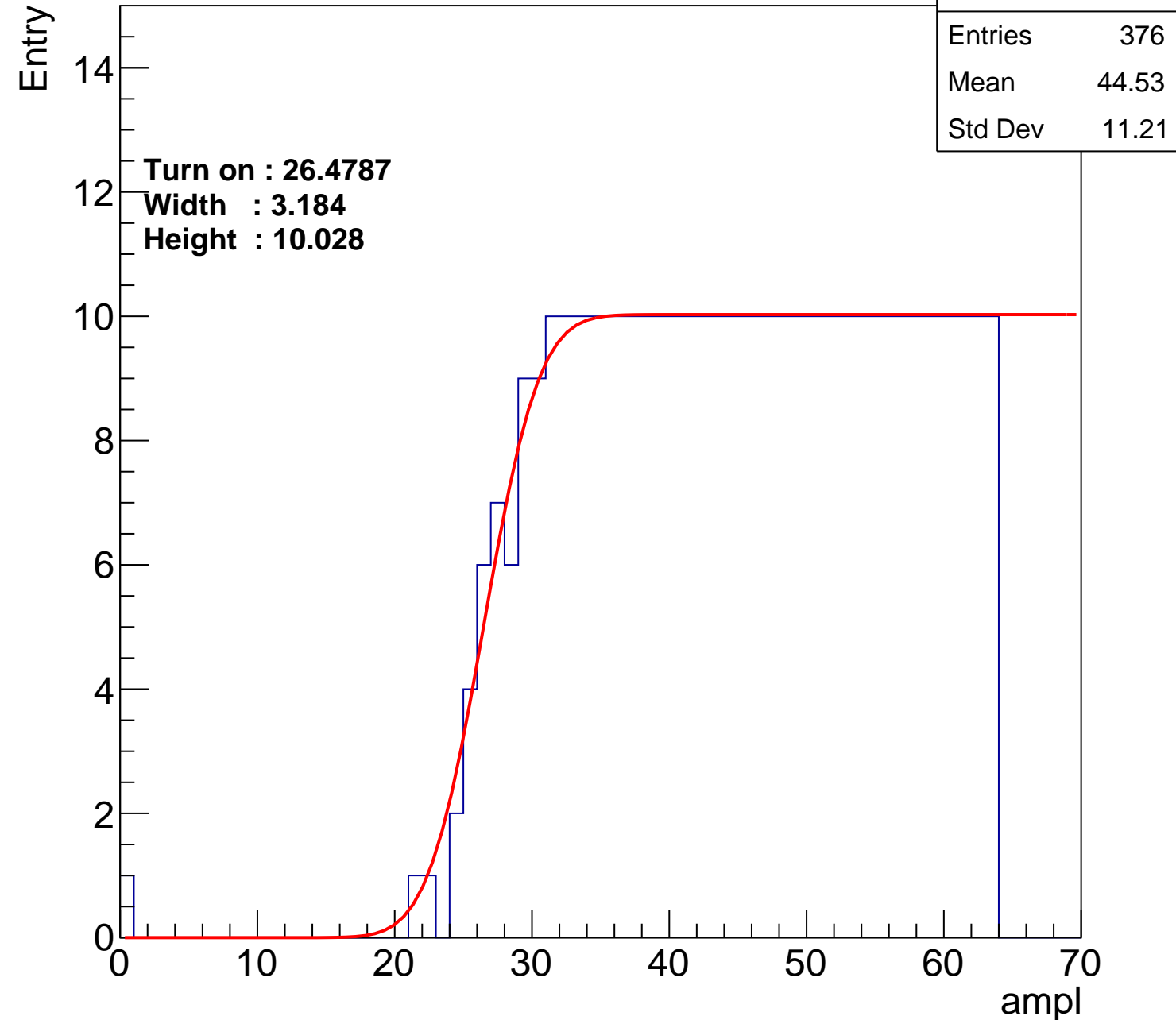
Width : 3.184

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	335
Mean	46.54
Std Dev	10.15

Turn on : 30.3273

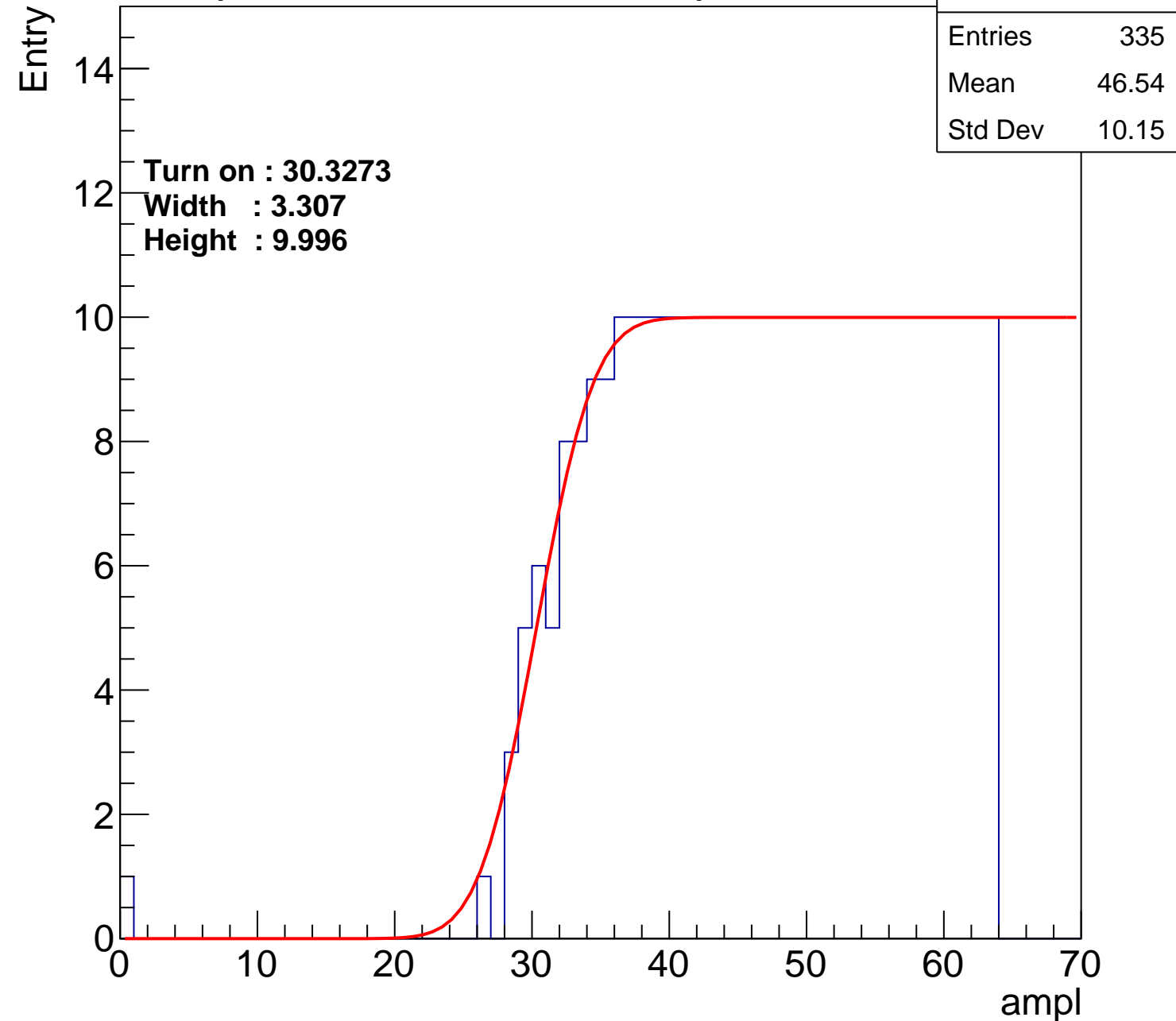
Width : 3.307

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.67
Std Dev	11.61

Turn on : 27.6283

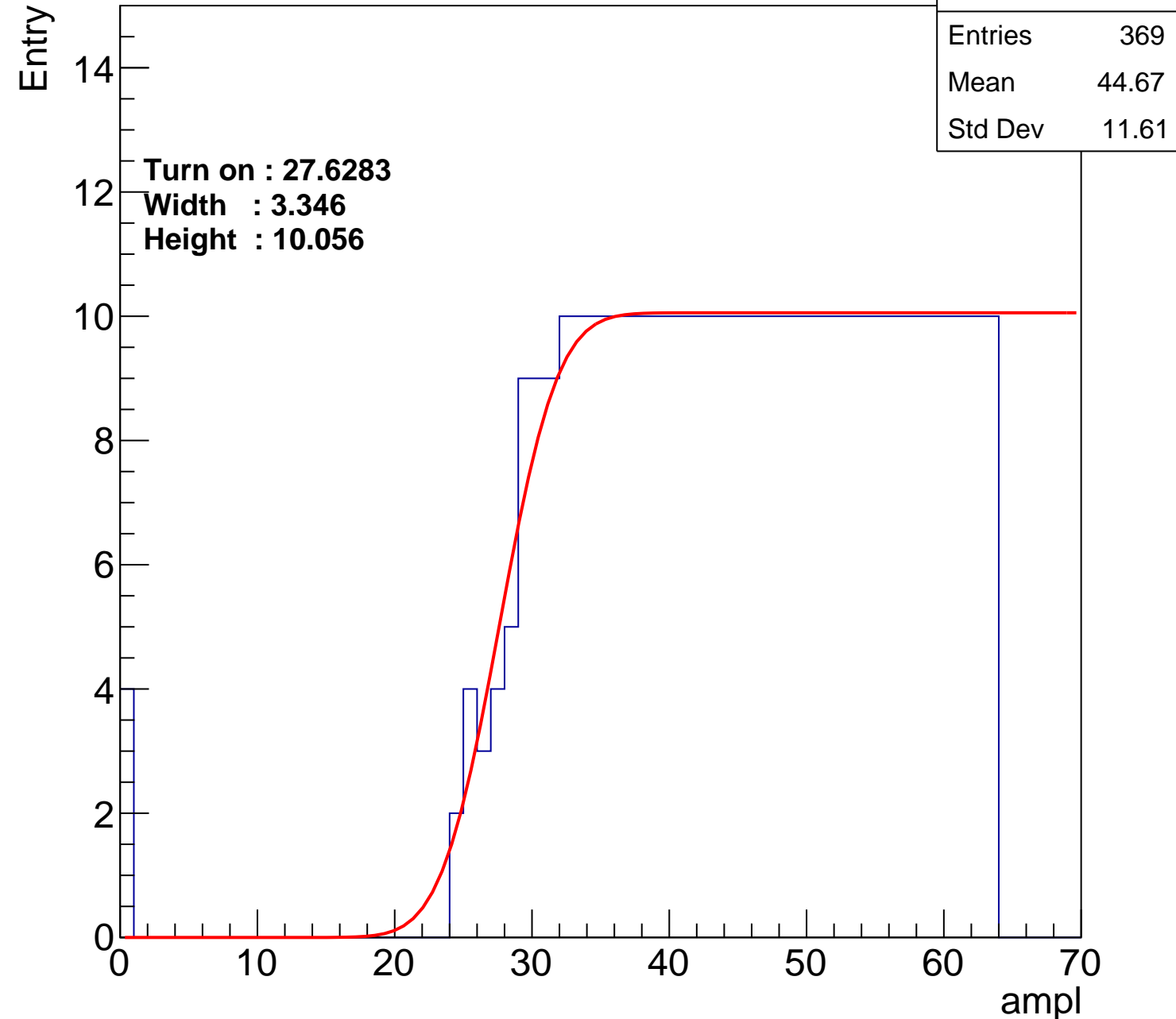
Width : 3.346

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch9

calib_packv5_042523_0143.root, FC#5, port B1

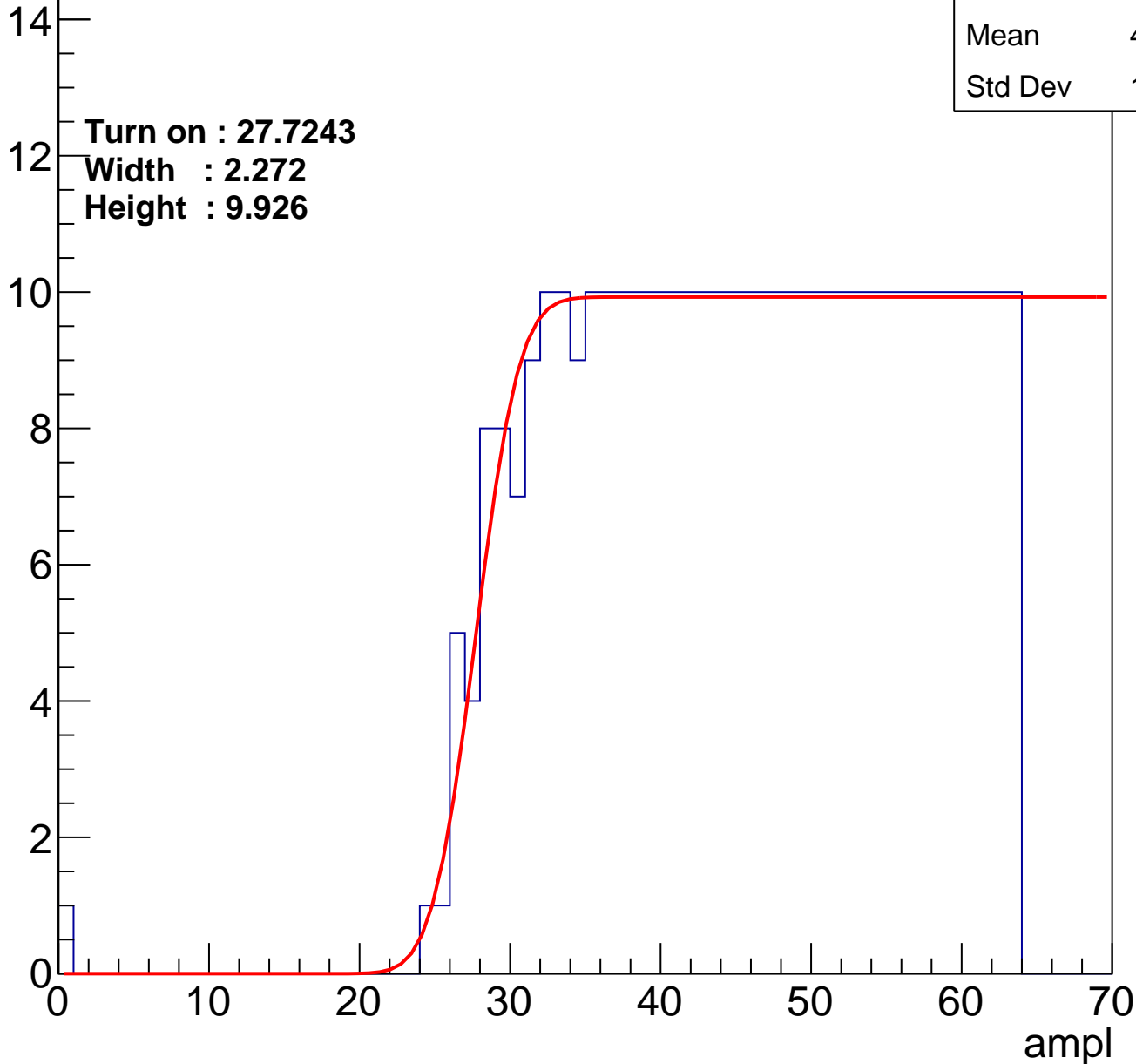
Entry

Entries	363
Mean	45.17
Std Dev	10.86

Turn on : 27.7243

Width : 2.272

Height : 9.926



B0L000S, U11-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.44
Std Dev	11.06

Turn on : 28.8617

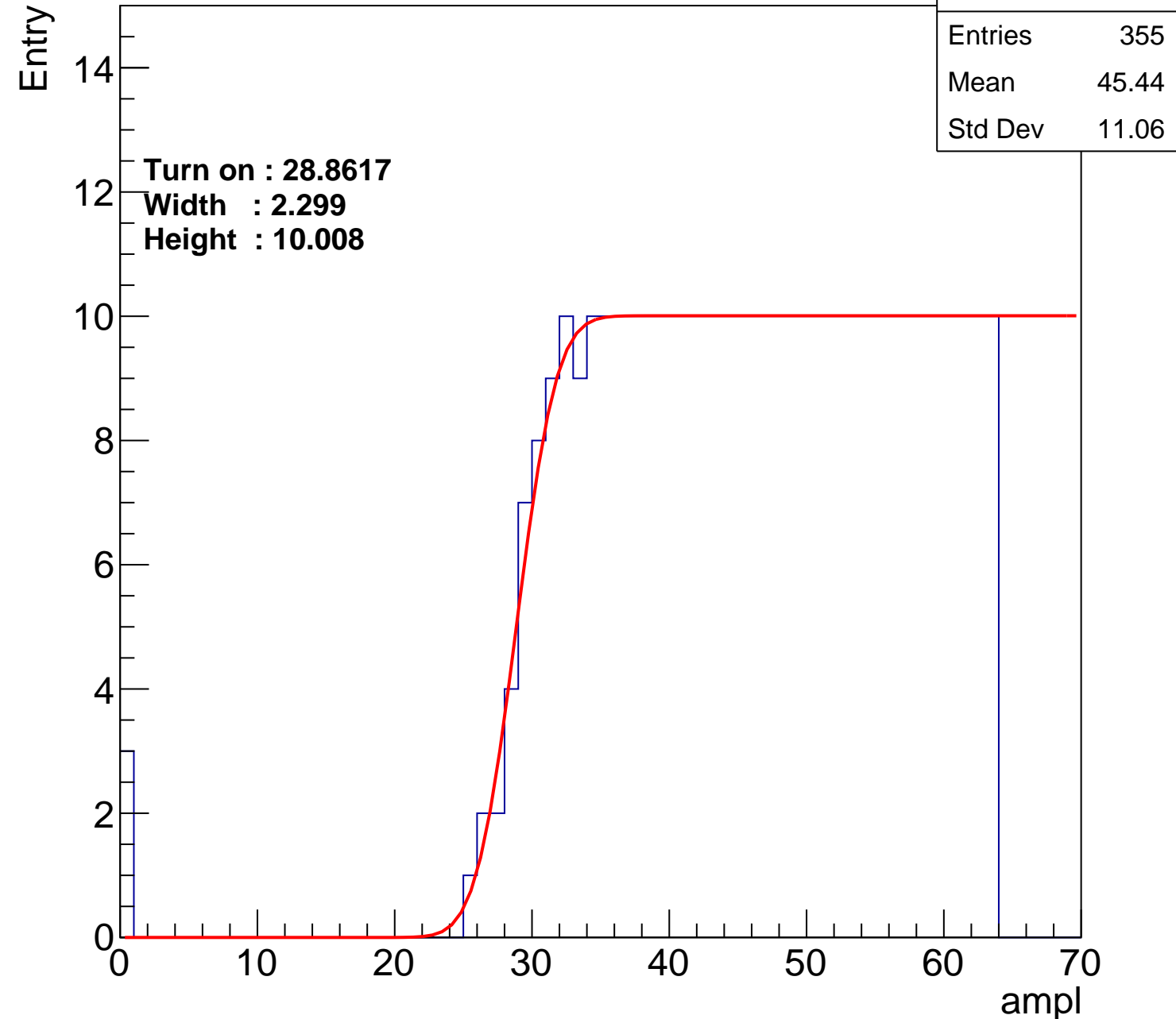
Width : 2.299

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.34
Std Dev	11.5

Turn on : 26.4748

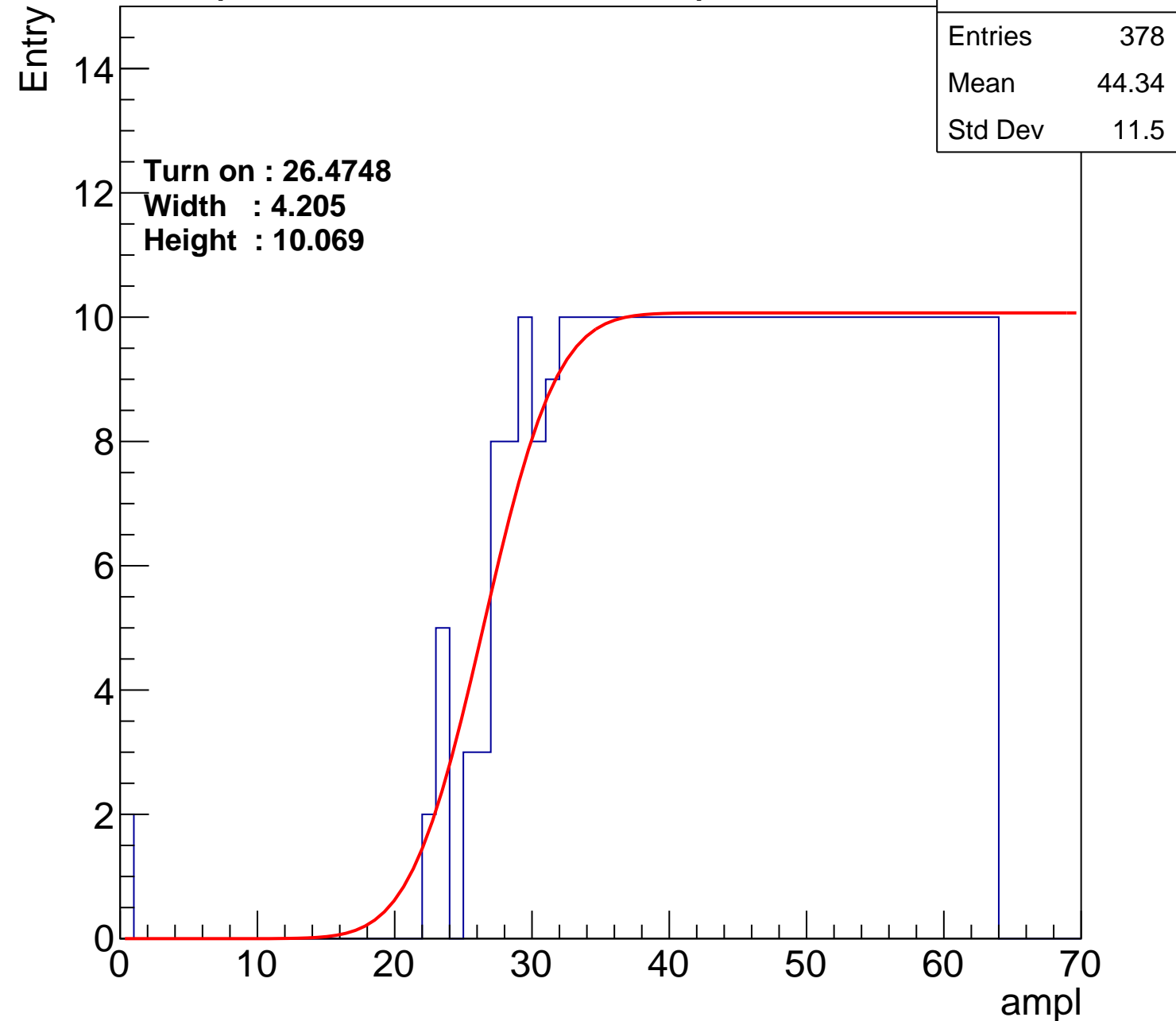
Width : 4.205

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.06
Std Dev	11.14

Turn on : 27.8889

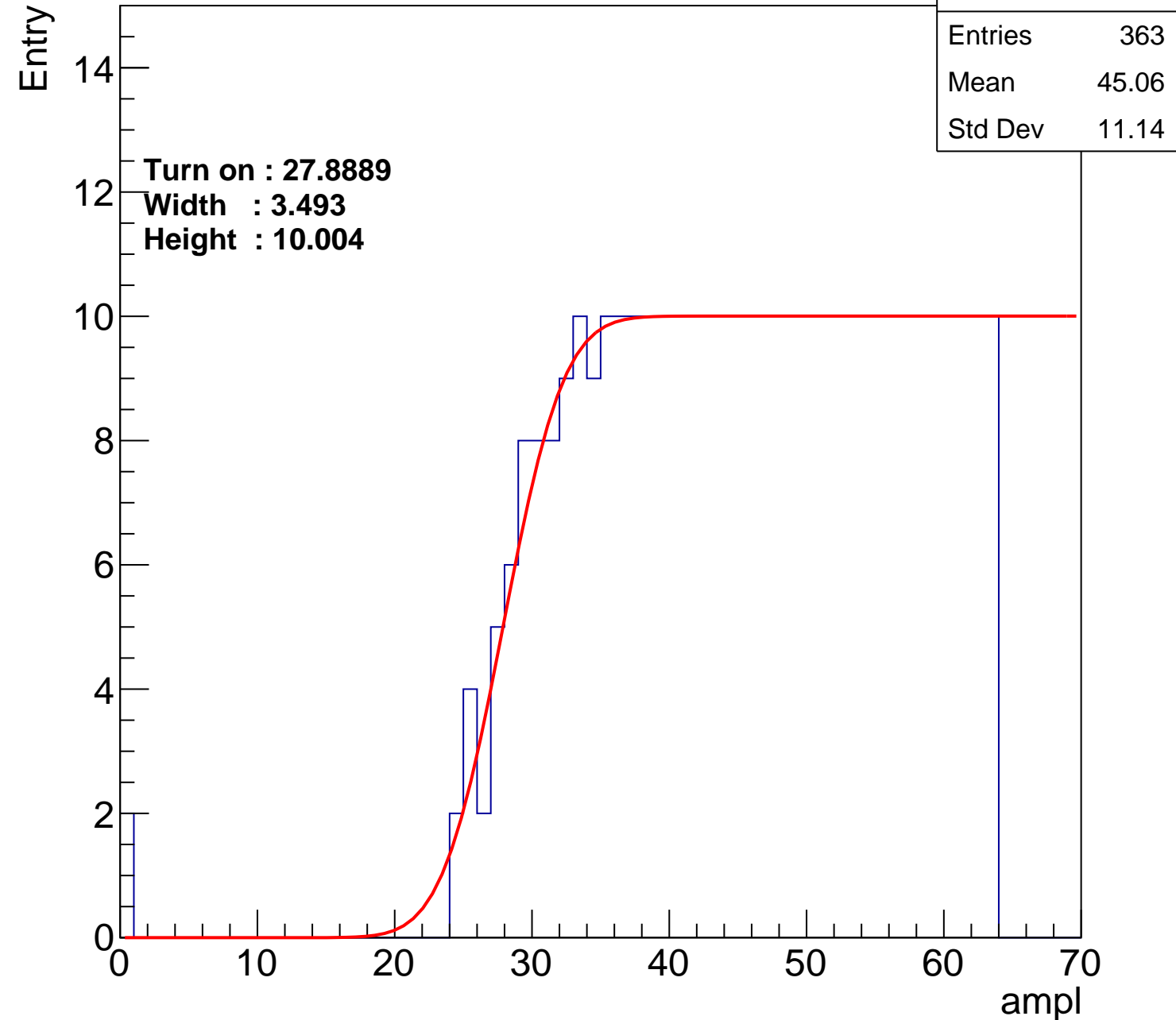
Width : 3.493

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch13

calib_packv5_042523_0143.root, FC#5, port B1

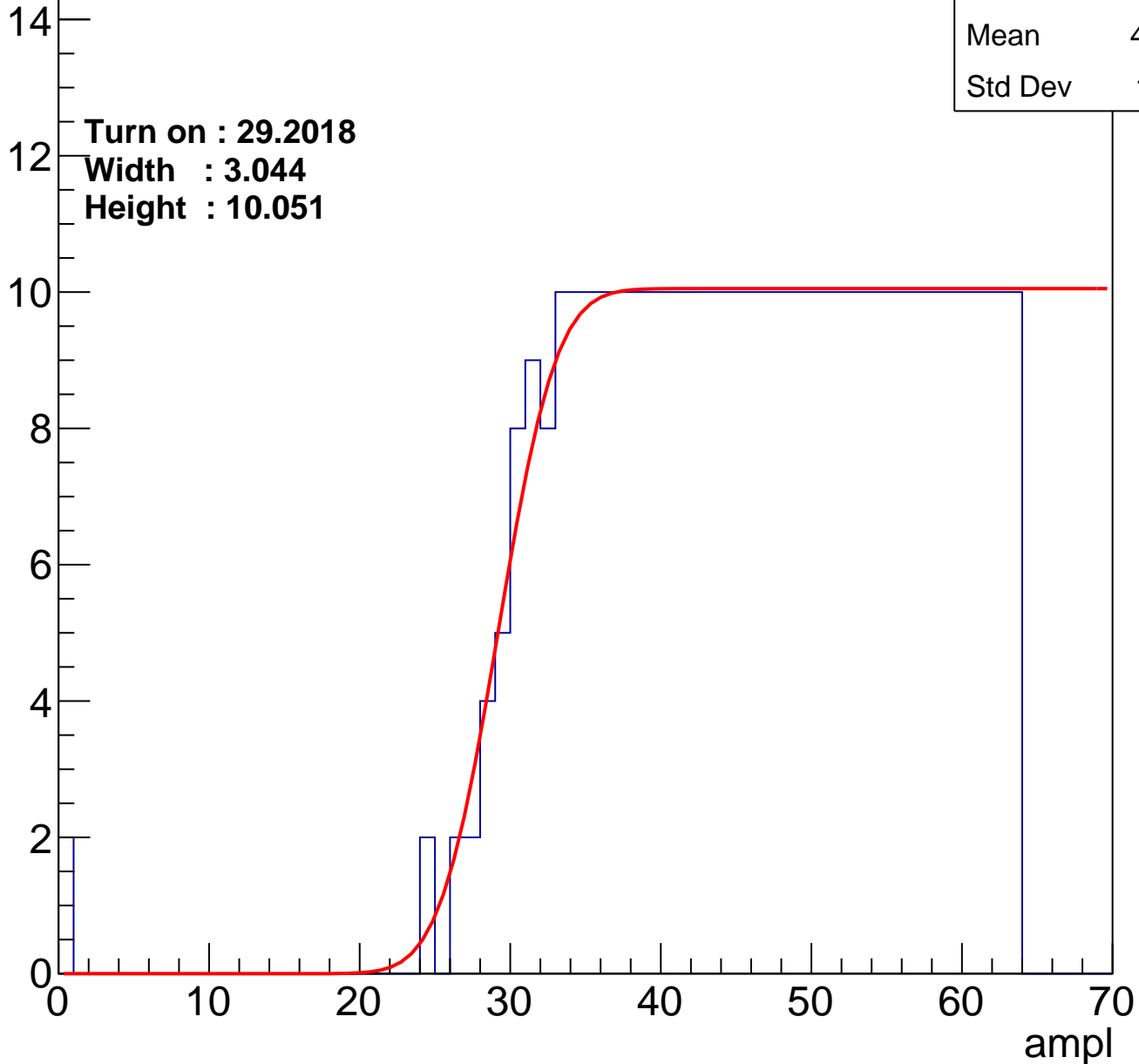
Entries	352
Mean	45.64
Std Dev	10.81

Turn on : 29.2018

Width : 3.044

Height : 10.051

Entry



B0L000S, U11-ch14

calib_packv5_042523_0143.root, FC#5, port B1

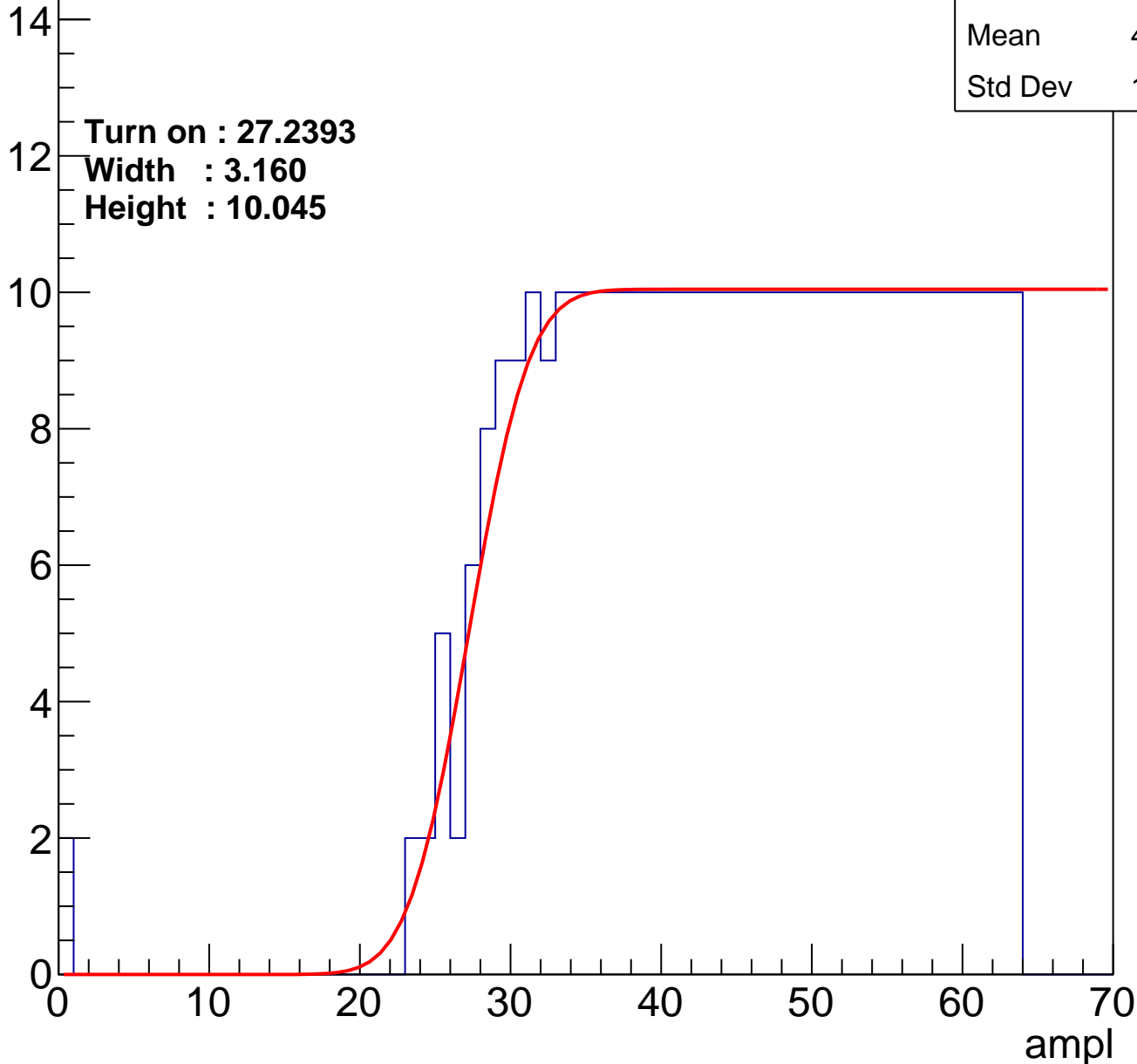
Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 27.2393

Width : 3.160

Height : 10.045

Entry



B0L000S, U11-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.04
Std Dev	11.51

Turn on : 28.6966

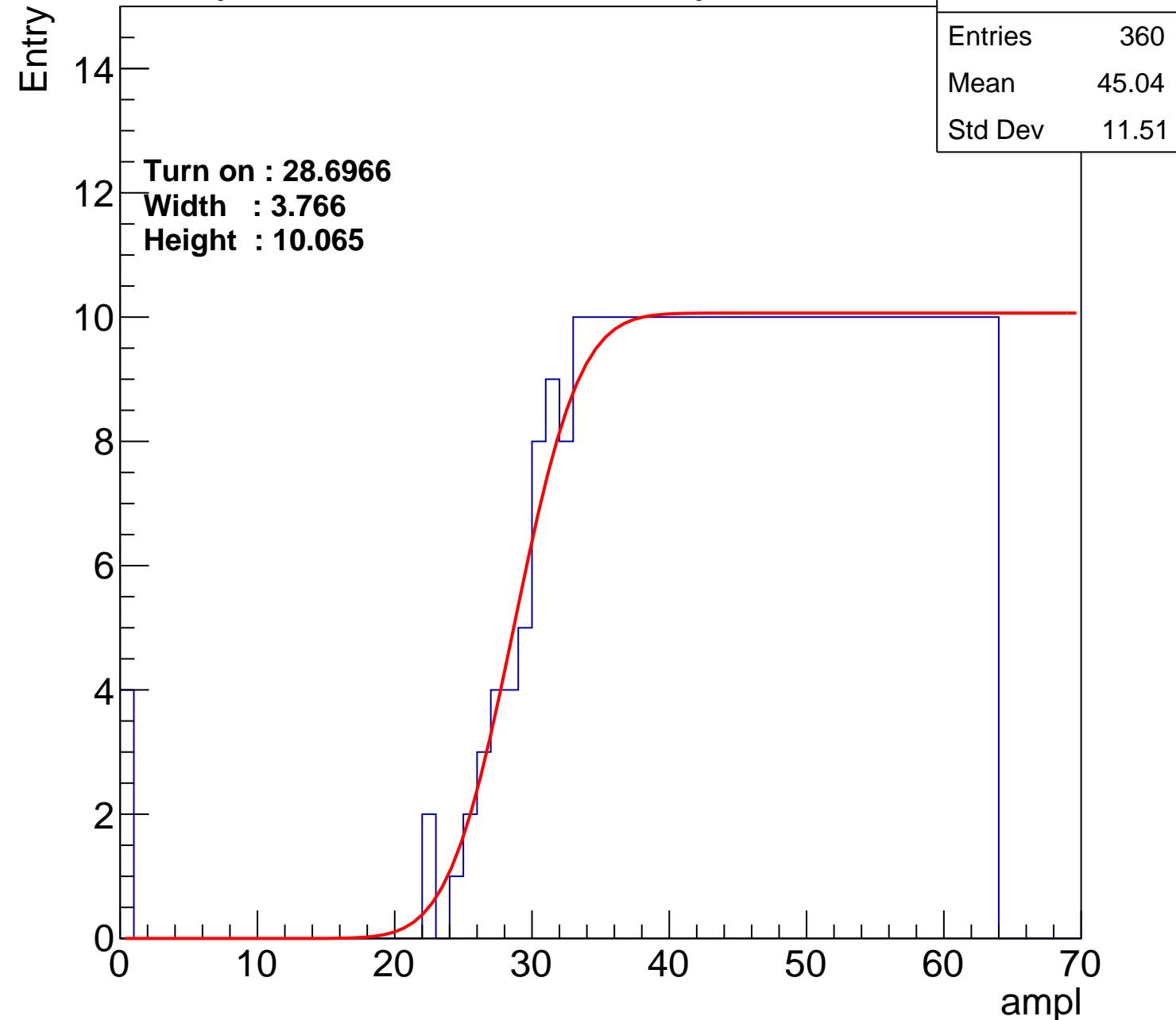
Width : 3.766

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.3801

Width : 2.880

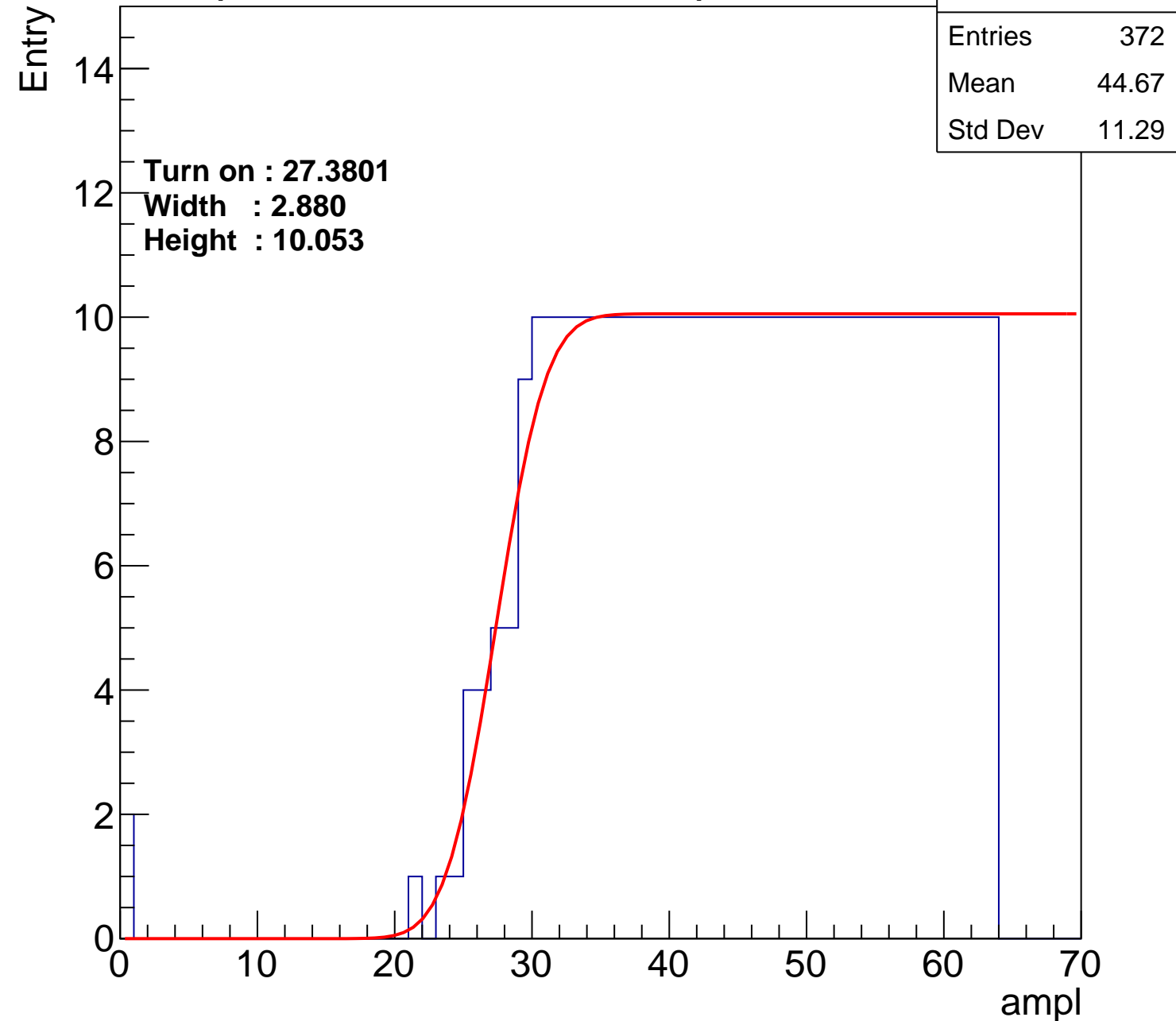
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U11-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.36
Std Dev	10.93

Turn on : 28.2173

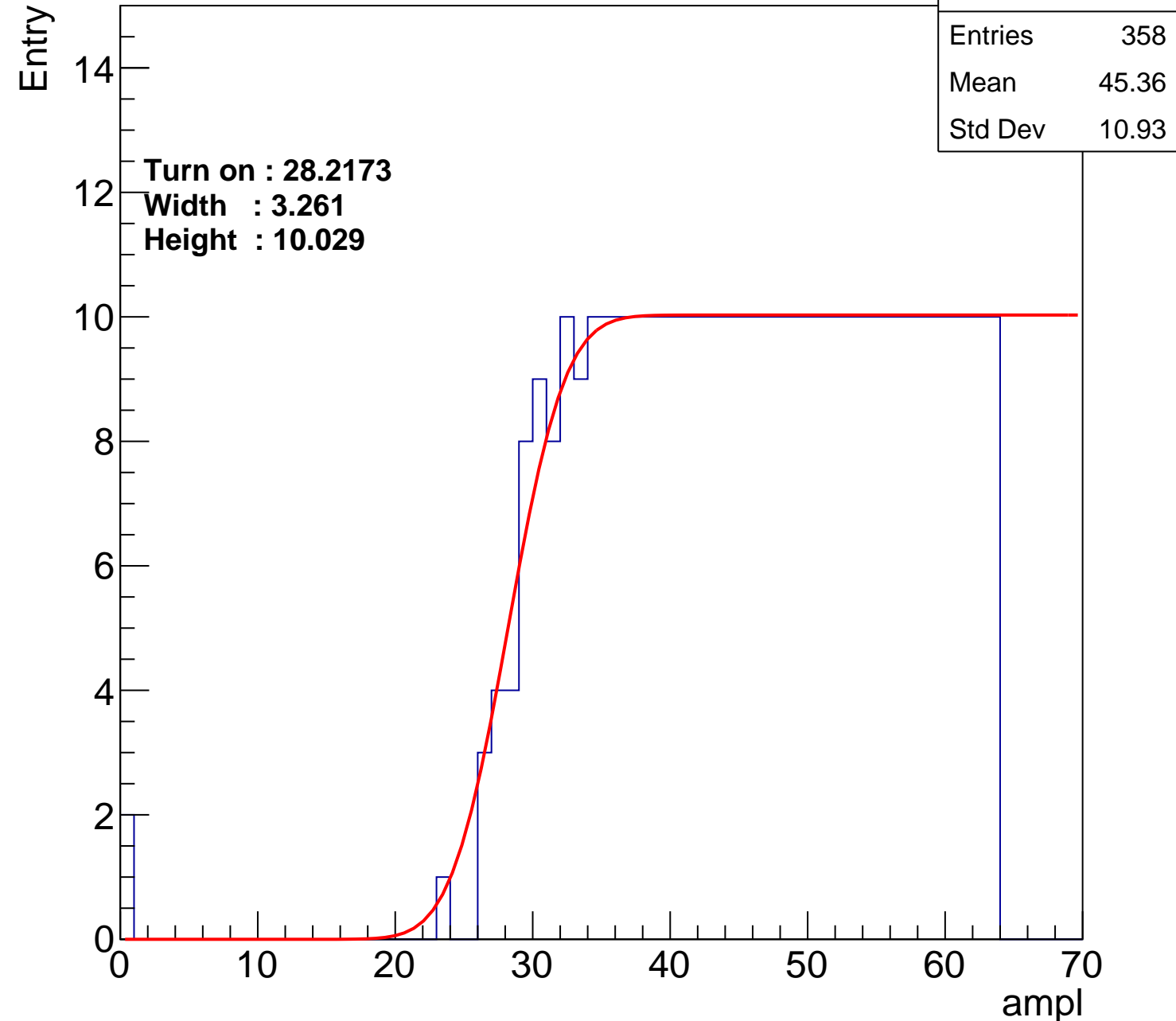
Width : 3.261

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch18

calib_packv5_042523_0143.root, FC#5, port B1

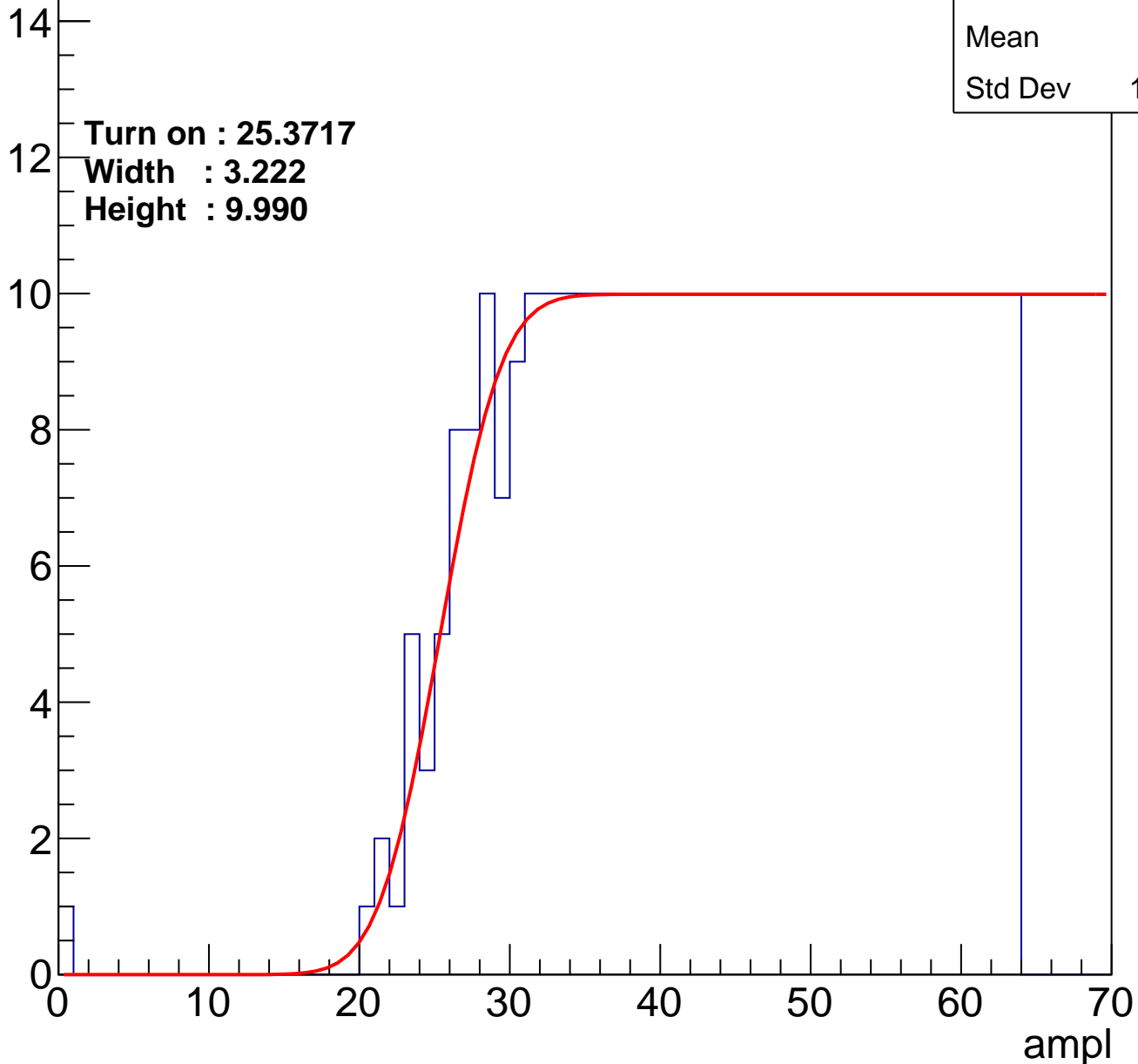
Entries	390
Mean	43.8
Std Dev	11.65

Turn on : 25.3717

Width : 3.222

Height : 9.990

Entry



B0L000S, U11-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.6545

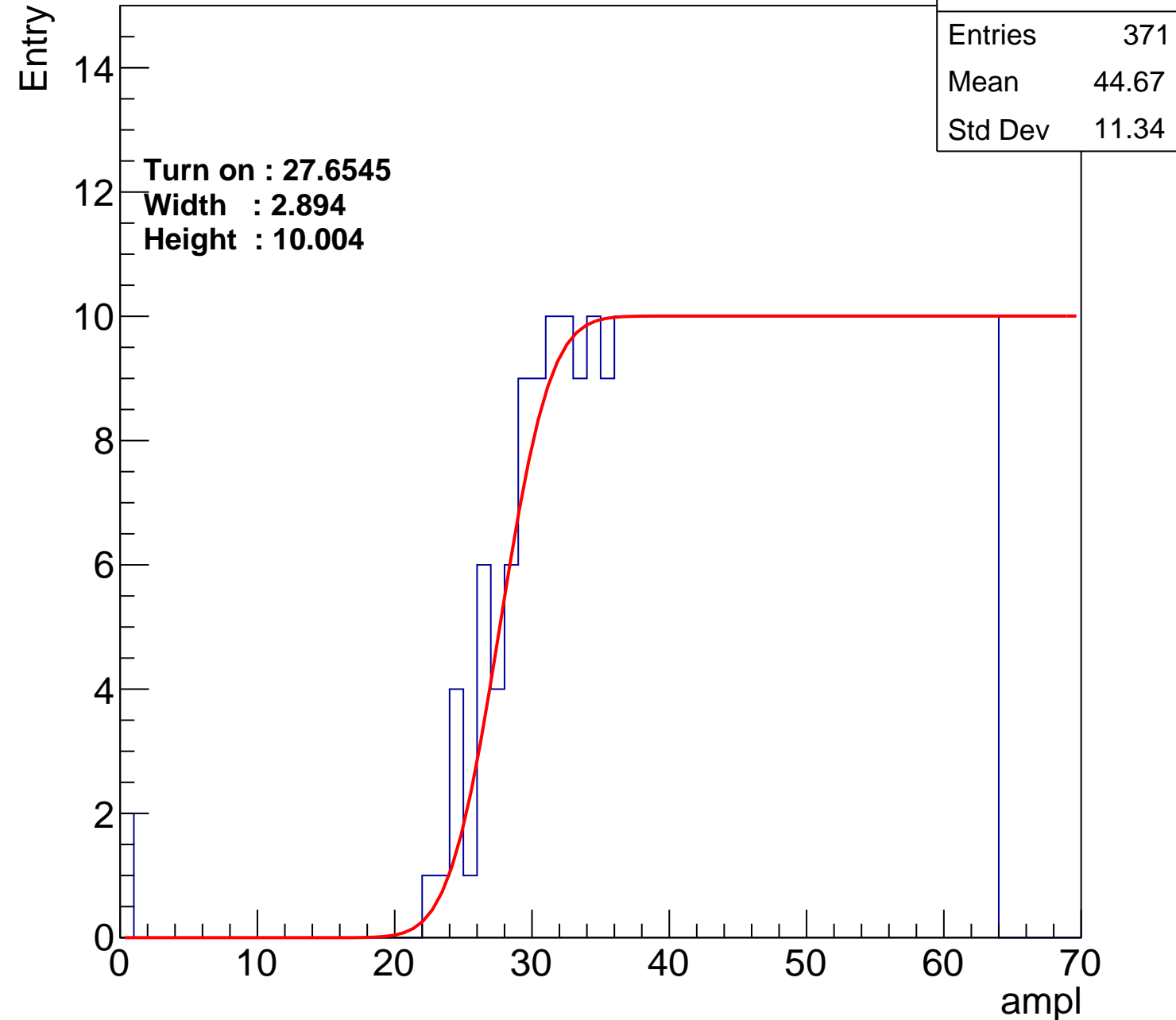
Width : 2.894

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch20

calib_packv5_042523_0143.root, FC#5, port B1

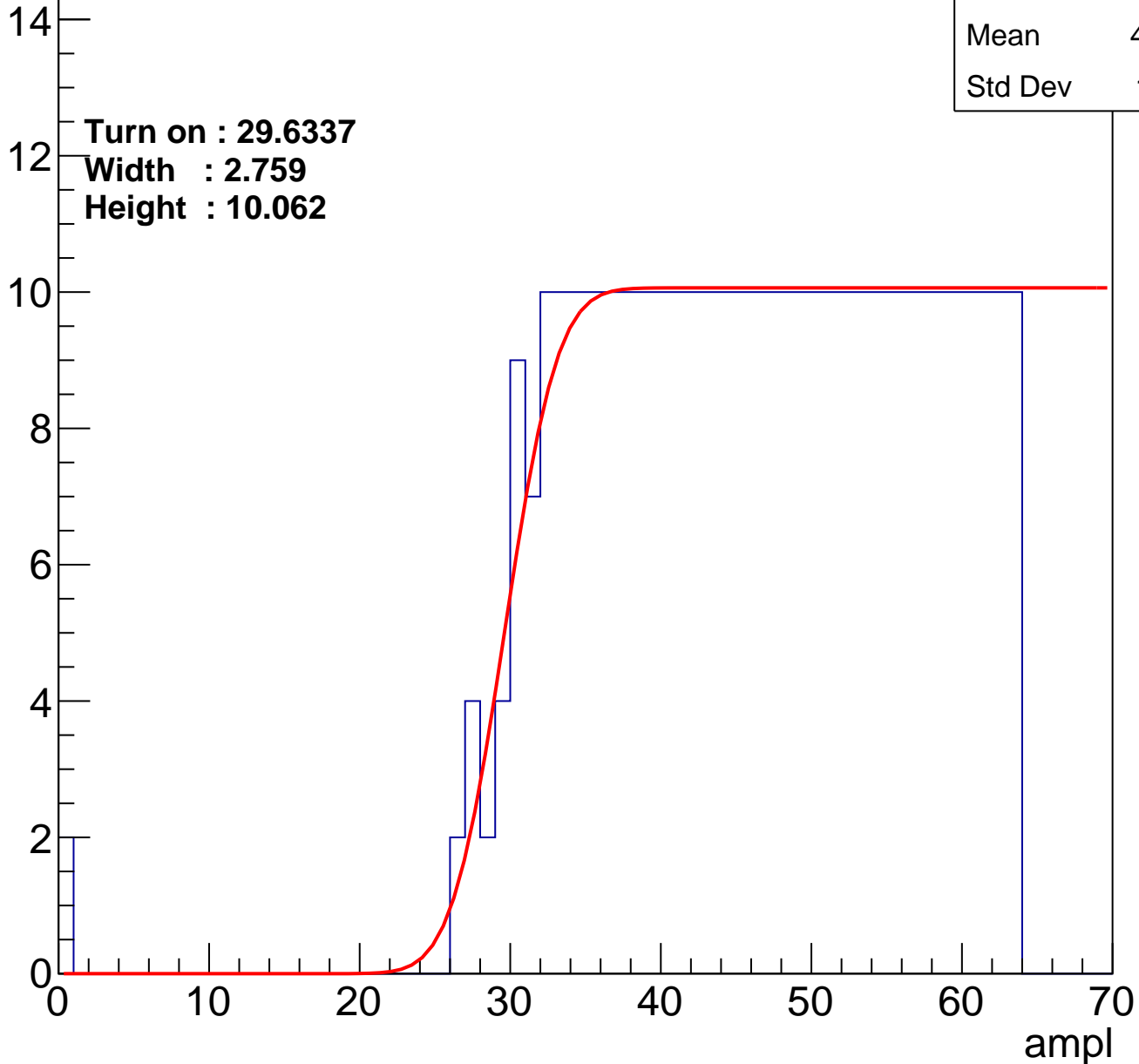
Entries	350
Mean	45.77
Std Dev	10.71

Turn on : 29.6337

Width : 2.759

Height : 10.062

Entry



B0L000S, U11-ch21

calib_packv5_042523_0143.root, FC#5, port B1

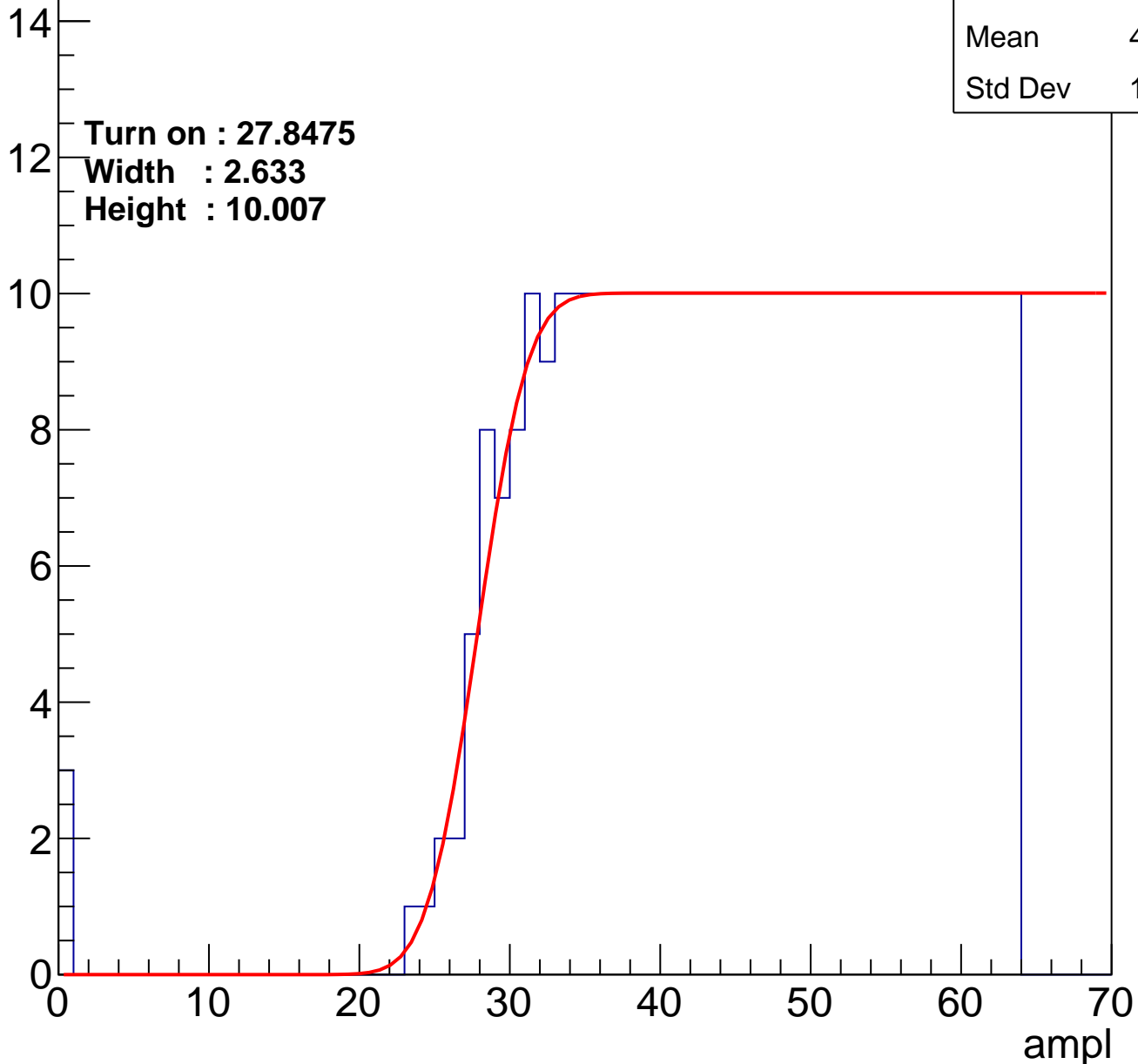
Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 27.8475

Width : 2.633

Height : 10.007

Entry



B0L000S, U11-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.78
Std Dev	11.47

Turn on : 27.9544

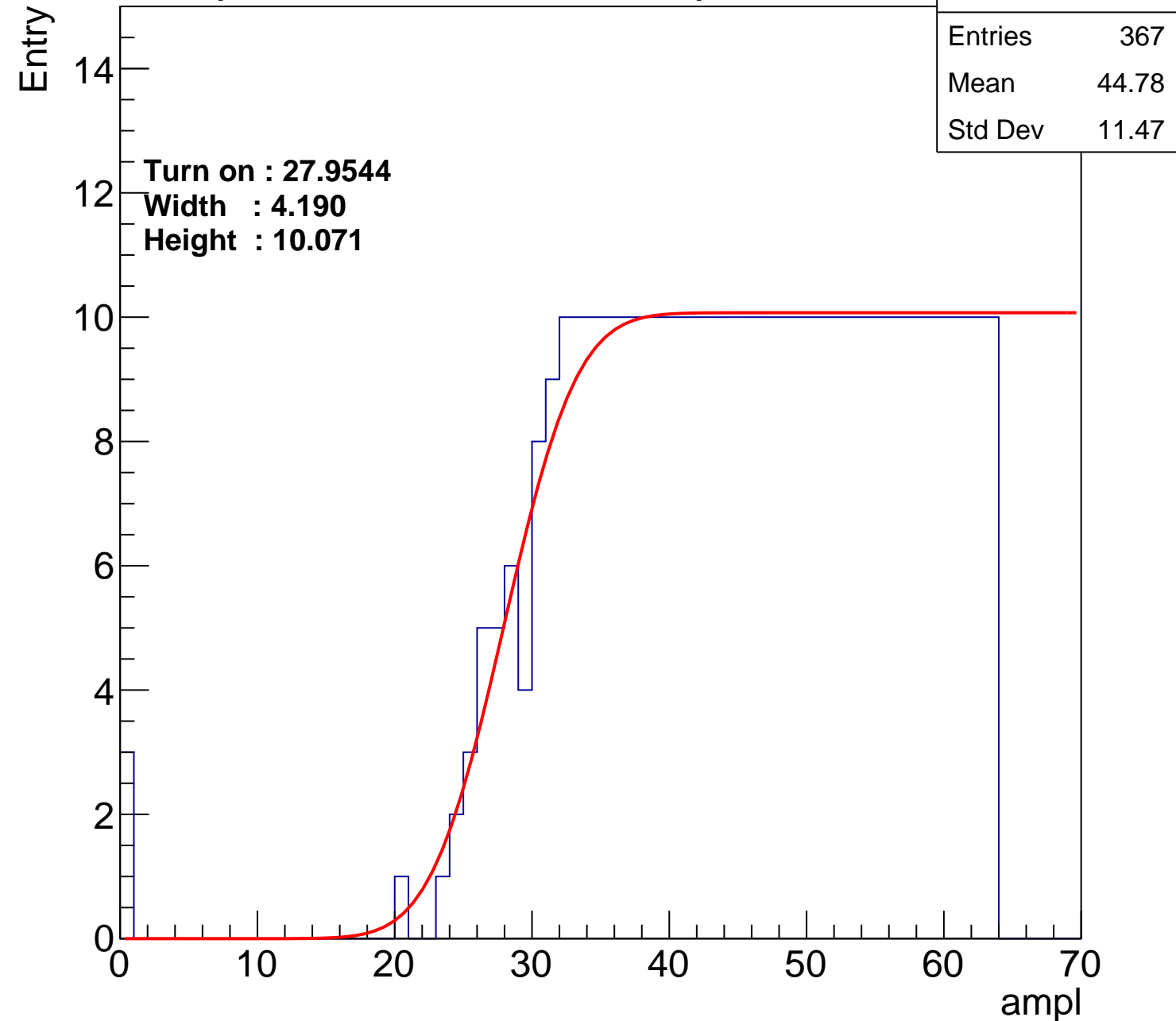
Width : 4.190

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.07
Std Dev	11.74

Turn on : 26.2440

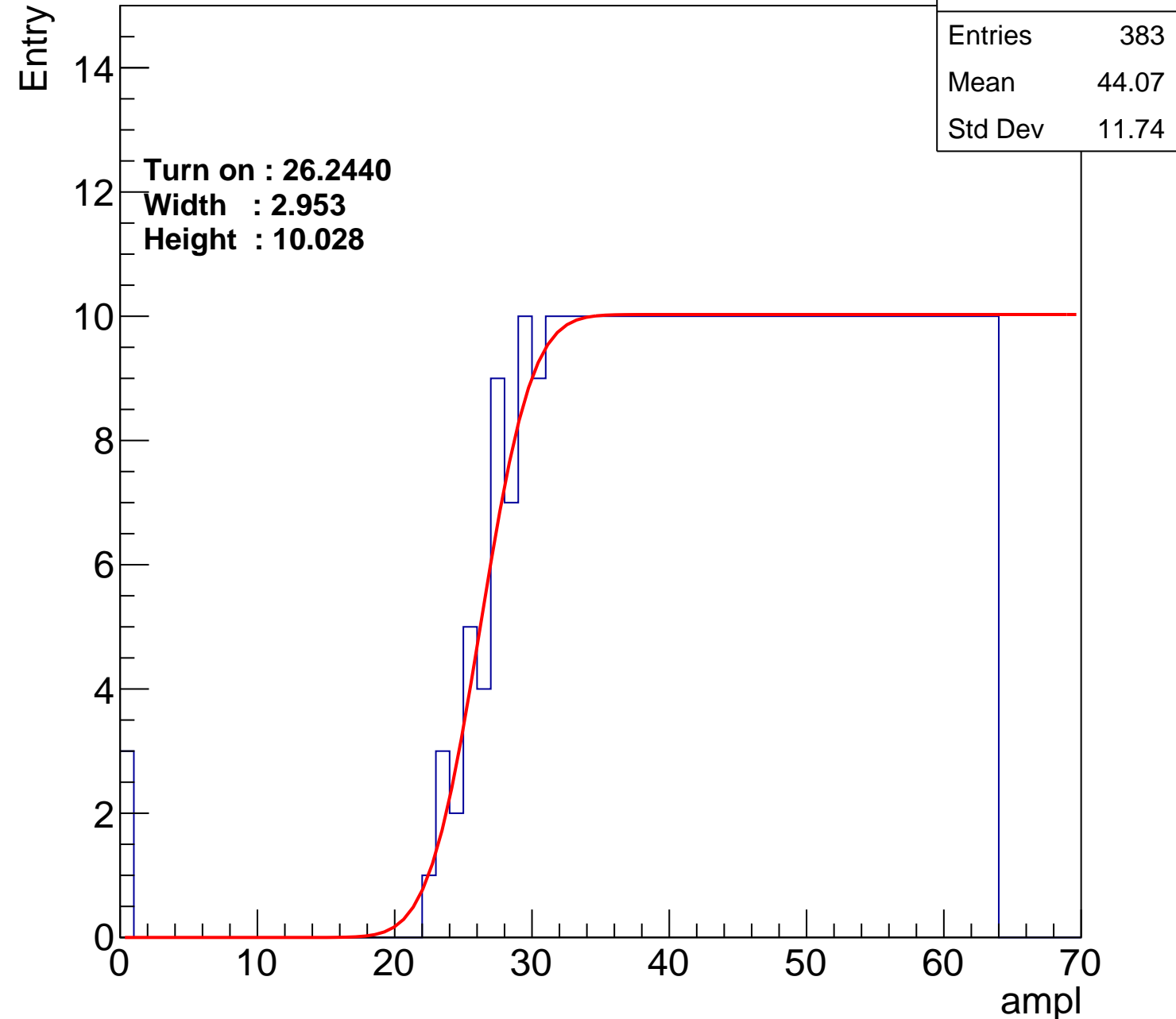
Width : 2.953

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch24

calib_packv5_042523_0143.root, FC#5, port B1

Entries	348
Mean	45.84
Std Dev	10.69

Turn on : 28.8484

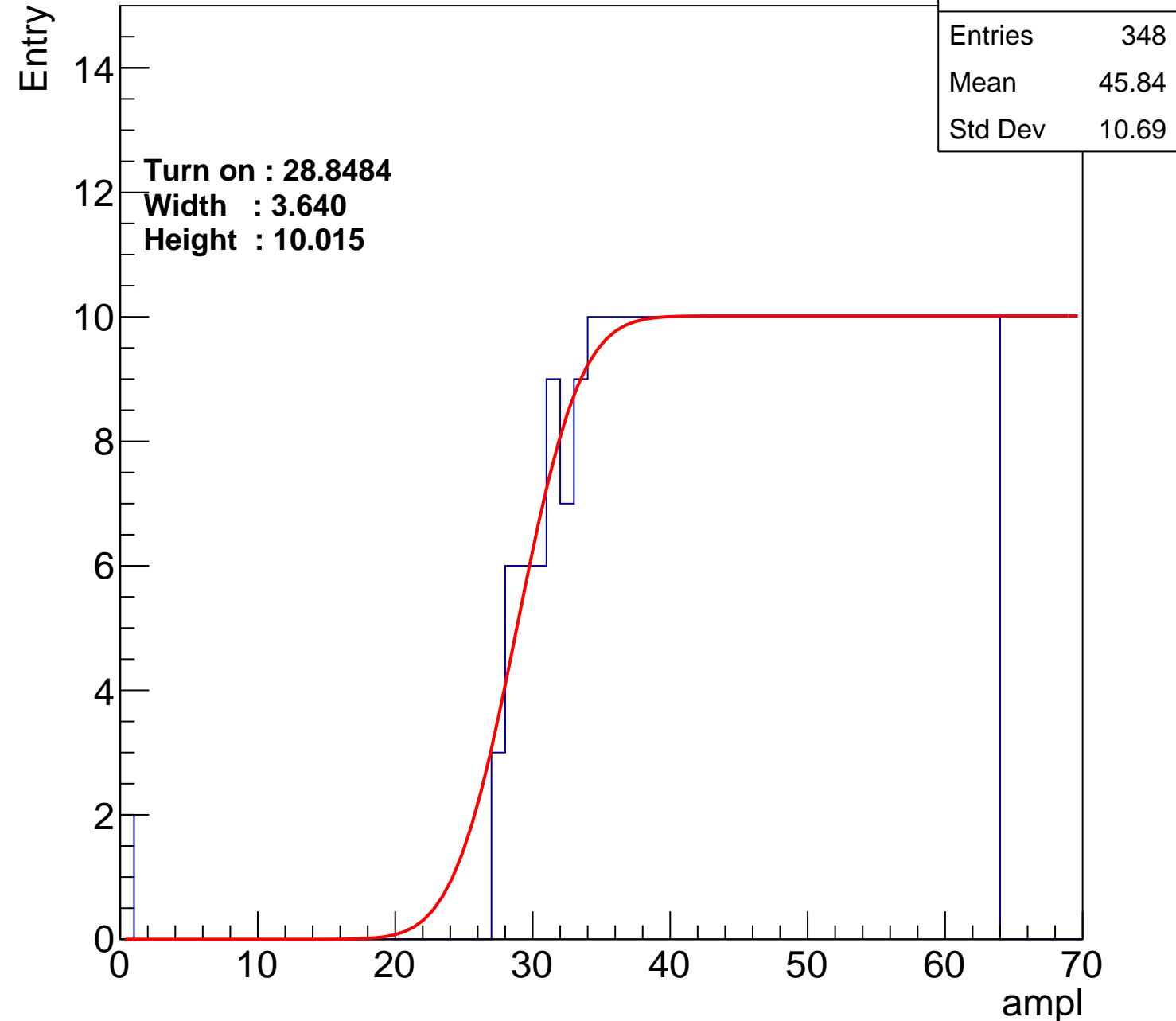
Width : 3.640

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.53
Std Dev	11.55

Turn on : 27.2871

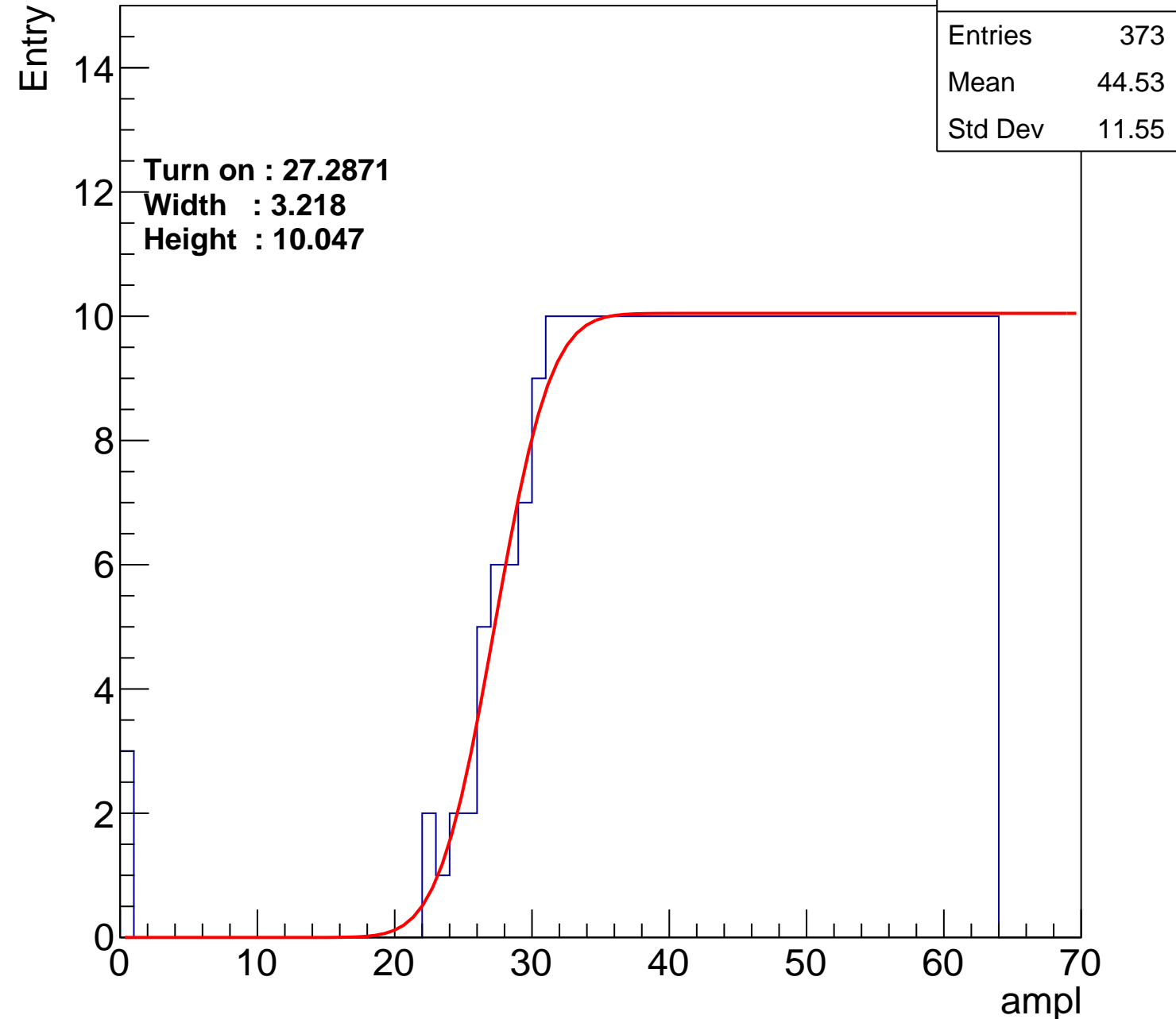
Width : 3.218

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.84
Std Dev	11.36

Turn on : 27.5853

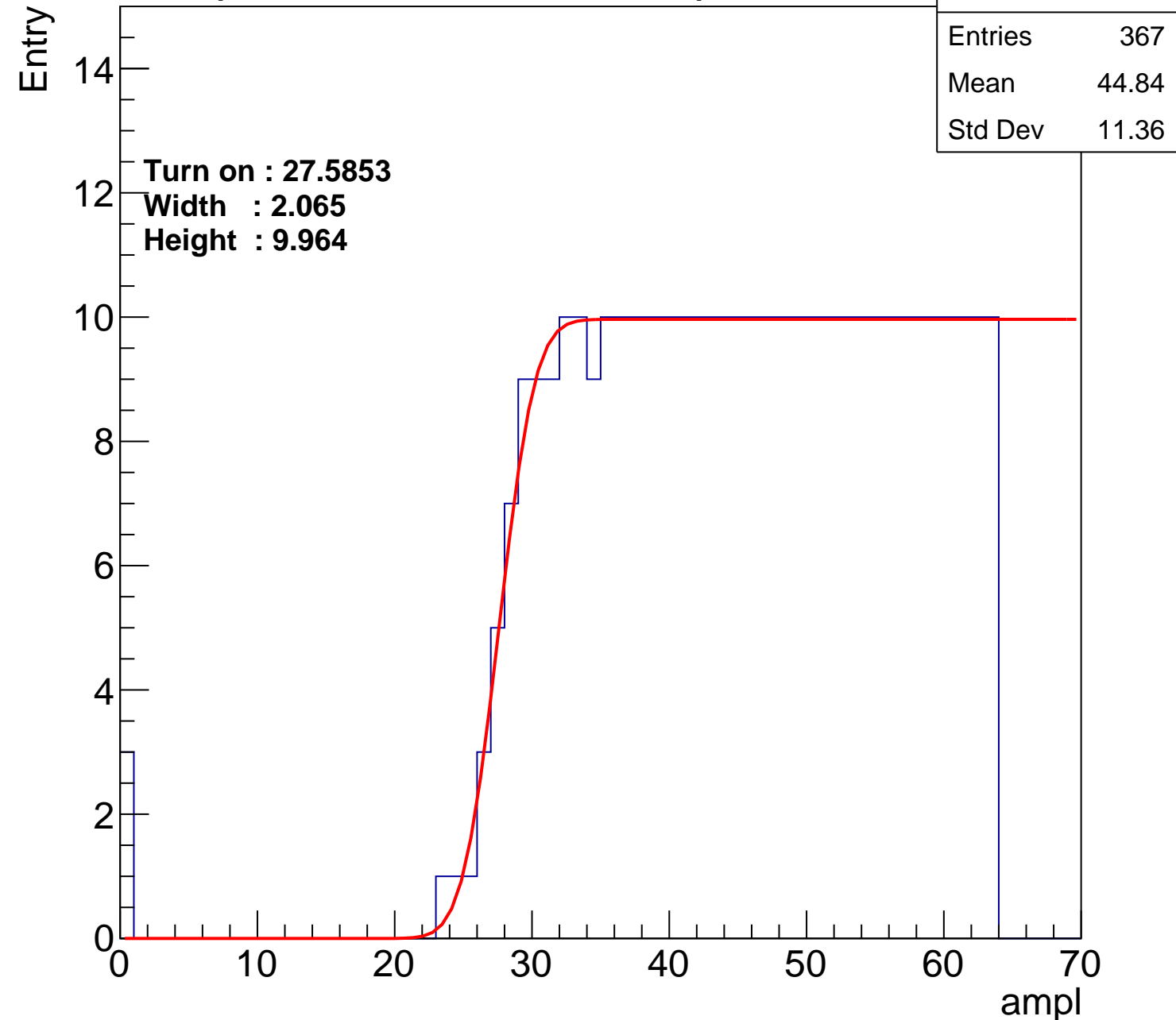
Width : 2.065

Height : 9.964

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch27

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.69
Std Dev	11.77

Turn on : 27.9098

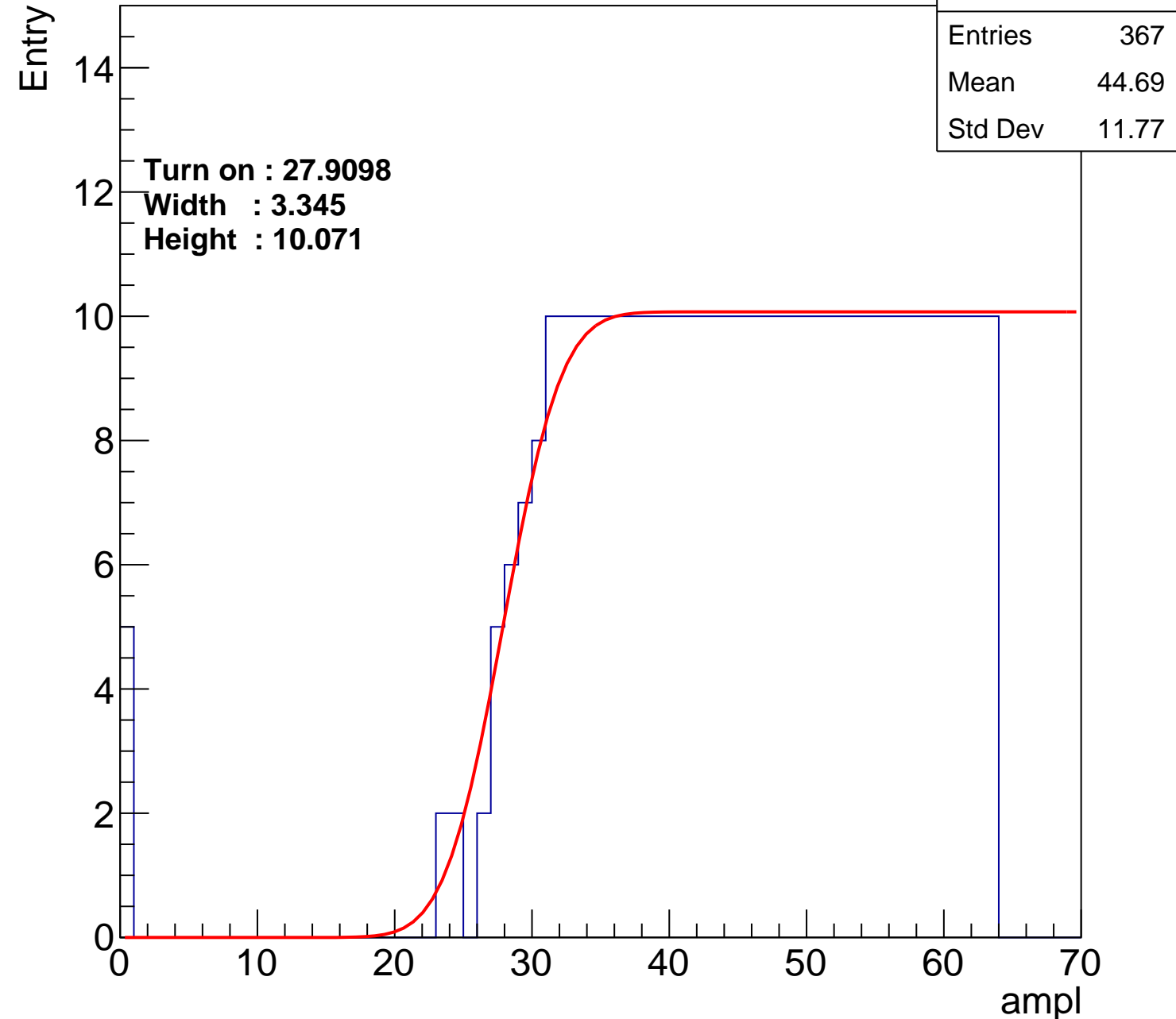
Width : 3.345

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.3
Std Dev	11.18

Turn on : 29.1249

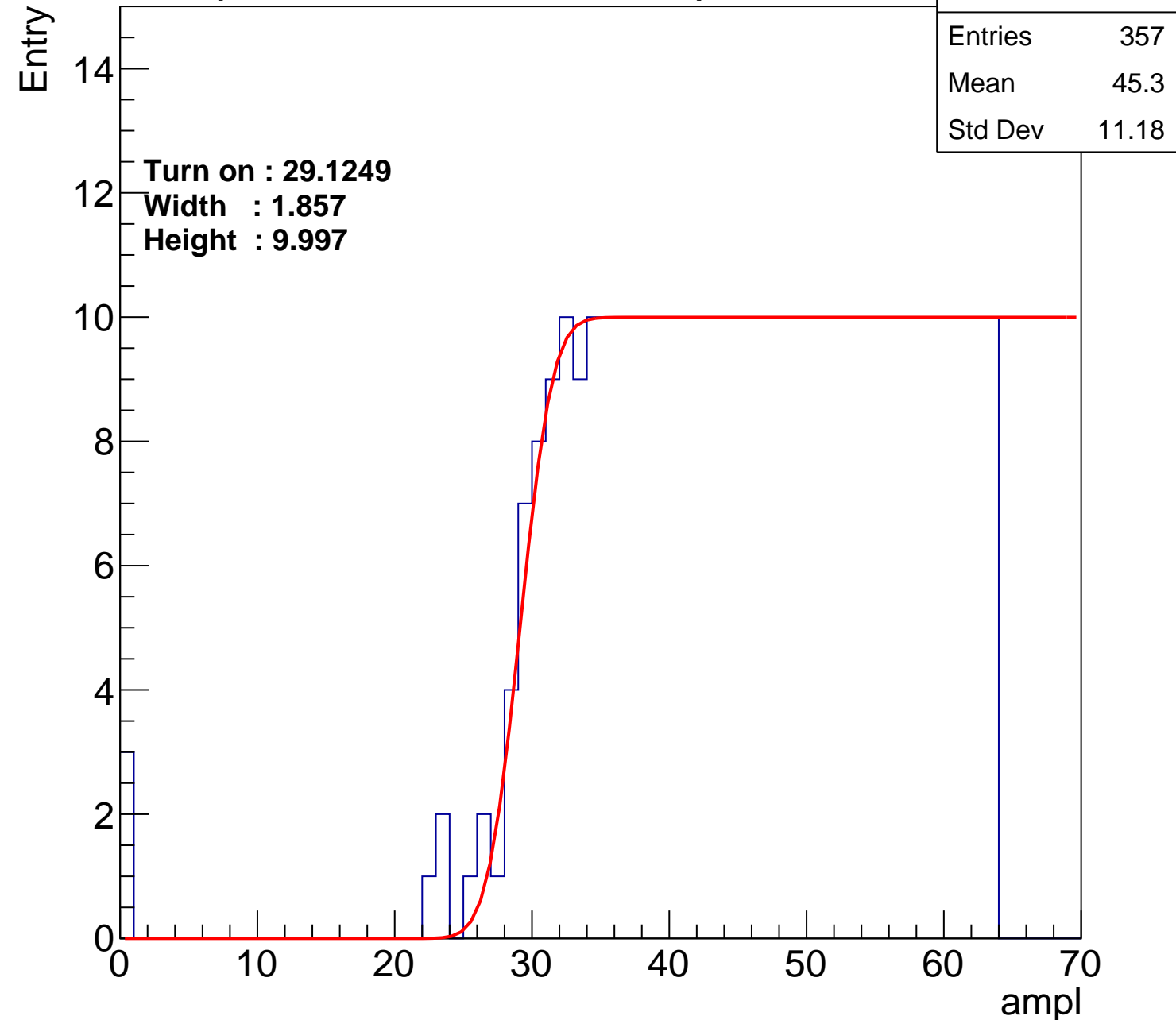
Width : 1.857

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch29

calib_packv5_042523_0143.root, FC#5, port B1

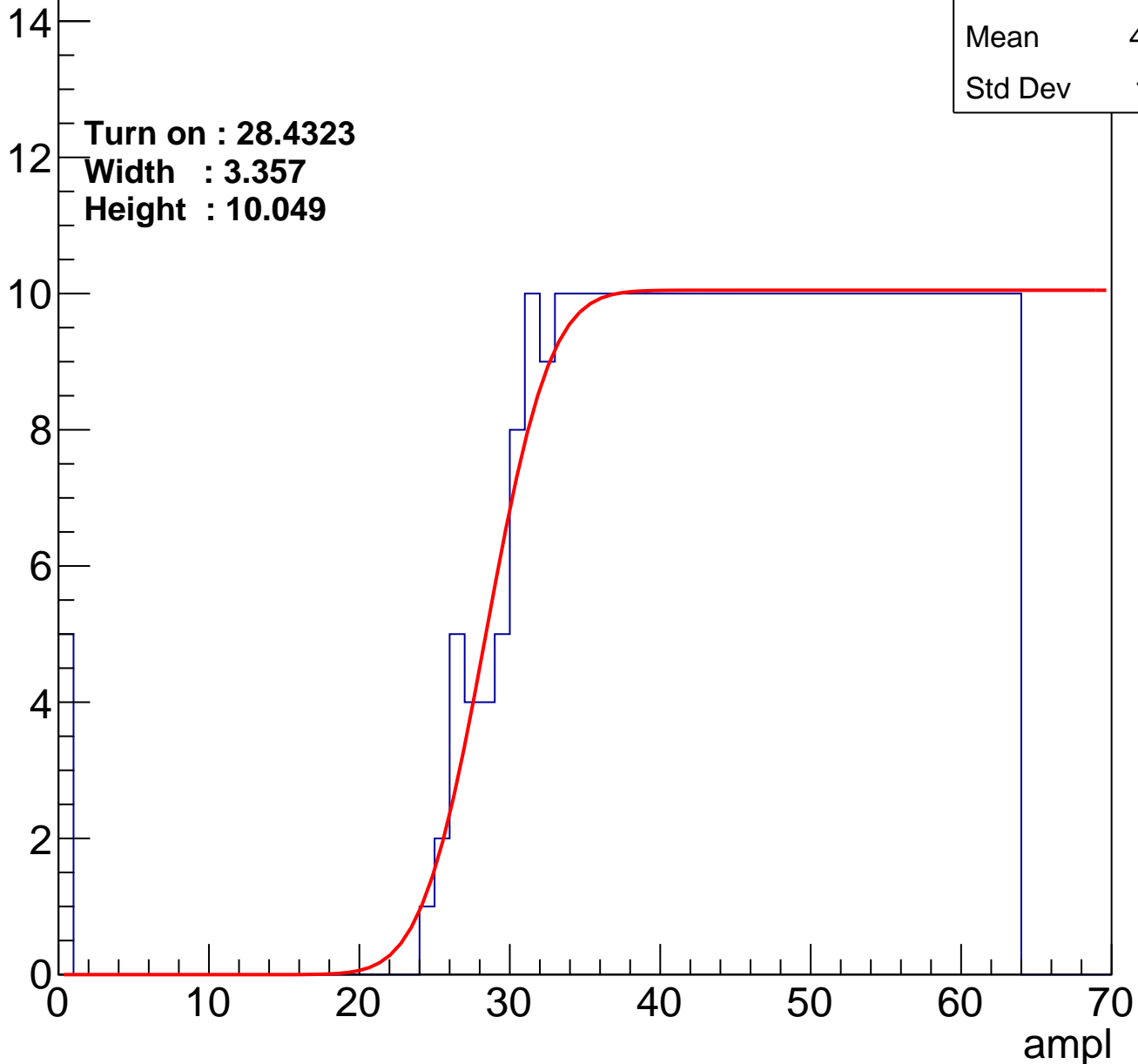
Entries	363
Mean	44.87
Std Dev	11.71

Turn on : 28.4323

Width : 3.357

Height : 10.049

Entry



B0L000S, U11-ch30

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.13
Std Dev	11.23

Turn on : 28.3125

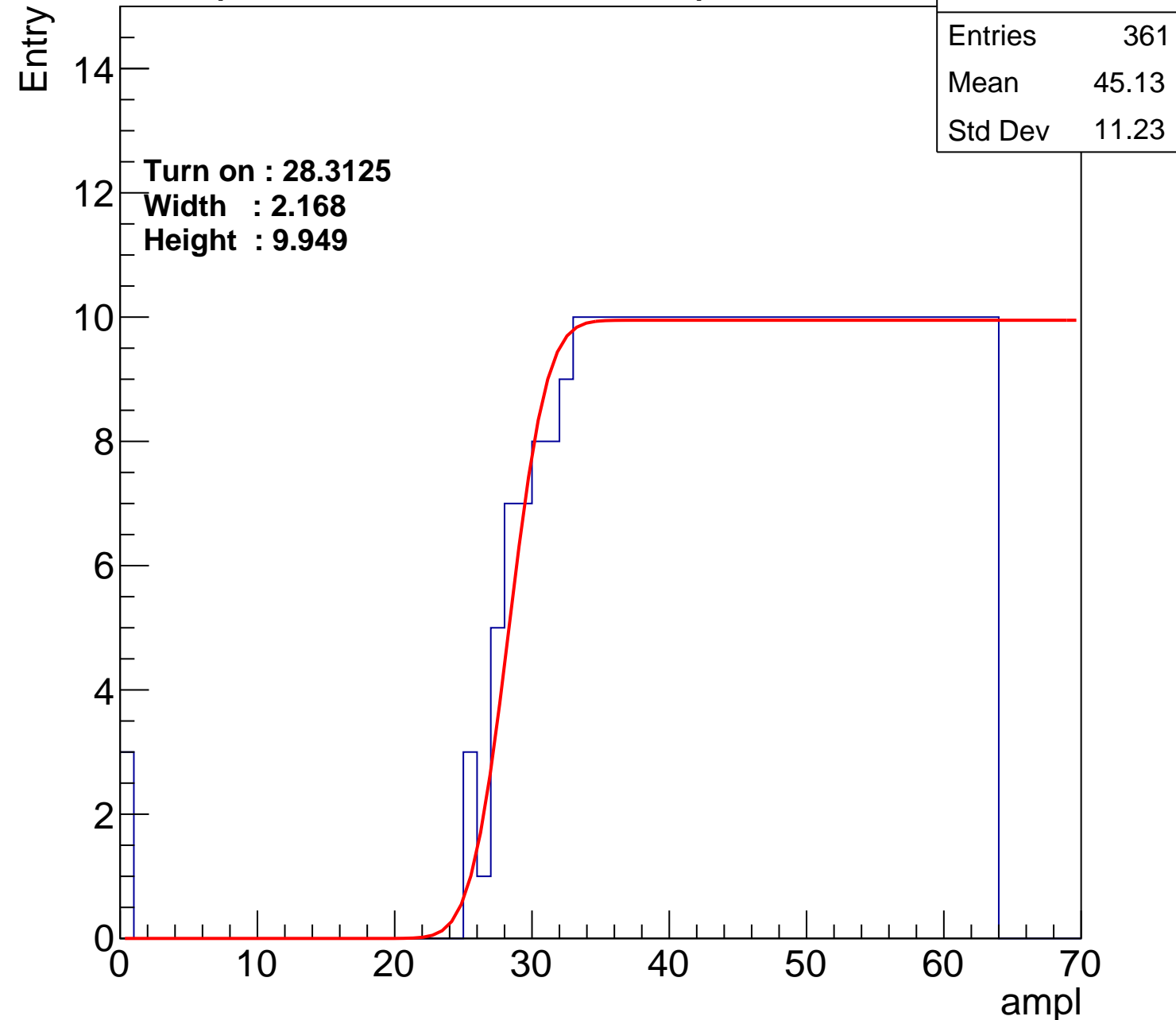
Width : 2.168

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch31

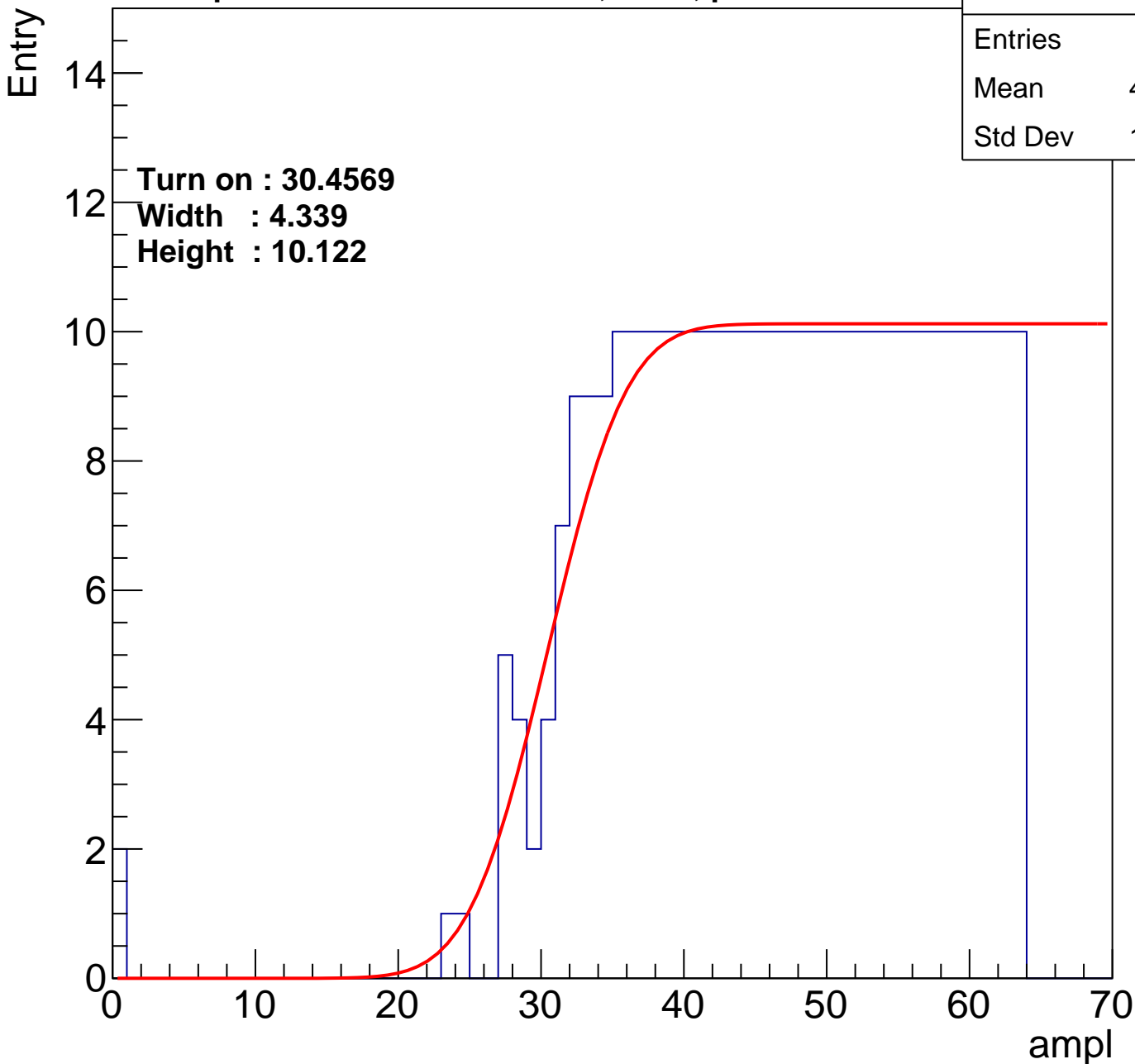
calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
Mean	46.03
Std Dev	10.66

Turn on : 30.4569

Width : 4.339

Height : 10.122



B0L000S, U11-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 26.1662

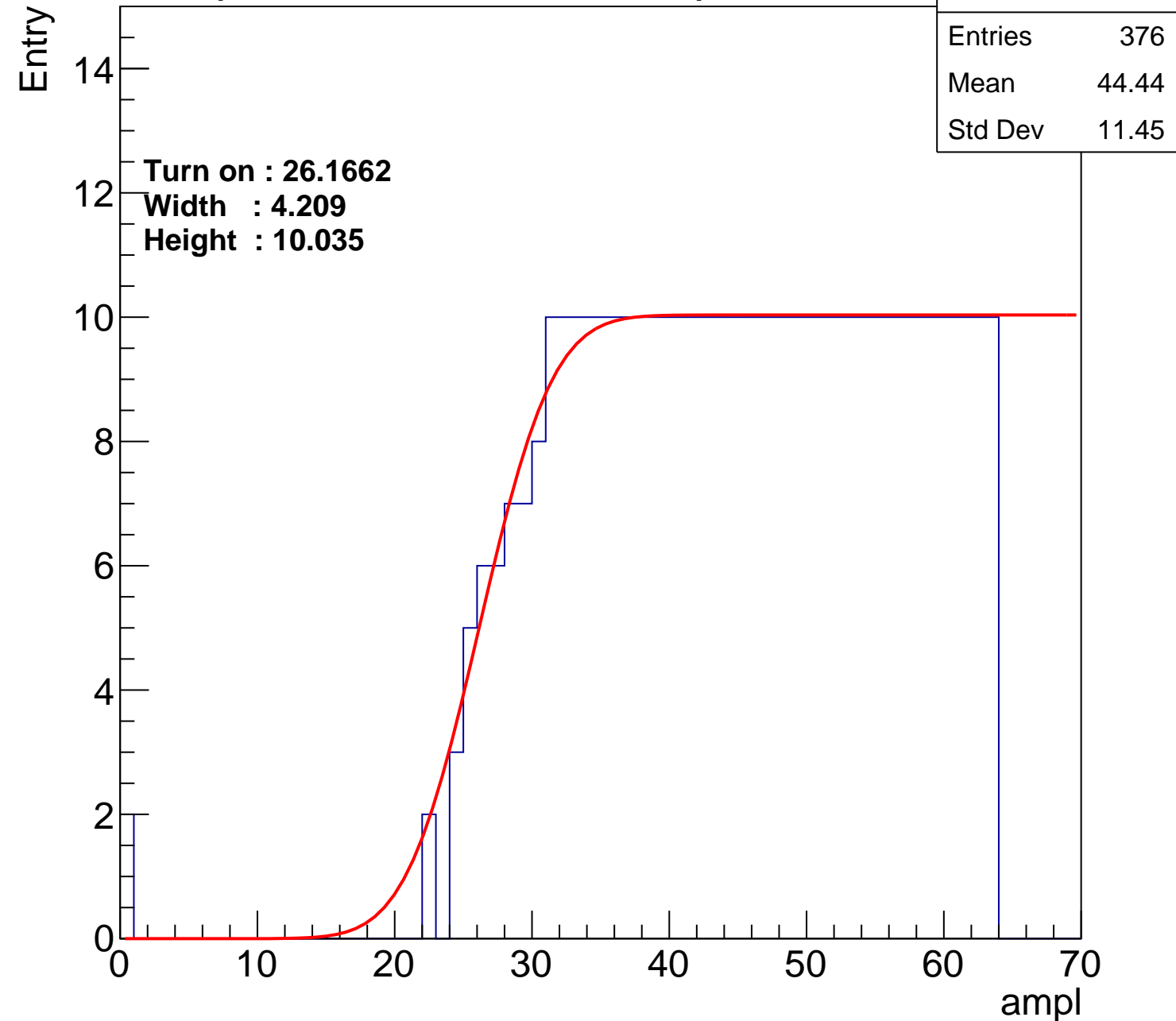
Width : 4.209

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch33

calib_packv5_042523_0143.root, FC#5, port B1

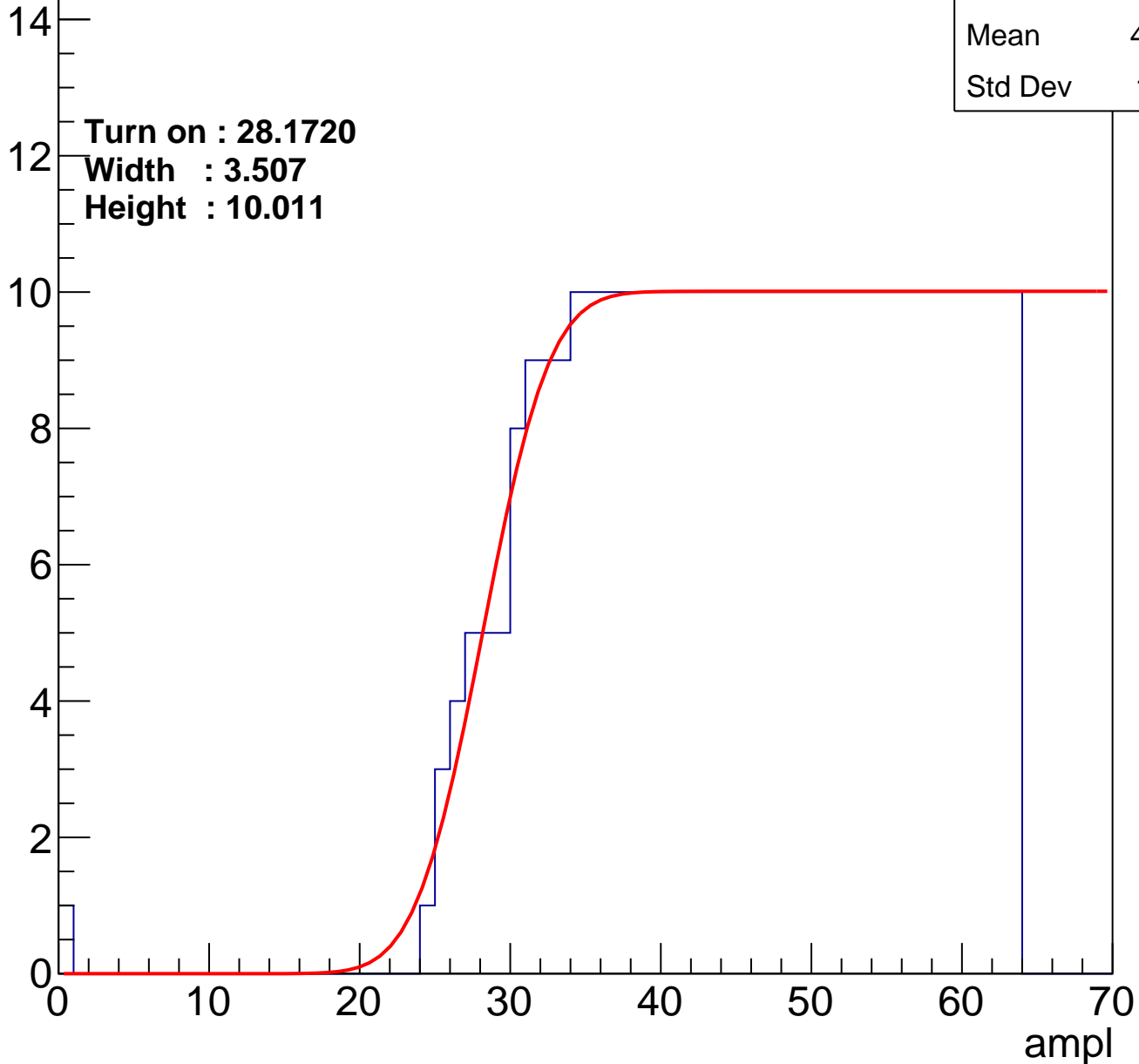
Entries	359
Mean	45.34
Std Dev	10.81

Turn on : 28.1720

Width : 3.507

Height : 10.011

Entry



B0L000S, U11-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.54
Std Dev	11.34

Turn on : 26.8045

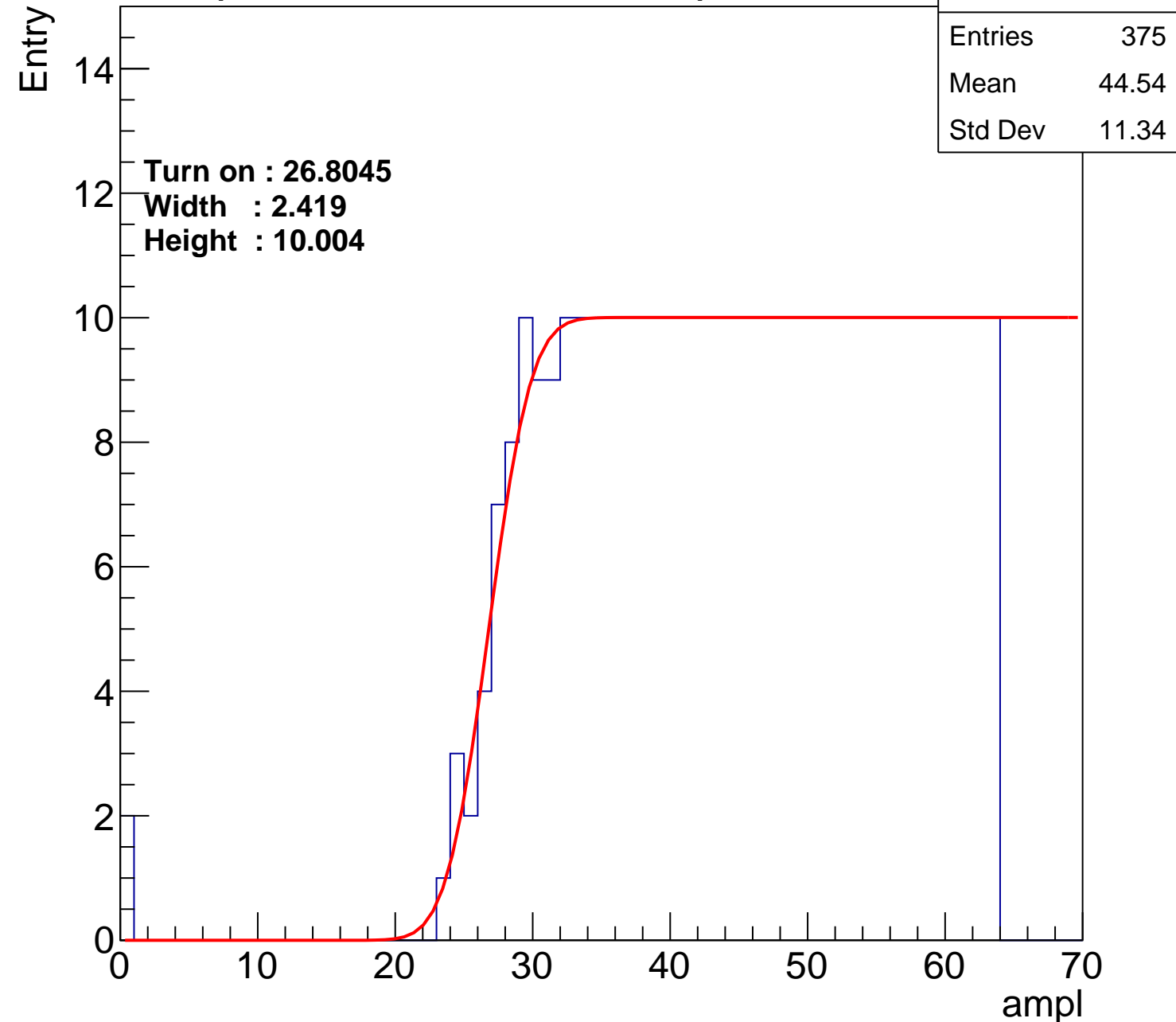
Width : 2.419

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch35

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.46
Std Dev	10.71

Turn on : 27.9819

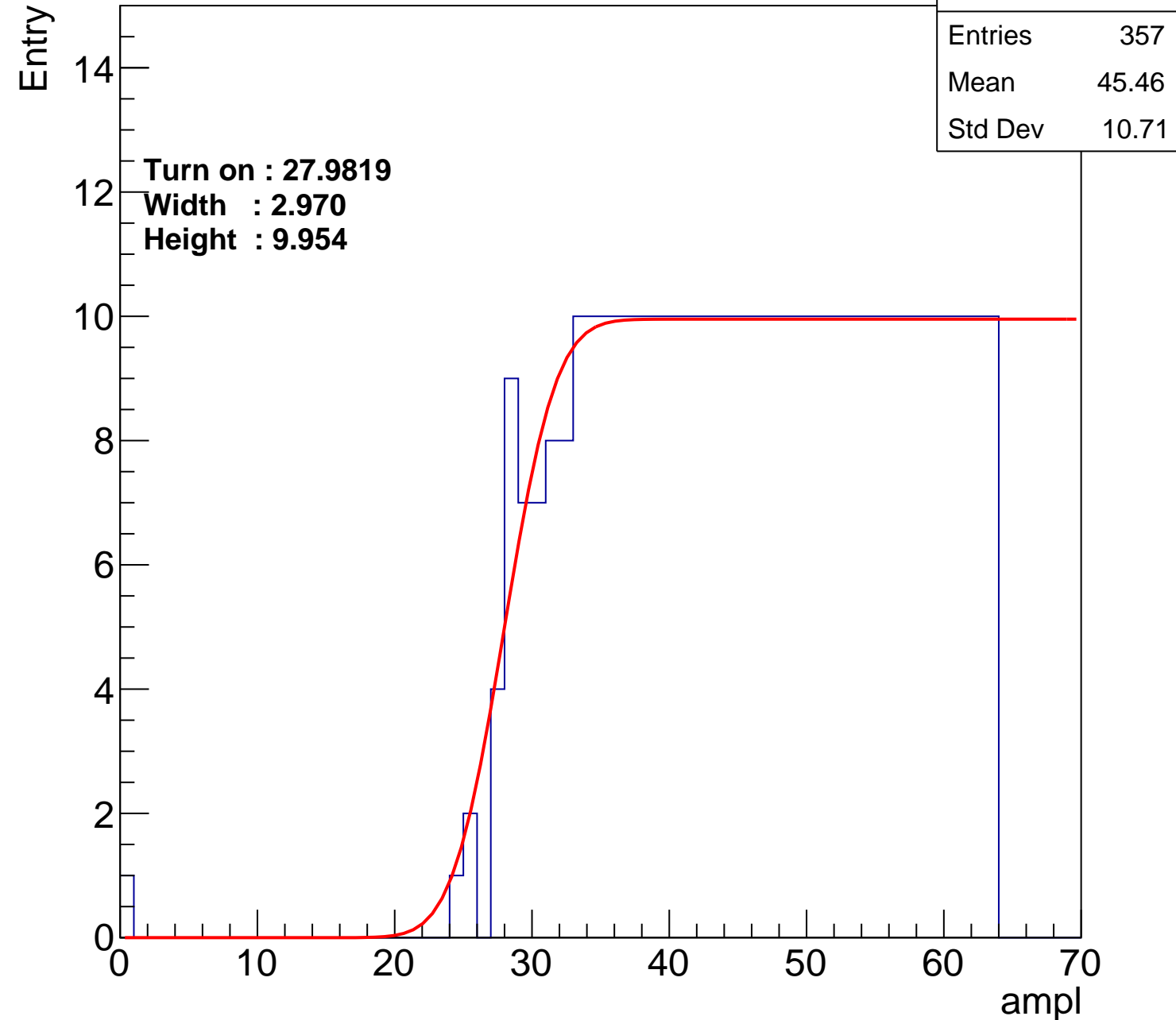
Width : 2.970

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl

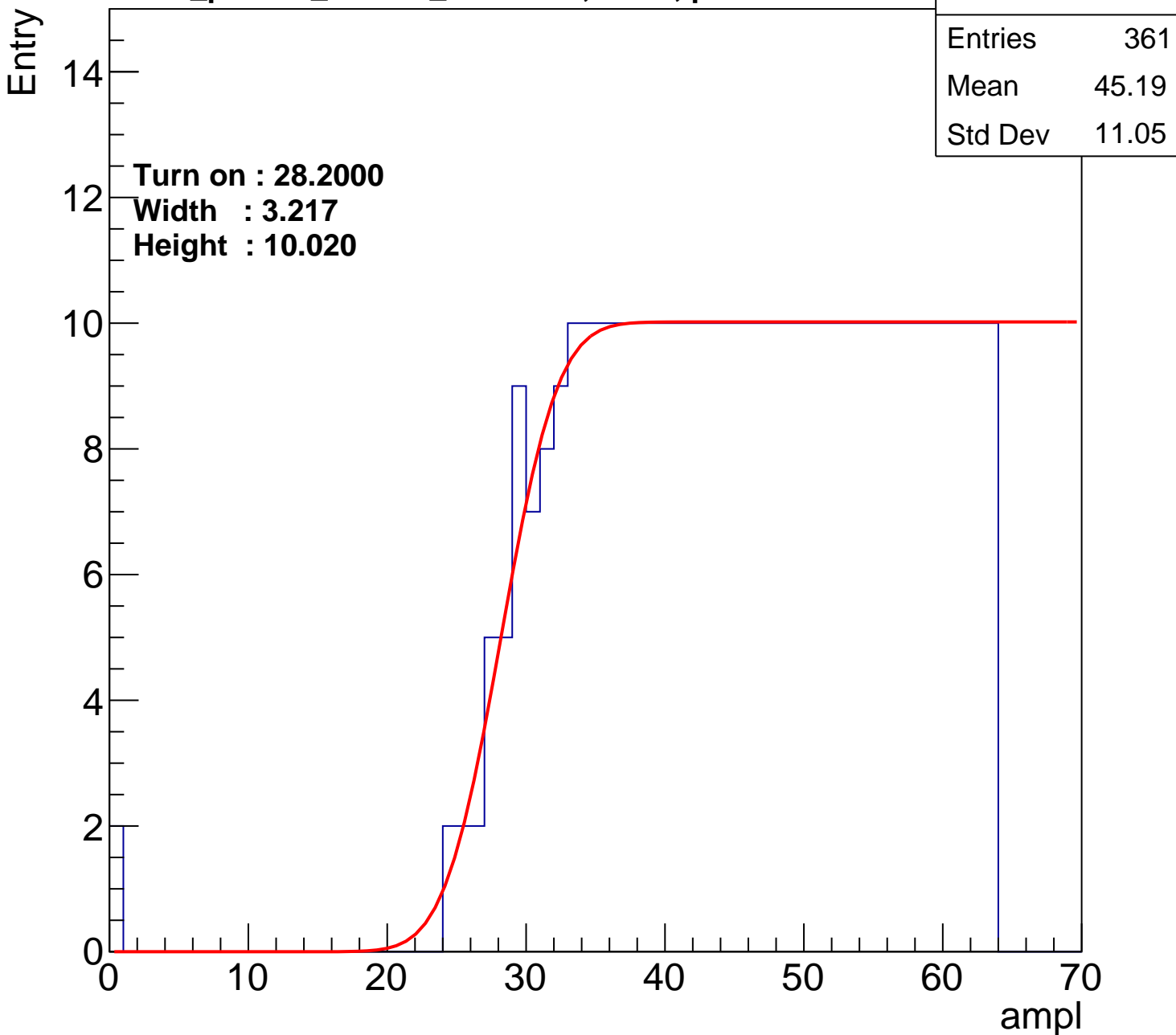


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.19
Std Dev	11.05

Height : 10.020



B0L000S, U11-ch37

calib_packv5_042523_0143.root, FC#5, port B1

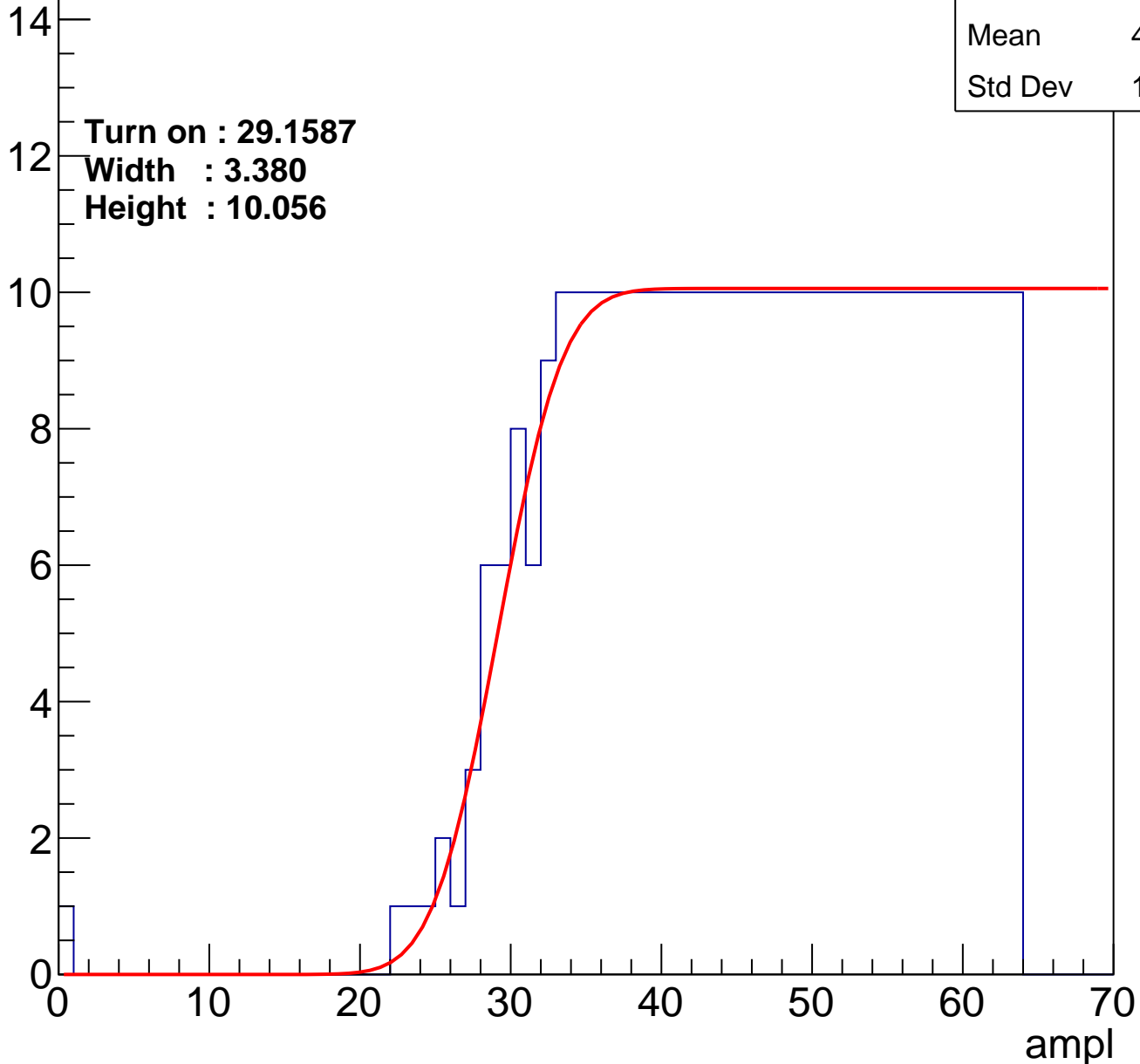
Entries	355
Mean	45.53
Std Dev	10.73

Turn on : 29.1587

Width : 3.380

Height : 10.056

Entry



B0L000S, U11-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.55
Std Dev	11.5

Turn on : 27.1997

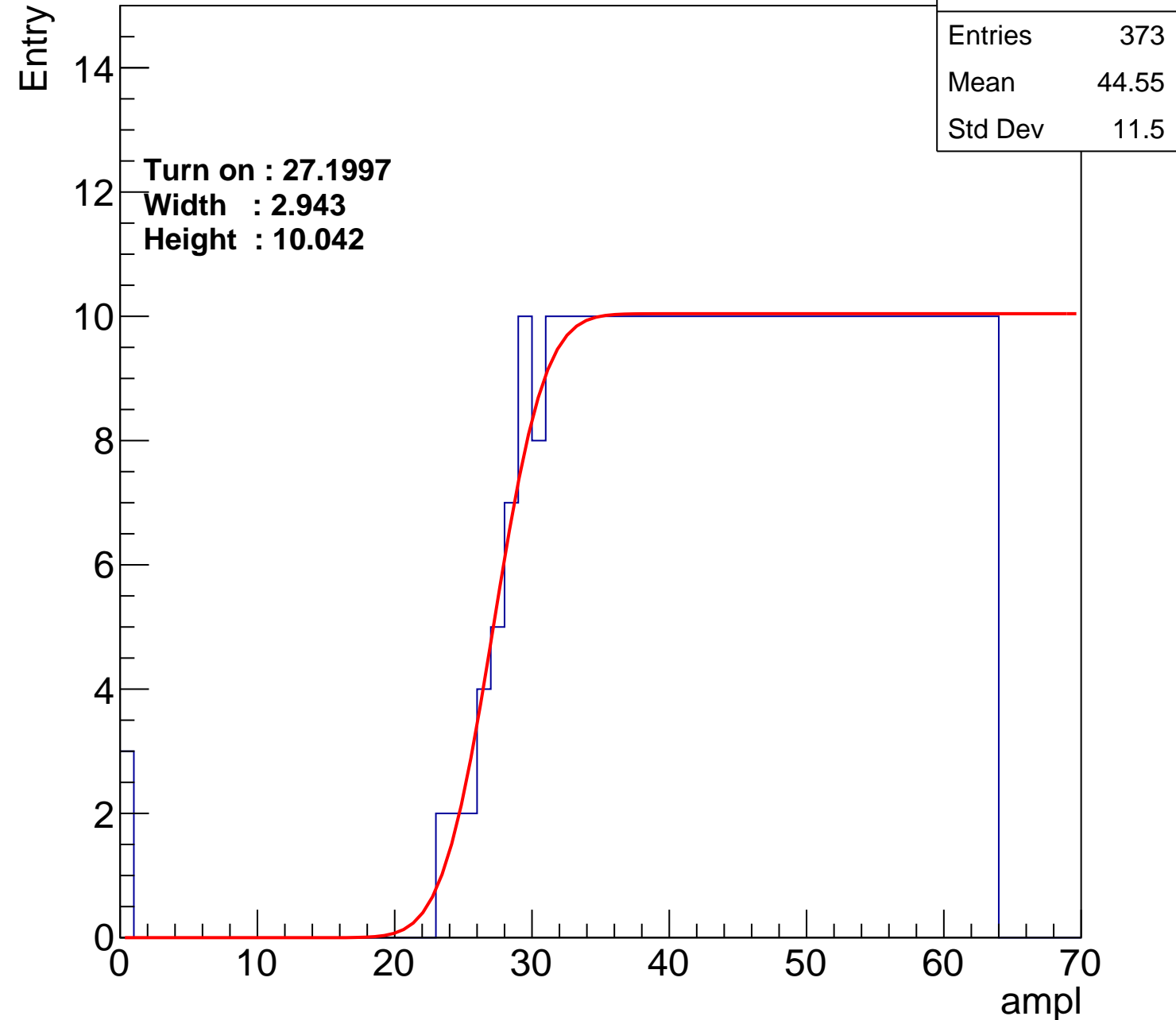
Width : 2.943

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch39

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 27.1879

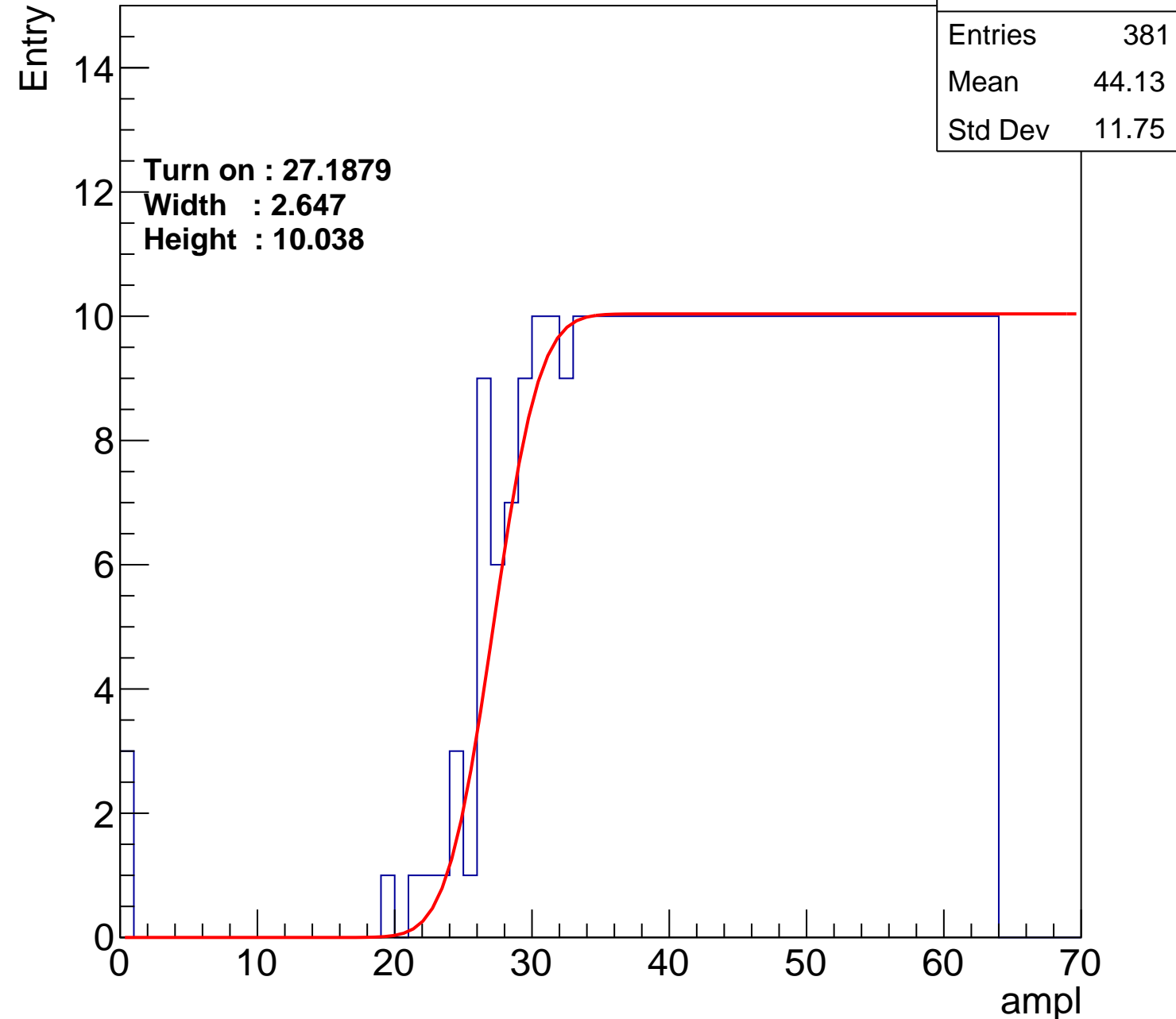
Width : 2.647

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch40

calib_packv5_042523_0143.root, FC#5, port B1

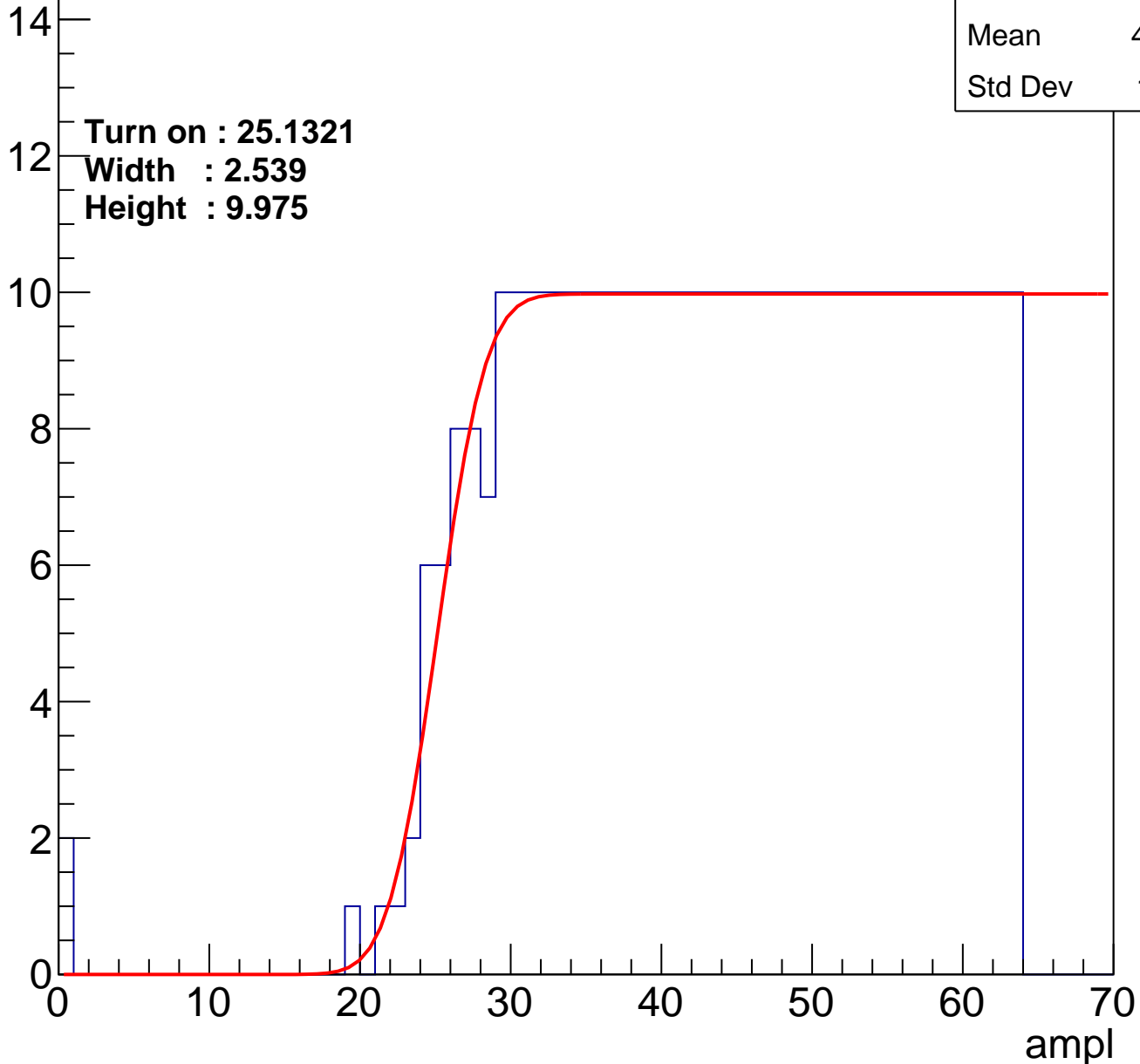
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.1321

Width : 2.539

Height : 9.975

Entry



B0L000S, U11-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.92
Std Dev	10.96

Turn on : 27.3847

Width : 2.373

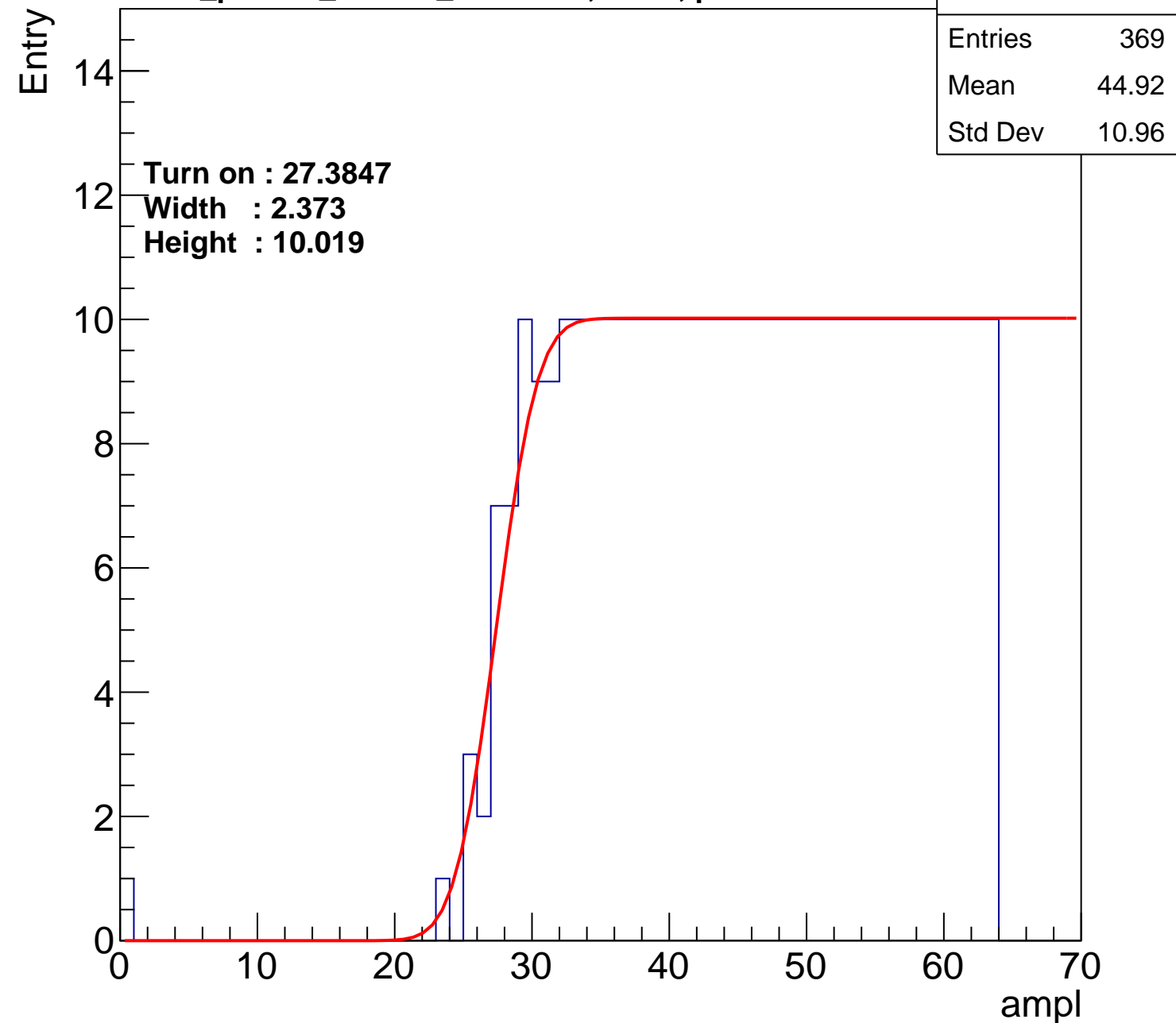
Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

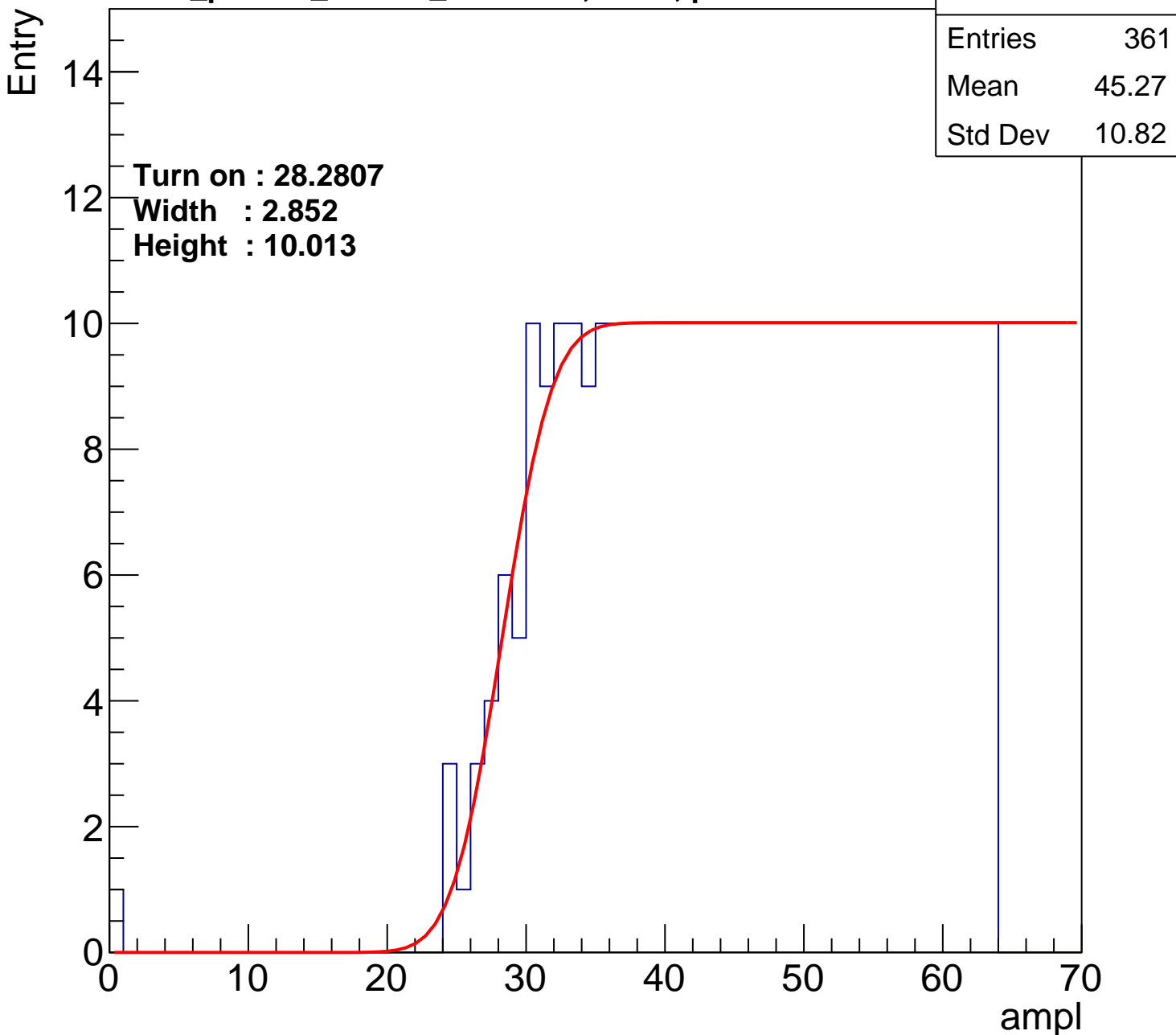


calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.27
Std Dev	10.82

Height : 10.013



B0L000S, U11-ch43

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.17
Std Dev	11.74

Turn on : 26.5026

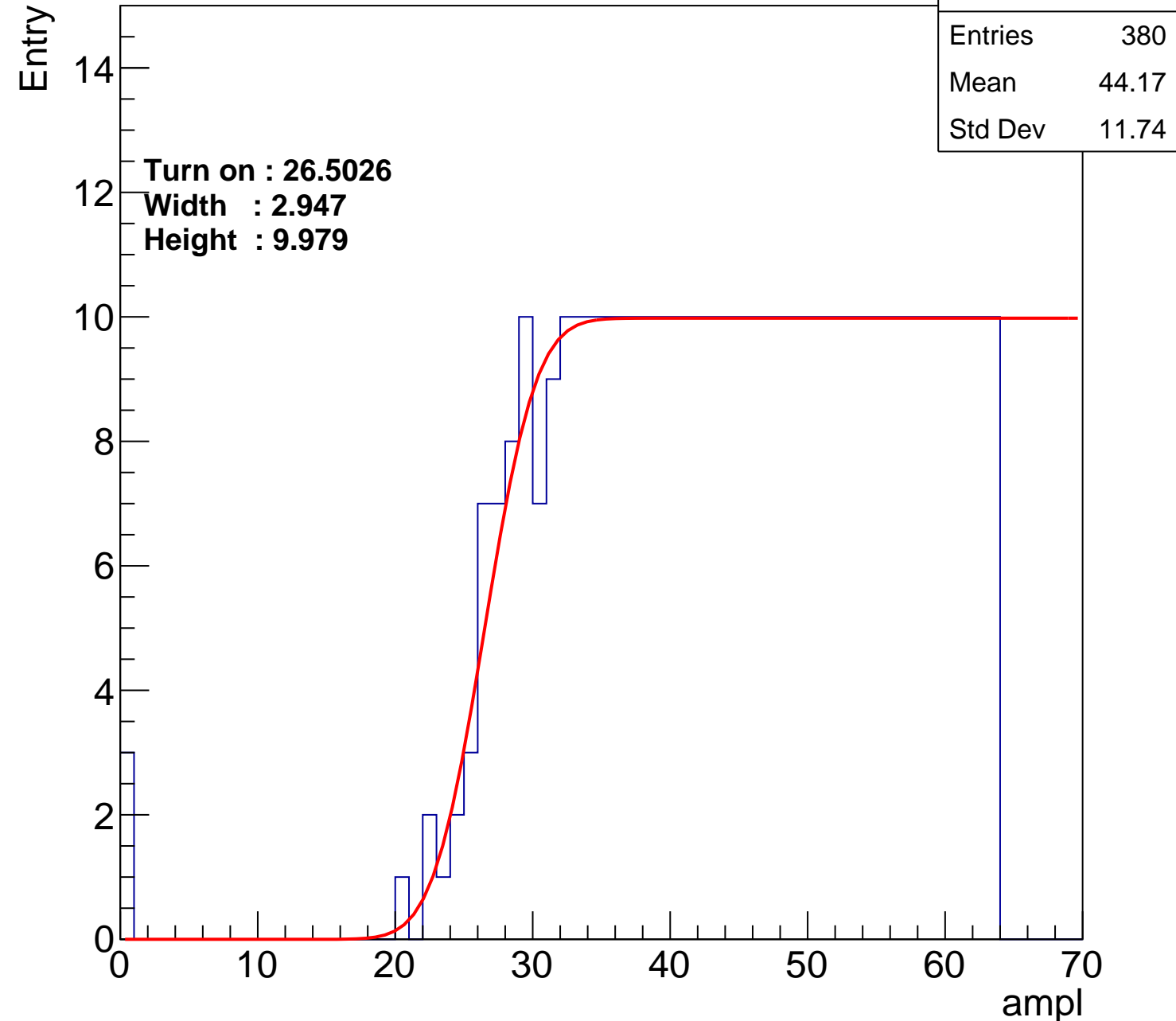
Width : 2.947

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch44

calib_packv5_042523_0143.root, FC#5, port B1

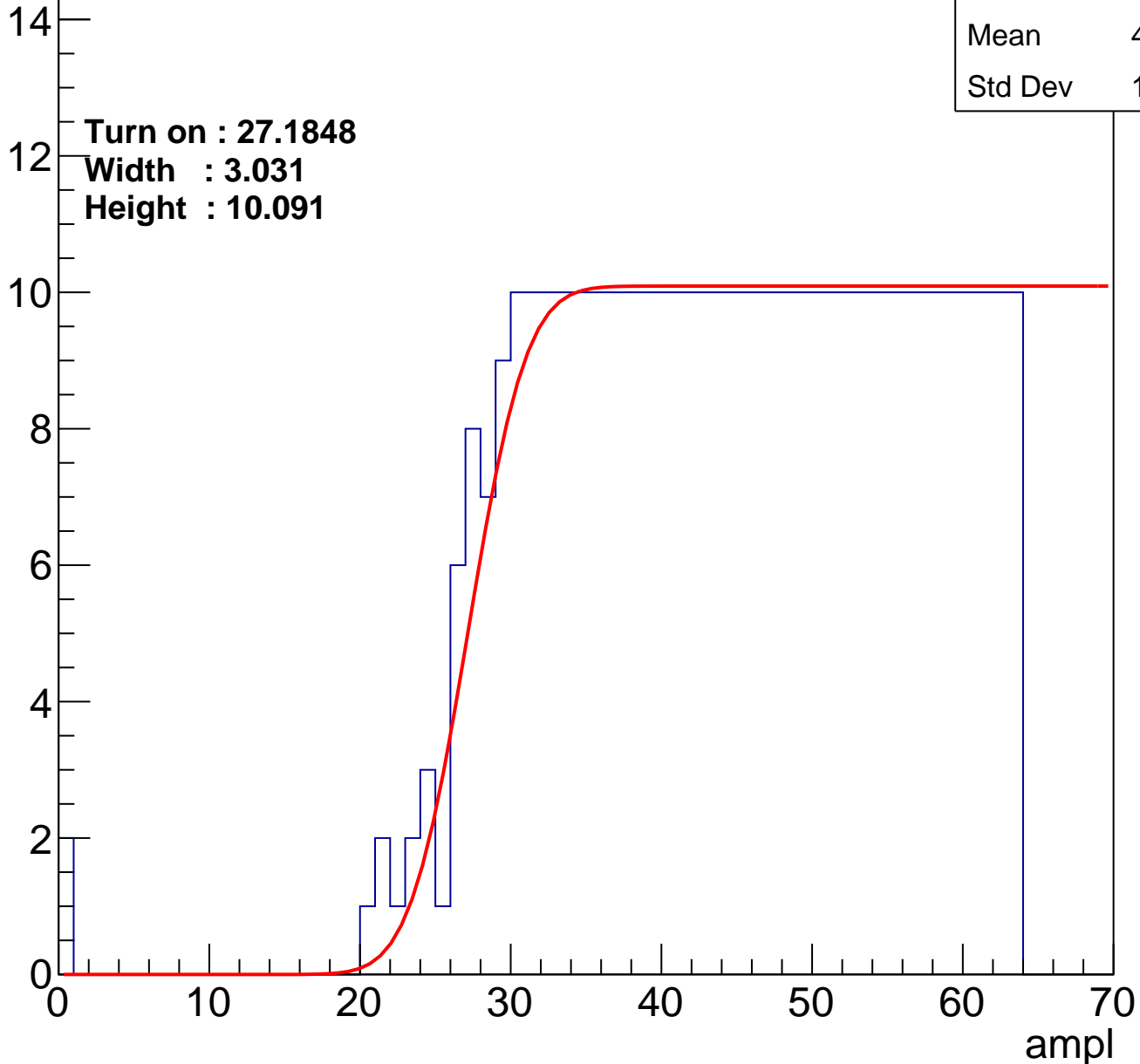
Entries	382
Mean	44.15
Std Dev	11.59

Turn on : 27.1848

Width : 3.031

Height : 10.091

Entry



B0L000S, U11-ch45

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.87
Std Dev	11.51

Turn on : 27.4498

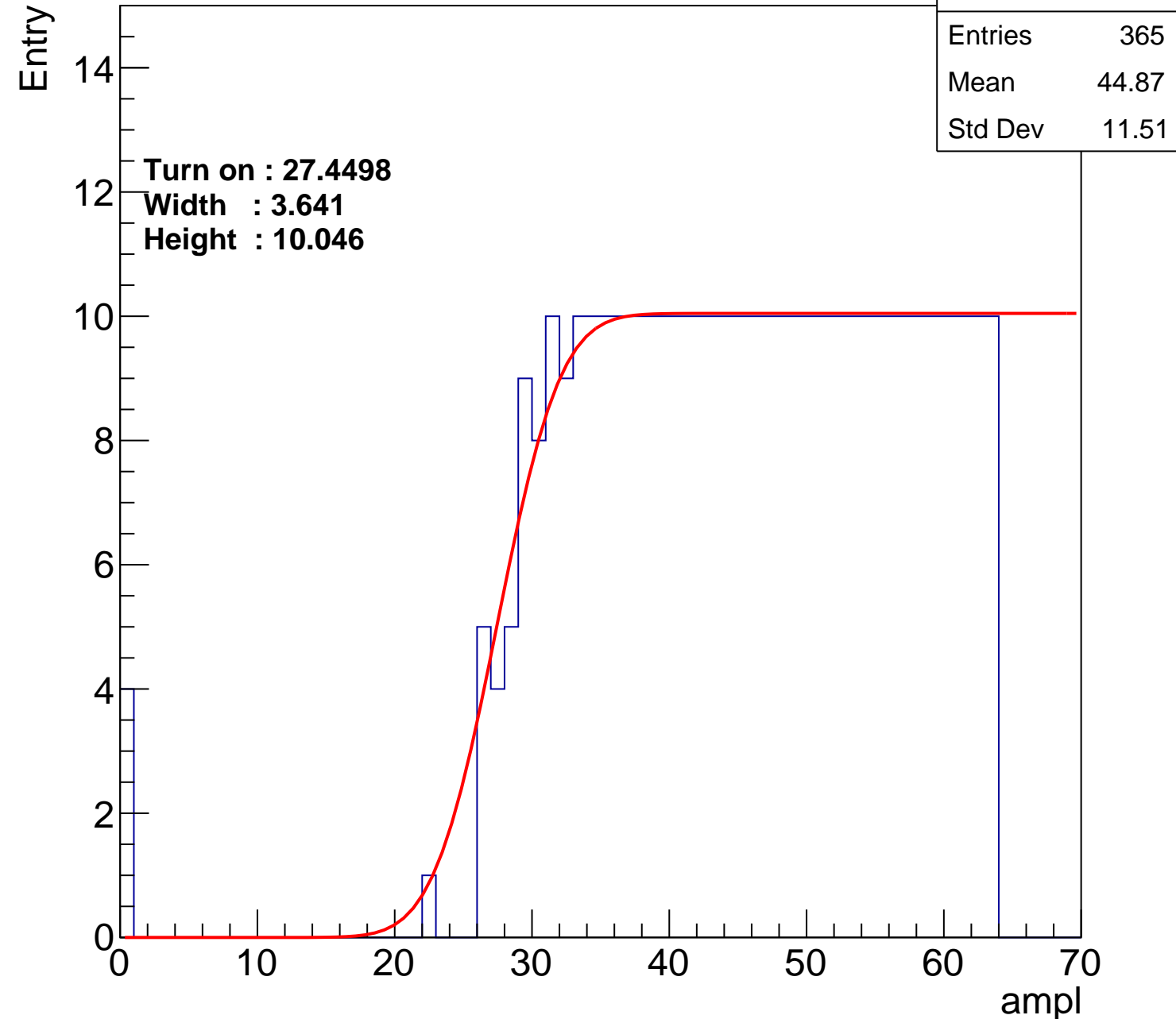
Width : 3.641

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch46

calib_packv5_042523_0143.root, FC#5, port B1

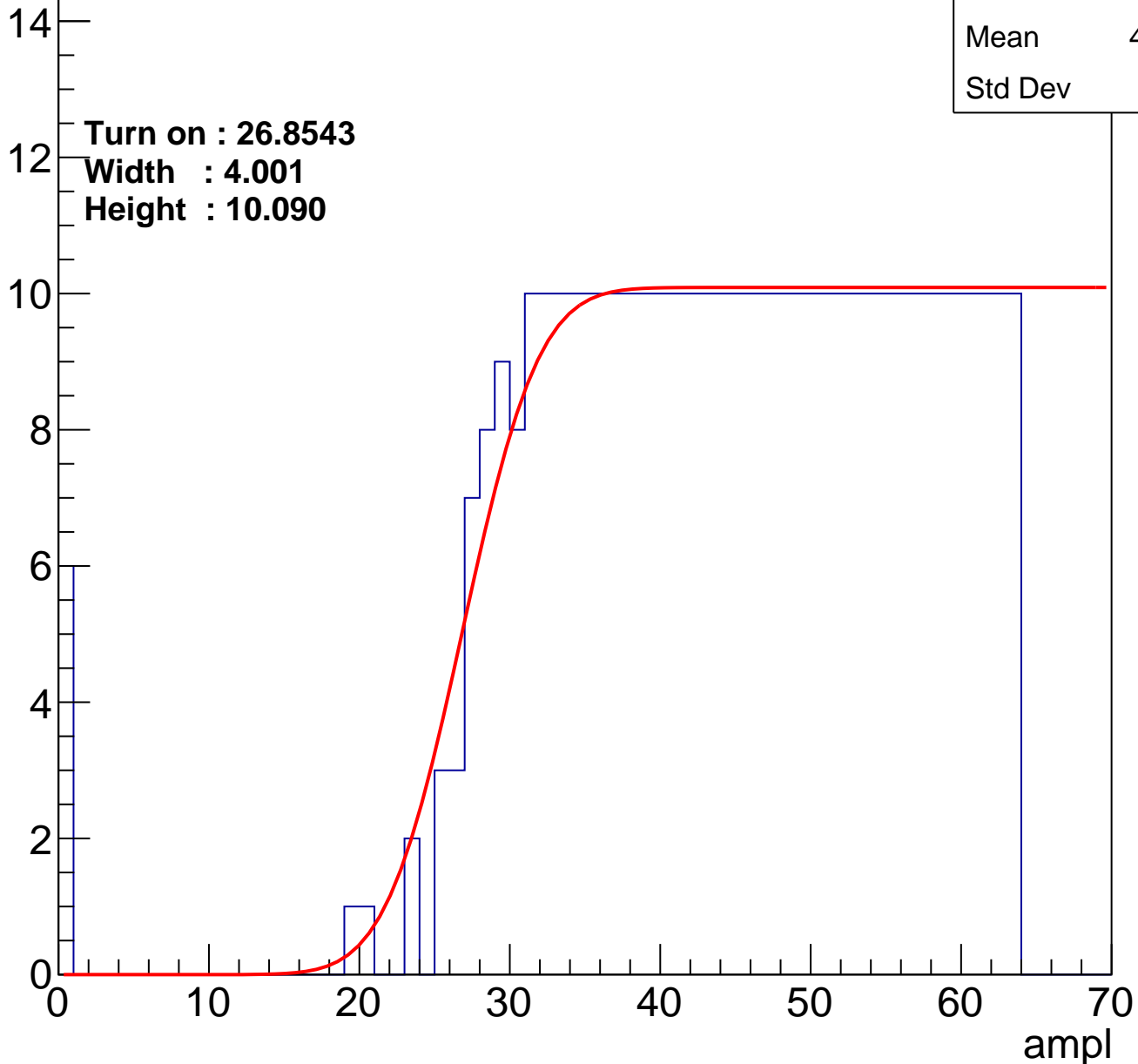
Entries	378
Mean	44.08
Std Dev	12.2

Turn on : 26.8543

Width : 4.001

Height : 10.090

Entry



B0L000S, U11-ch47

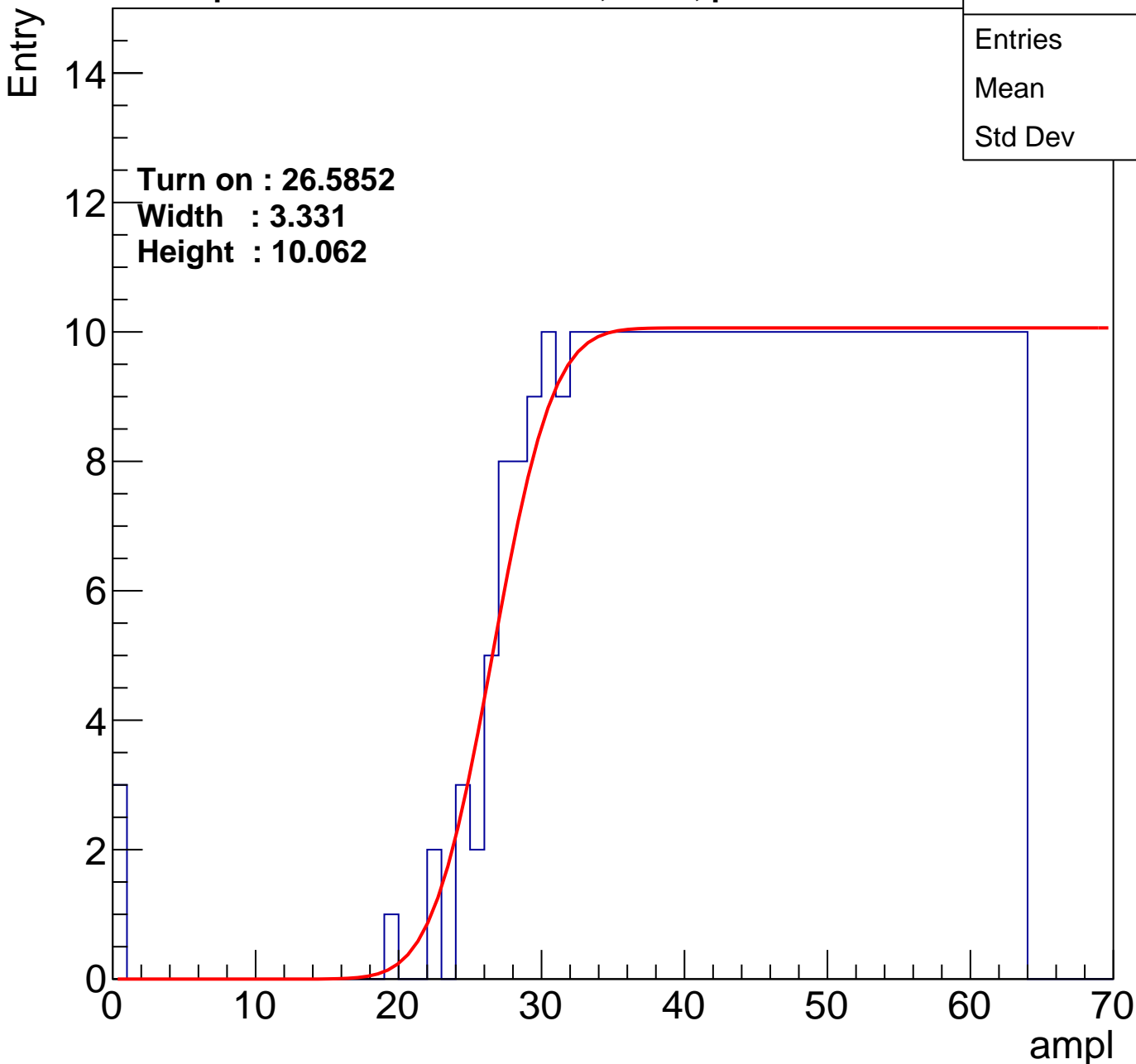
calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.2
Std Dev	11.7

Turn on : 26.5852

Width : 3.331

Height : 10.062



B0L000S, U11-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.15
Std Dev	11.4

Turn on : 29.1224

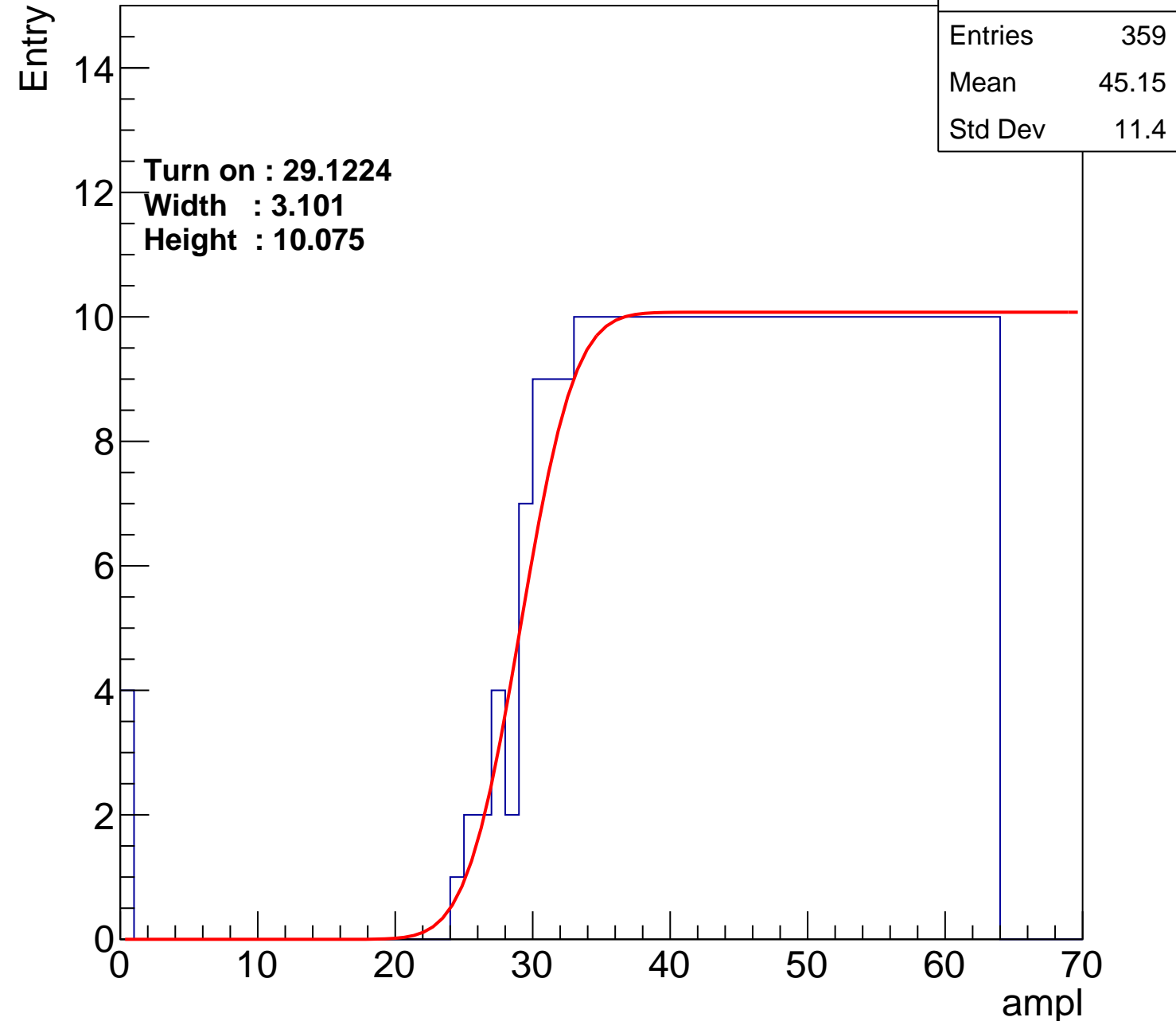
Width : 3.101

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.6
Std Dev	11.54

Turn on : 27.5308

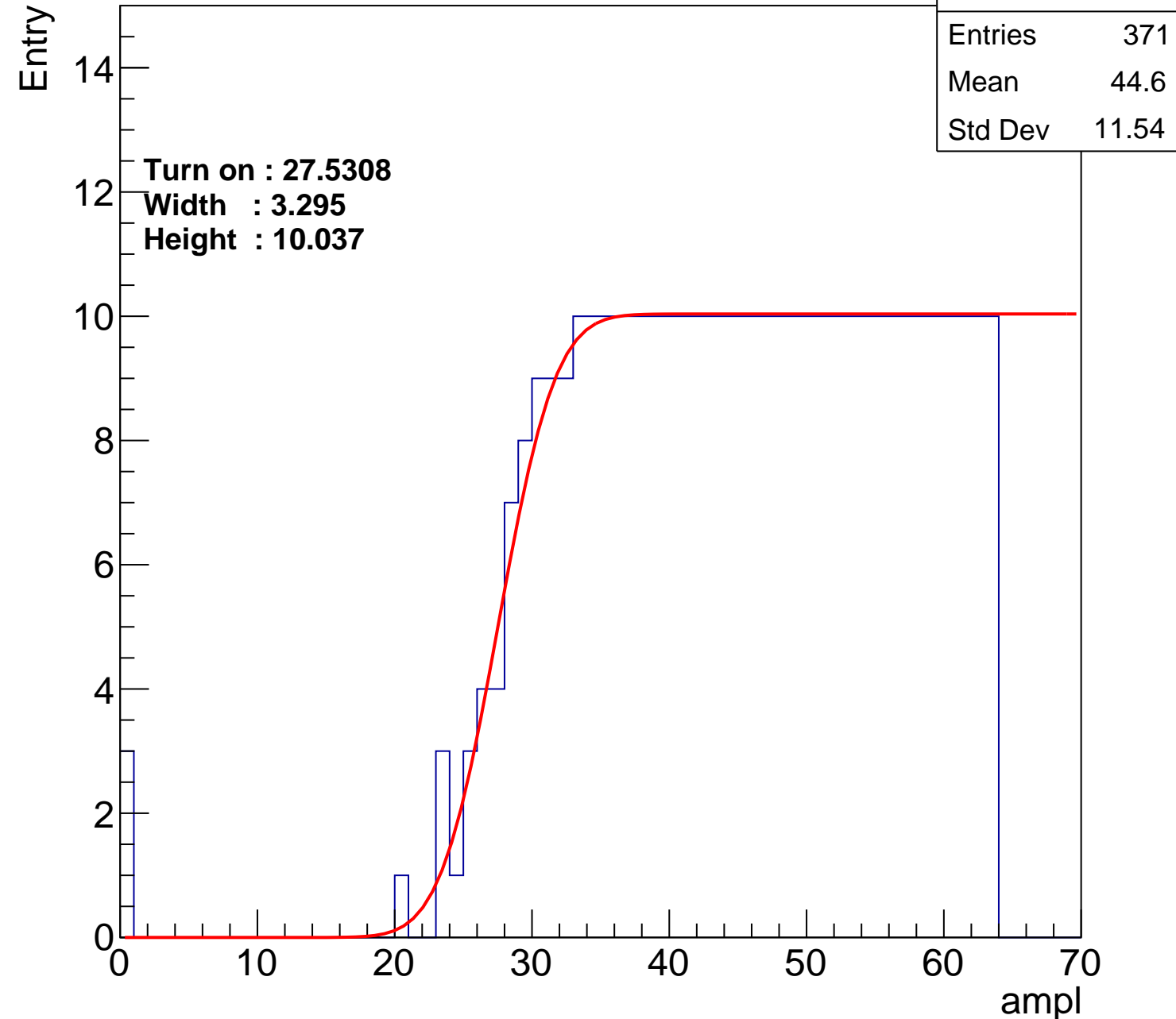
Width : 3.295

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.46
Std Dev	11.6

Turn on : 27.2860

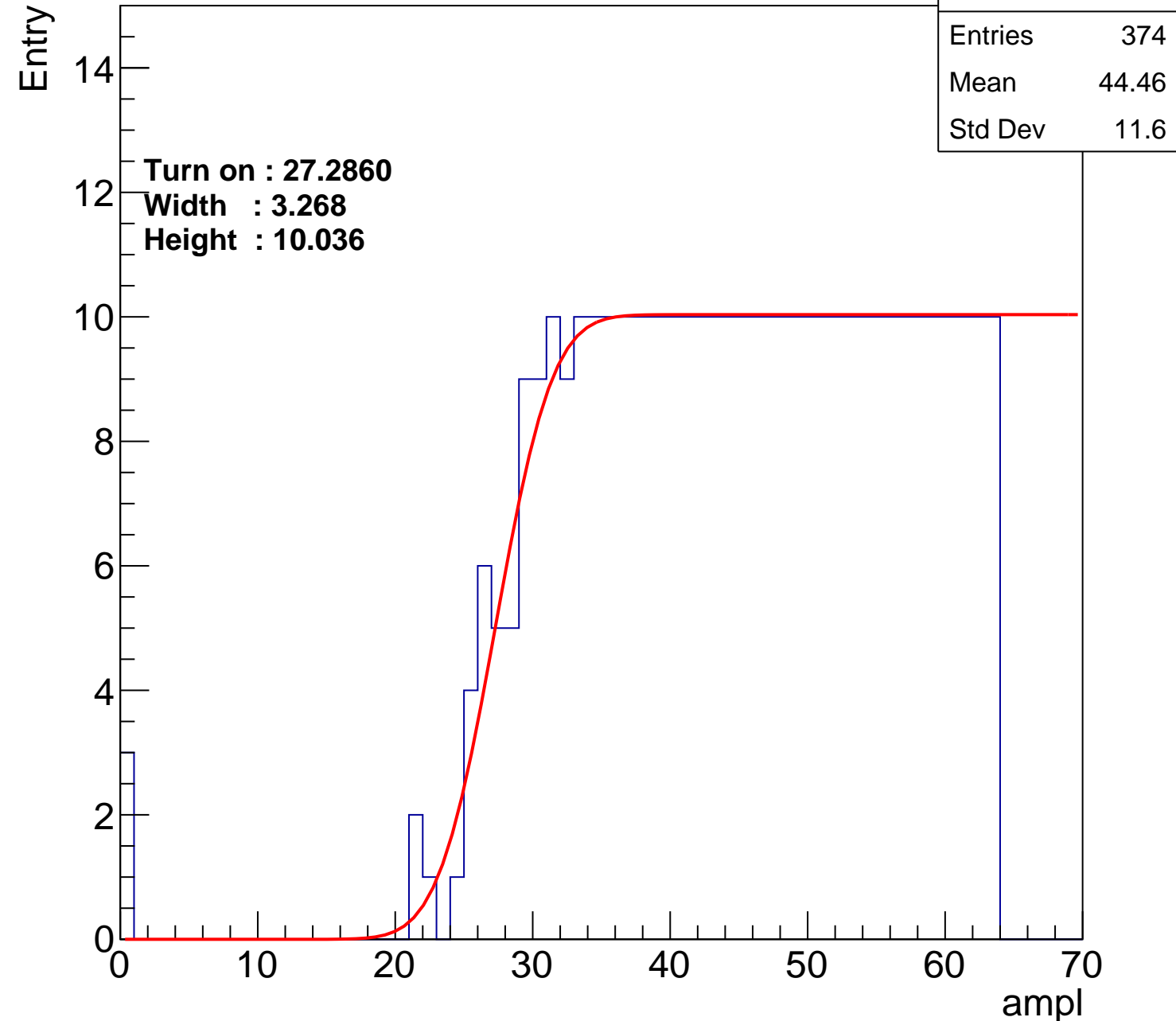
Width : 3.268

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.25
Std Dev	10.86

Turn on : 28.1695

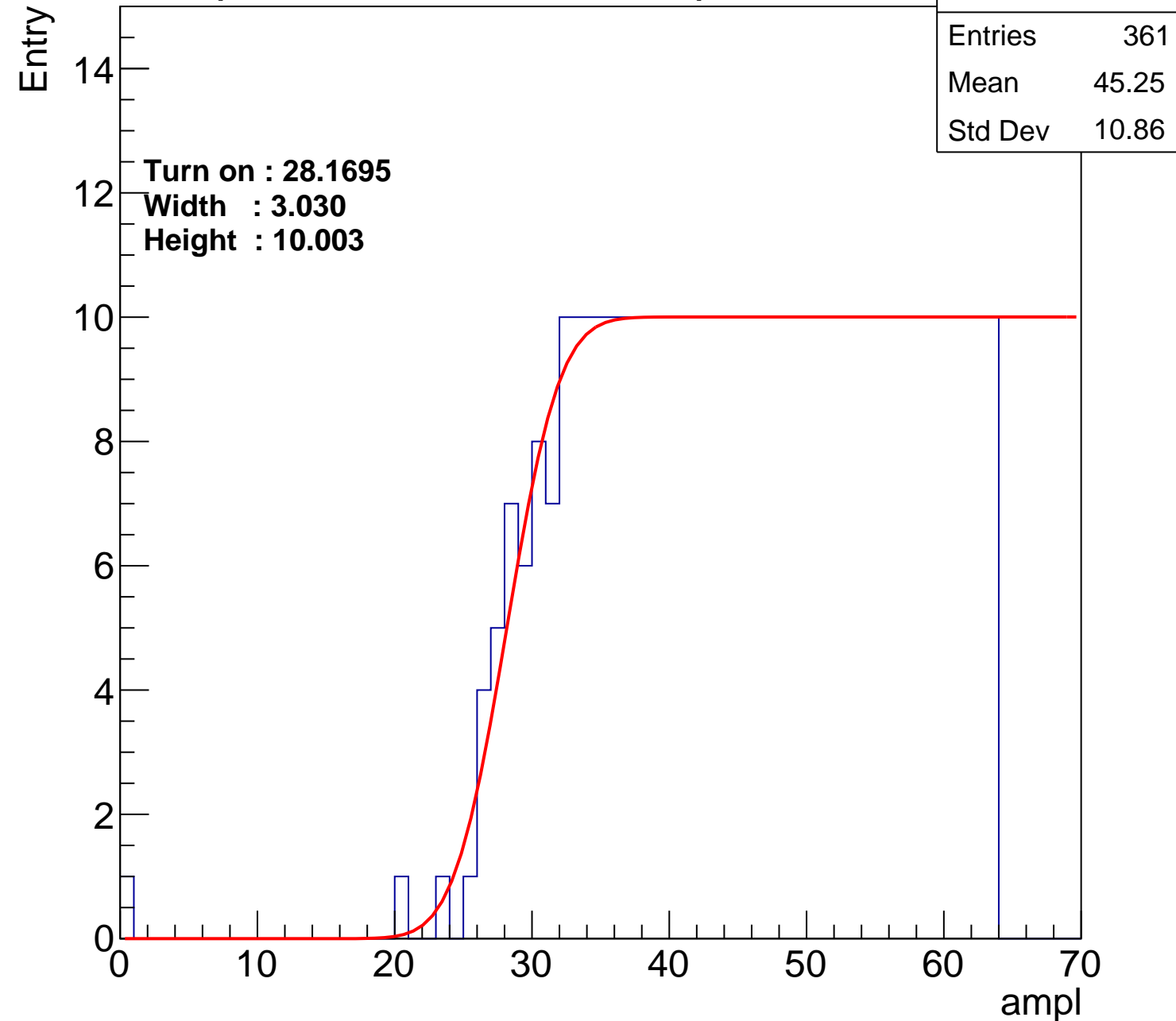
Width : 3.030

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.8146

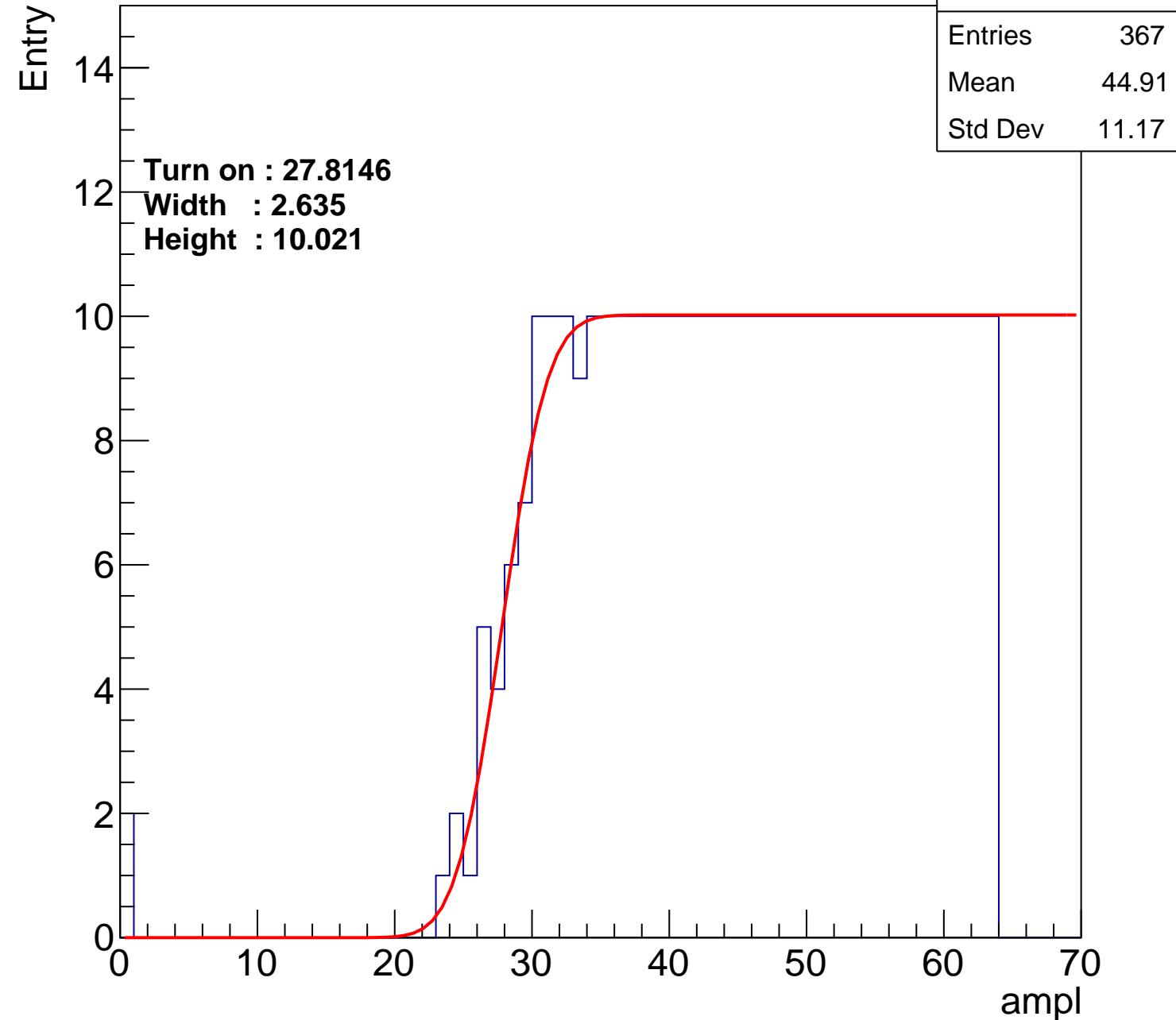
Width : 2.635

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.06
Std Dev	11.11

Turn on : 28.1818

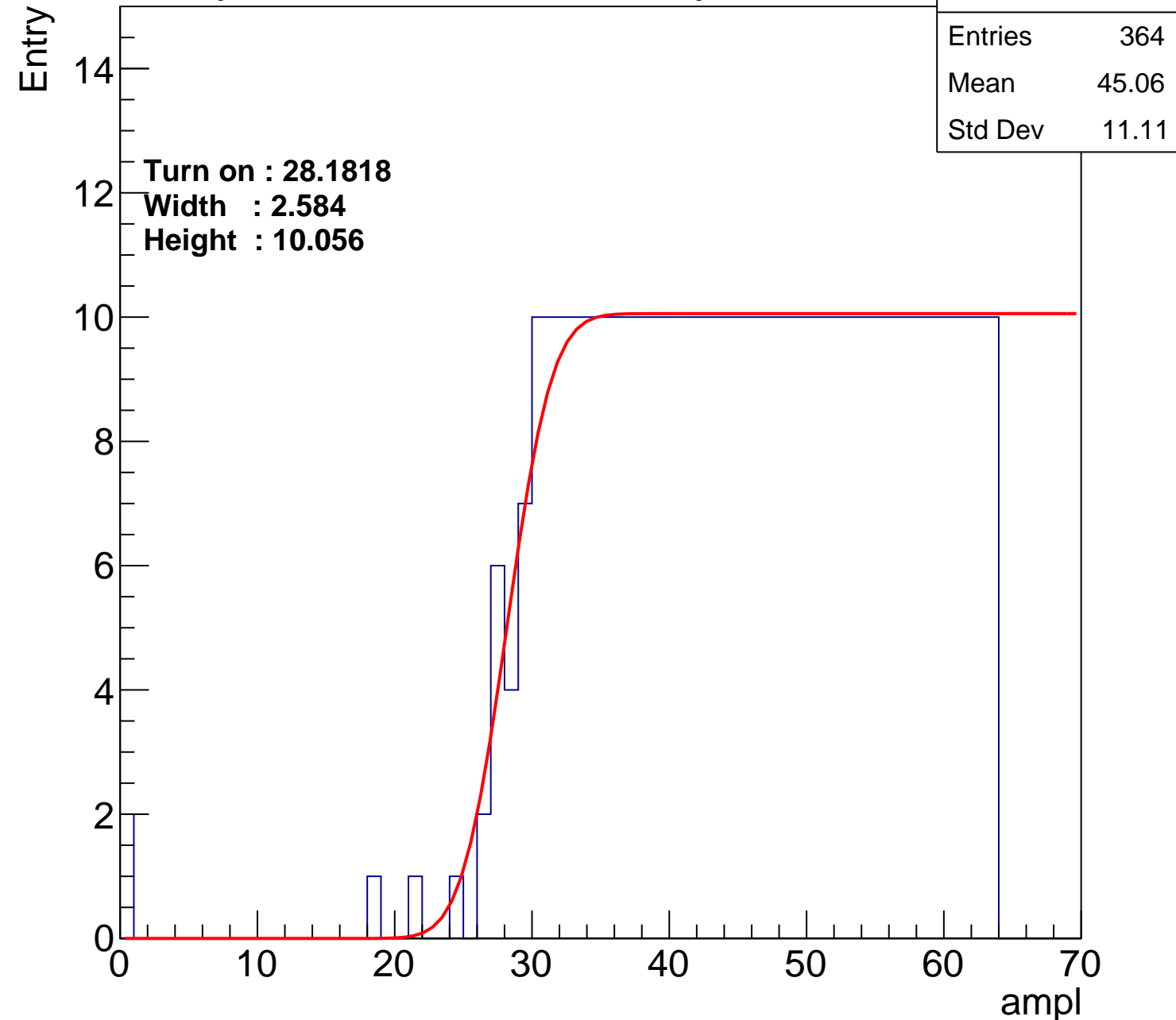
Width : 2.584

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch54

calib_packv5_042523_0143.root, FC#5, port B1

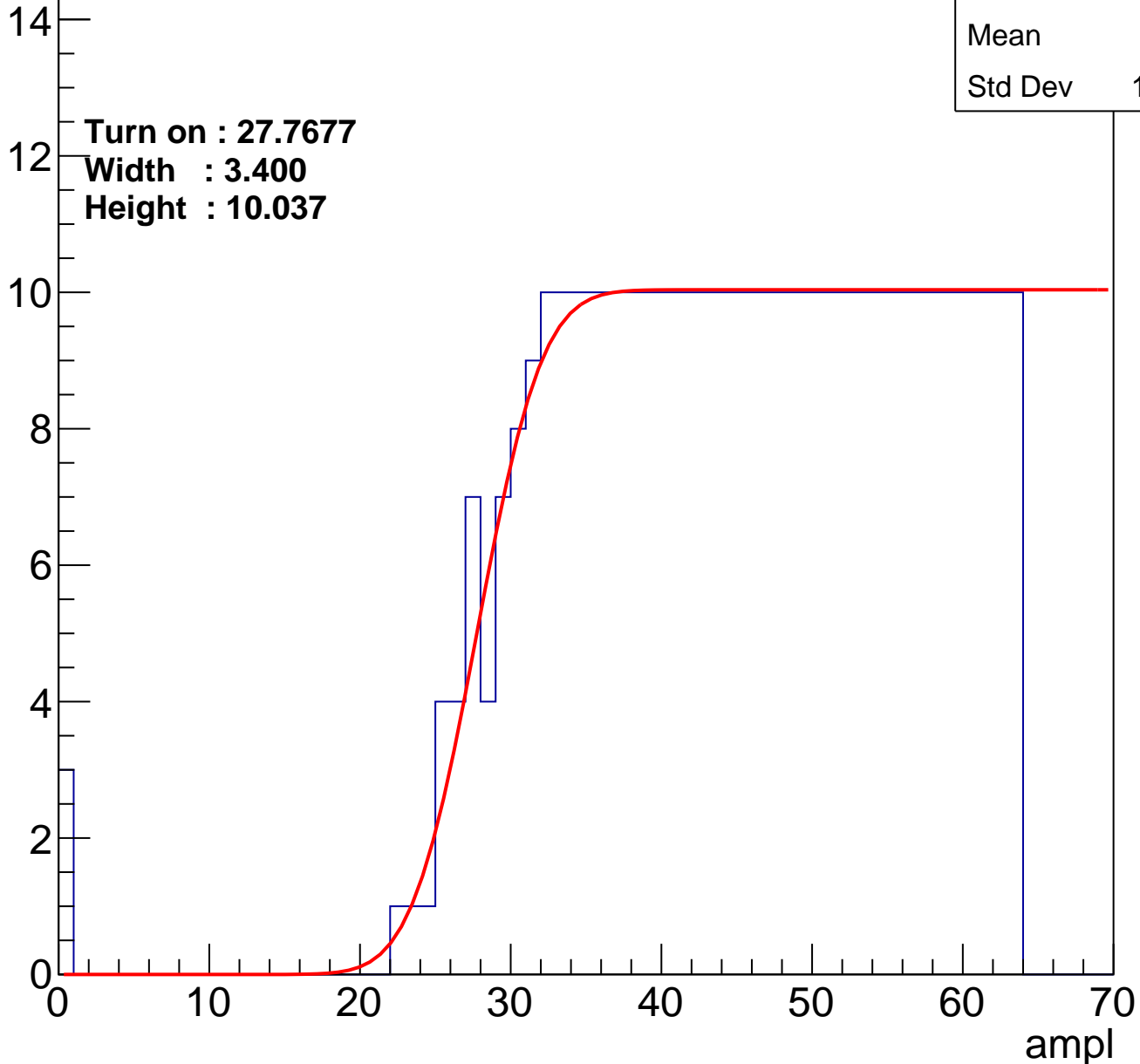
Entries	369
Mean	44.7
Std Dev	11.48

Turn on : 27.7677

Width : 3.400

Height : 10.037

Entry



B0L000S, U11-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.5
Std Dev	11.35

Turn on : 27.2879

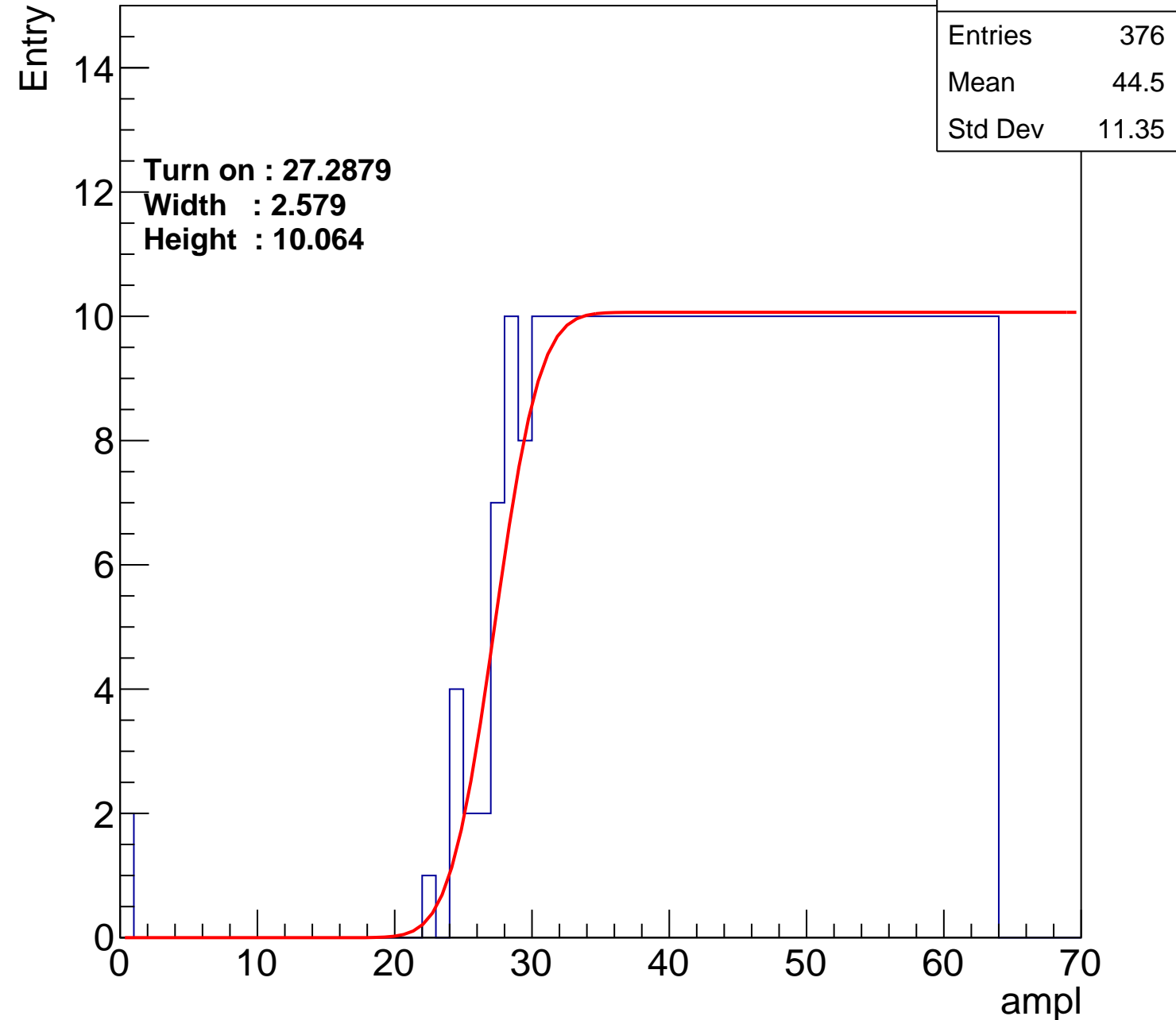
Width : 2.579

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch56

calib_packv5_042523_0143.root, FC#5, port B1

Entries	347
Mean	45.85
Std Dev	10.75

Turn on : 29.7917

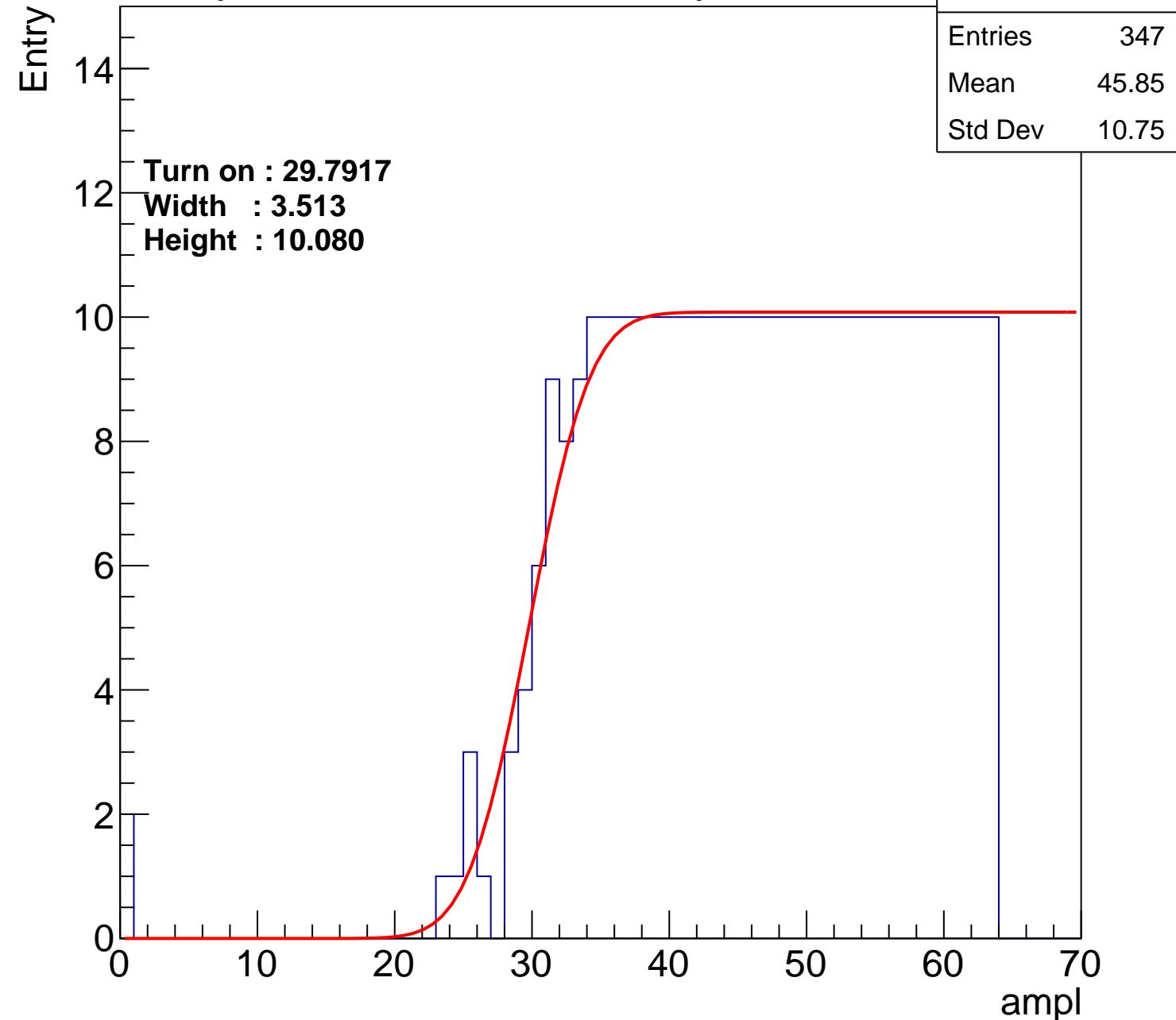
Width : 3.513

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



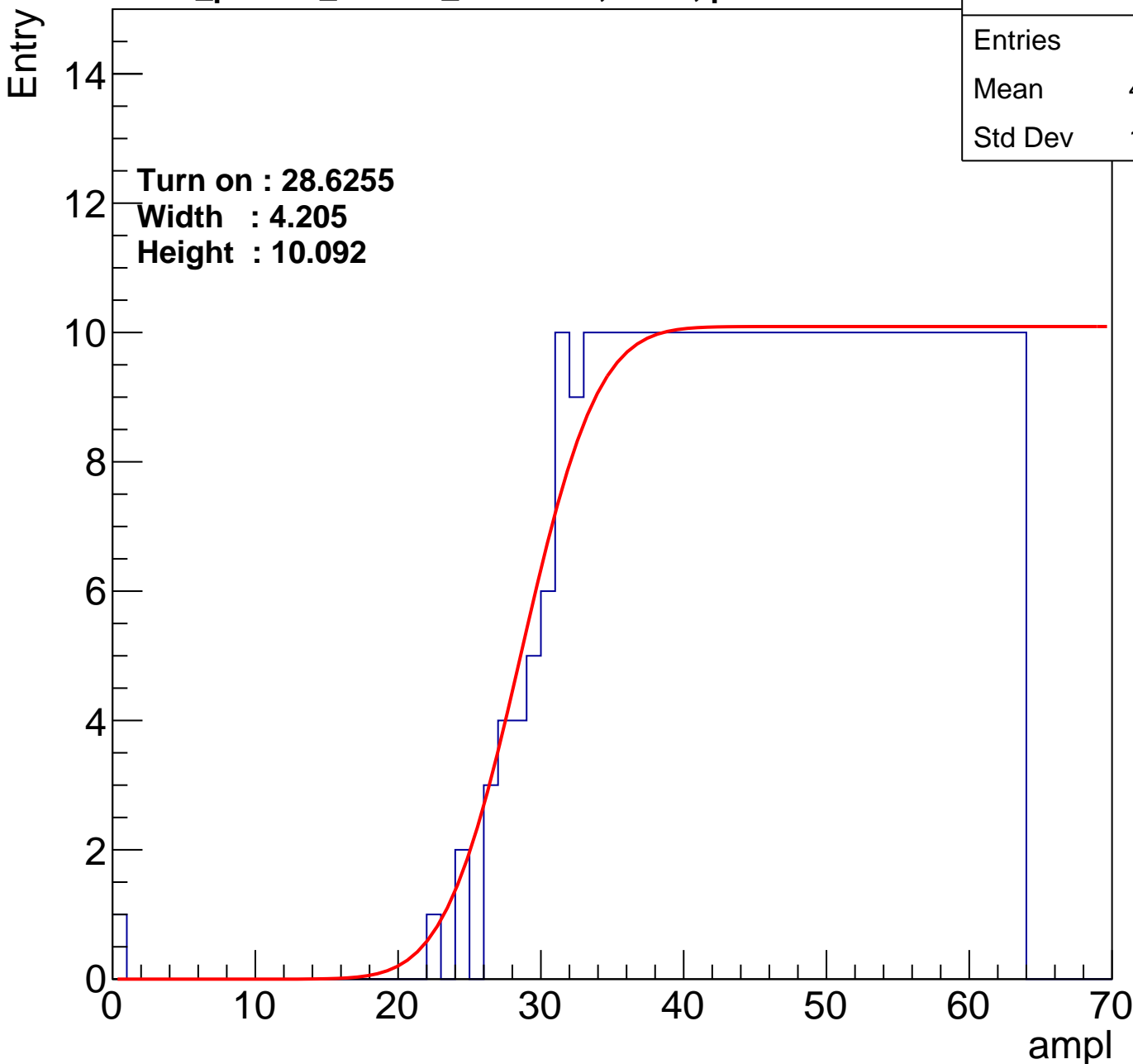
calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.55
Std Dev	10.69

Width : 4.205

Height : 10.092



B0L000S, U11-ch58

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 27.2827

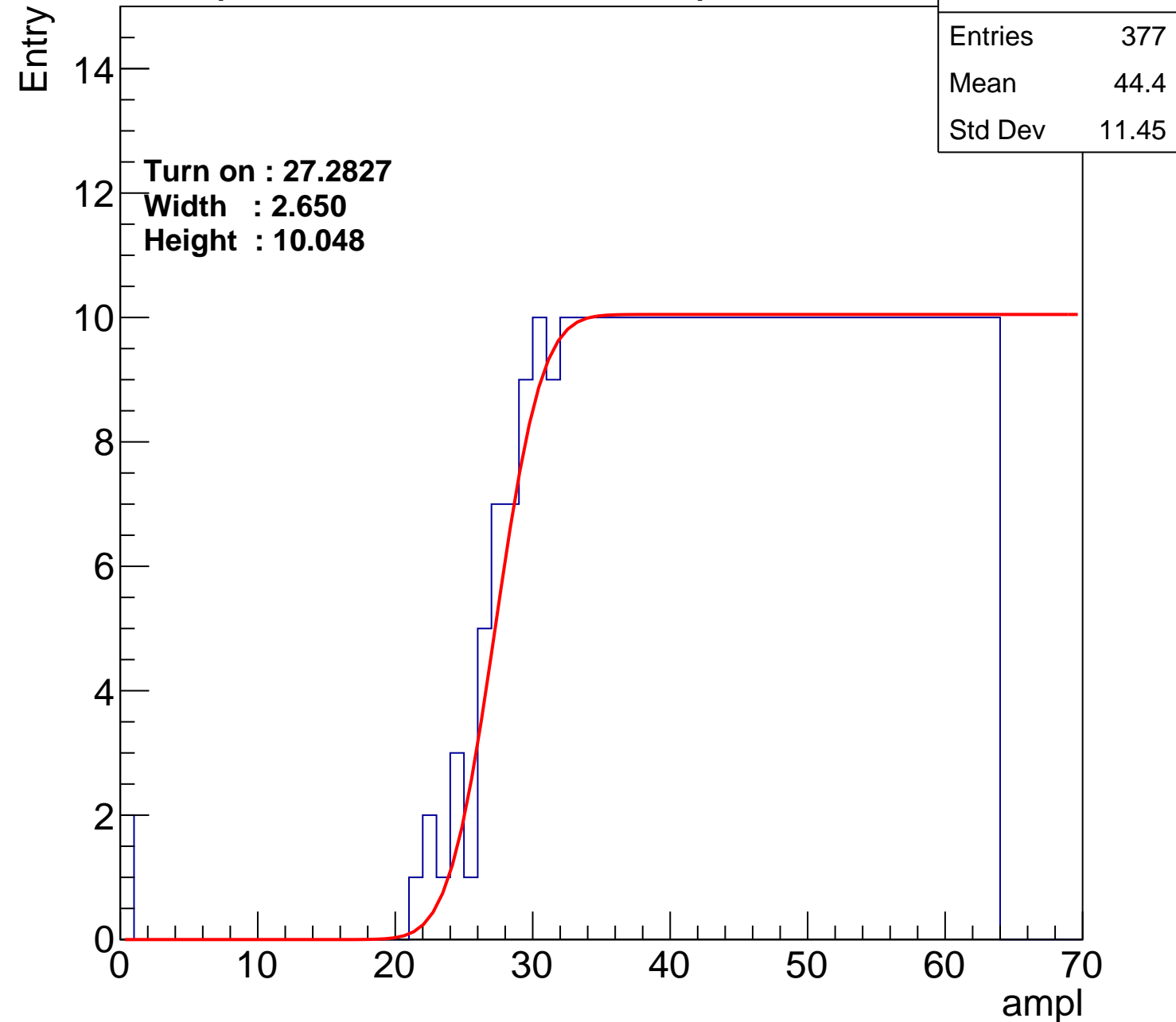
Width : 2.650

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch59

calib_packv5_042523_0143.root, FC#5, port B1

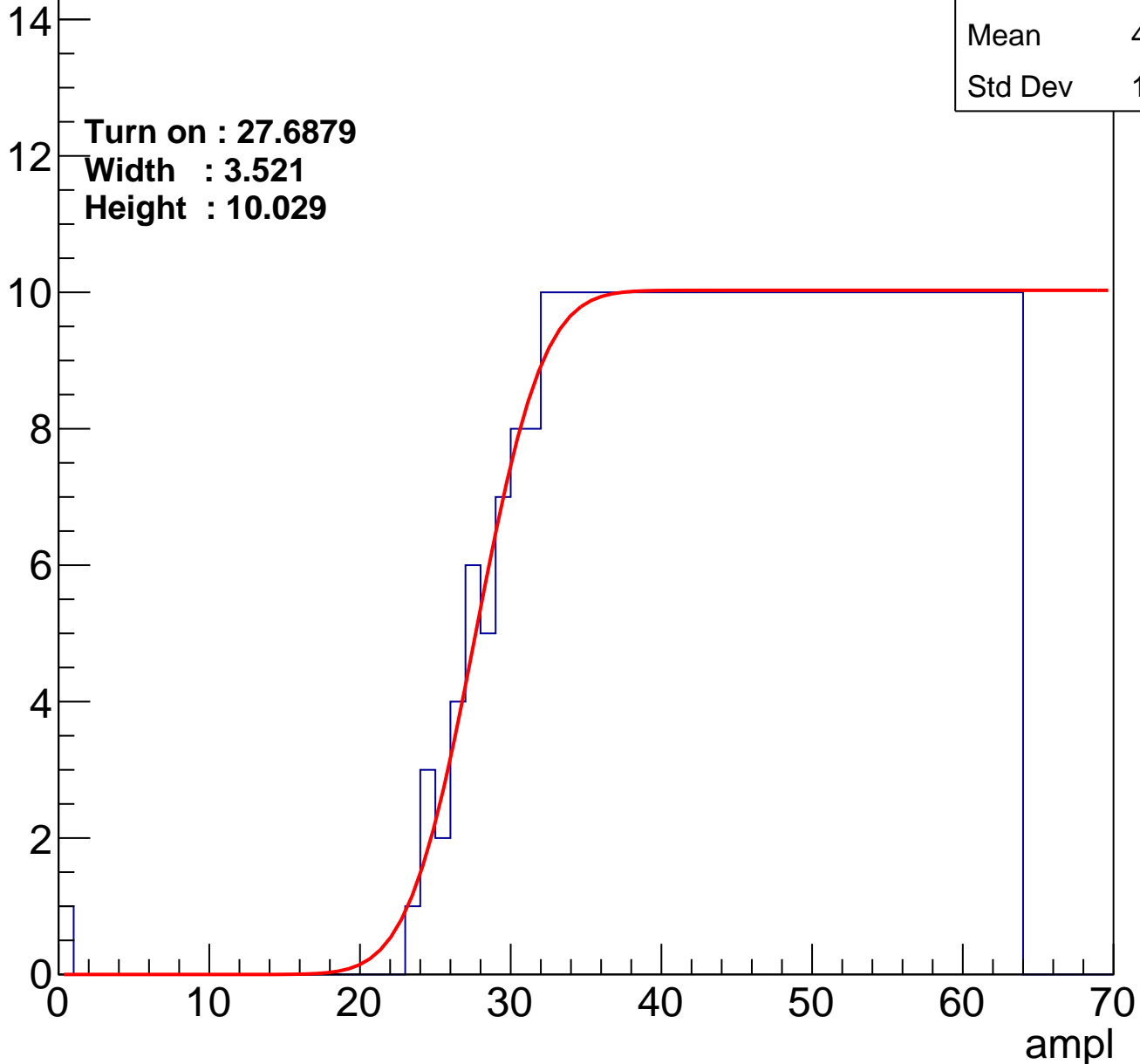
Entries	365
Mean	45.05
Std Dev	10.97

Turn on : 27.6879

Width : 3.521

Height : 10.029

Entry



B0L000S, U11-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.15
Std Dev	10.91

Turn on : 27.9884

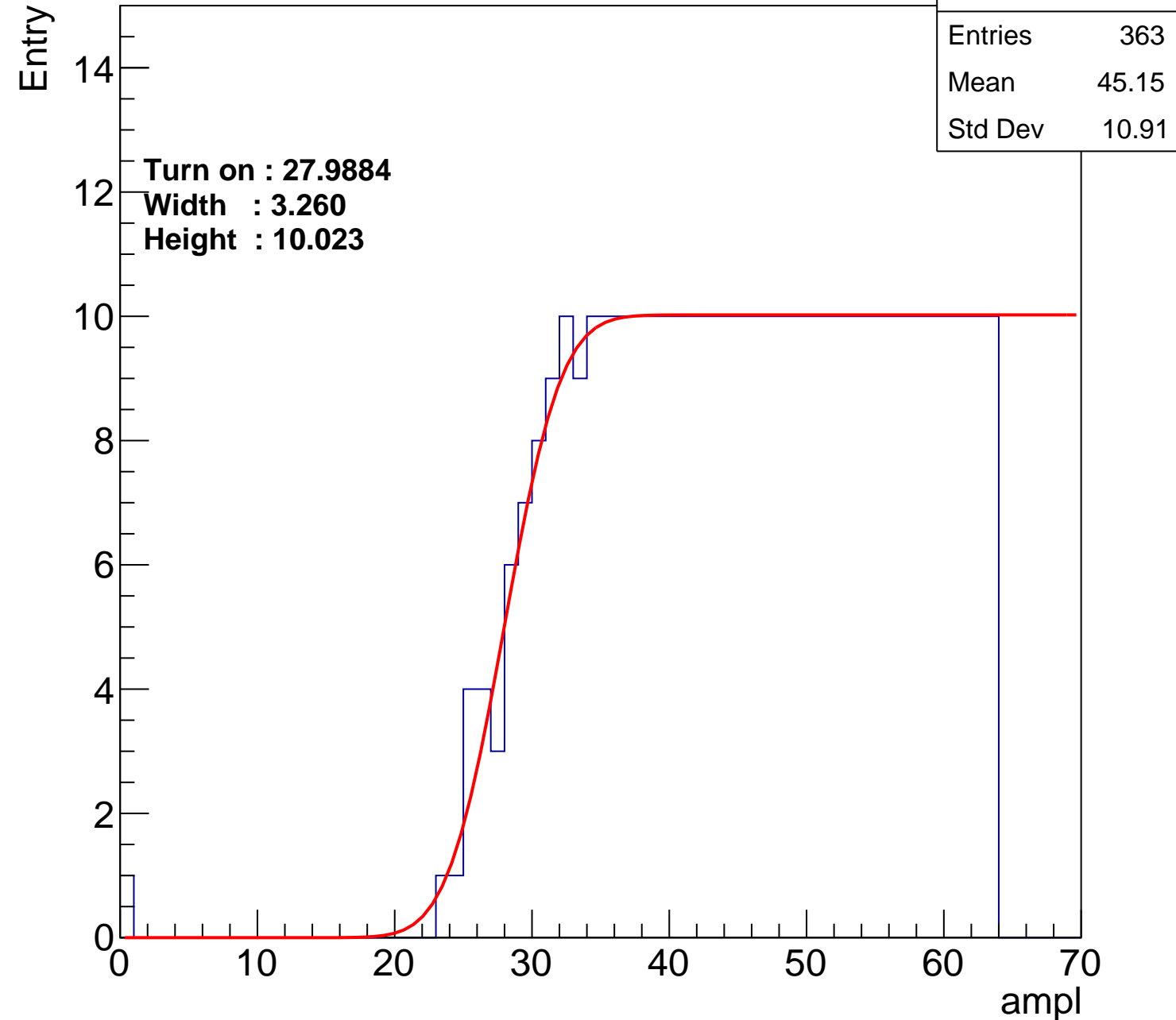
Width : 3.260

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.72
Std Dev	11.14

Turn on : 27.7652

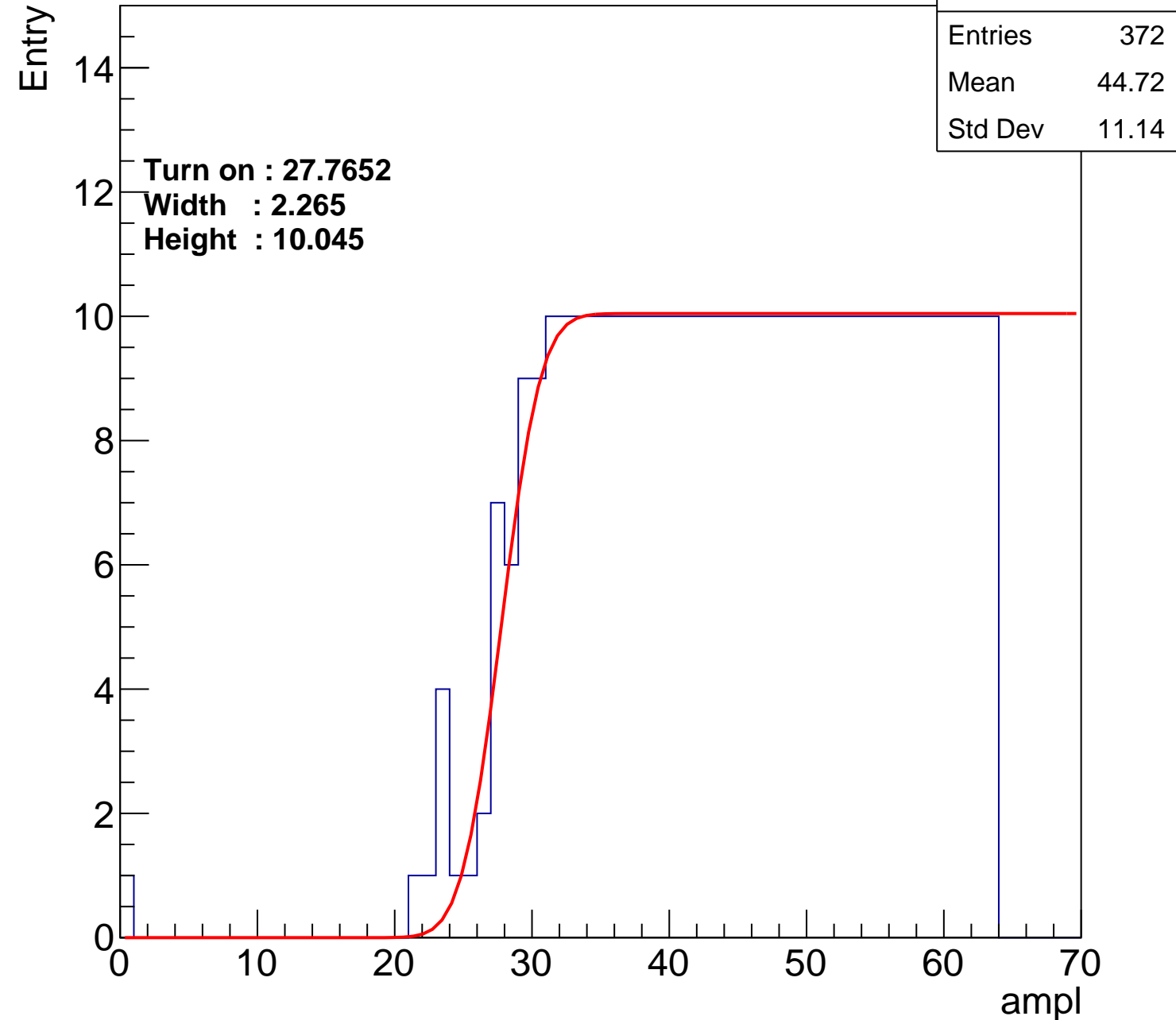
Width : 2.265

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch62

calib_packv5_042523_0143.root, FC#5, port B1

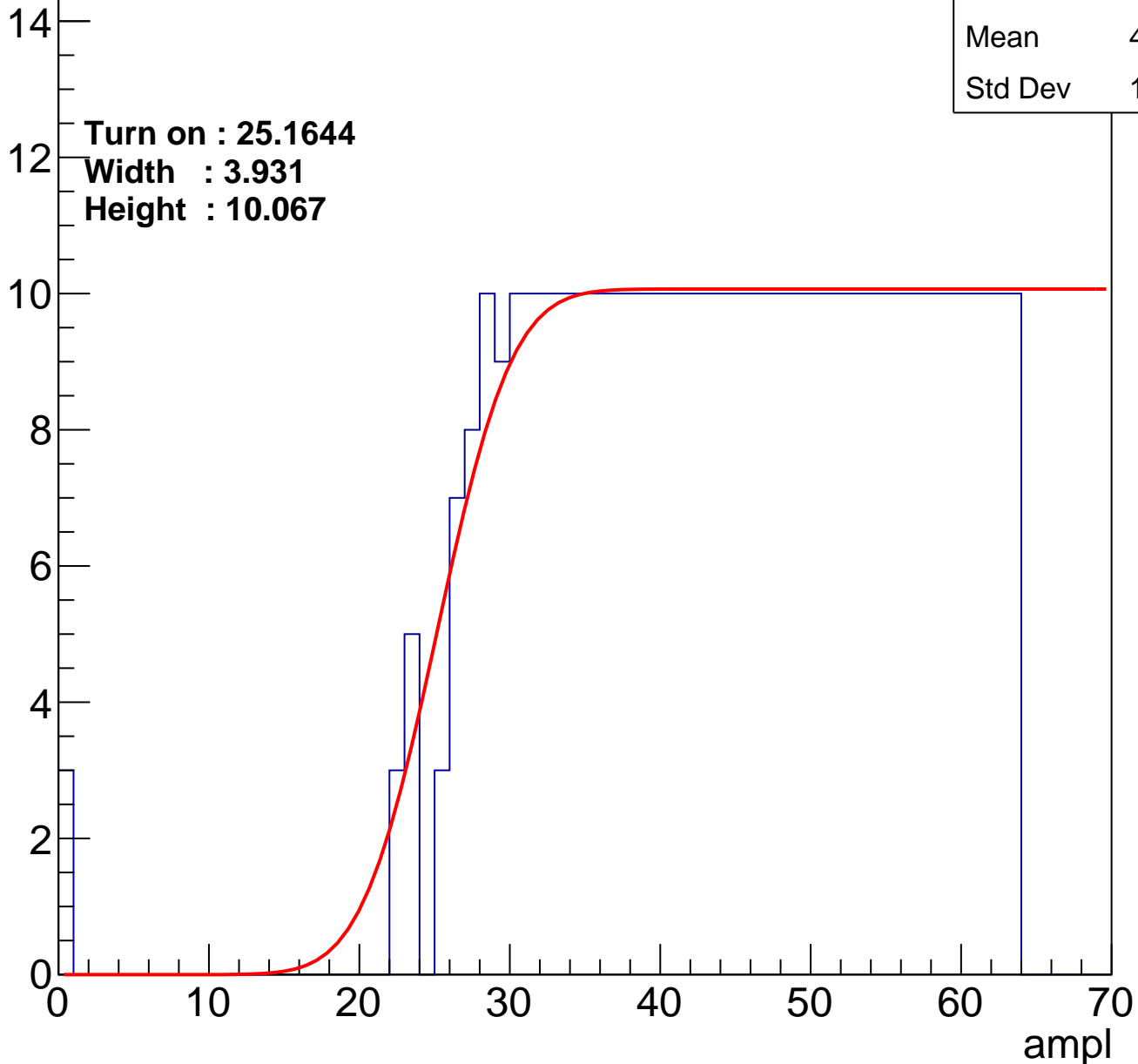
Entries	388
Mean	43.83
Std Dev	11.86

Turn on : 25.1644

Width : 3.931

Height : 10.067

Entry



B0L000S, U11-ch63

calib_packv5_042523_0143.root, FC#5, port B1

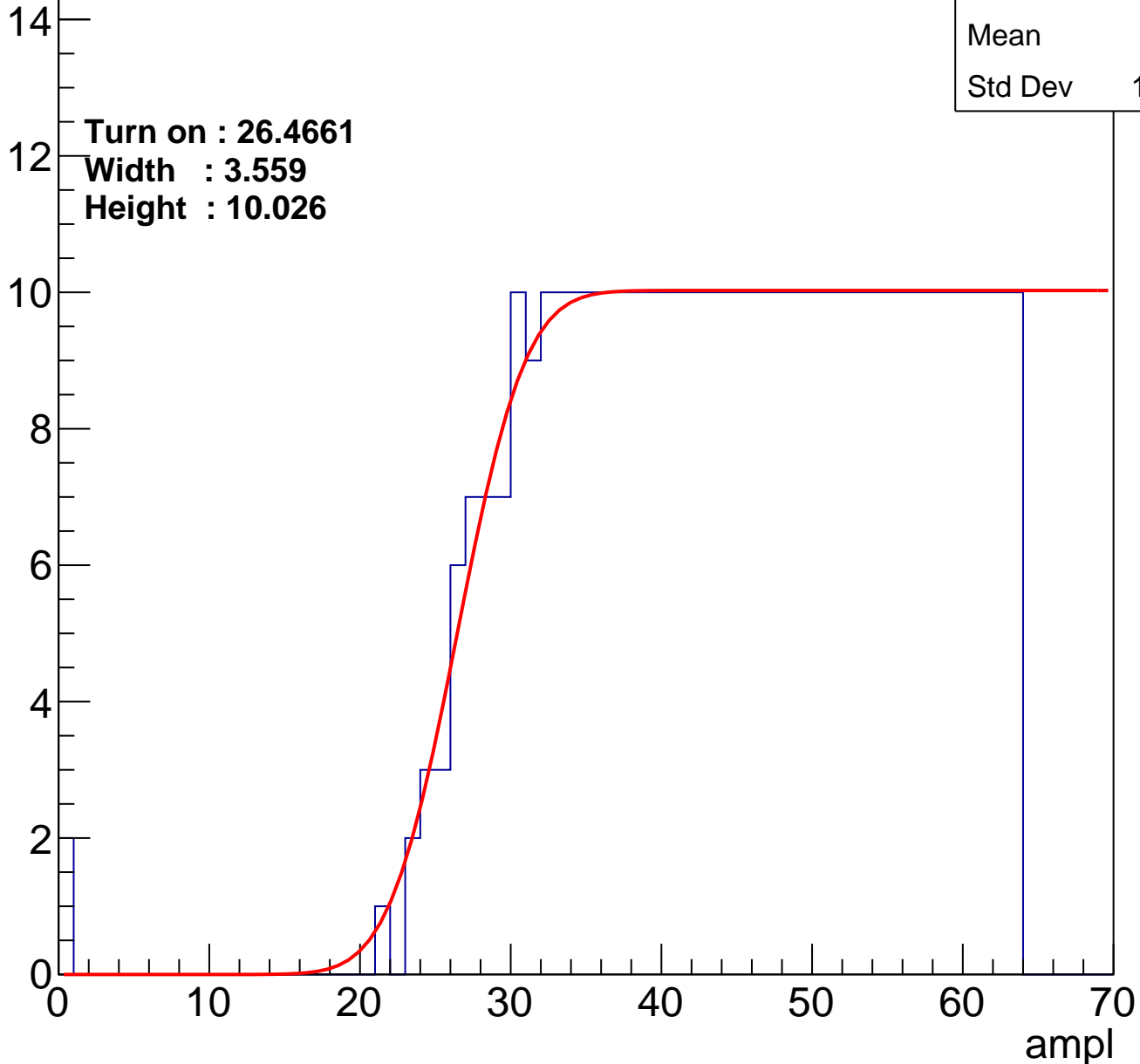
Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 26.4661

Width : 3.559

Height : 10.026

Entry



B0L000S, U11-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.1997

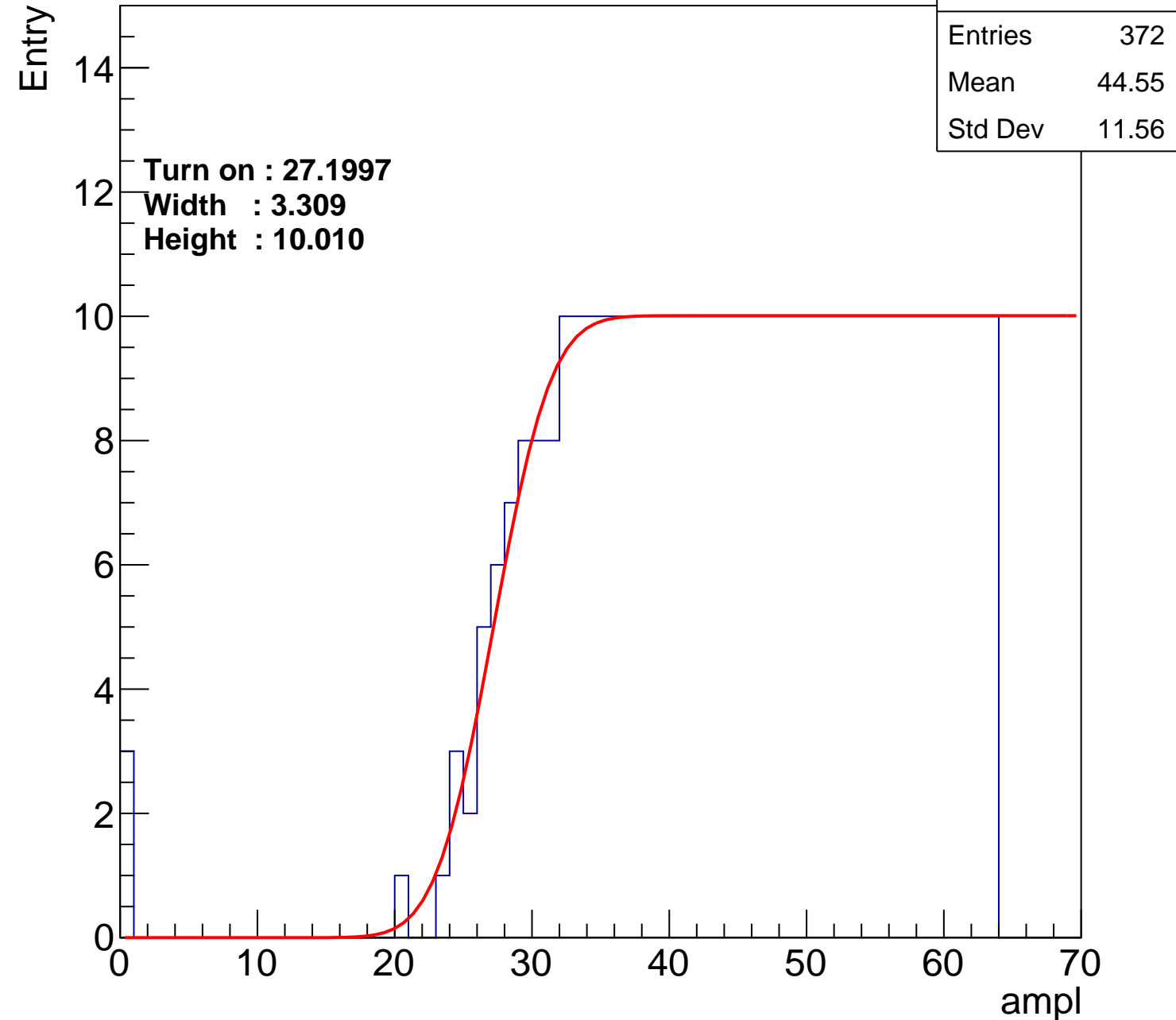
Width : 3.309

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch65

calib_packv5_042523_0143.root, FC#5, port B1

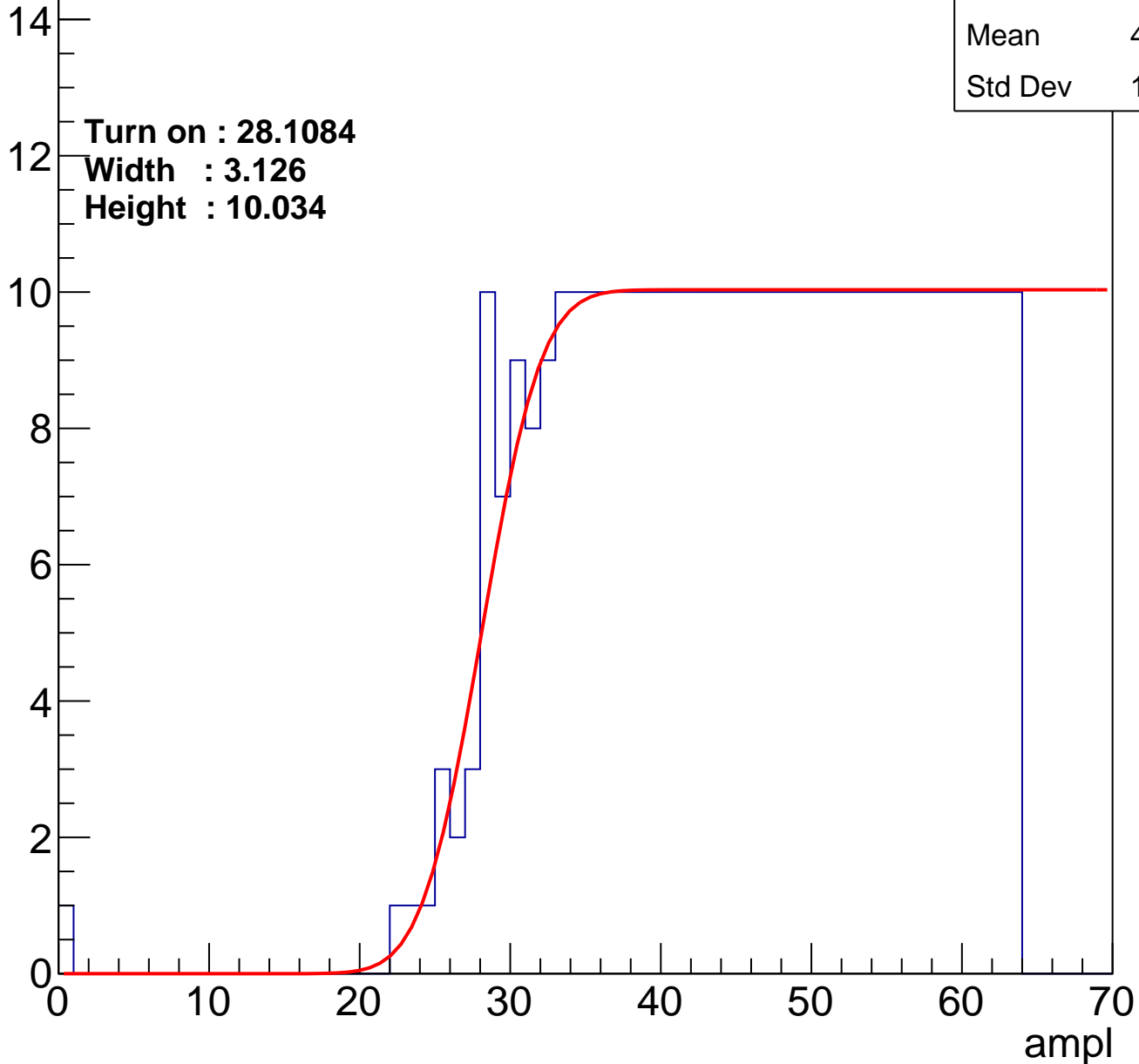
Entries	365
Mean	45.06
Std Dev	10.95

Turn on : 28.1084

Width : 3.126

Height : 10.034

Entry



B0L000S, U11-ch66

calib_packv5_042523_0143.root, FC#5, port B1

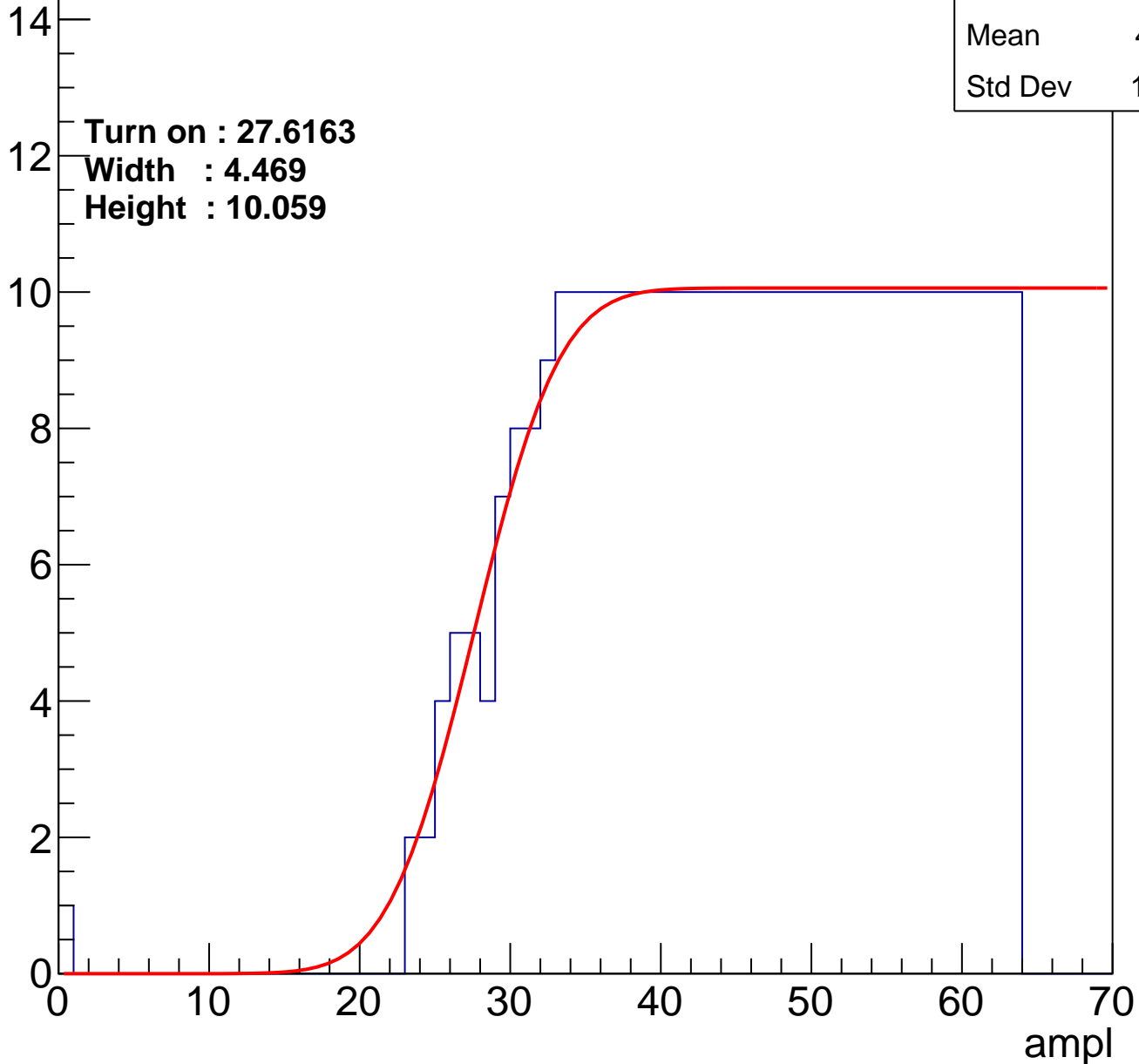
Entries	365
Mean	45.01
Std Dev	11.02

Turn on : 27.6163

Width : 4.469

Height : 10.059

Entry



B0L000S, U11-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.75
Std Dev	11.09

Turn on : 27.2274

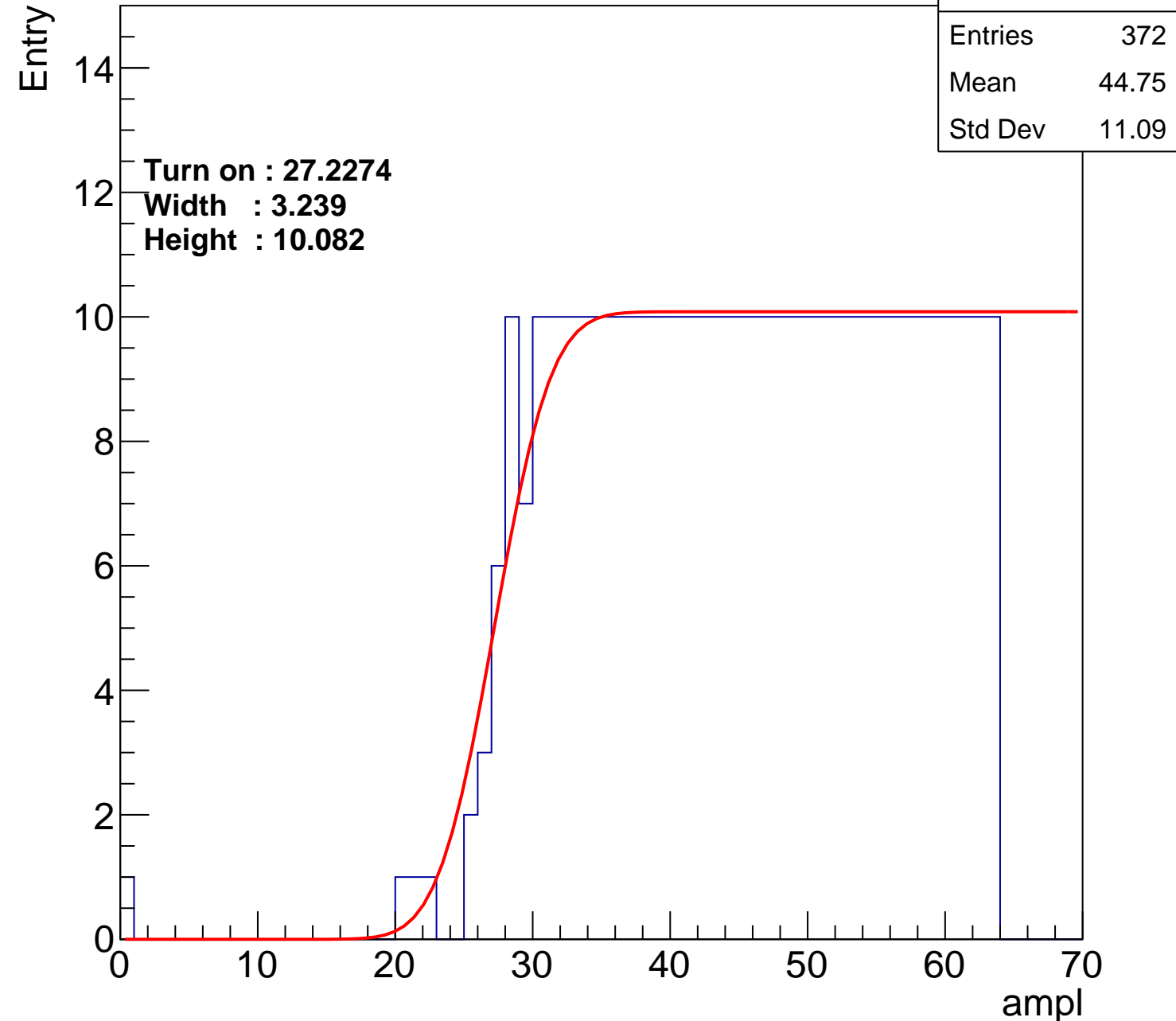
Width : 3.239

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch68

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 27.3265

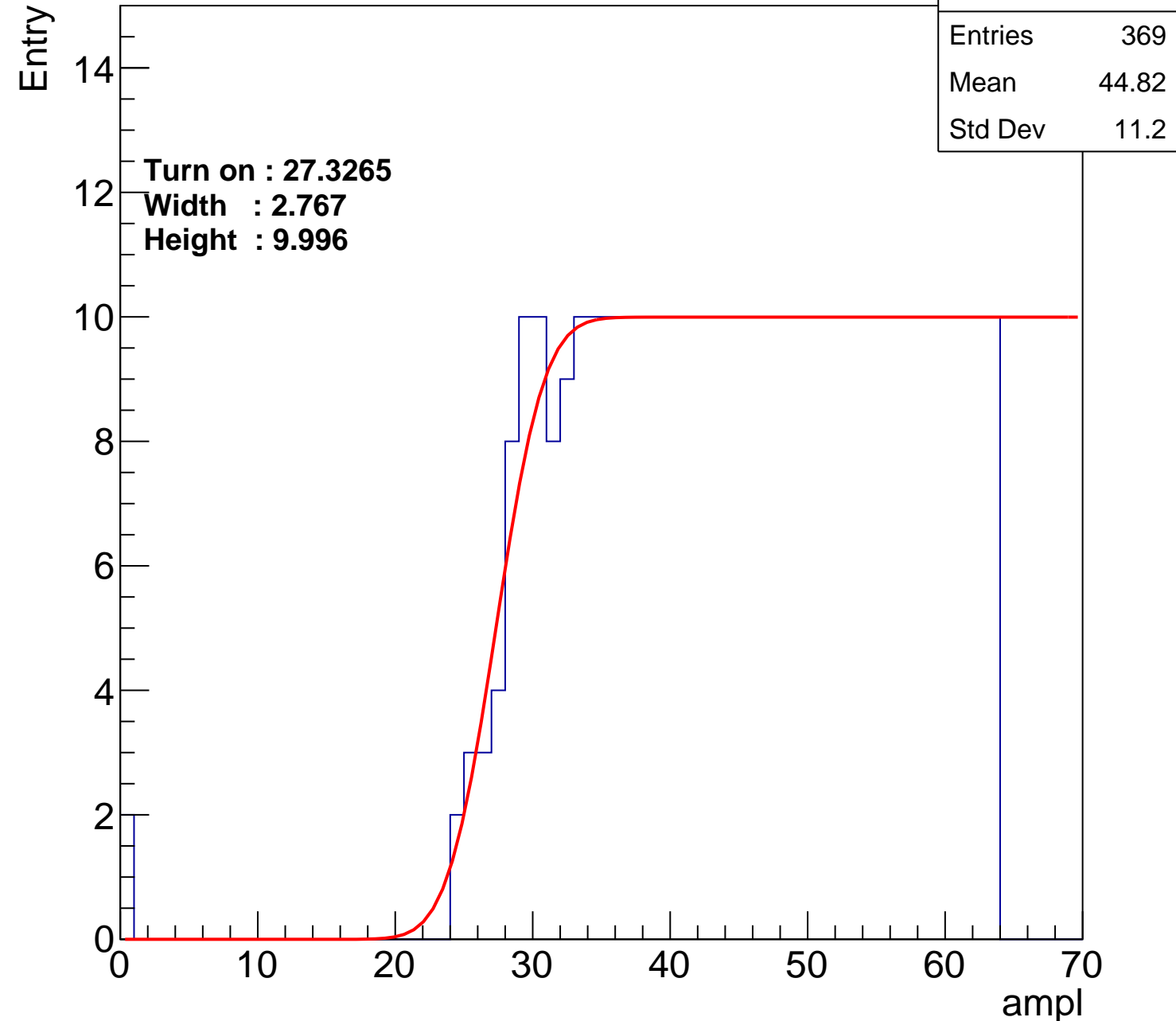
Width : 2.767

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch69

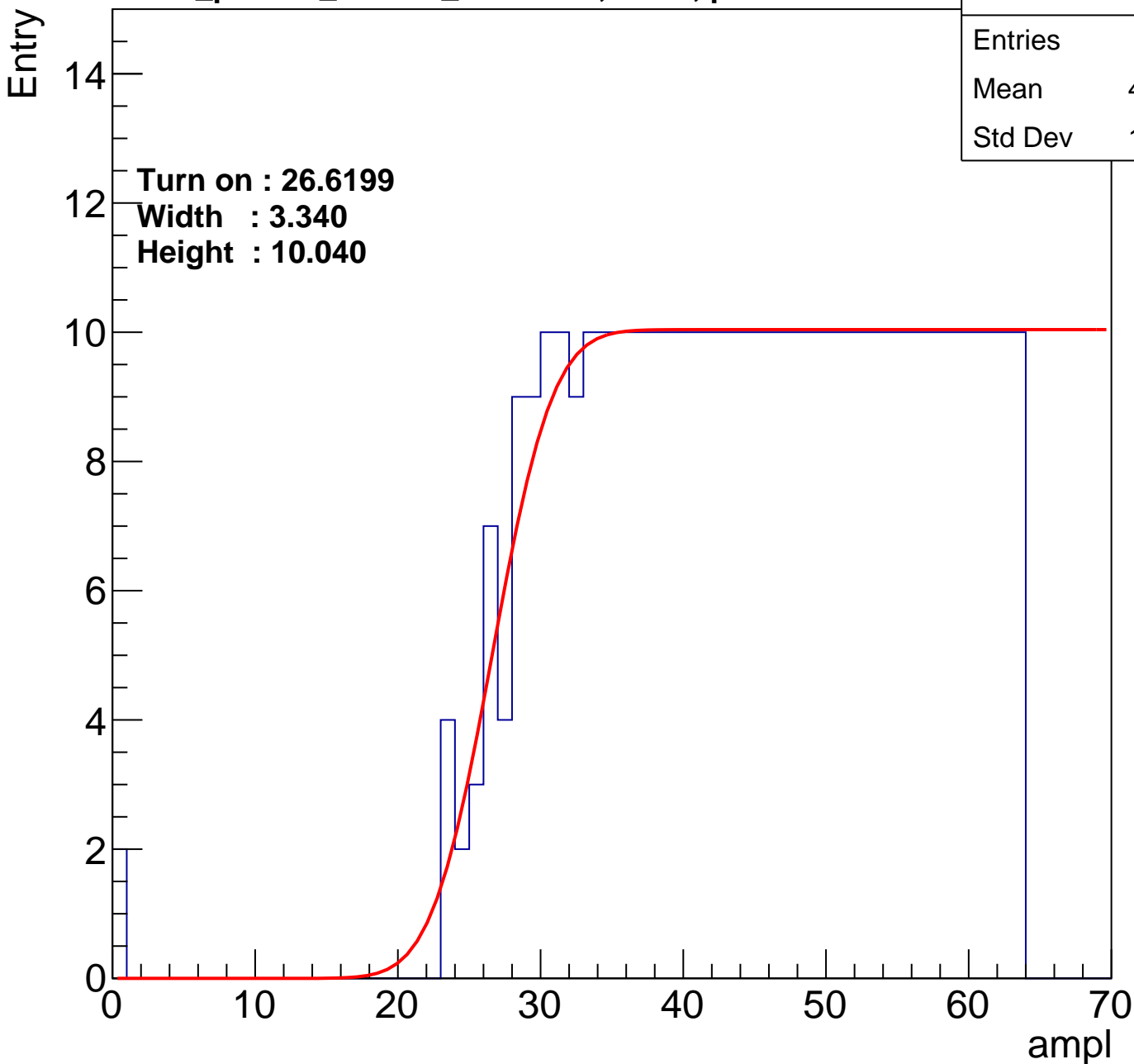
calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.6199

Width : 3.340

Height : 10.040



B0L000S, U11-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.3
Std Dev	11.18

Turn on : 28.7407

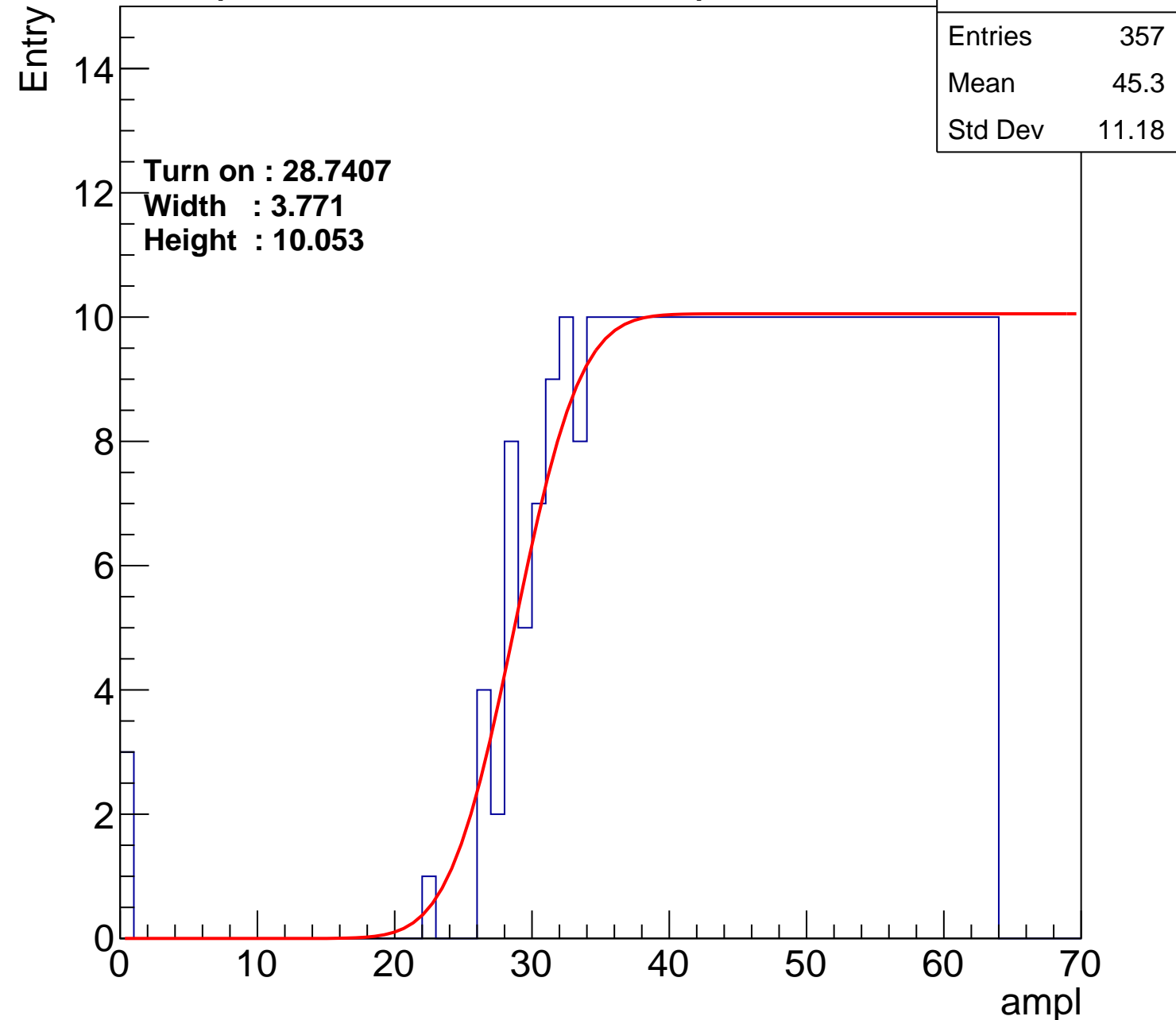
Width : 3.771

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.6
Std Dev	11.48

Turn on : 26.8330

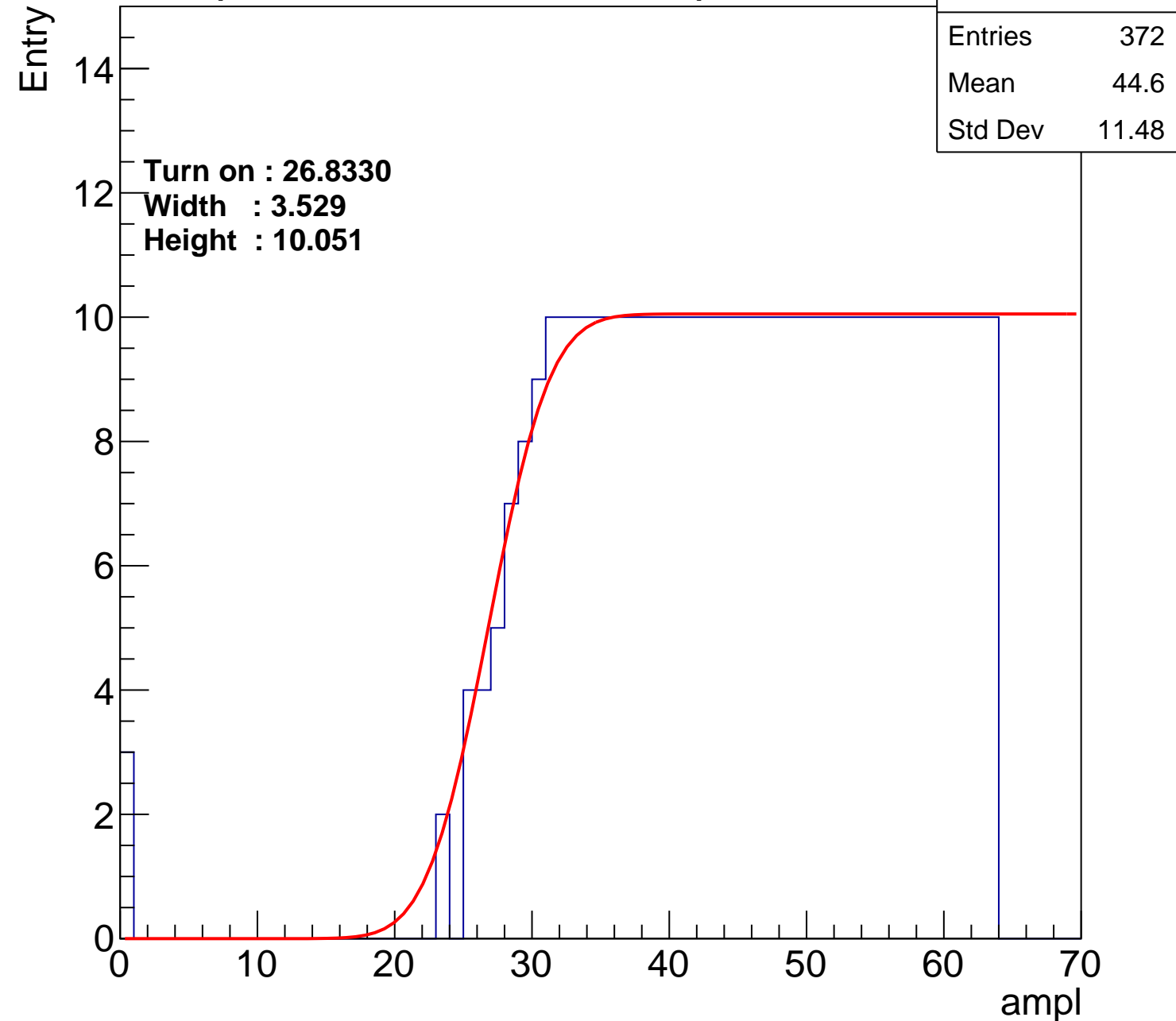
Width : 3.529

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.37
Std Dev	11.36

Turn on : 26.6073

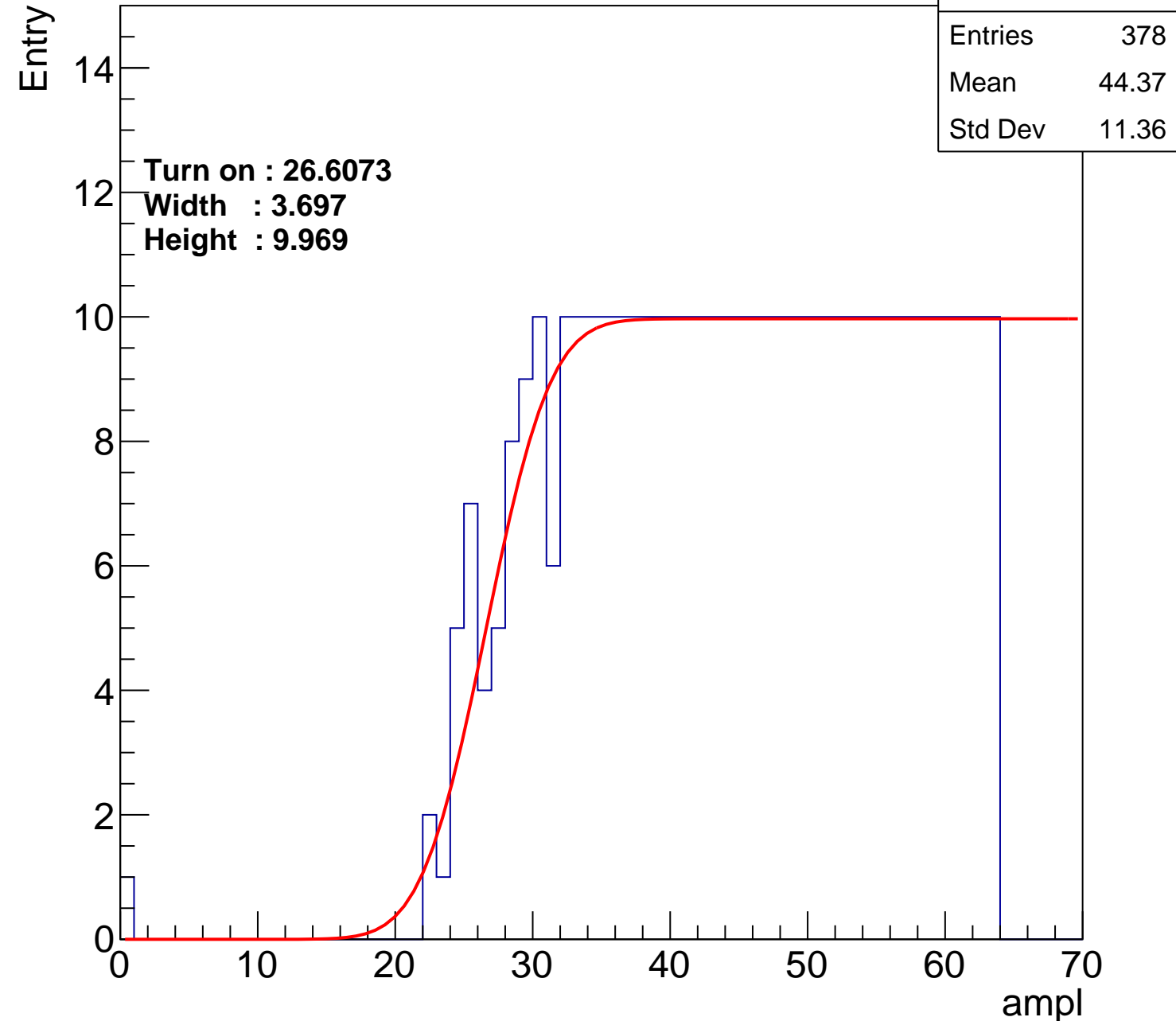
Width : 3.697

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.68
Std Dev	10.73

Turn on : 28.5795

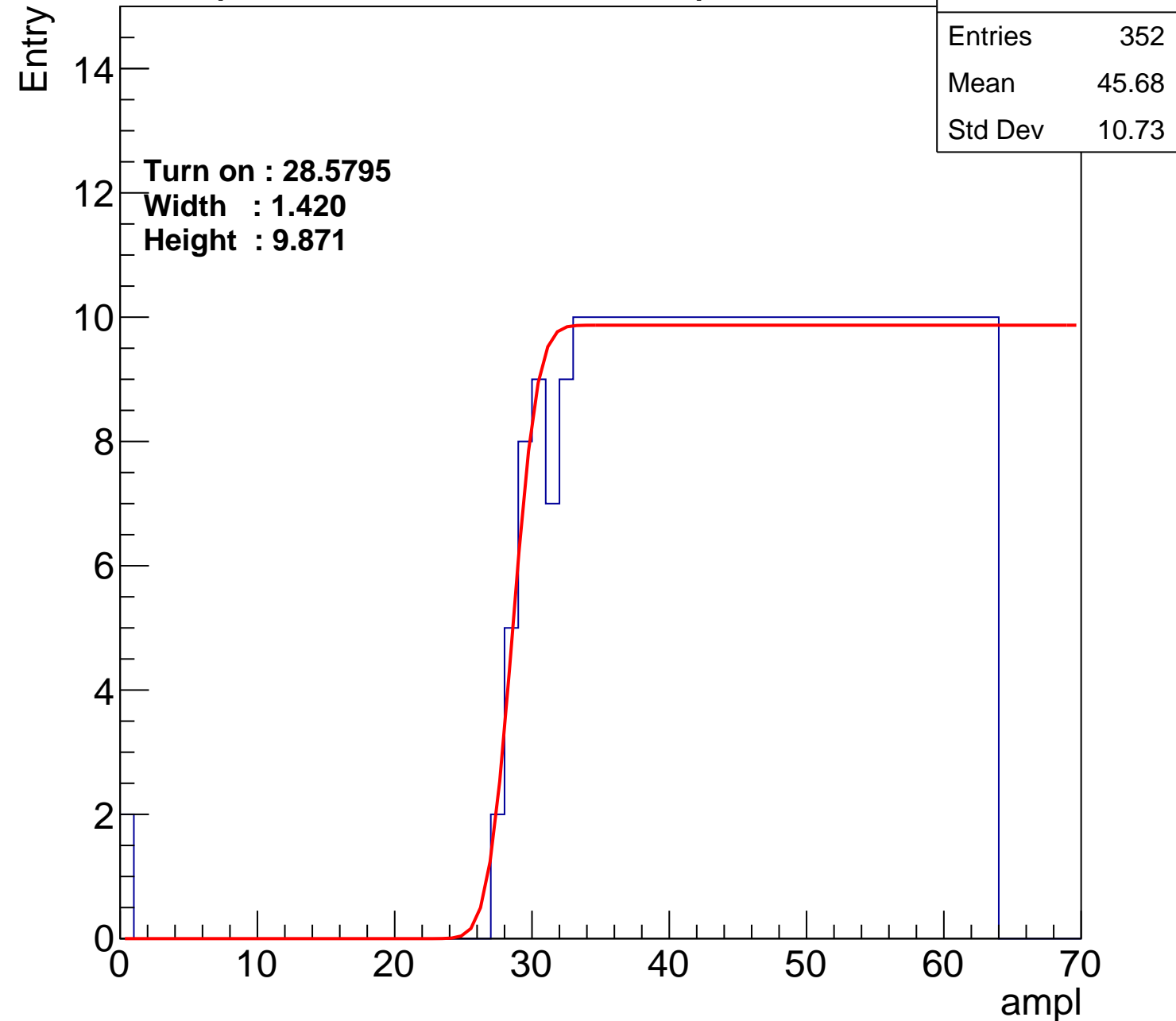
Width : 1.420

Height : 9.871

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.06
Std Dev	12.35

Turn on : 27.5645

Width : 3.271

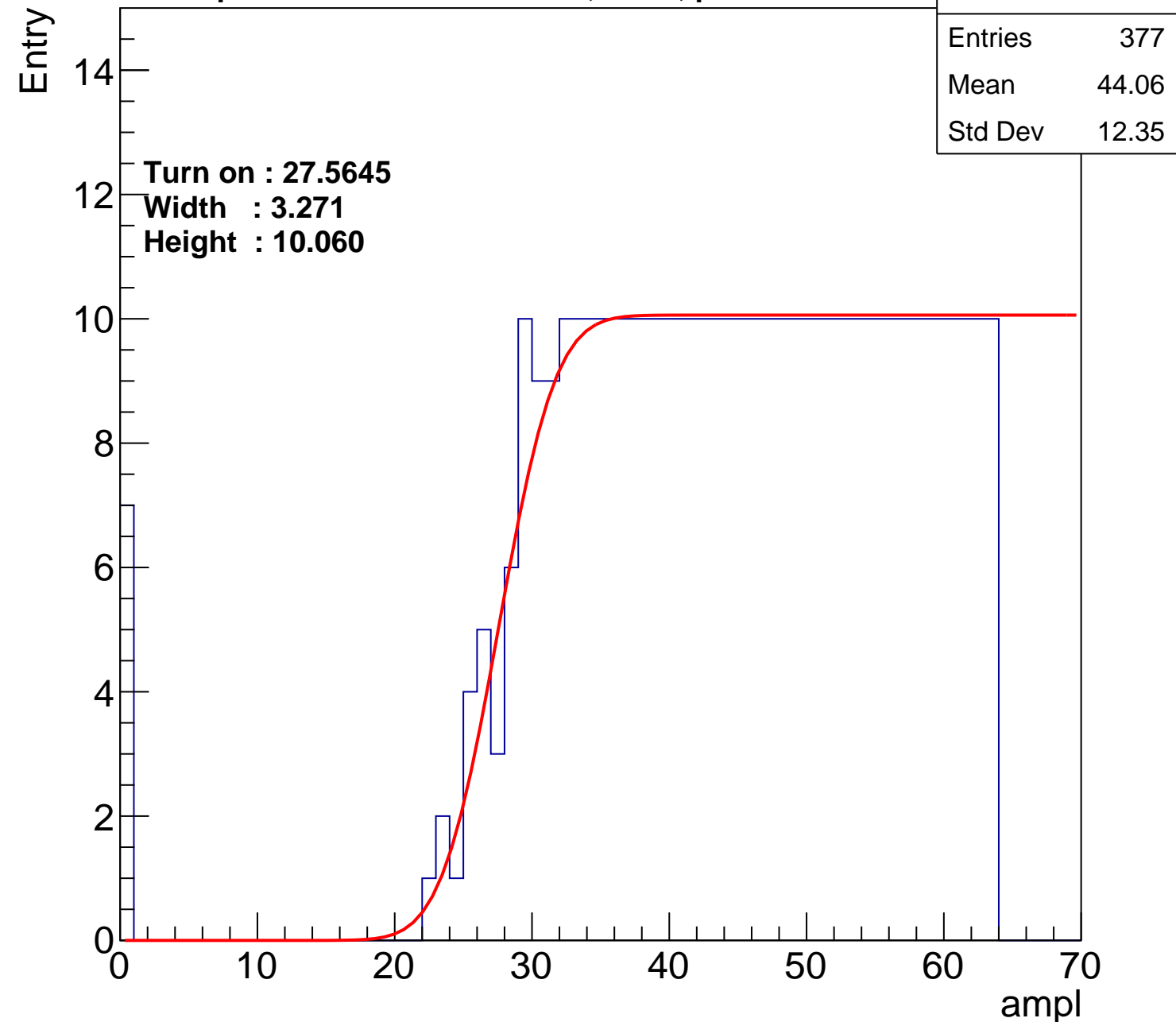
Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U11-ch75

calib_packv5_042523_0143.root, FC#5, port B1

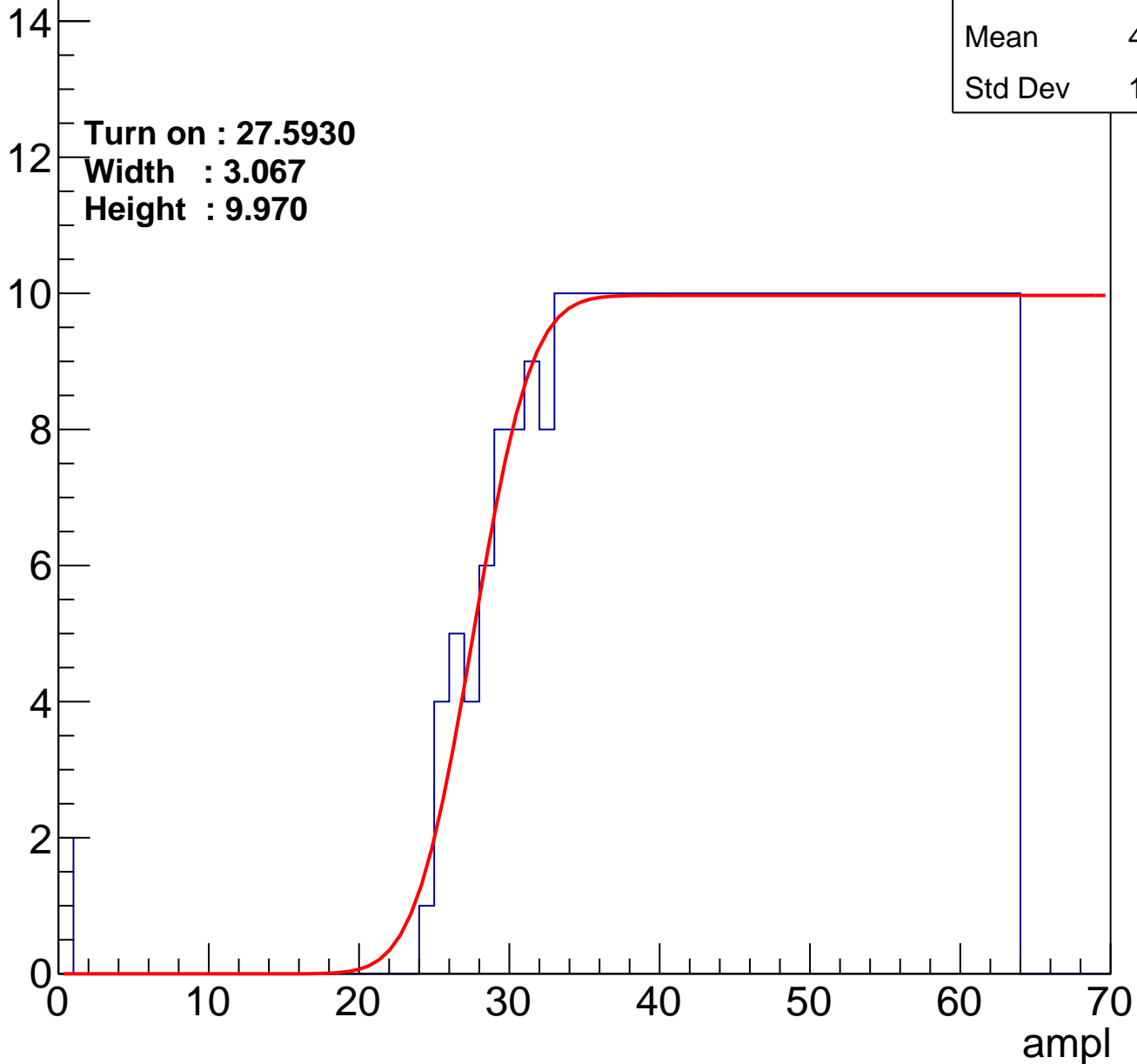
Entries	365
Mean	44.98
Std Dev	11.16

Turn on : 27.5930

Width : 3.067

Height : 9.970

Entry



B0L000S, U11-ch76

calib_packv5_042523_0143.root, FC#5, port B1

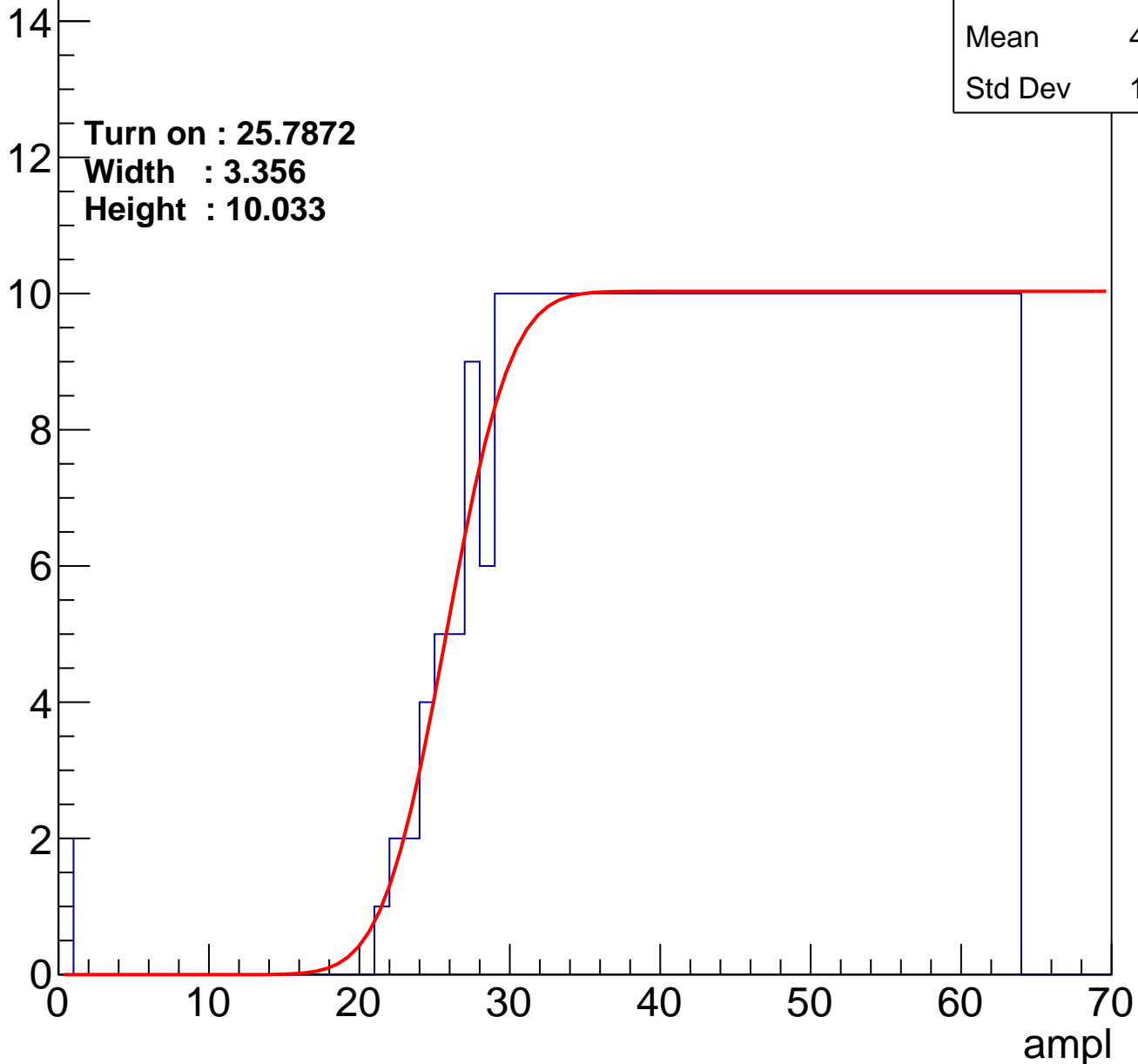
Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.7872

Width : 3.356

Height : 10.033

Entry



B0L000S, U11-ch77

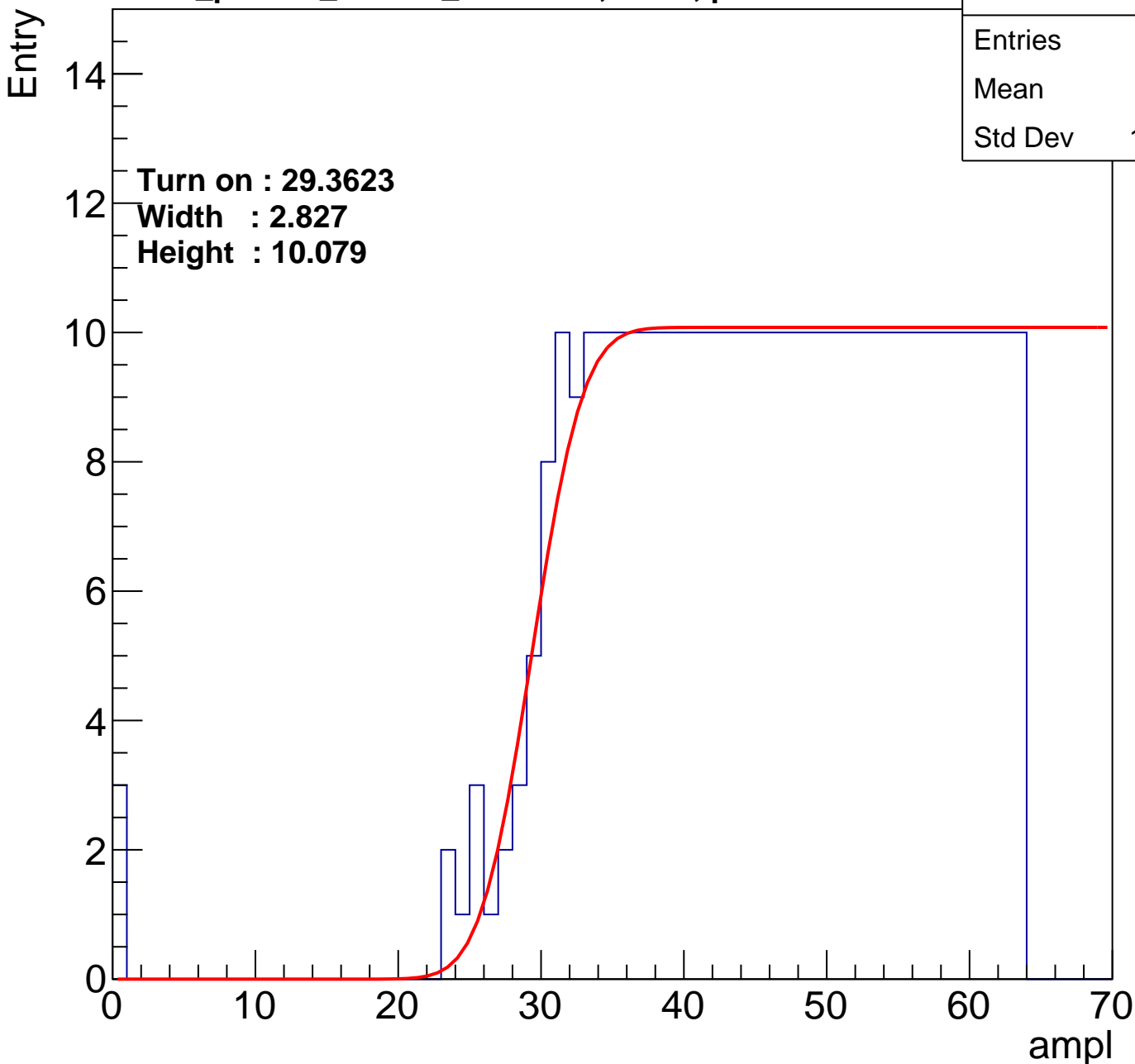
calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.3
Std Dev	11.19

Turn on : 29.3623

Width : 2.827

Height : 10.079



B0L000S, U11-ch78

calib_packv5_042523_0143.root, FC#5, port B1

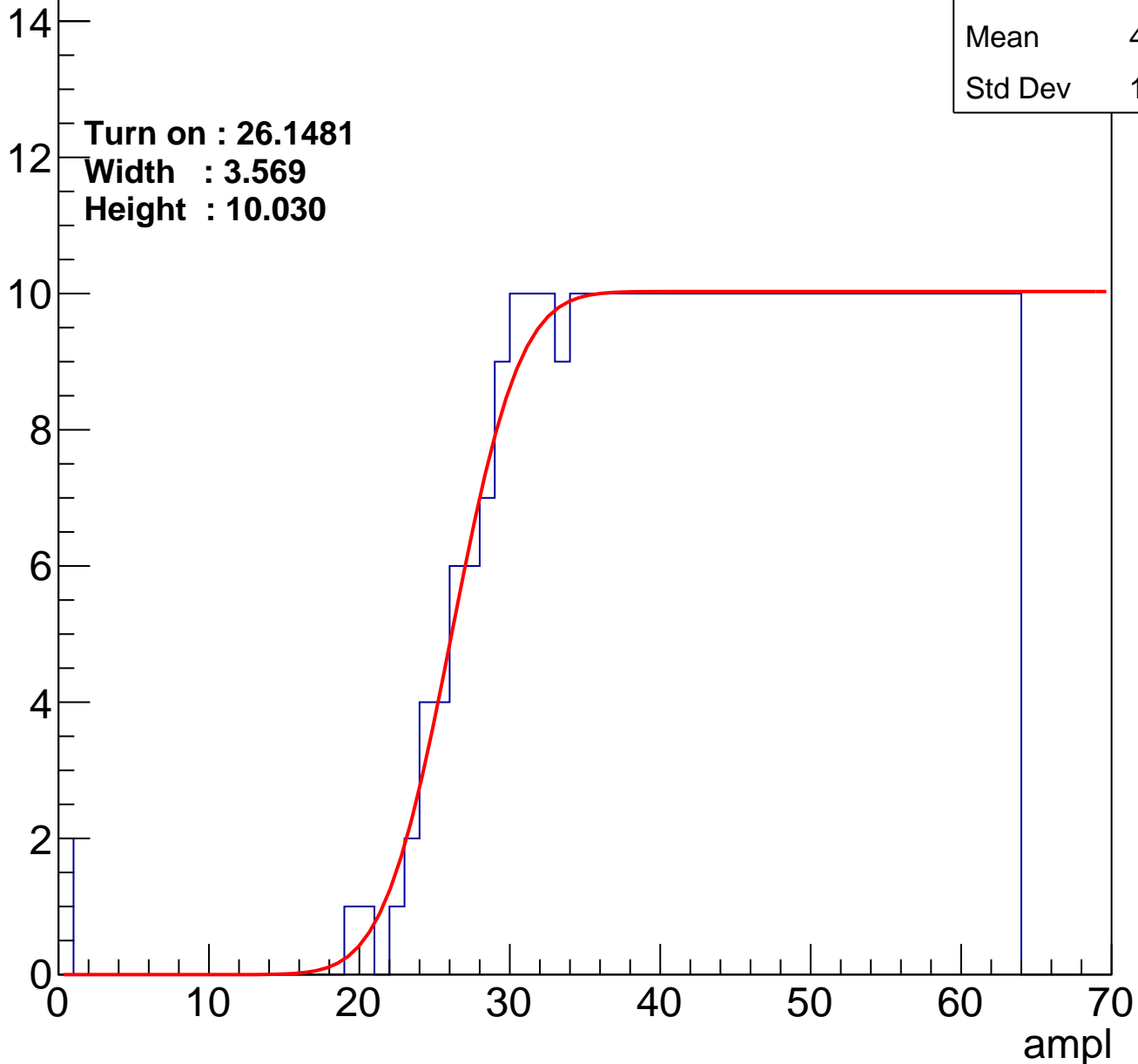
Entries	382
Mean	44.12
Std Dev	11.63

Turn on : 26.1481

Width : 3.569

Height : 10.030

Entry



B0L000S, U11-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	44.01
Std Dev	11.6

Turn on : 25.5155

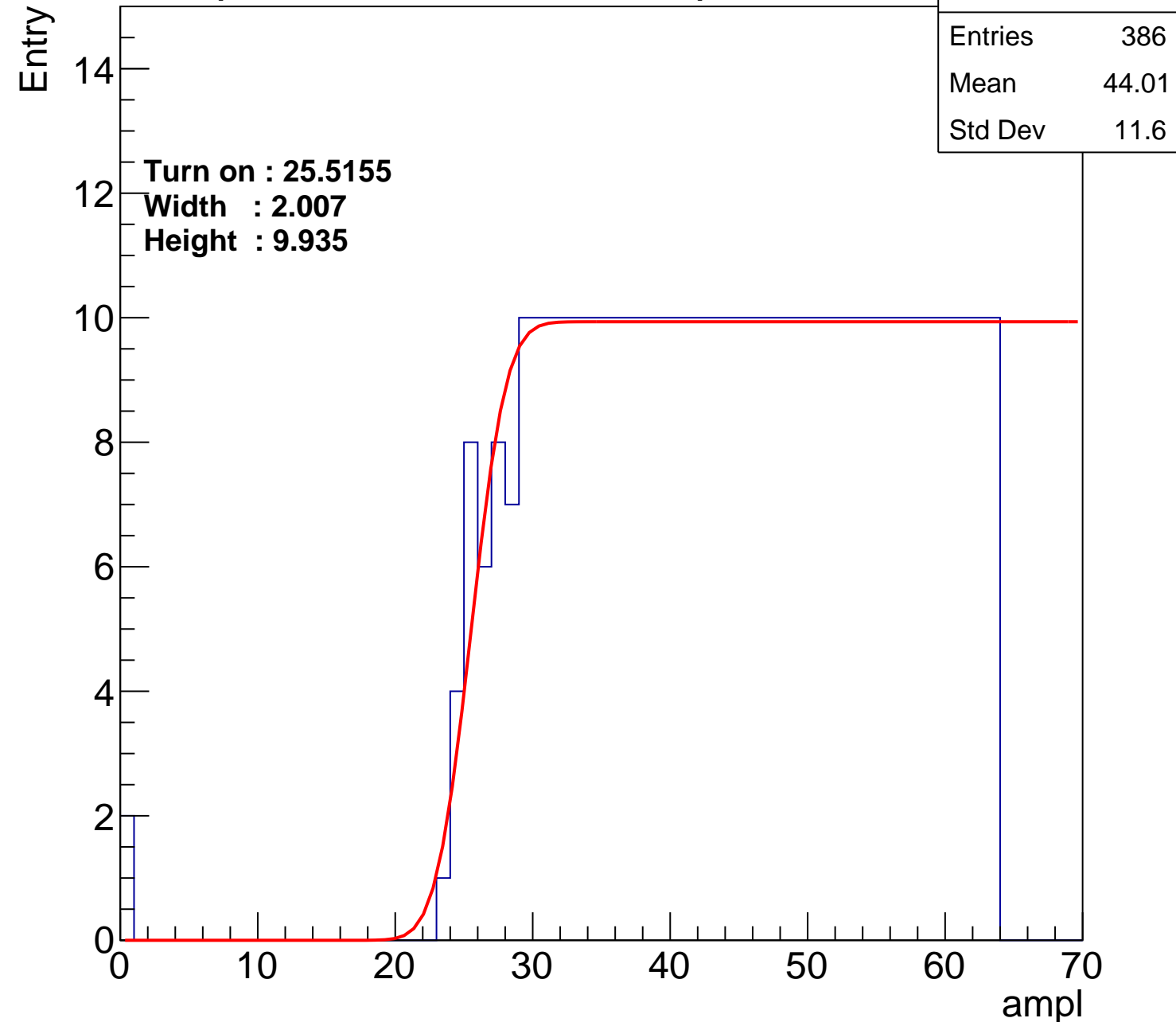
Width : 2.007

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.98
Std Dev	11.71

Turn on : 26.6532

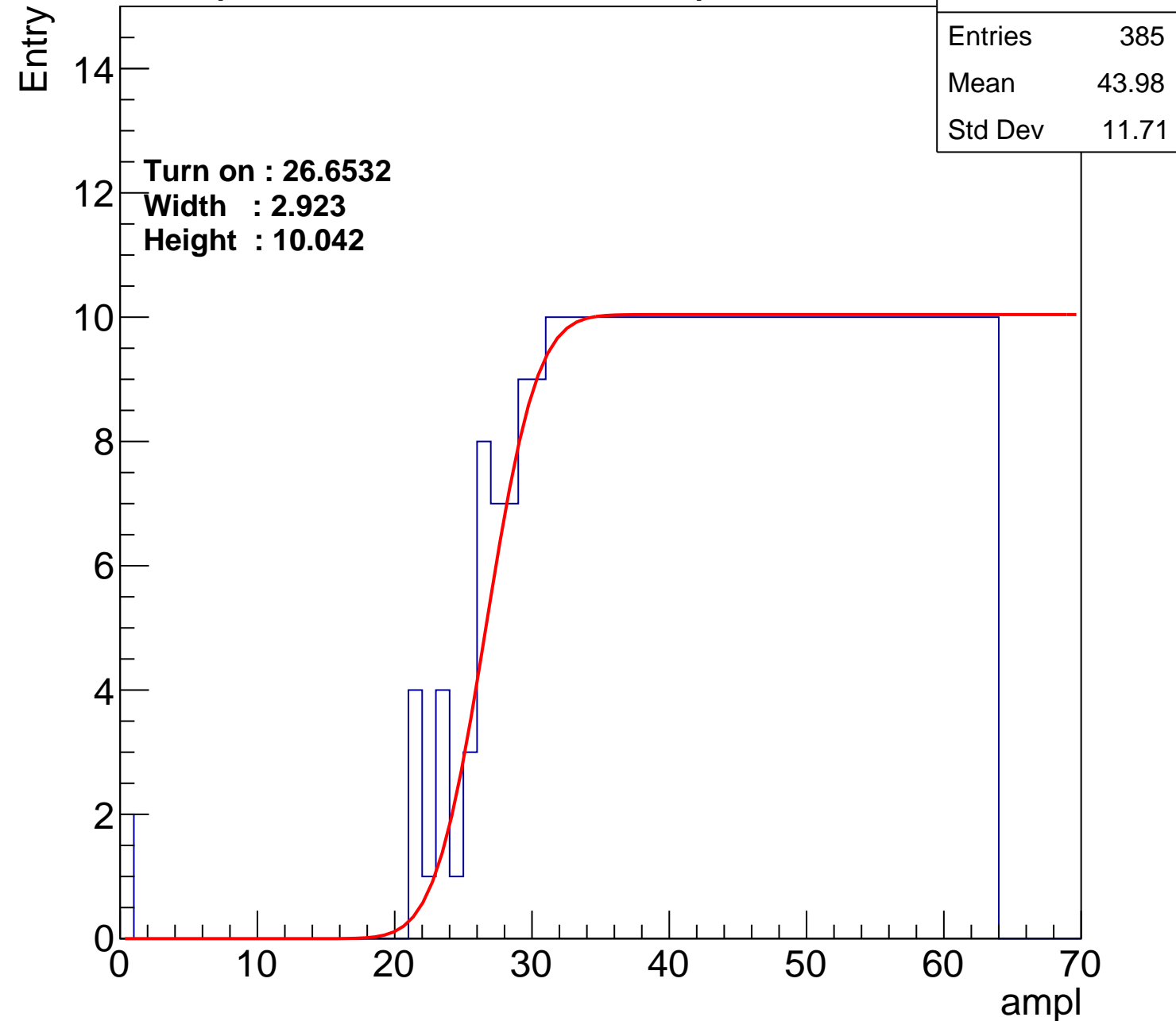
Width : 2.923

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch81

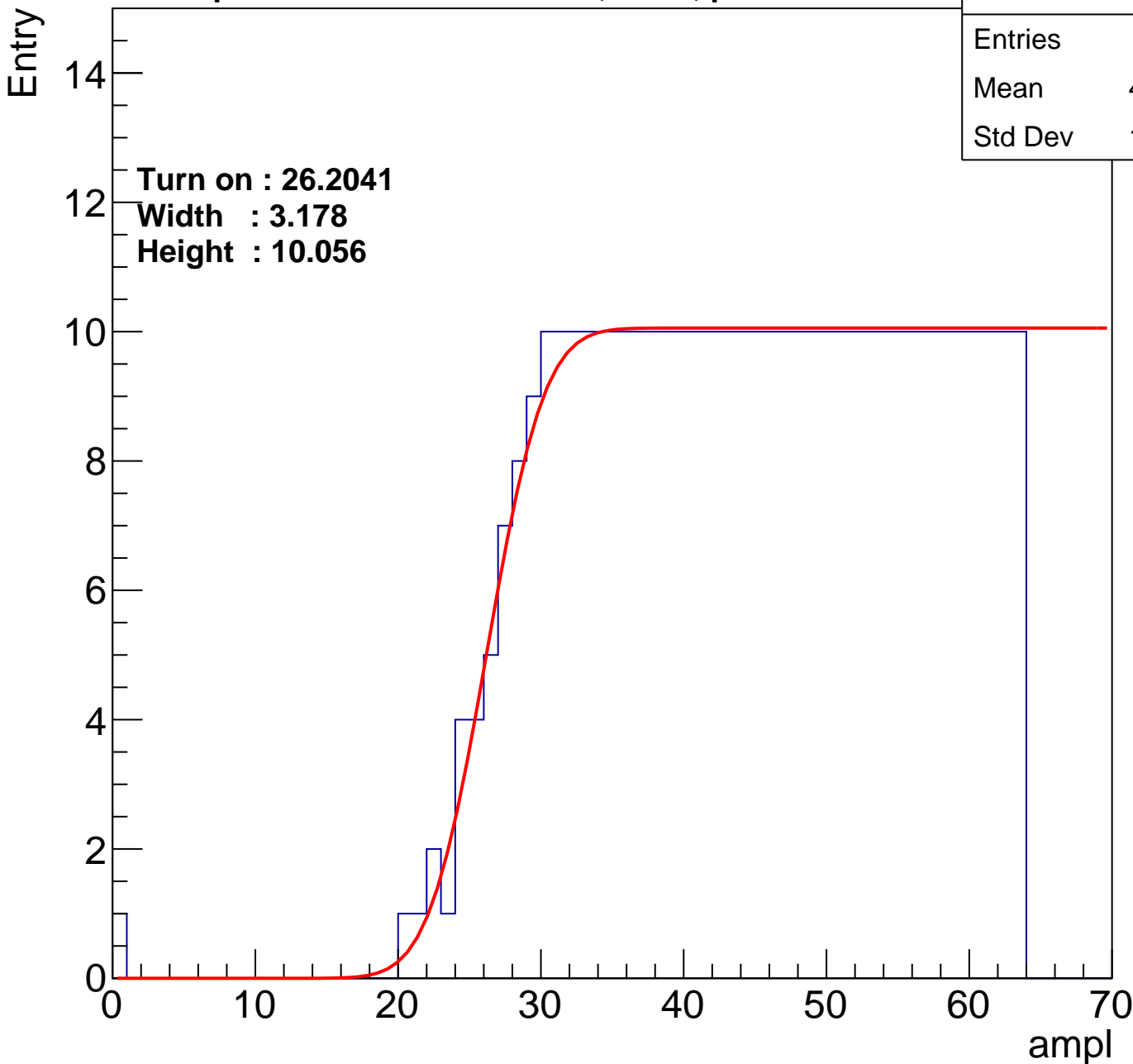
calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.17
Std Dev	11.43

Turn on : 26.2041

Width : 3.178

Height : 10.056



B0L000S, U11-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.84
Std Dev	11.6

Turn on : 25.0144

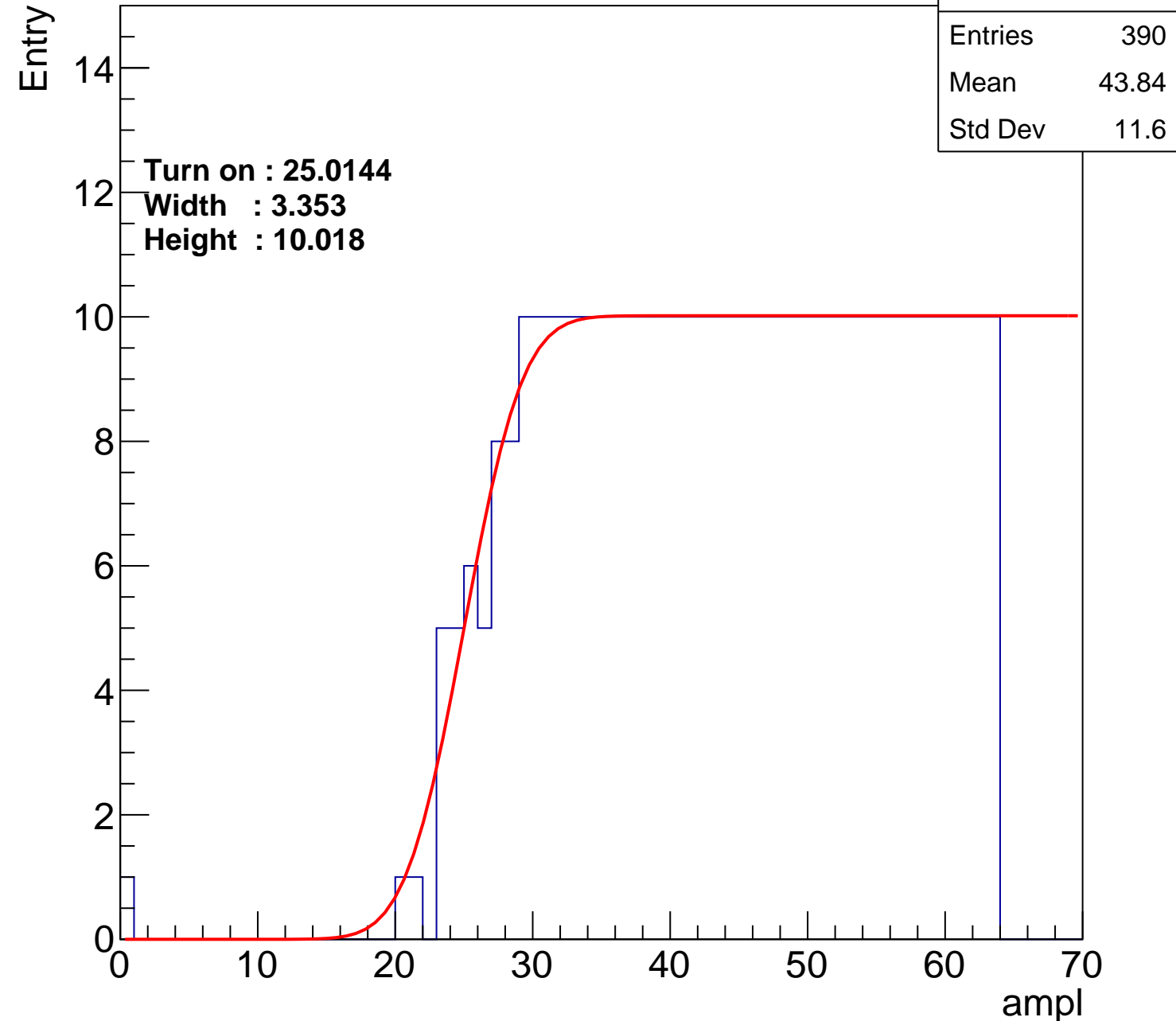
Width : 3.353

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.02
Std Dev	11.1

Turn on : 28.0708

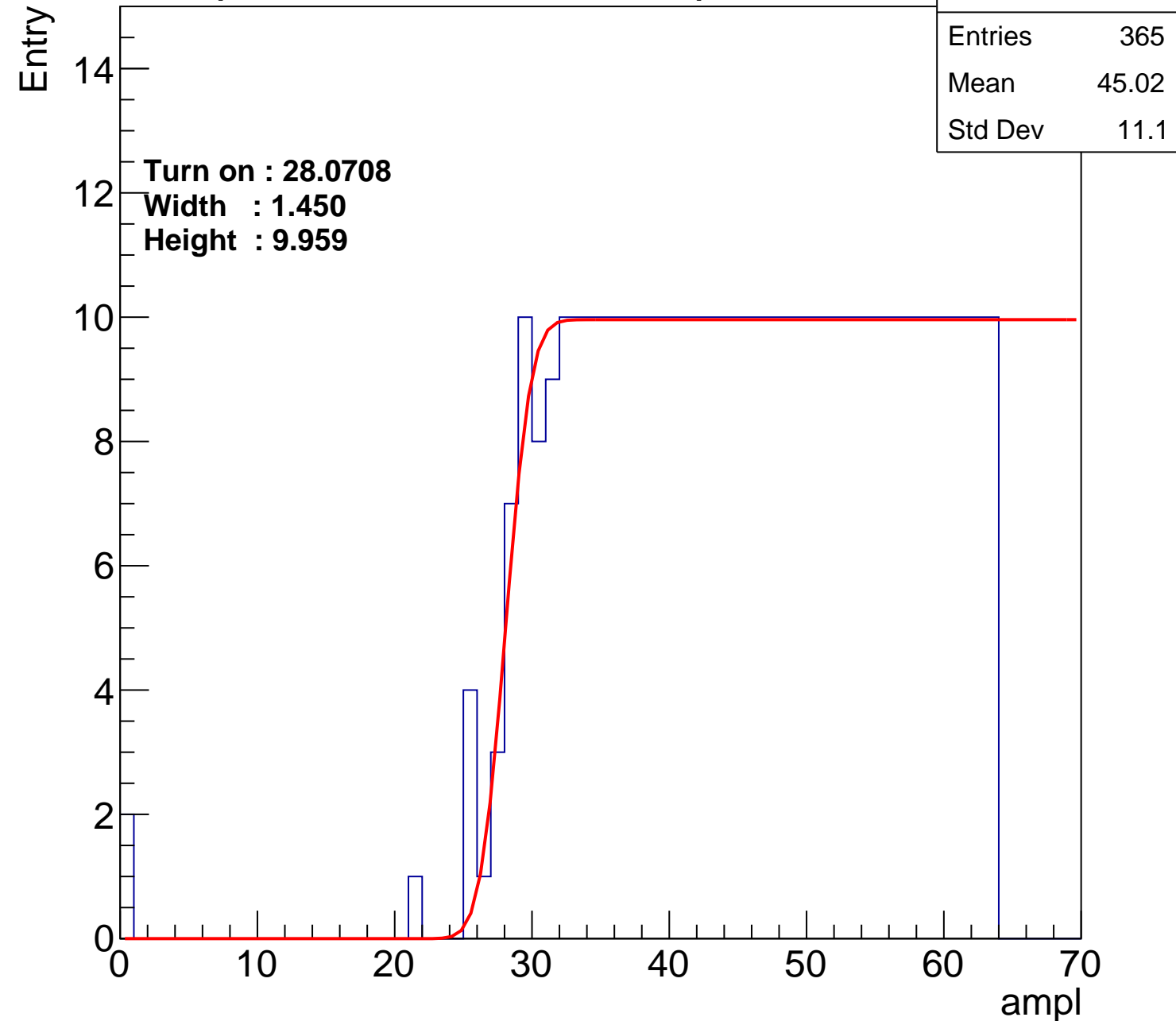
Width : 1.450

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.21
Std Dev	11.73

Turn on : 26.5942

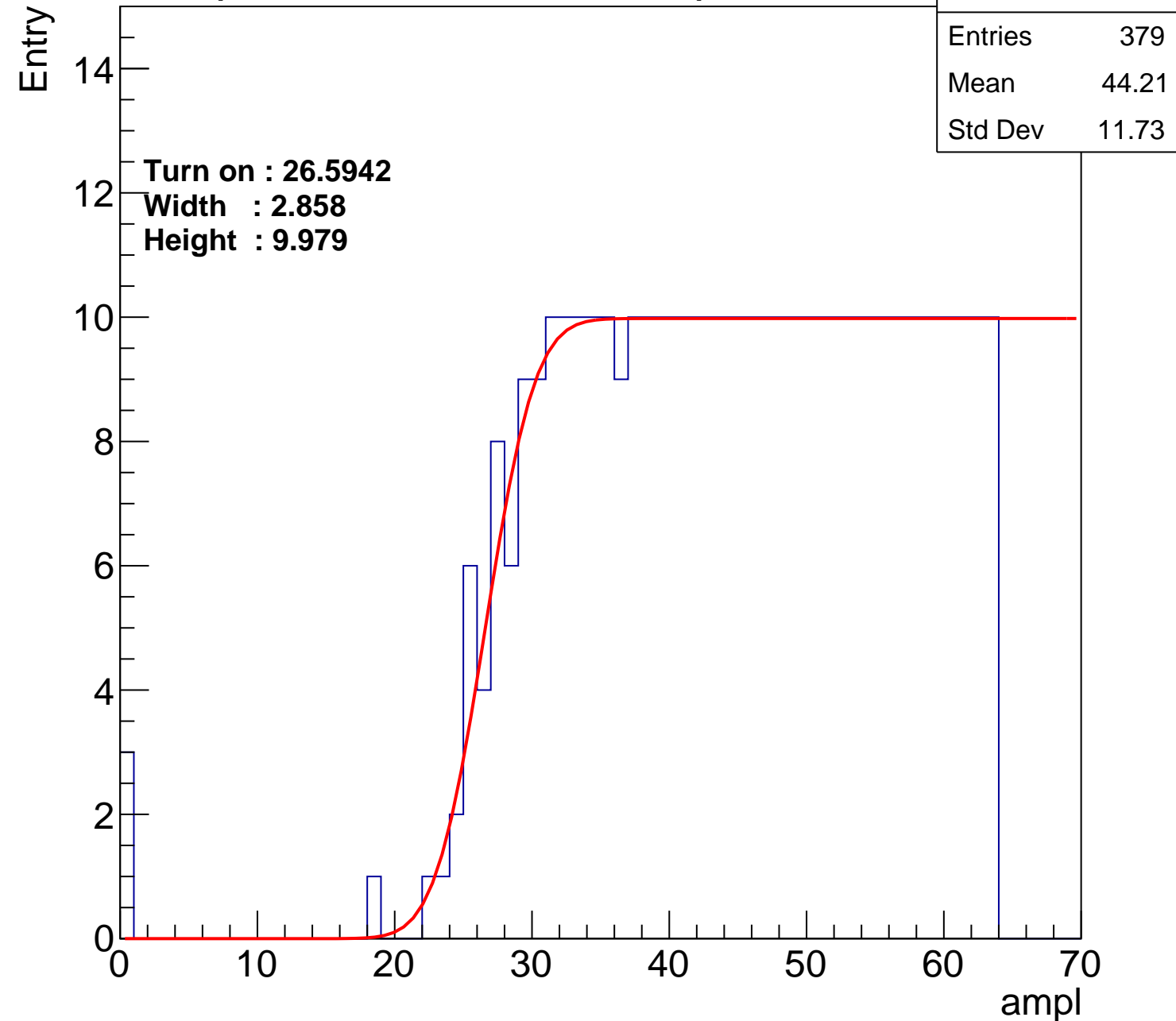
Width : 2.858

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.44
Std Dev	11.7

Turn on : 27.0736

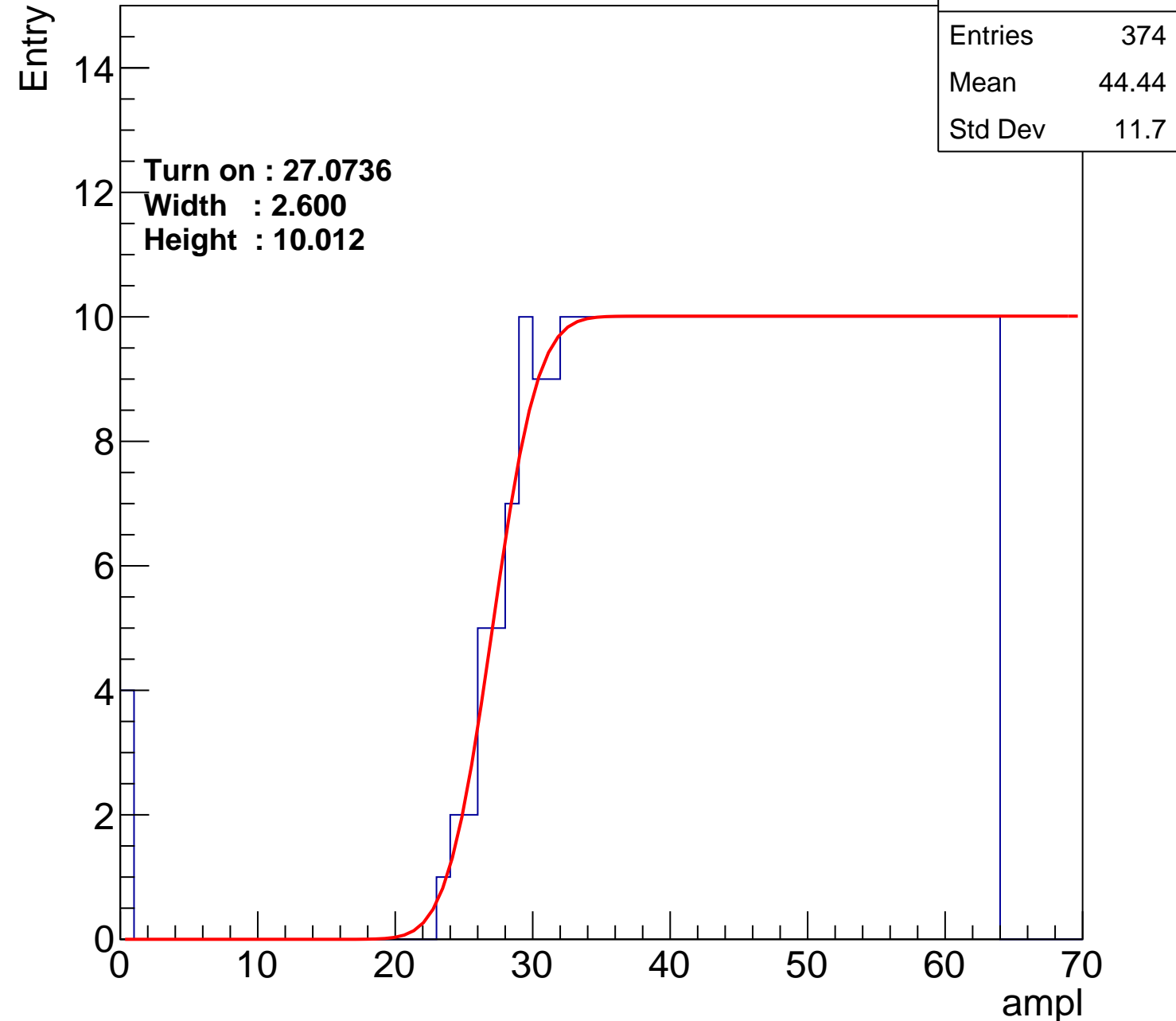
Width : 2.600

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch86

calib_packv5_042523_0143.root, FC#5, port B1

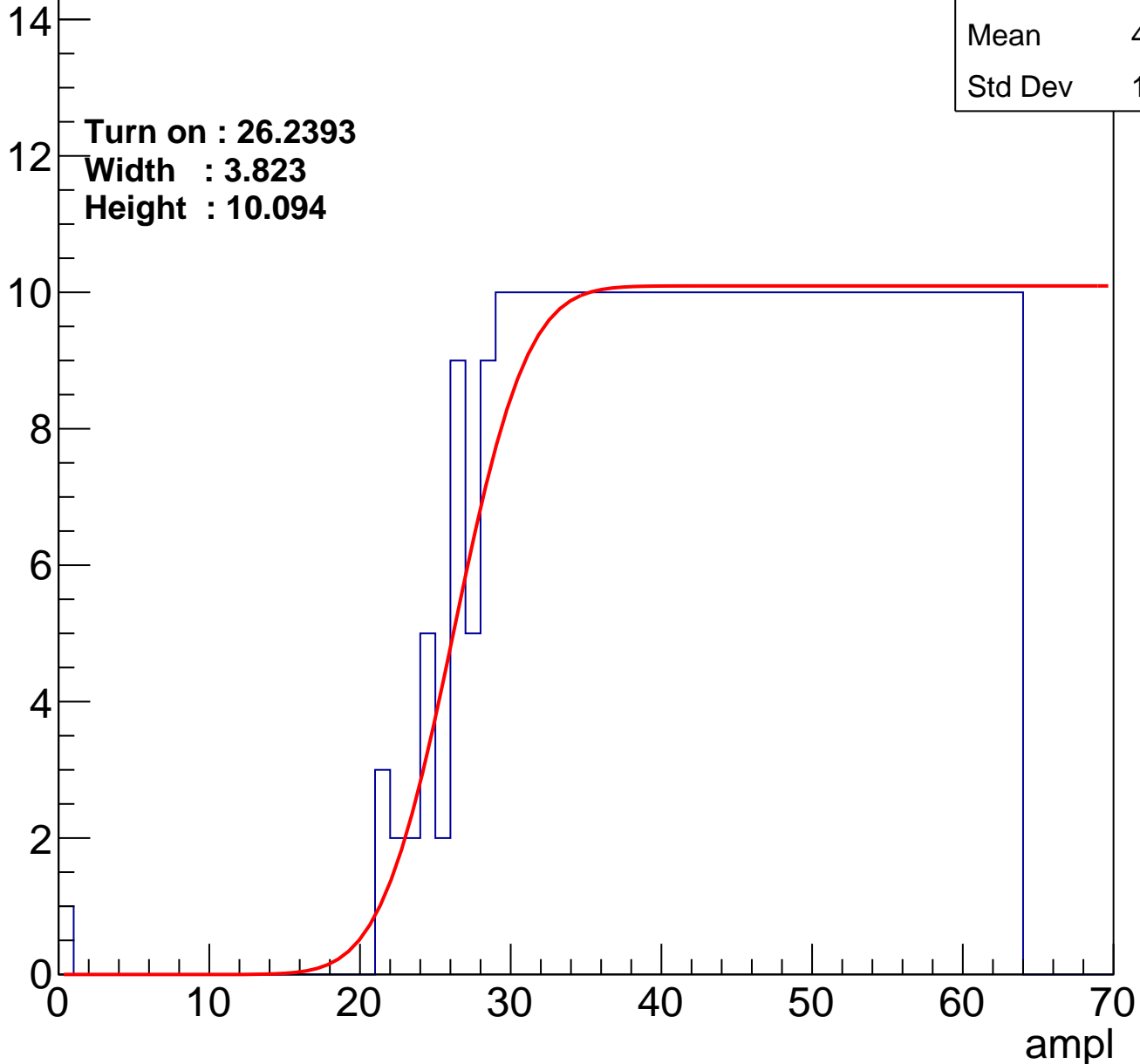
Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 26.2393

Width : 3.823

Height : 10.094

Entry



B0L000S, U11-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.43
Std Dev	10.77

Turn on : 27.9075

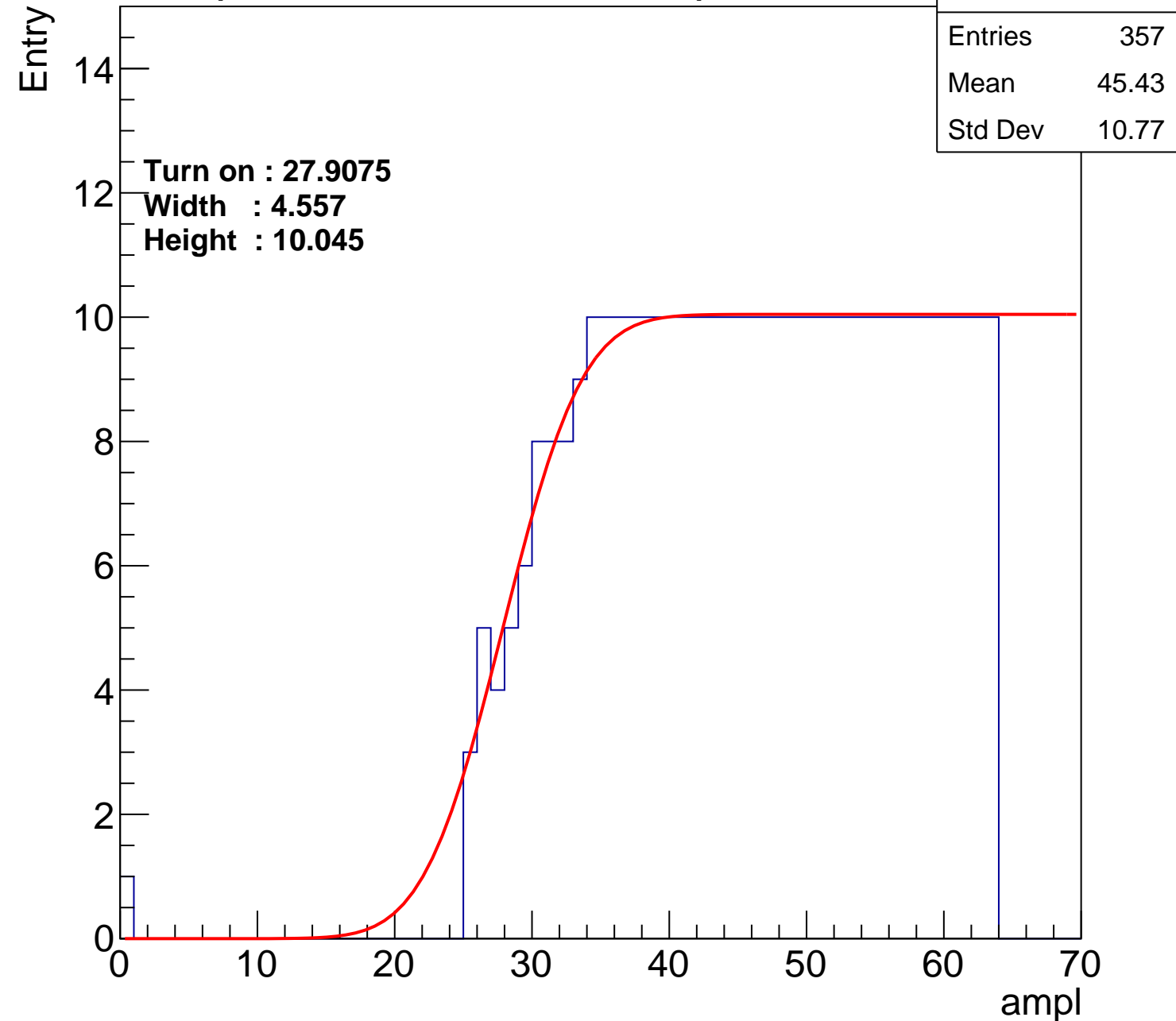
Width : 4.557

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch88

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.3

Turn on : 26.5858

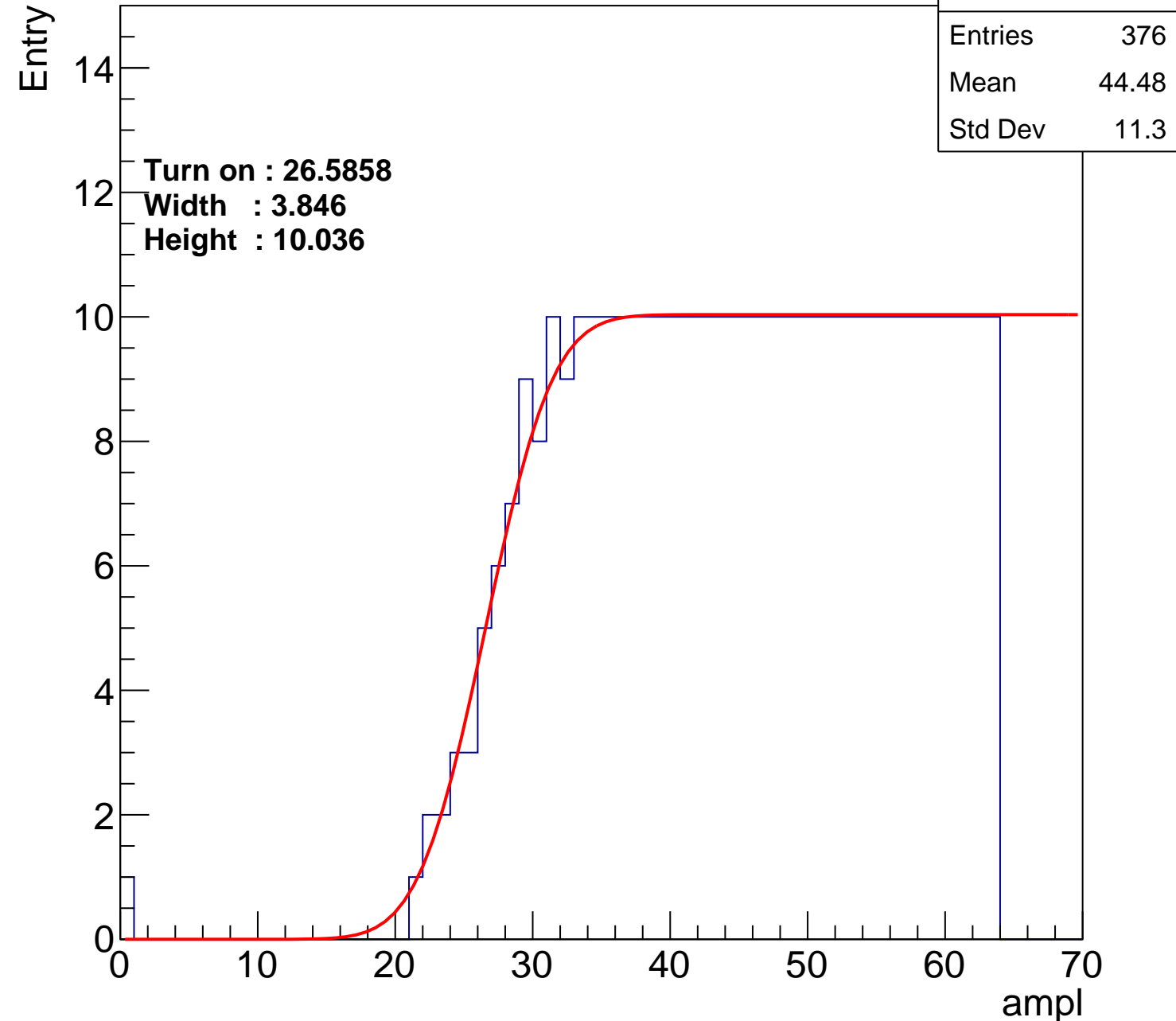
Width : 3.846

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 26.3238

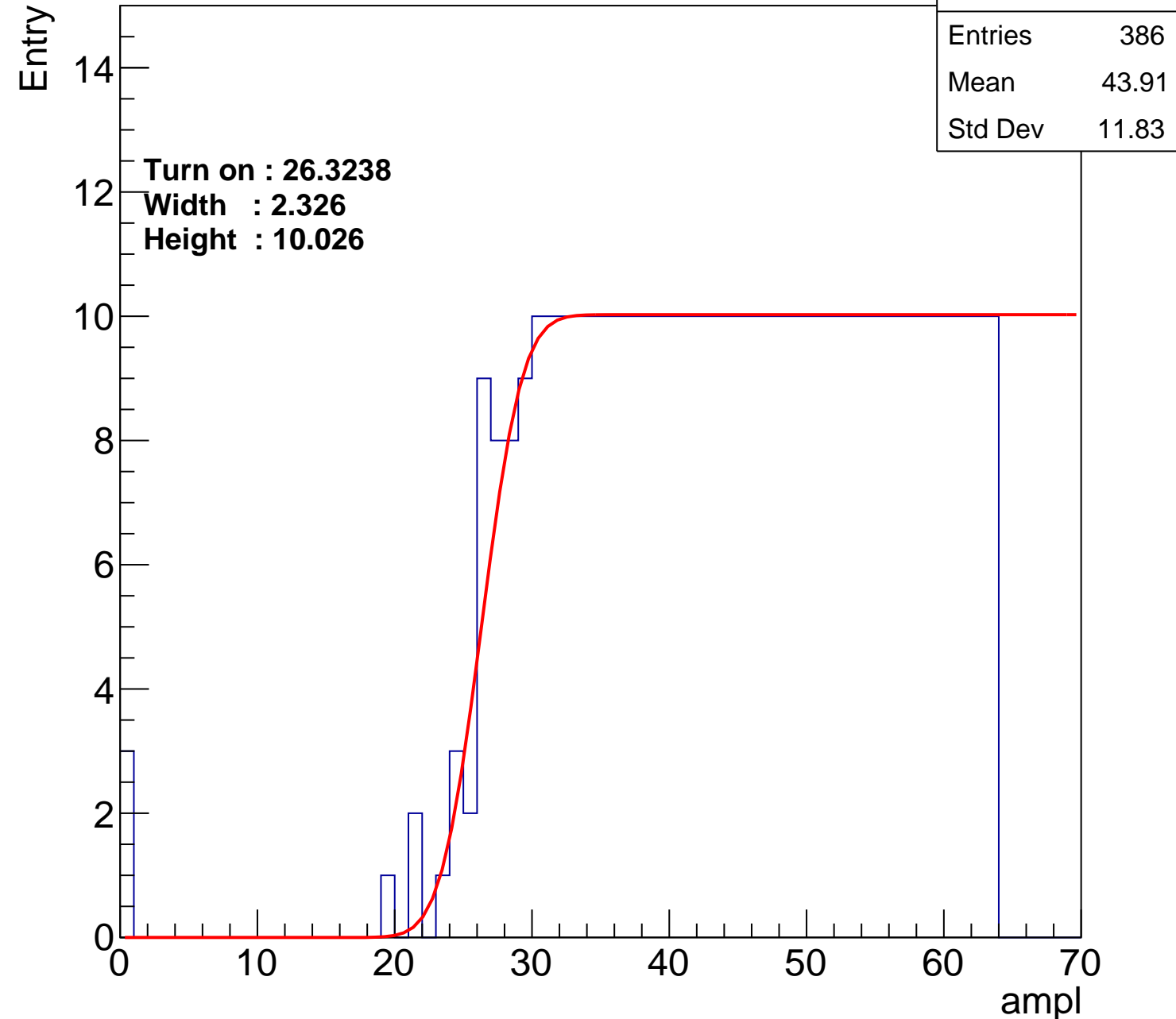
Width : 2.326

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch90

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.48
Std Dev	10.91

Turn on : 29.0012

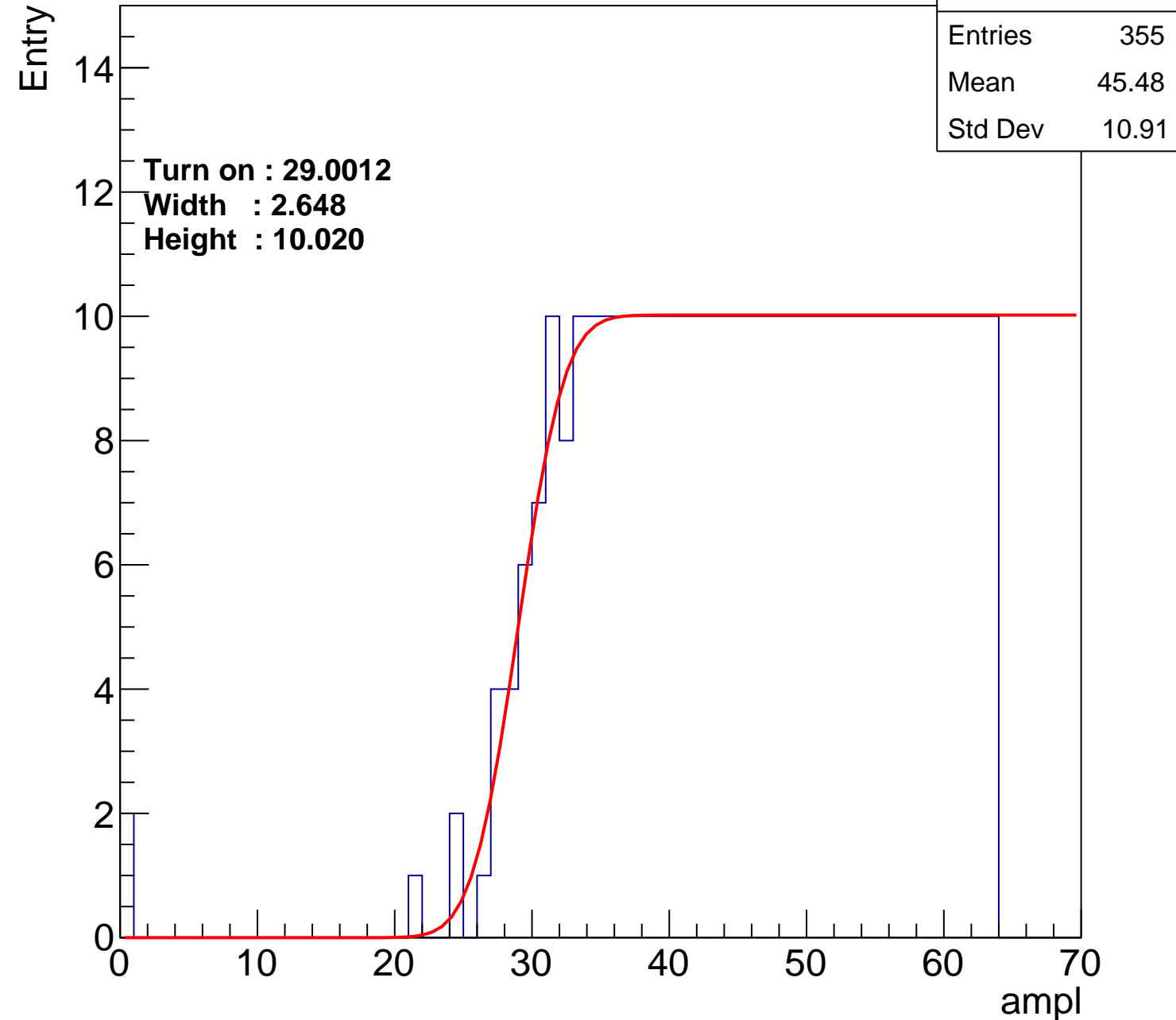
Width : 2.648

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.6360

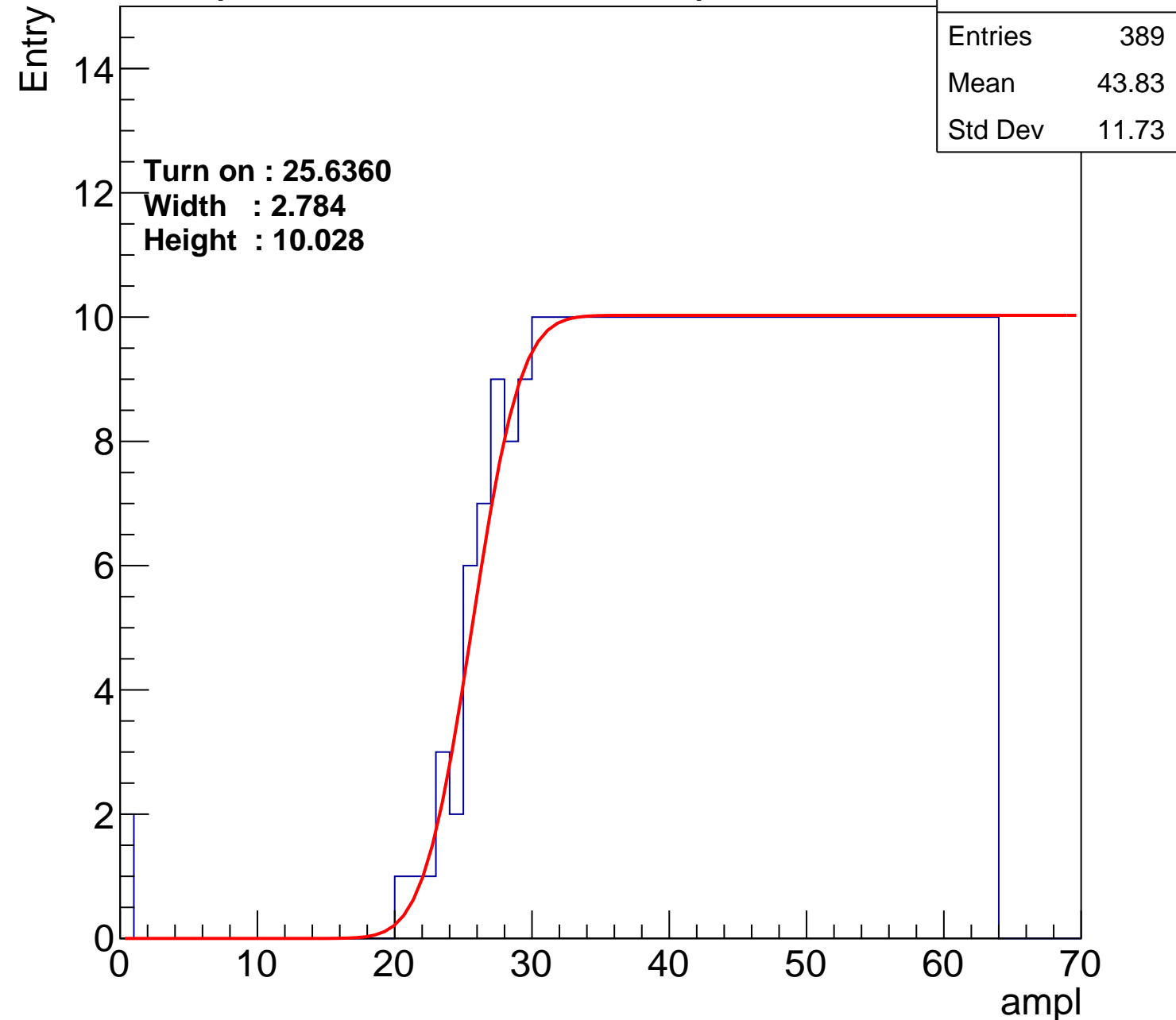
Width : 2.784

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.57
Std Dev	11.16

Turn on : 26.4537

Width : 3.062

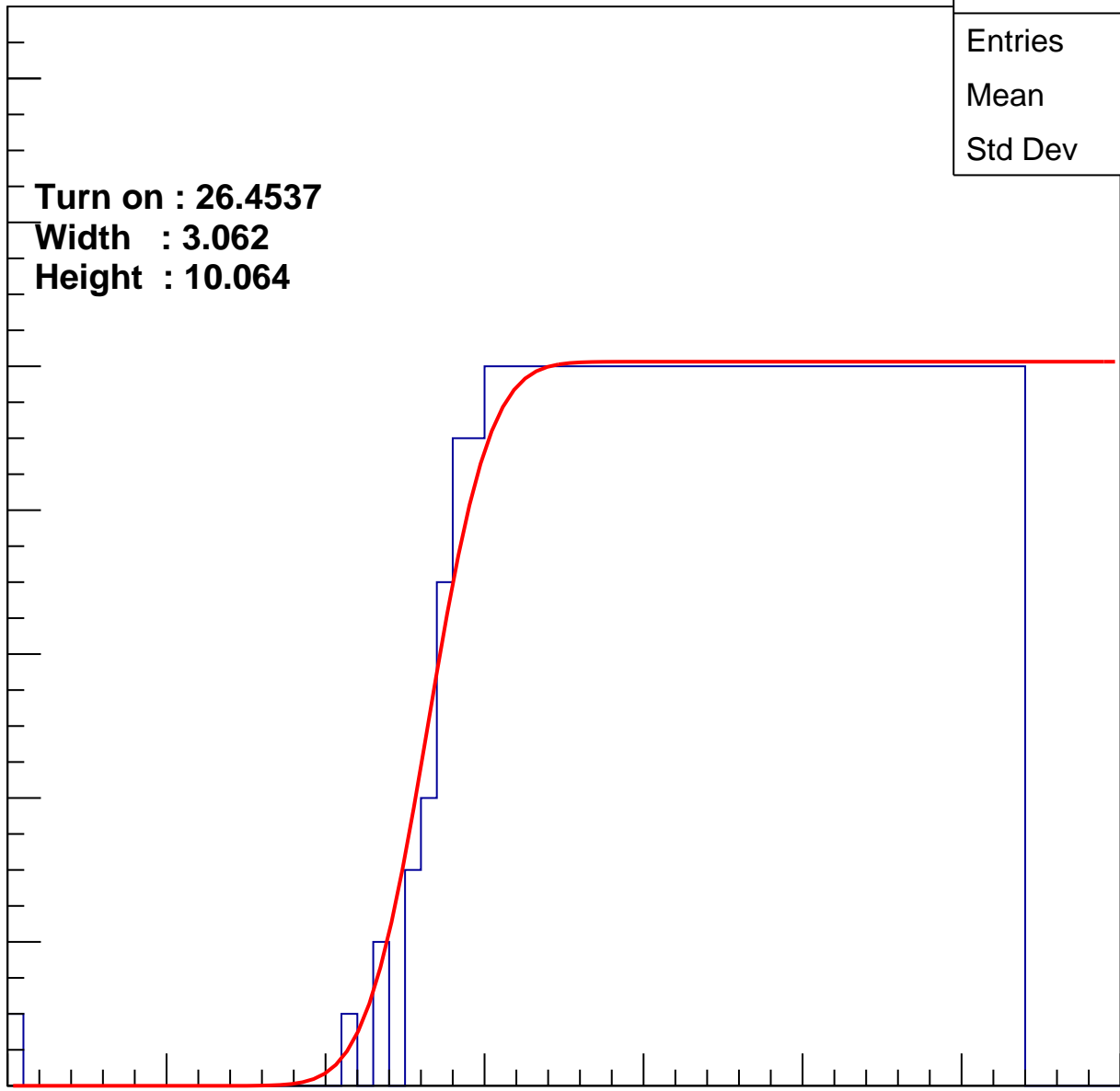
Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U11-ch93

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.1
Std Dev	11.67

Turn on : 26.6934

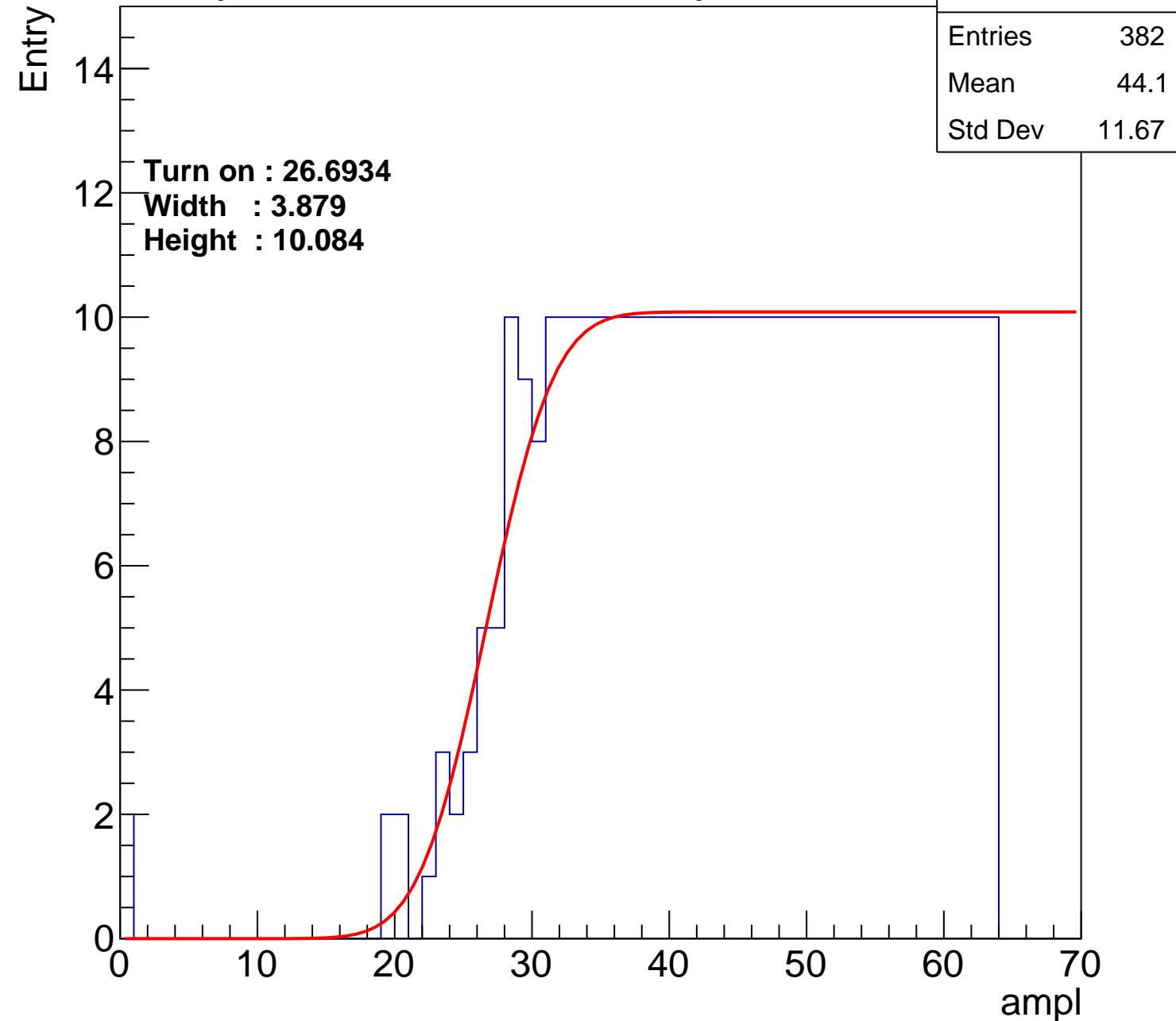
Width : 3.879

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch94

calib_packv5_042523_0143.root, FC#5, port B1

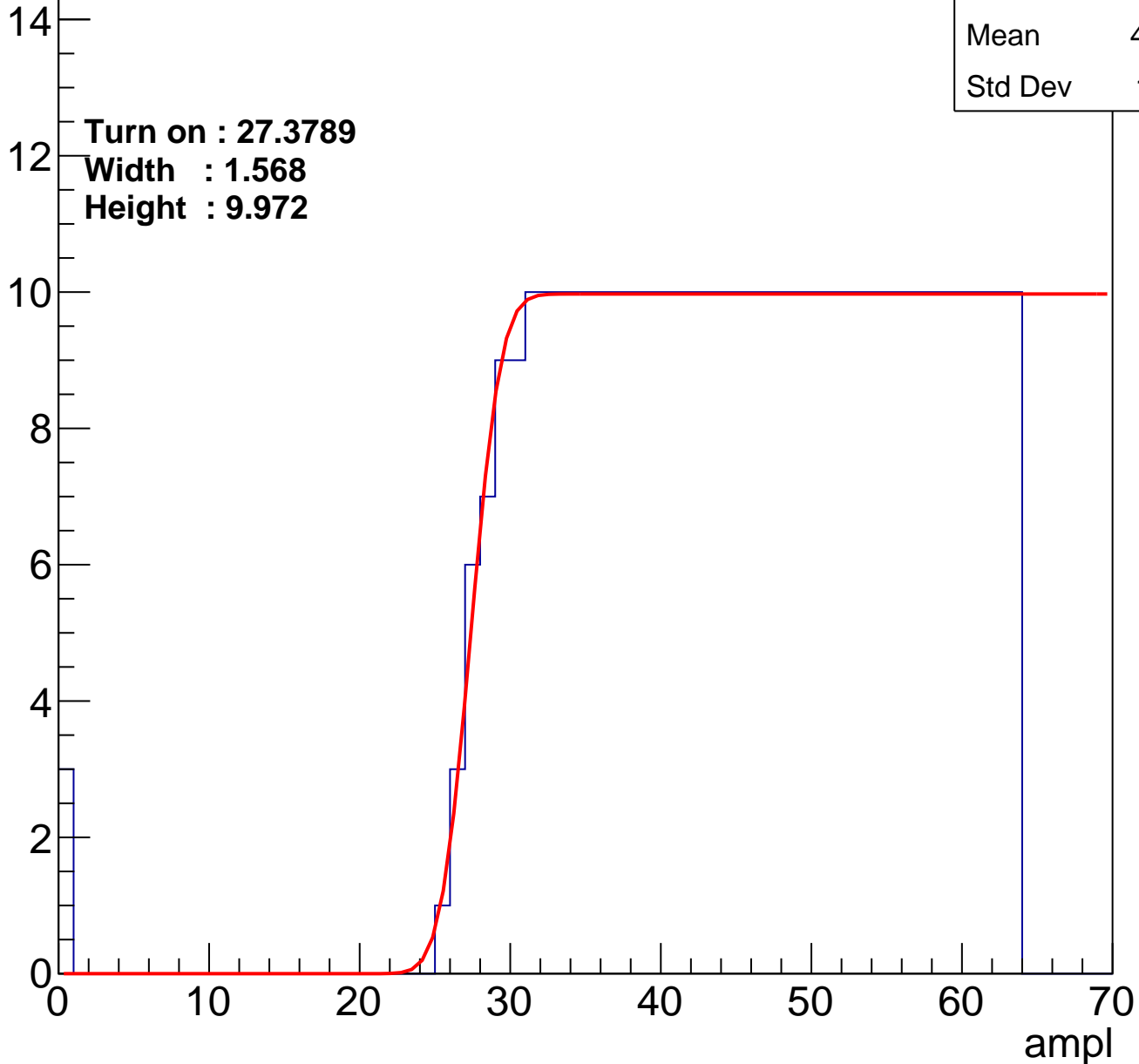
Entries	368
Mean	44.84
Std Dev	11.31

Turn on : 27.3789

Width : 1.568

Height : 9.972

Entry



B0L000S, U11-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.64
Std Dev	11.6

Turn on : 27.0688

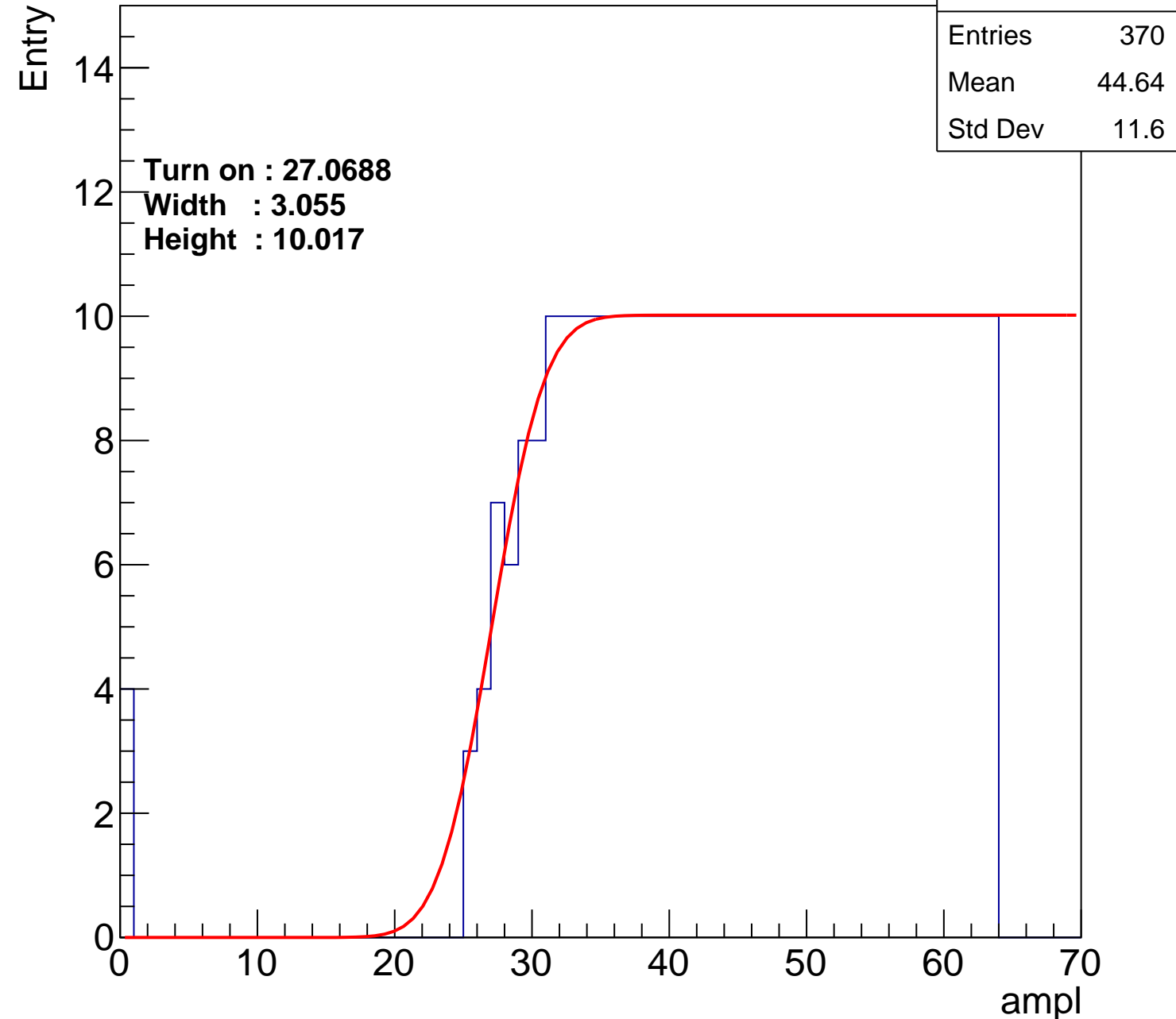
Width : 3.055

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.15
Std Dev	11.05

Turn on : 27.9099

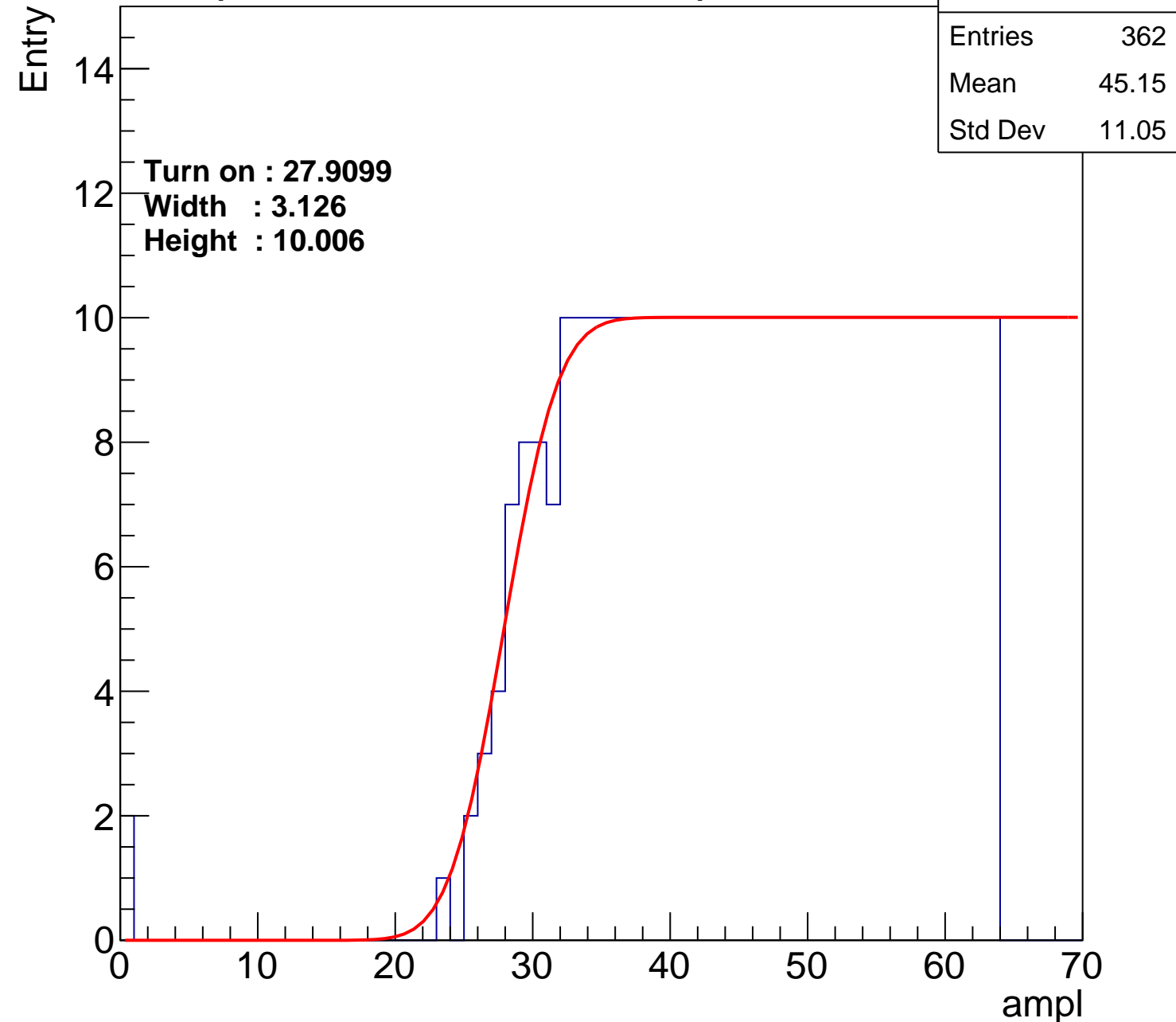
Width : 3.126

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch97

calib_packv5_042523_0143.root, FC#5, port B1

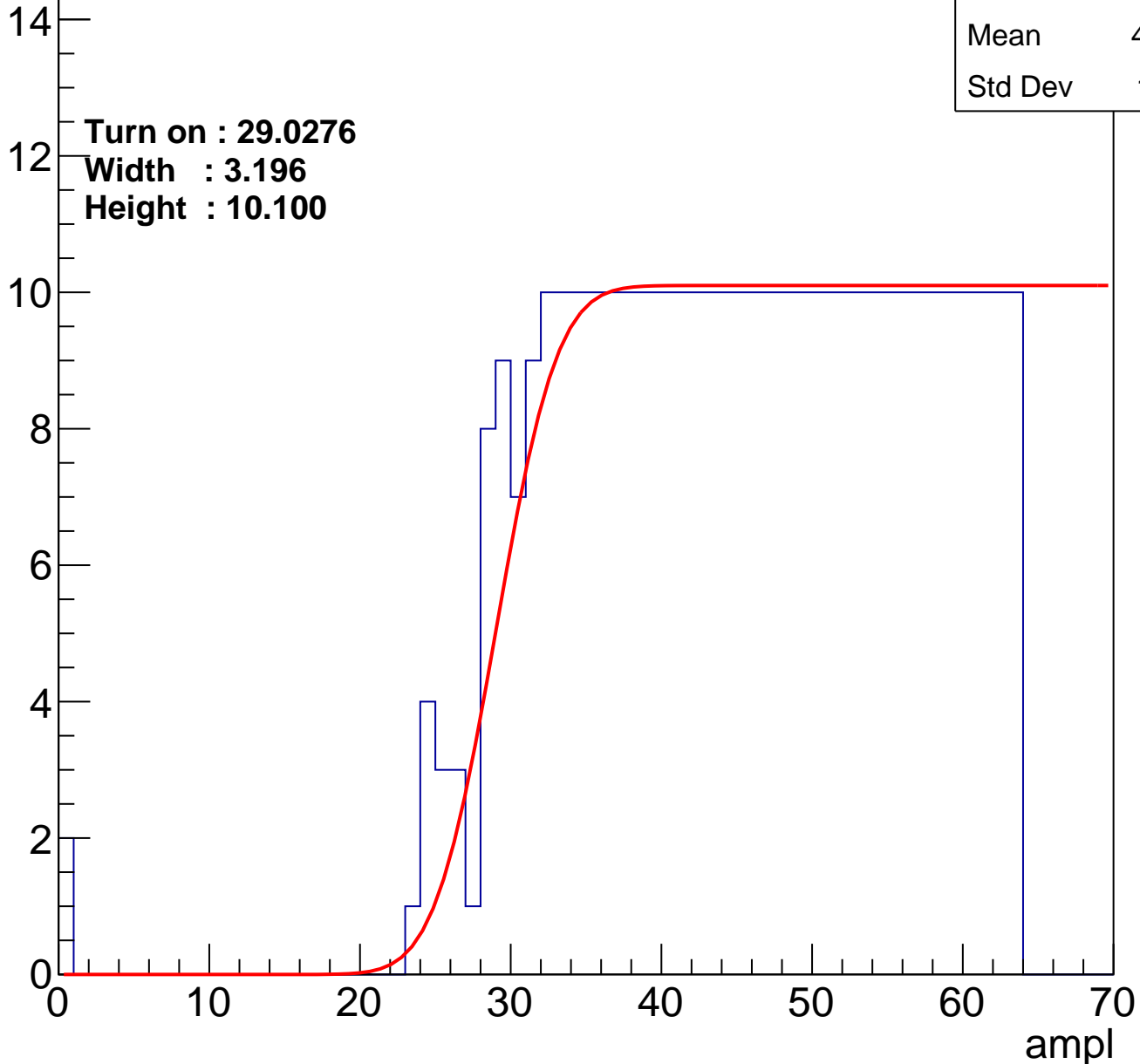
Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 29.0276

Width : 3.196

Height : 10.100

Entry



B0L000S, U11-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.74
Std Dev	11.82

Turn on : 25.9311

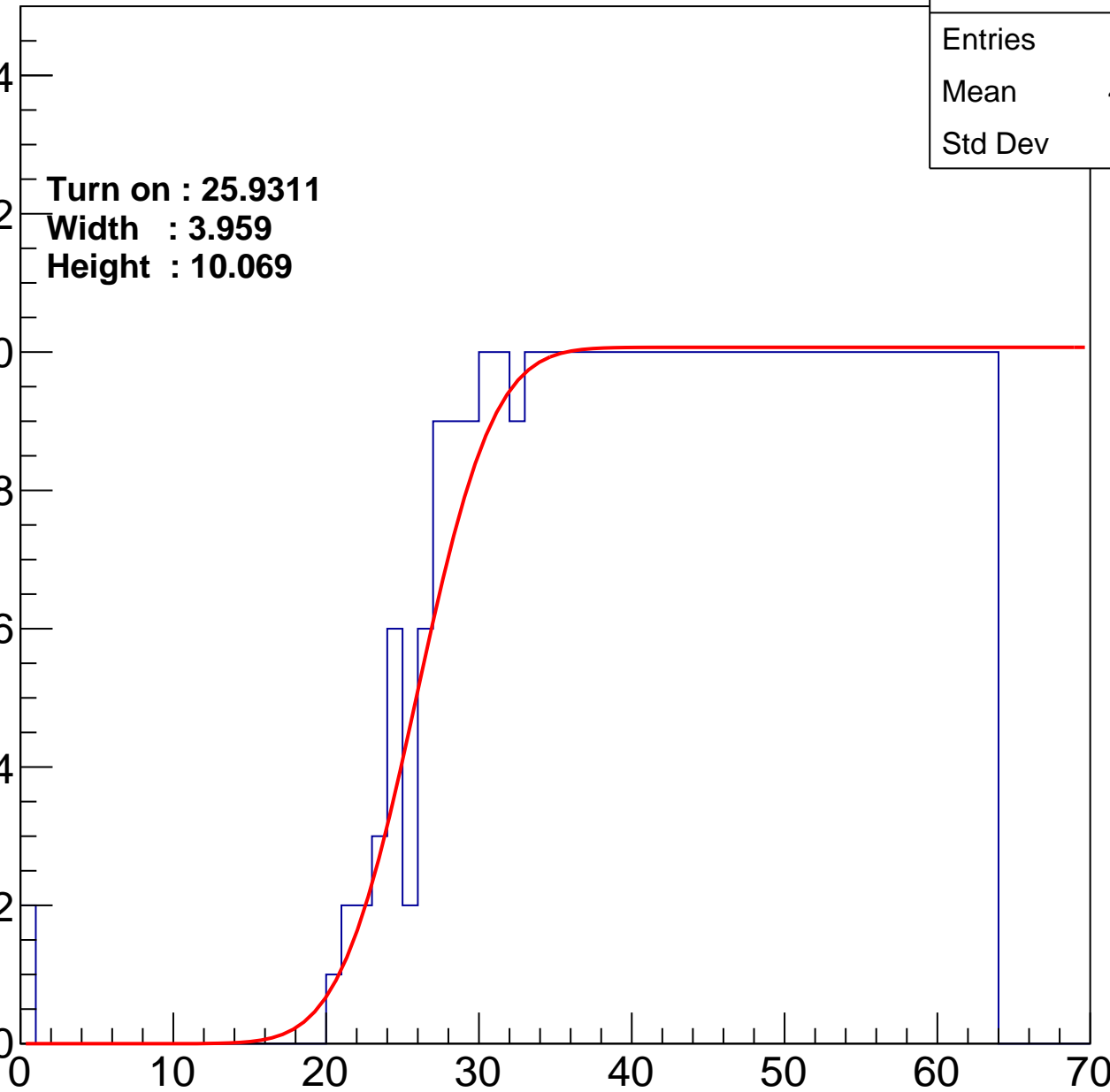
Width : 3.959

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.66
Std Dev	11.17

Turn on : 27.2767

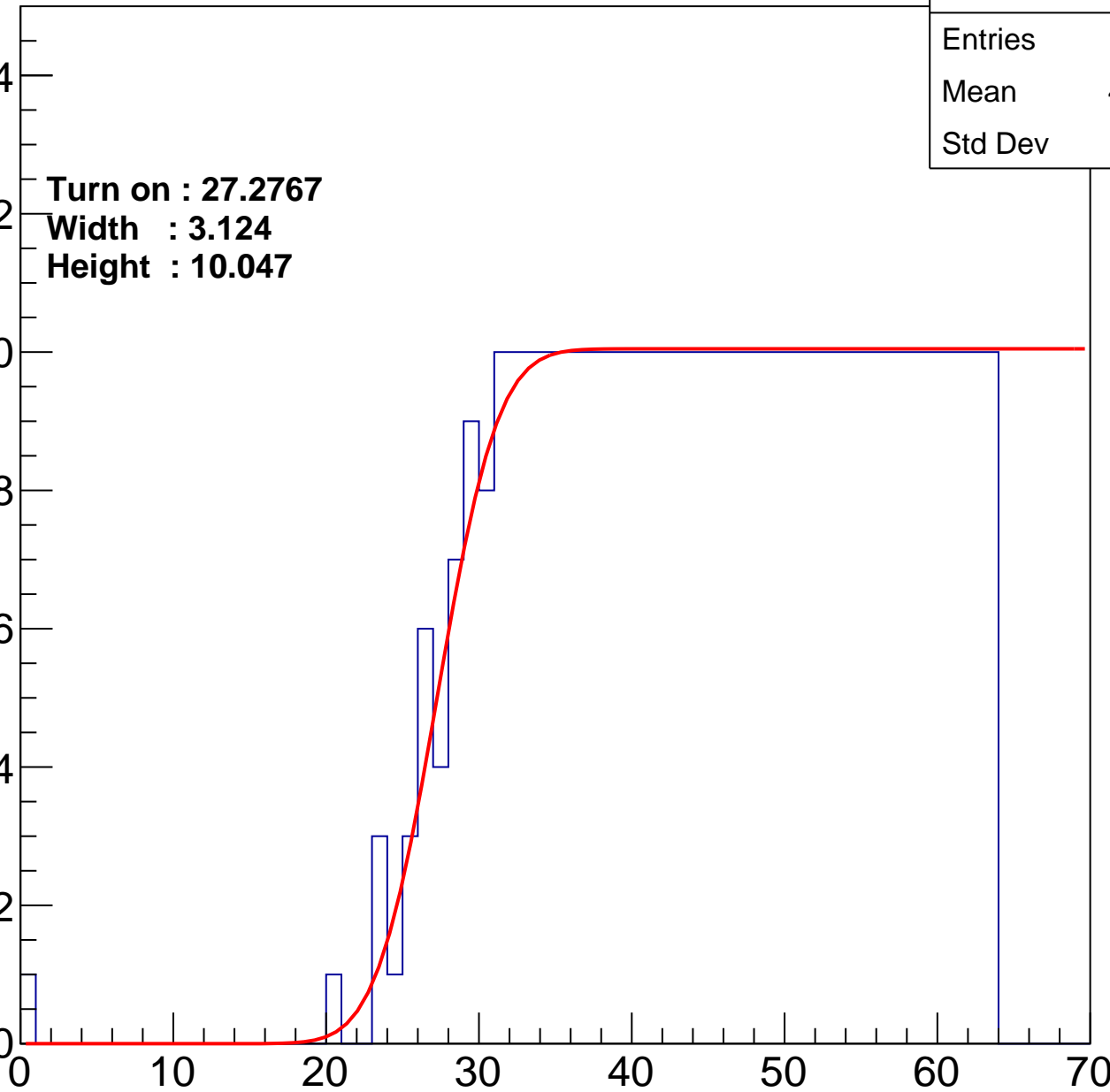
Width : 3.124

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch100

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.39
Std Dev	11.65

Turn on : 27.0707

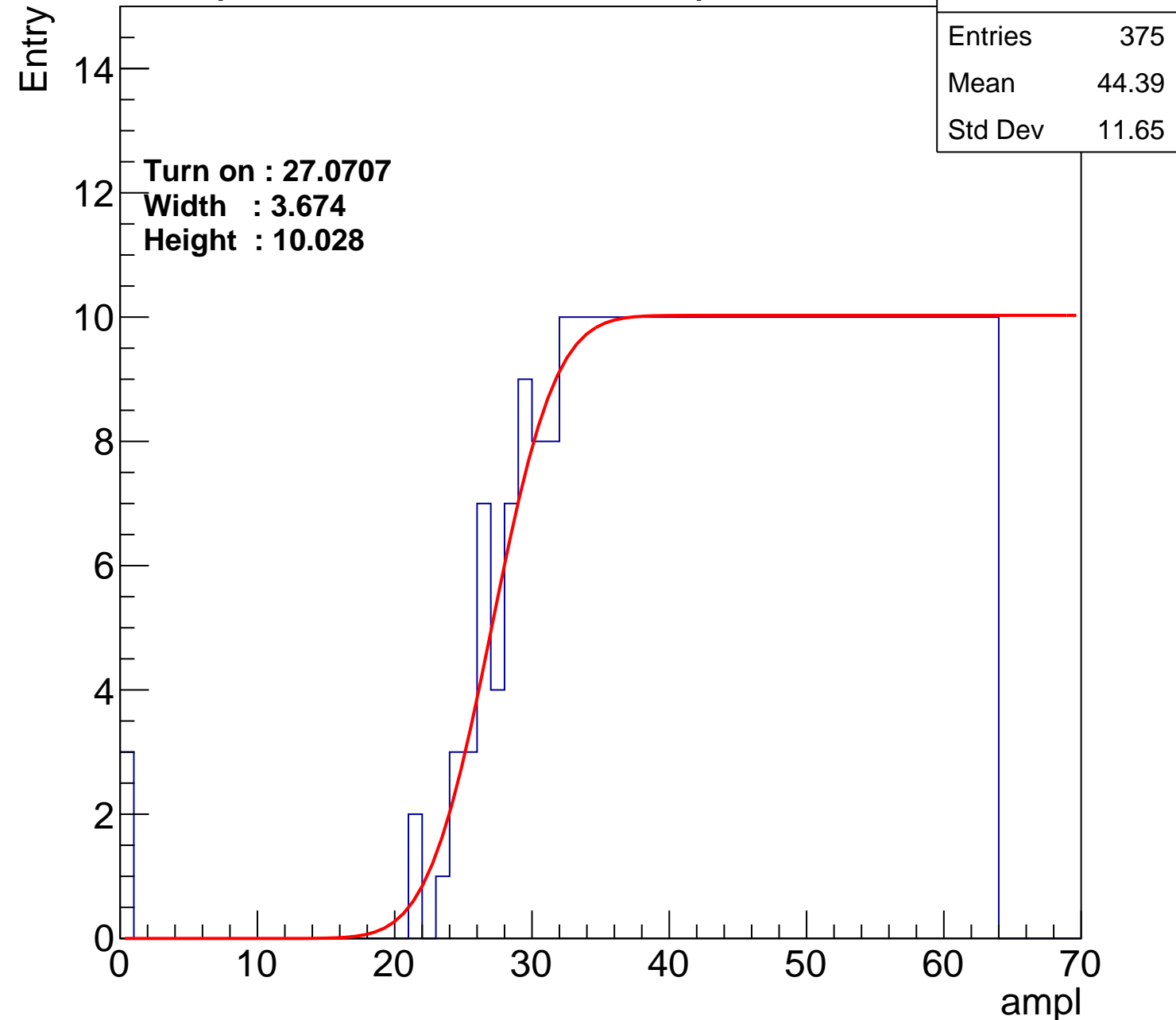
Width : 3.674

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.8388

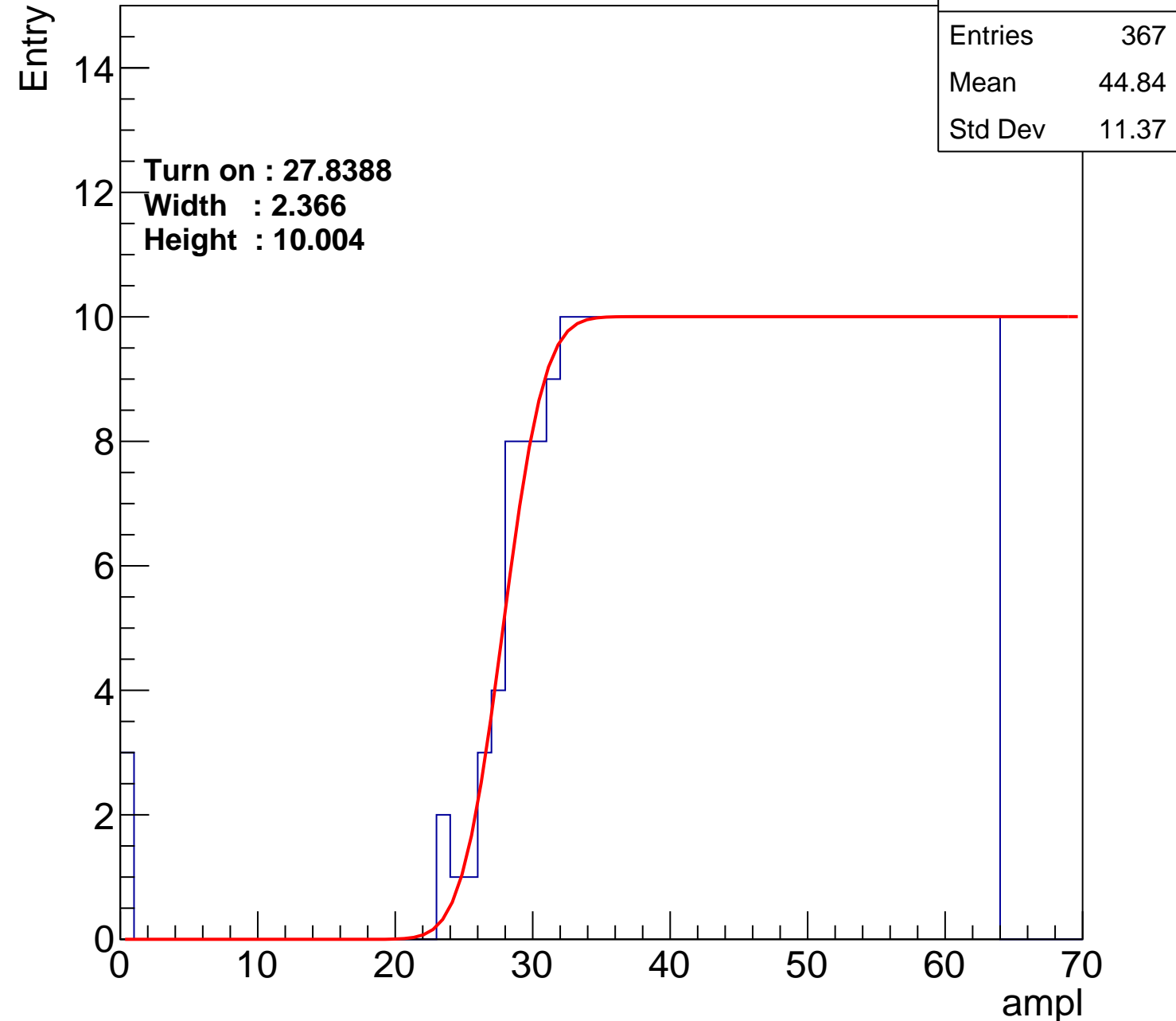
Width : 2.366

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.29
Std Dev	10.79

Turn on : 28.0766

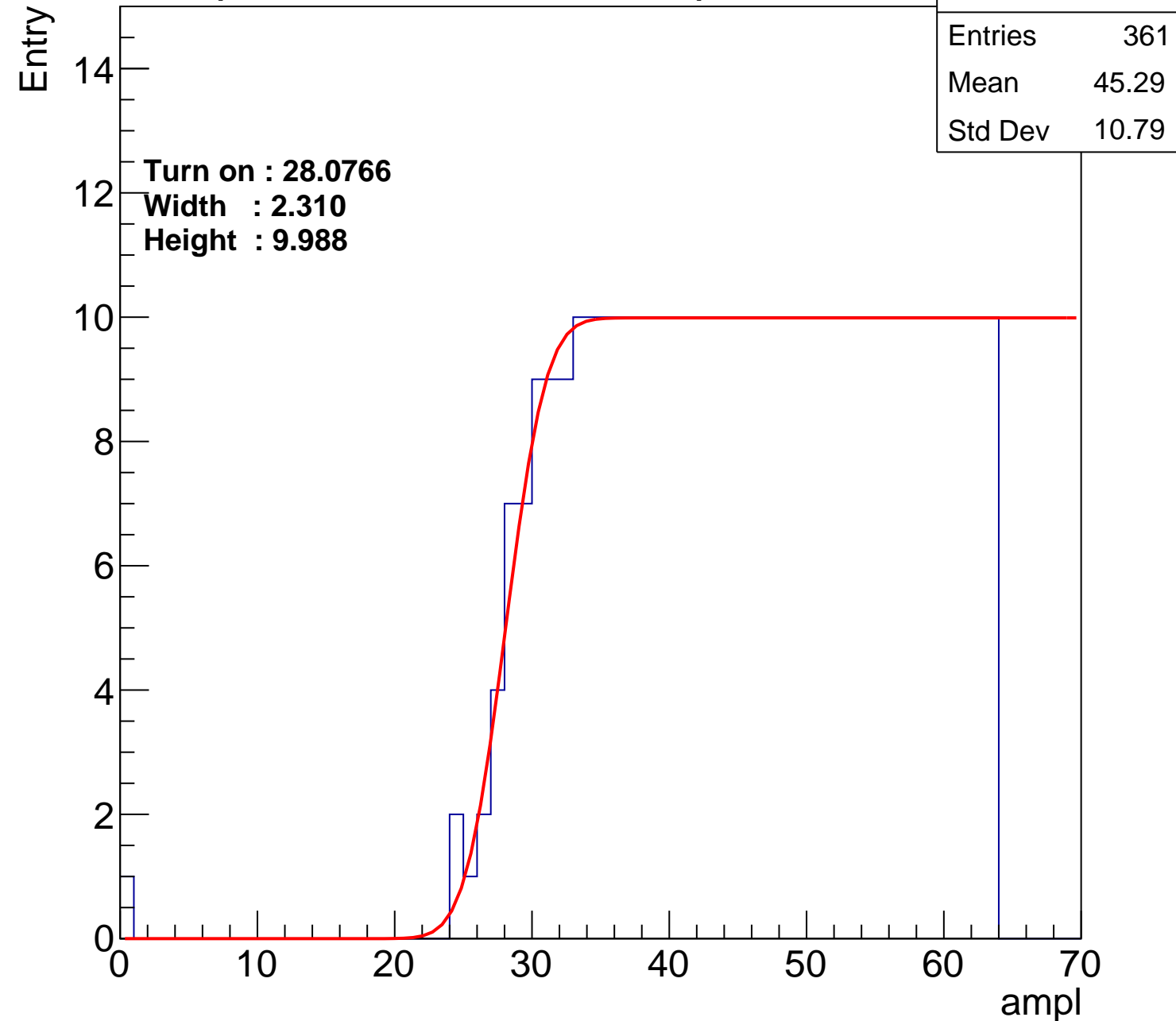
Width : 2.310

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.9408

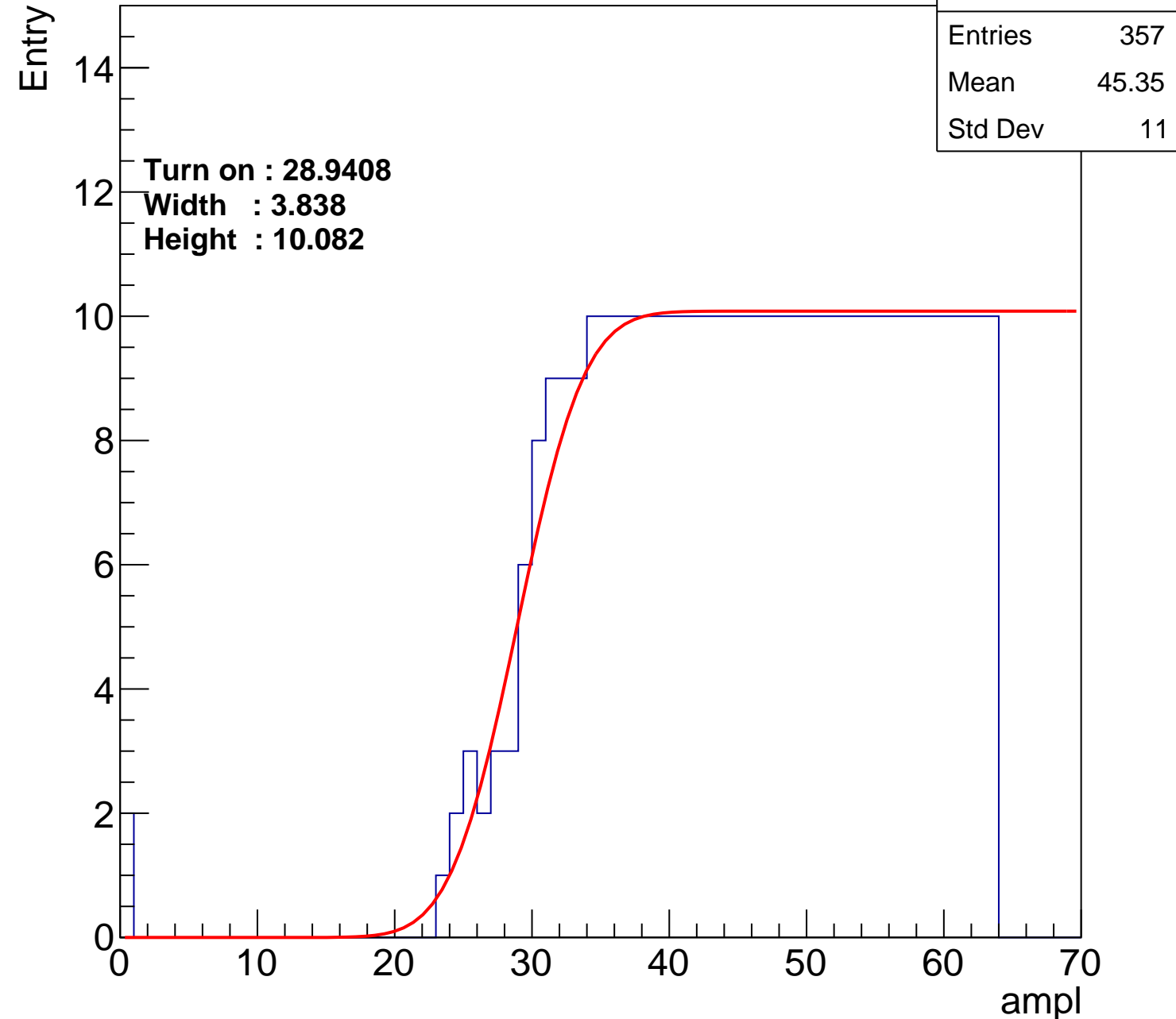
Width : 3.838

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch104

calib_packv5_042523_0143.root, FC#5, port B1

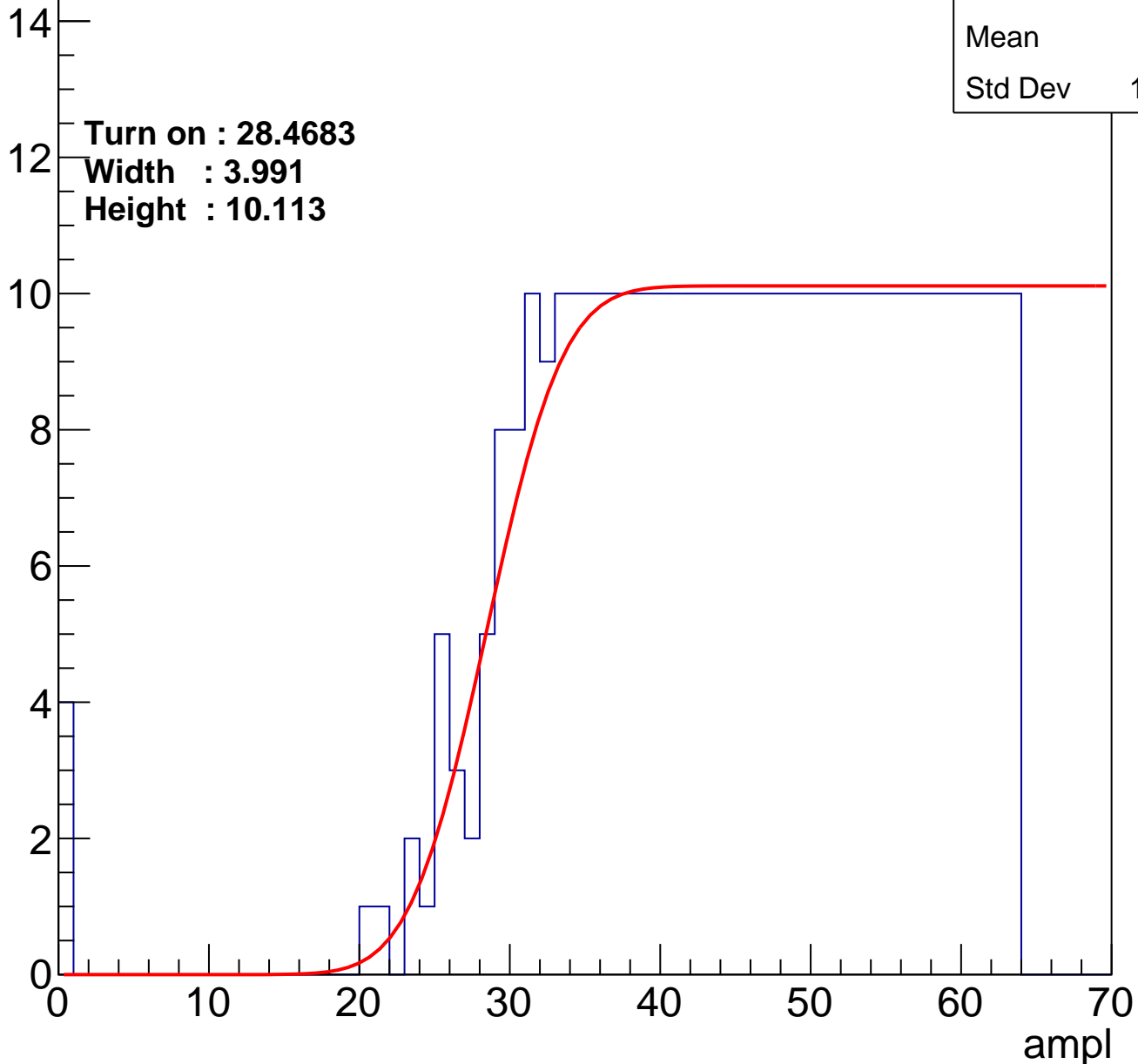
Entries	369
Mean	44.6
Std Dev	11.73

Turn on : 28.4683

Width : 3.991

Height : 10.113

Entry



B0L000S, U11-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.48
Std Dev	11.31

Turn on : 27.2385

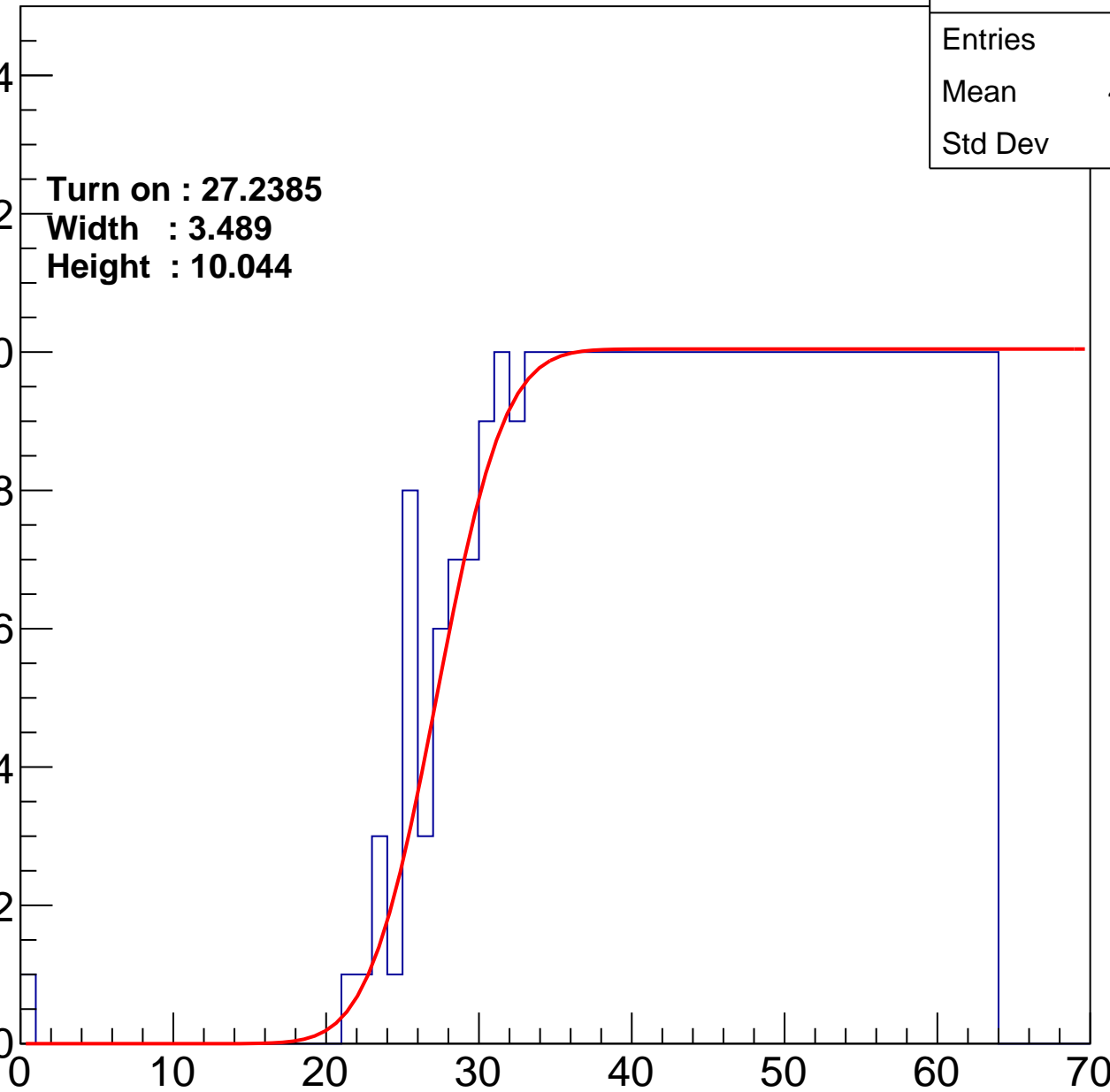
Width : 3.489

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.09
Std Dev	11.92

Turn on : 26.0287

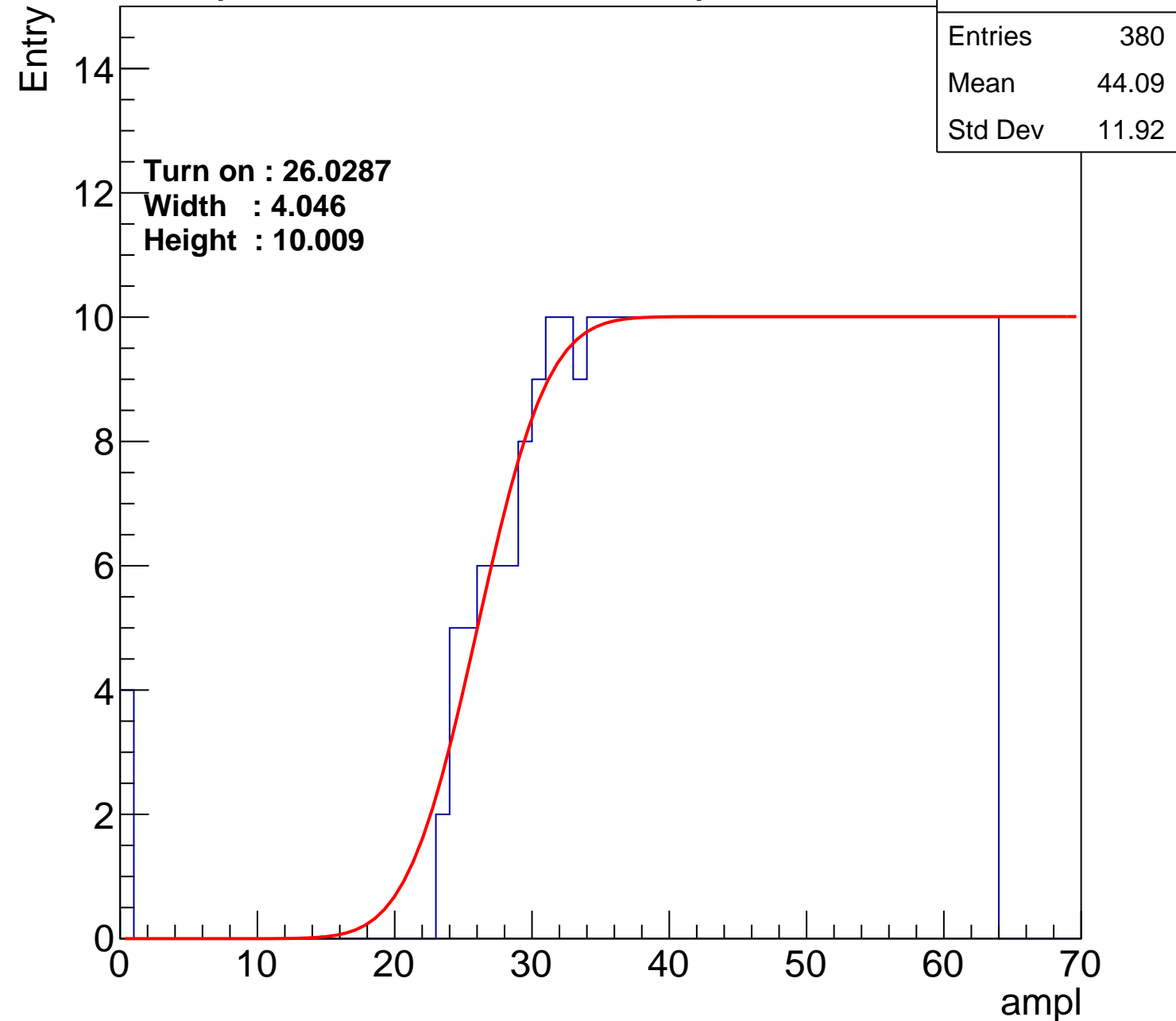
Width : 4.046

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch107

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.5609

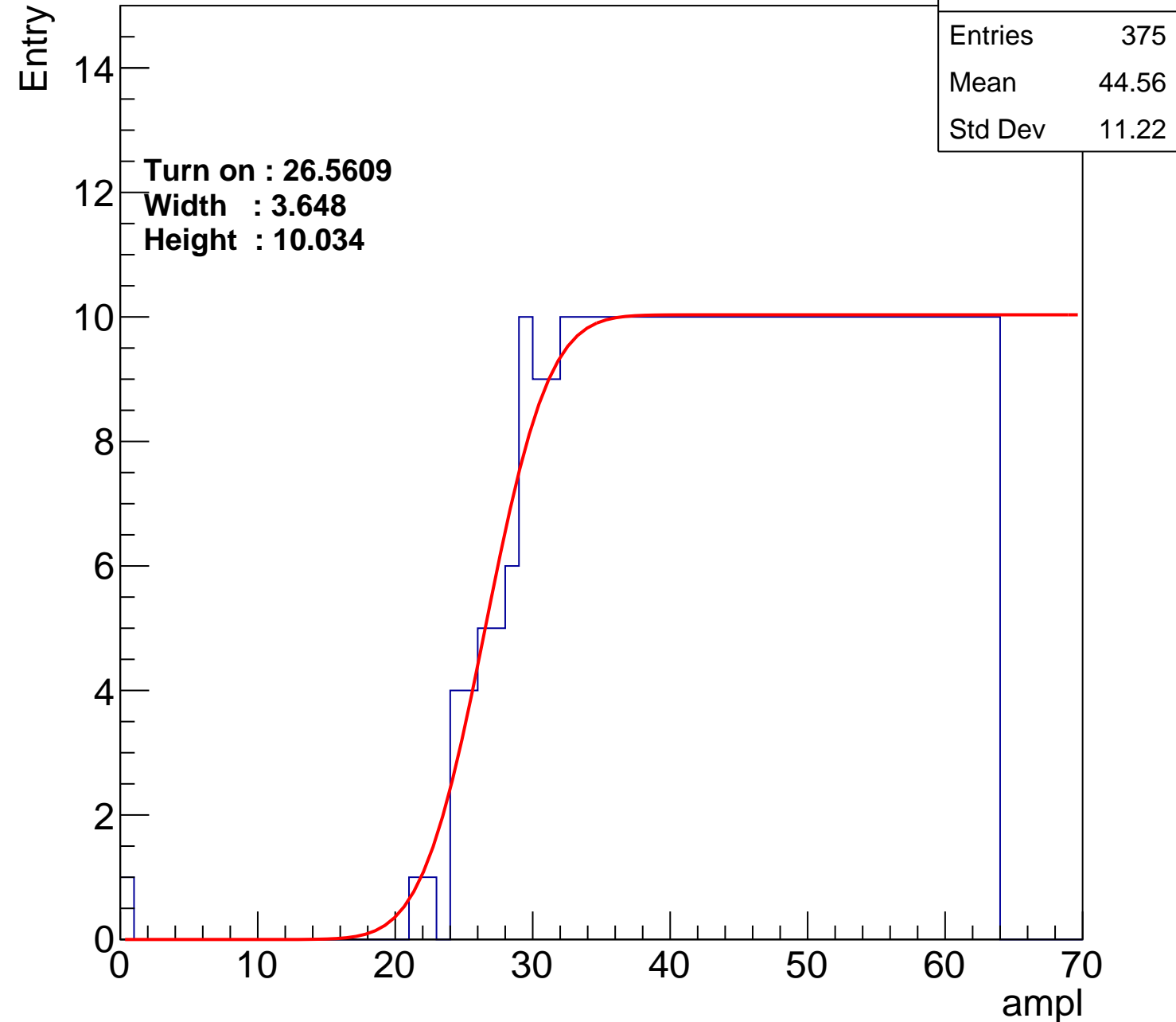
Width : 3.648

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch108

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.38
Std Dev	11.92

Turn on : 27.4391

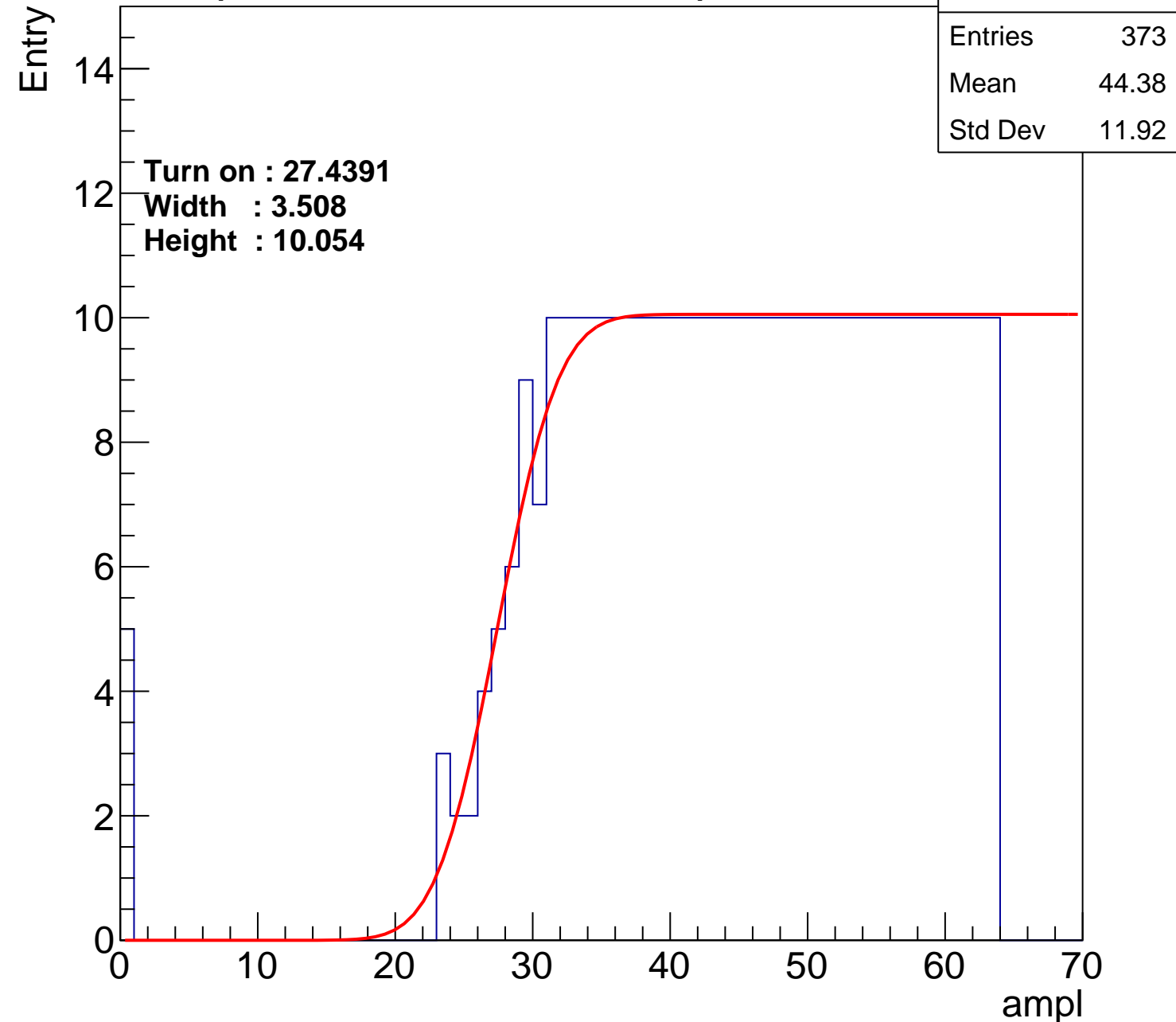
Width : 3.508

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.5
Std Dev	11.36

Turn on : 27.0894

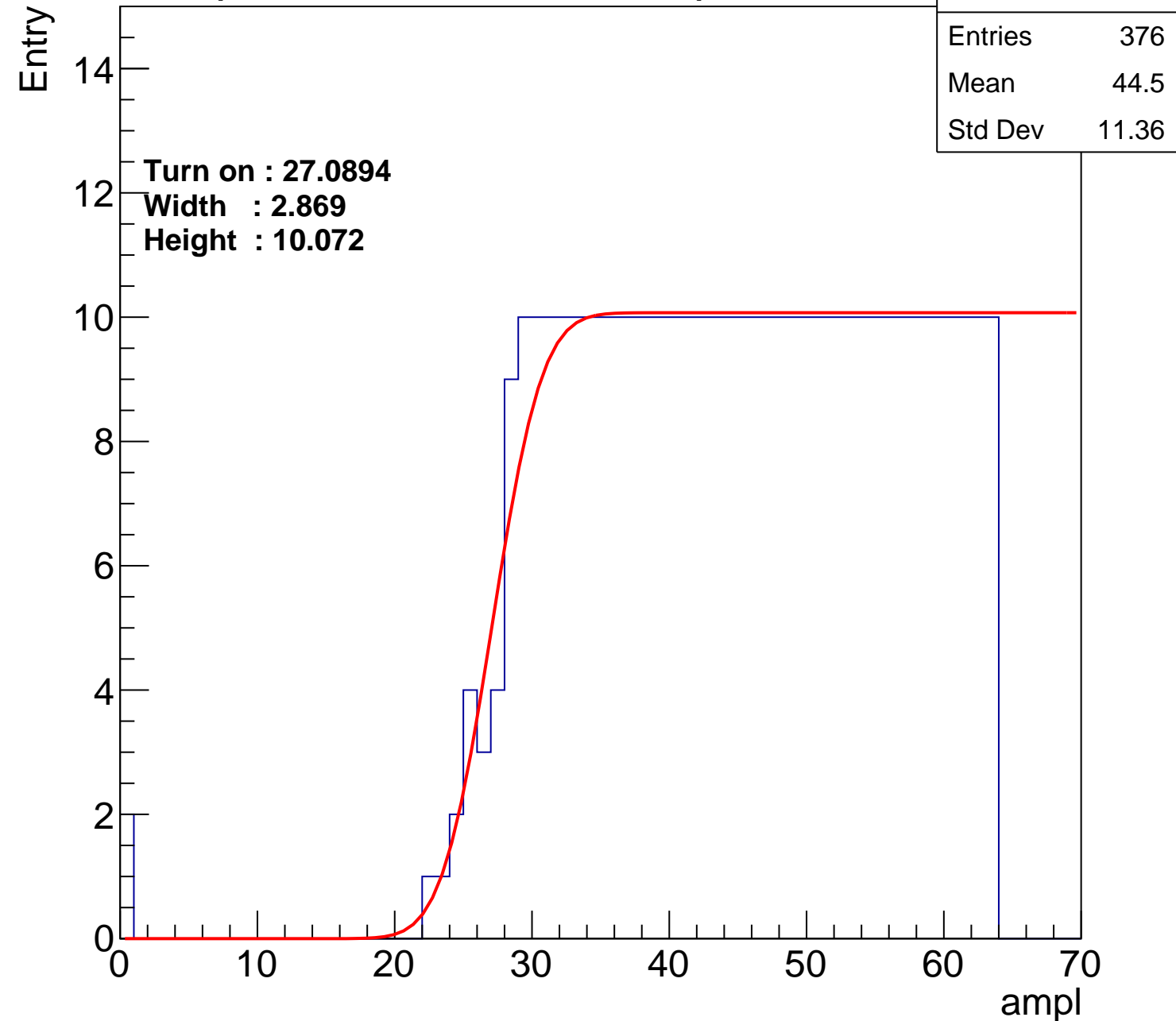
Width : 2.869

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch110

calib_packv5_042523_0143.root, FC#5, port B1

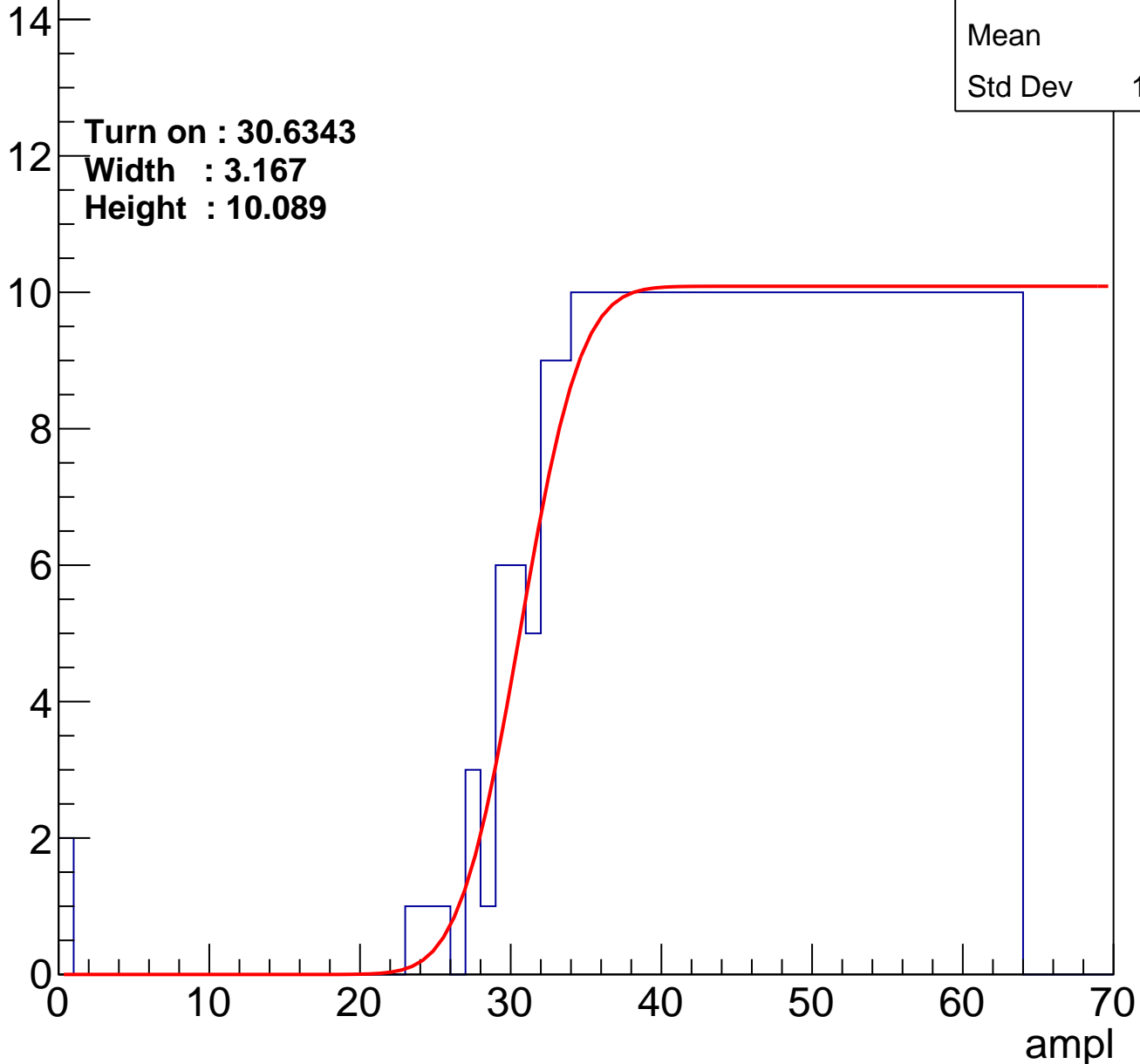
Entries	344
Mean	46
Std Dev	10.66

Turn on : 30.6343

Width : 3.167

Height : 10.089

Entry



B0L000S, U11-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.16
Std Dev	10.89

Turn on : 27.9016

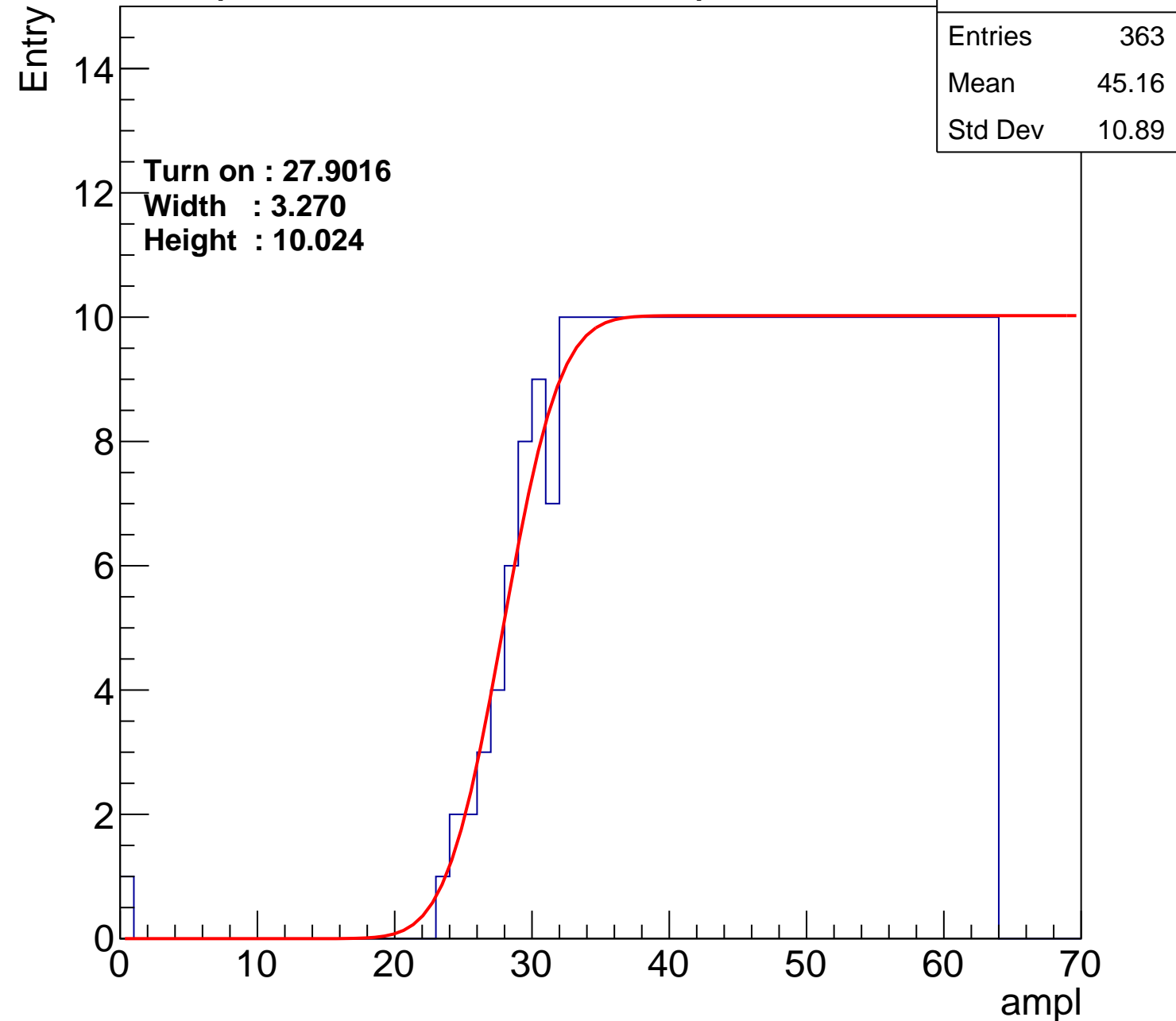
Width : 3.270

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch112

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.2597

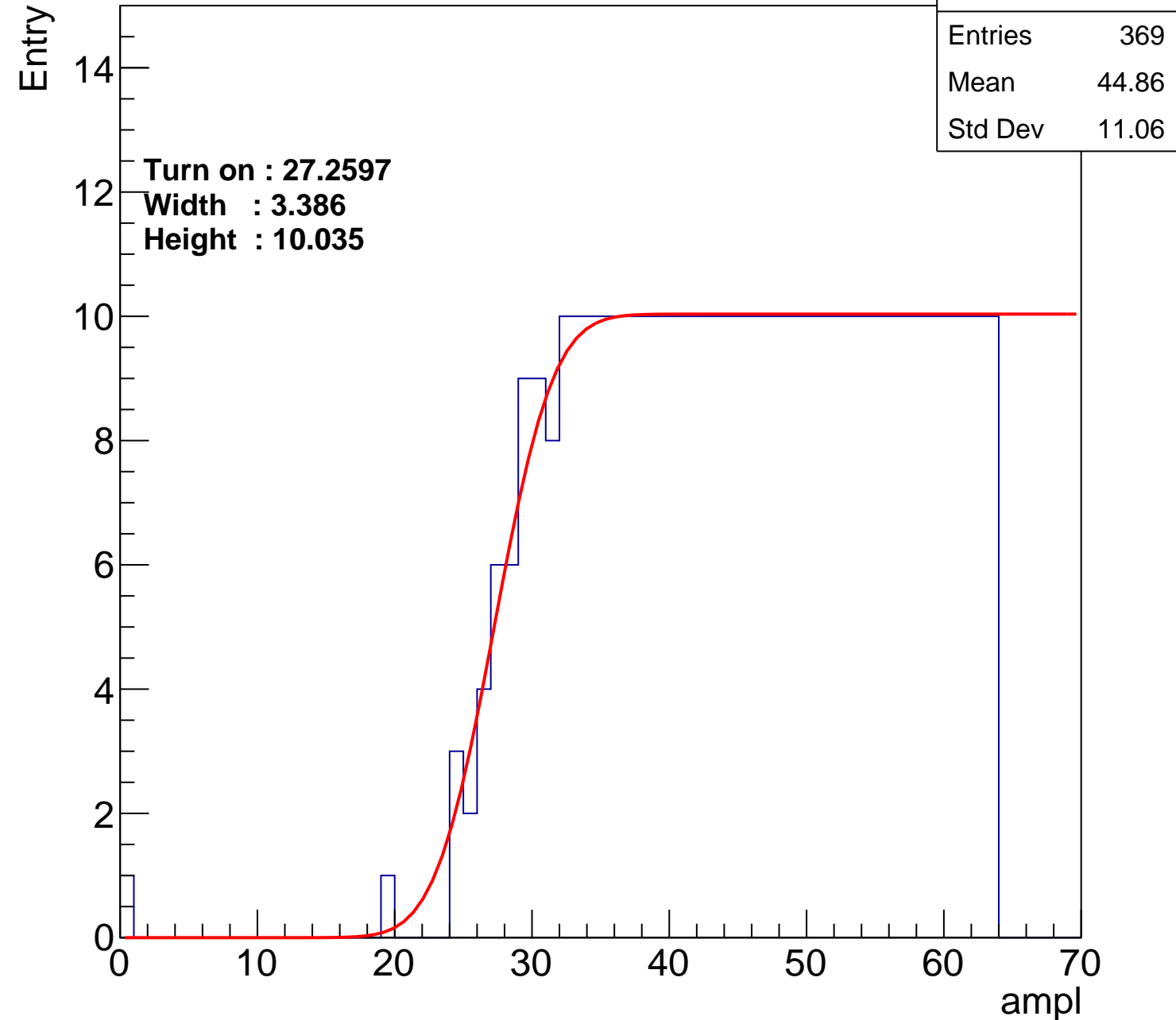
Width : 3.386

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.55
Std Dev	11.56

Turn on : 27.2147

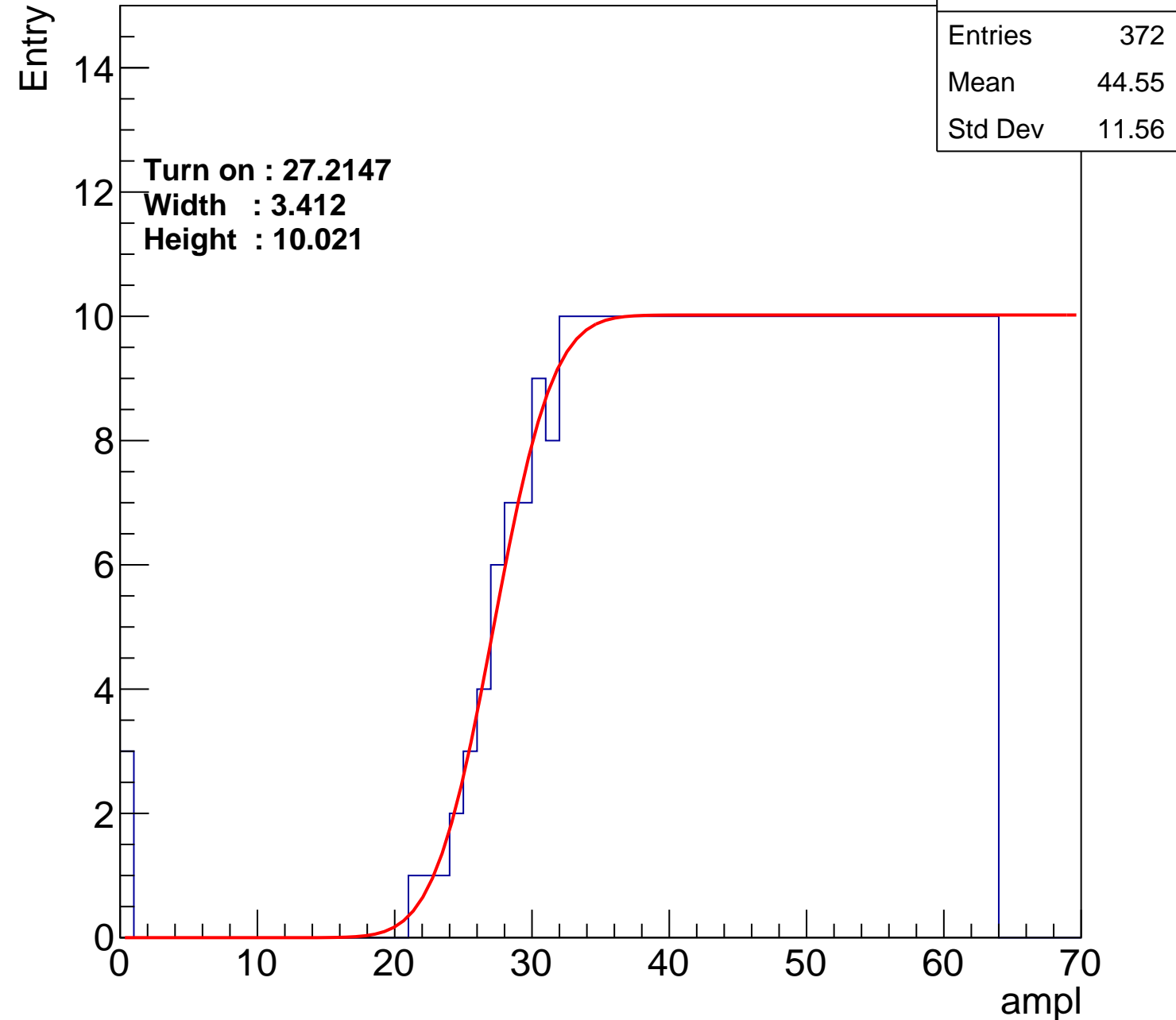
Width : 3.412

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch114

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.24
Std Dev	11.68

Turn on : 27.0491

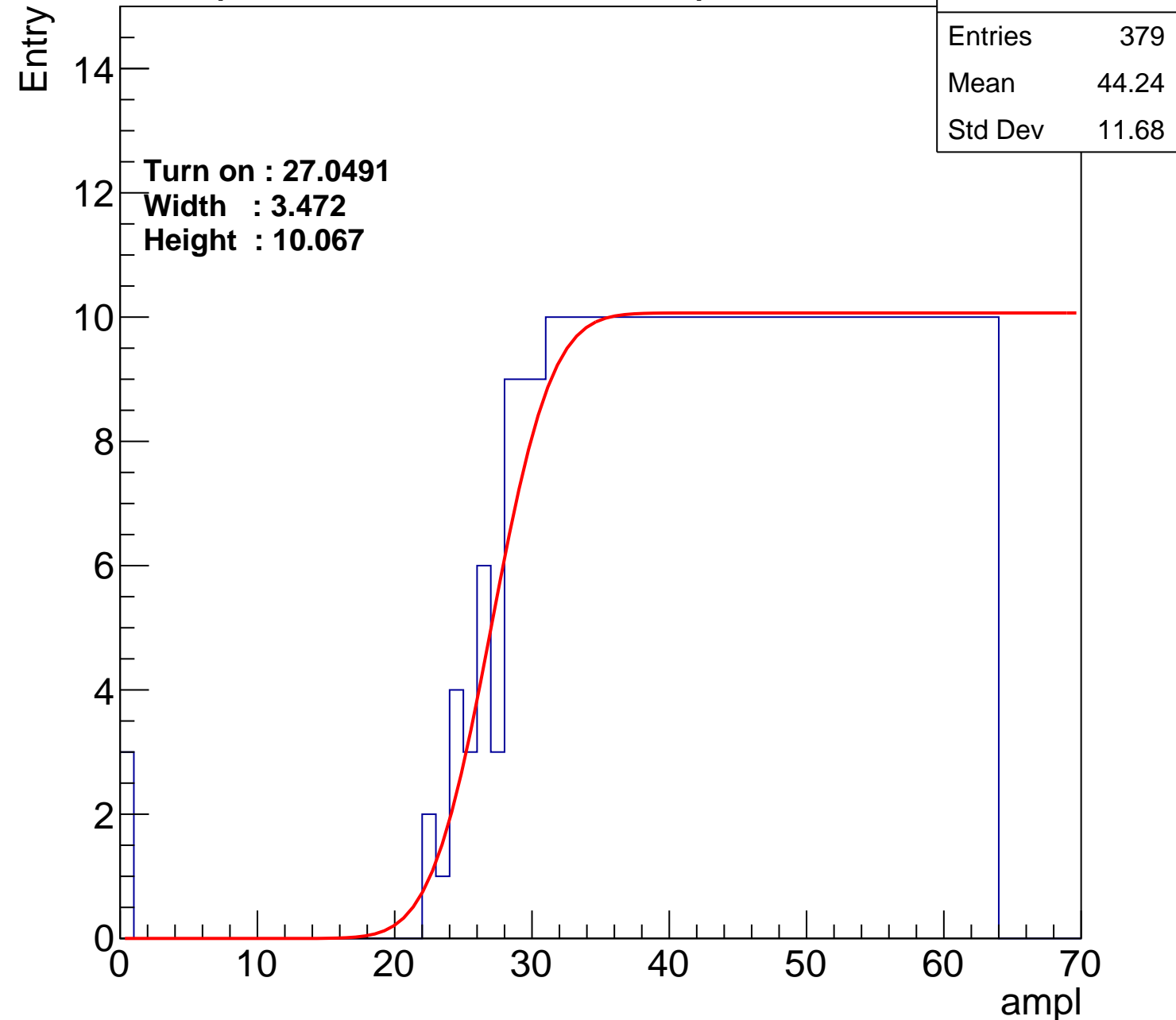
Width : 3.472

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
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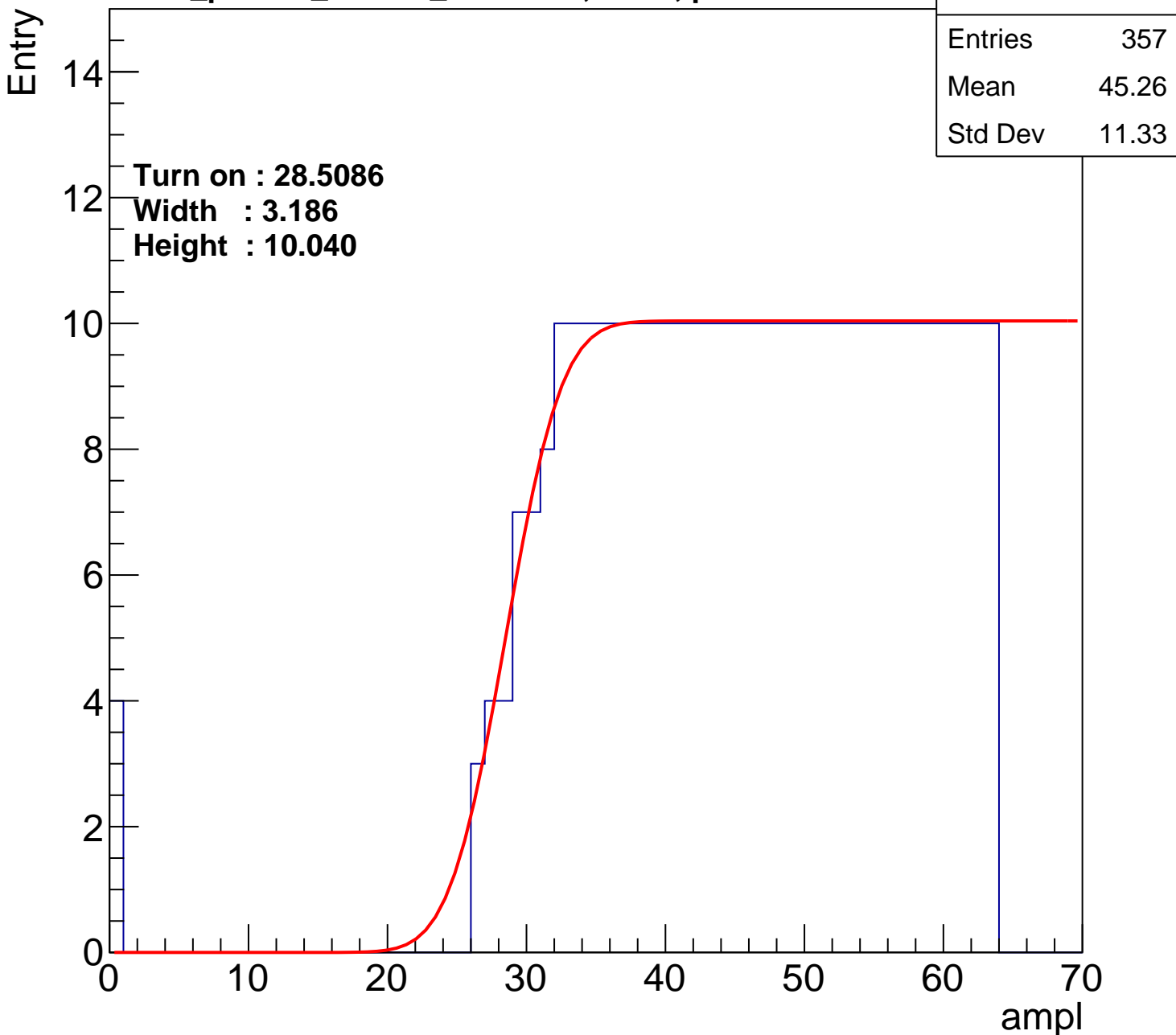
Mean	45.26
------	-------

Std Dev	11.33
---------	-------

Turn on : 28.5086

Width : 3.186

Height : 10.040



B0L000S, U11-ch116

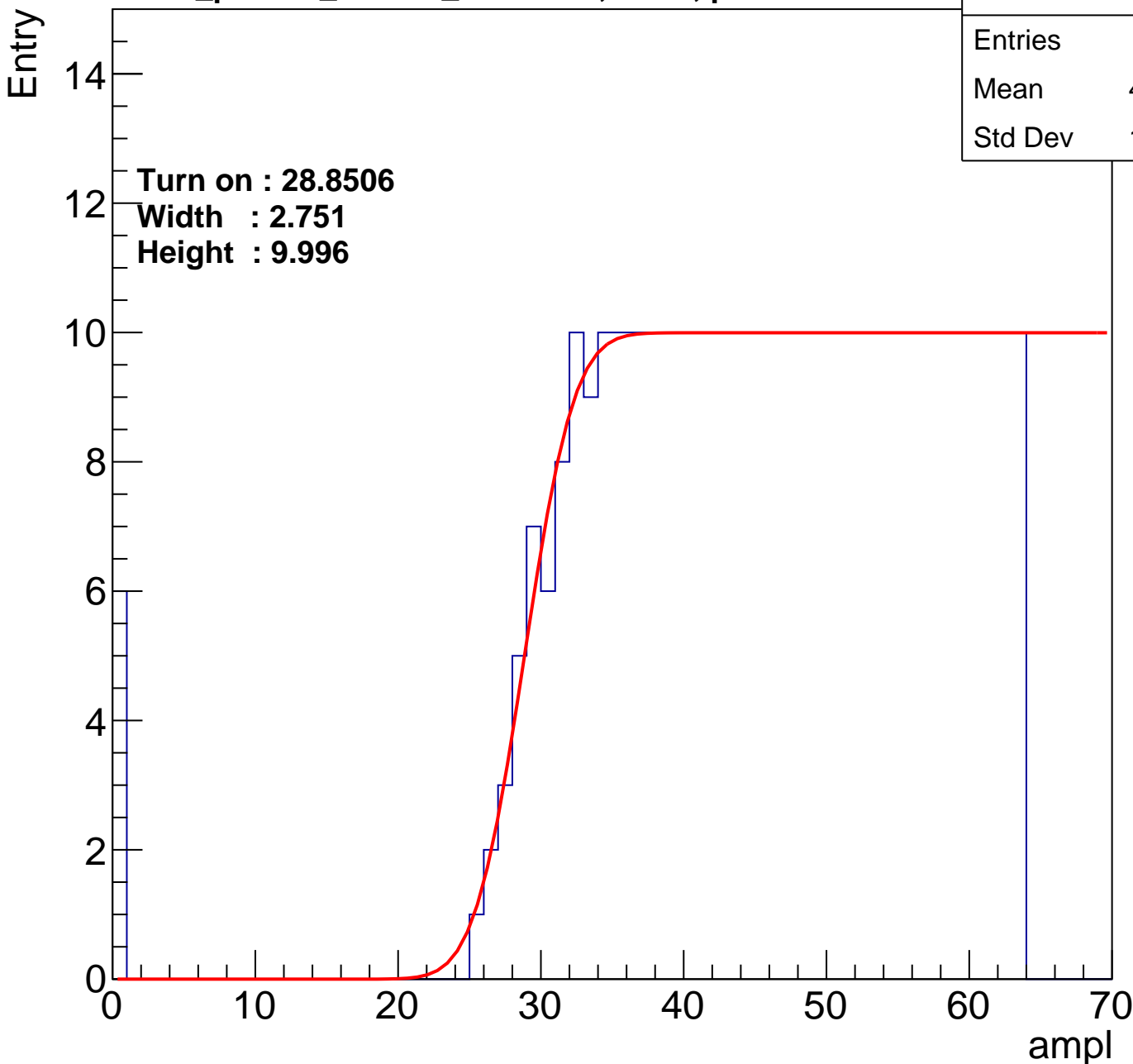
calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.09
Std Dev	11.78

Turn on : 28.8506

Width : 2.751

Height : 9.996



B0L000S, U11-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.9623

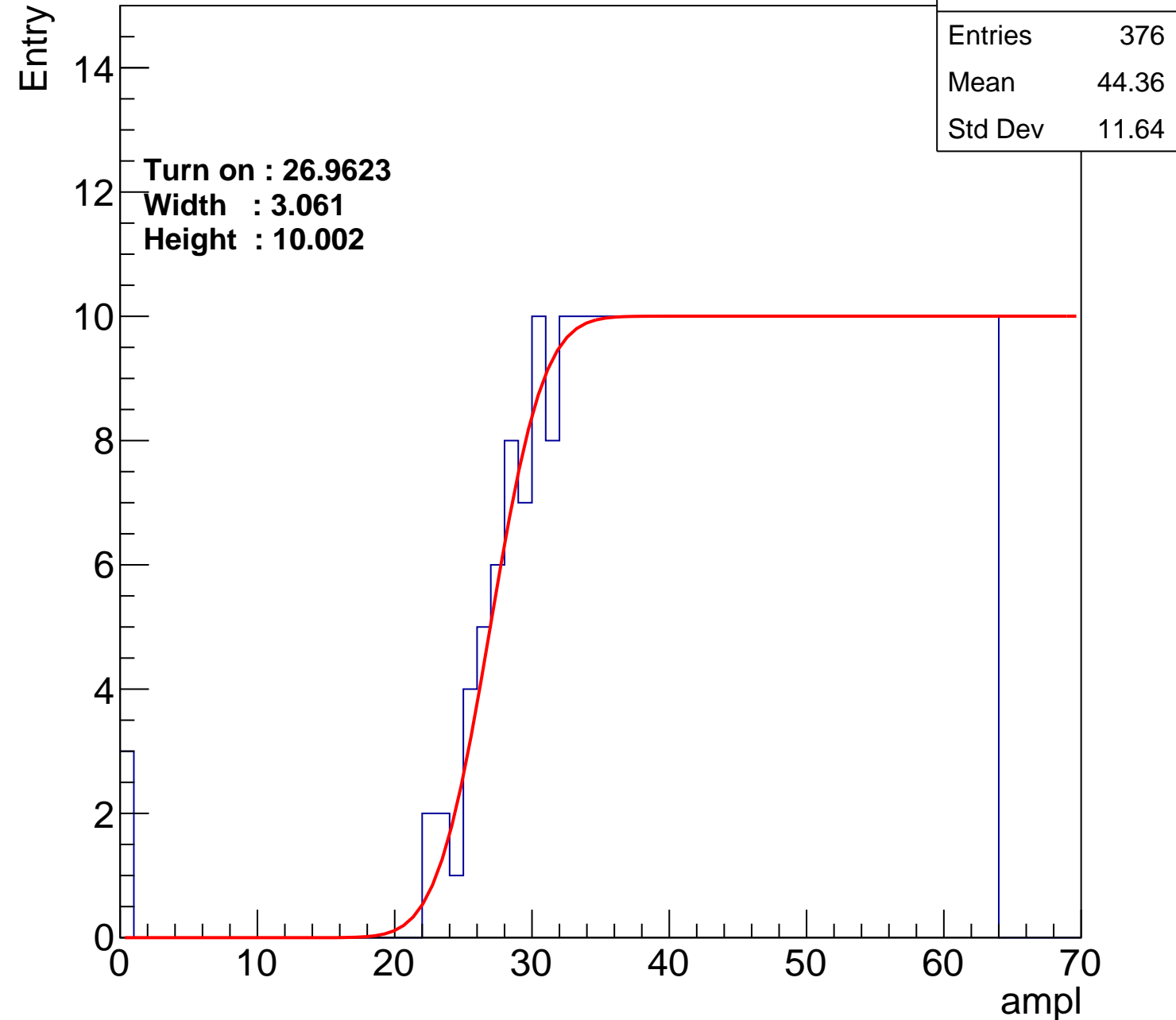
Width : 3.061

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.24
Std Dev	11.01

Turn on : 28.7789

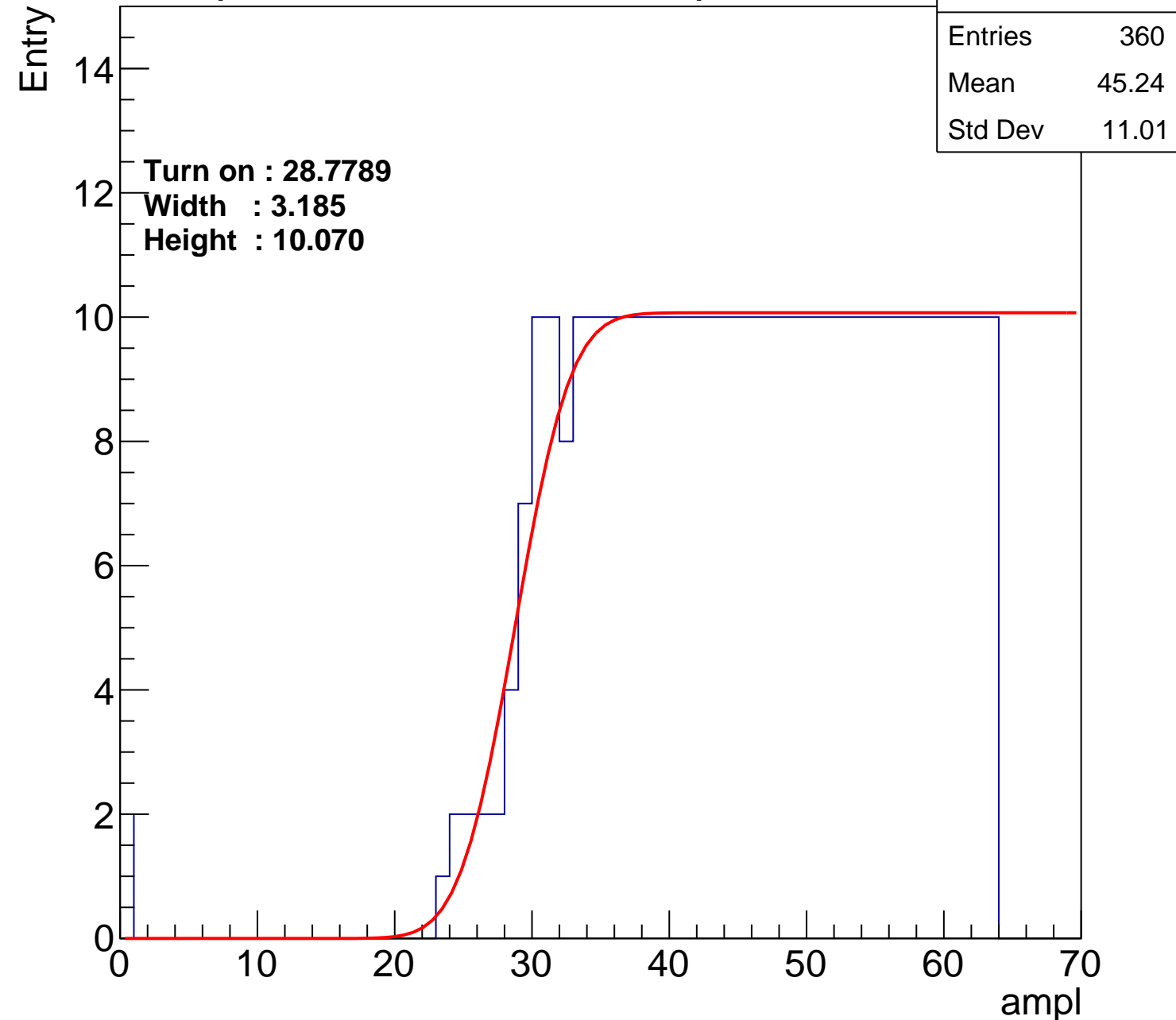
Width : 3.185

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.78
Std Dev	12.16

Turn on : 25.9111

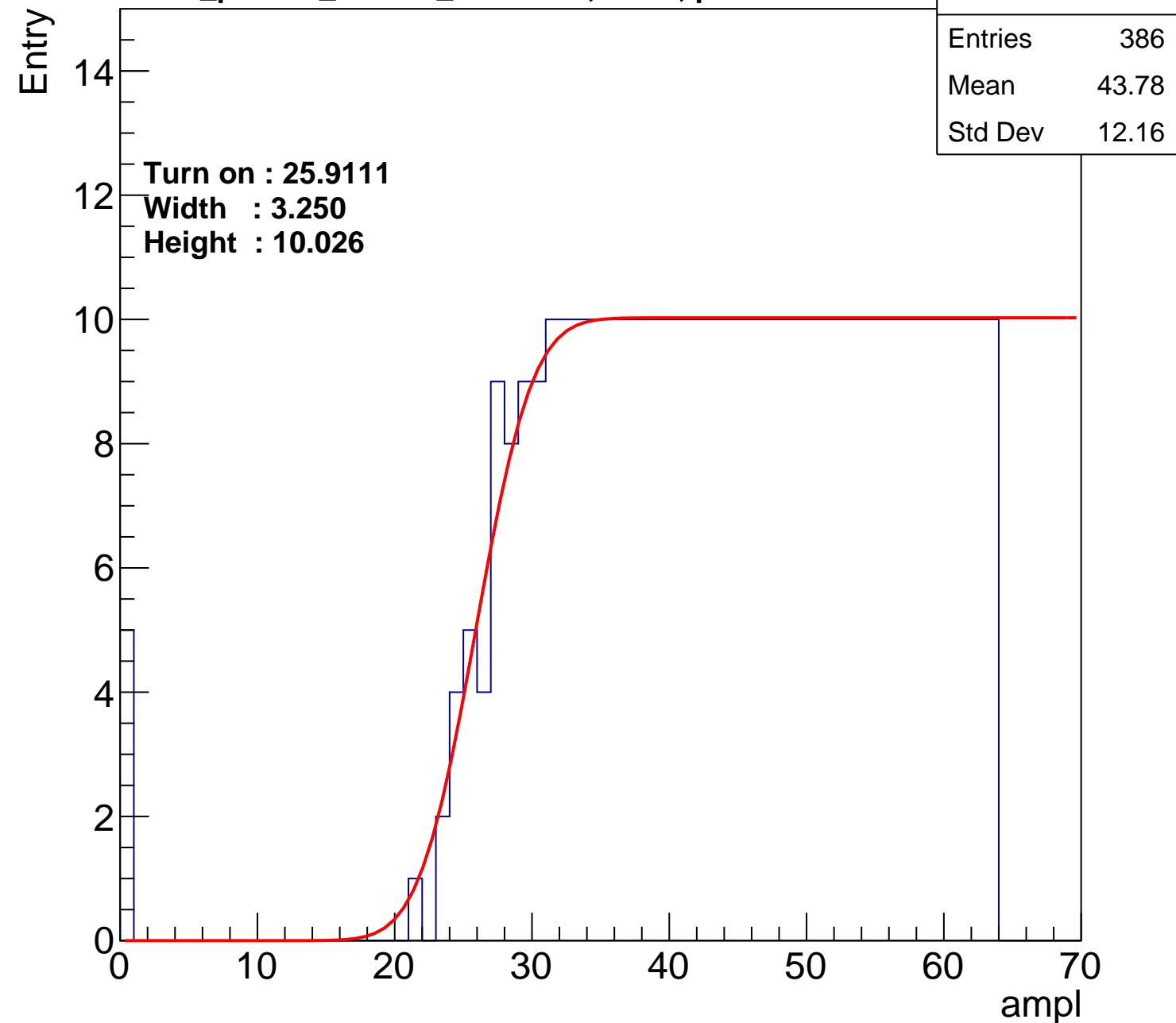
Width : 3.250

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.12
Std Dev	11.04

Turn on : 26.7104

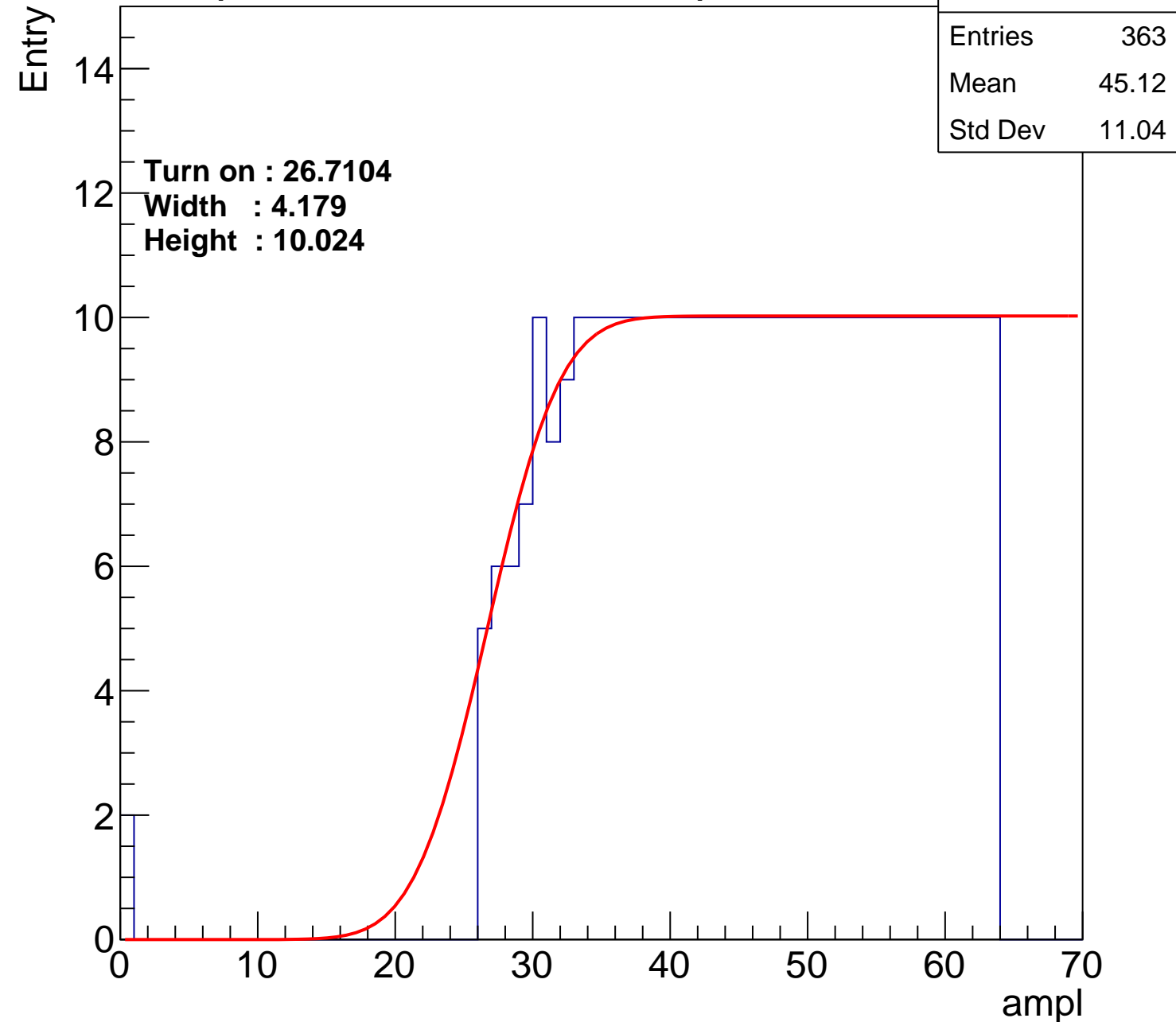
Width : 4.179

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	343
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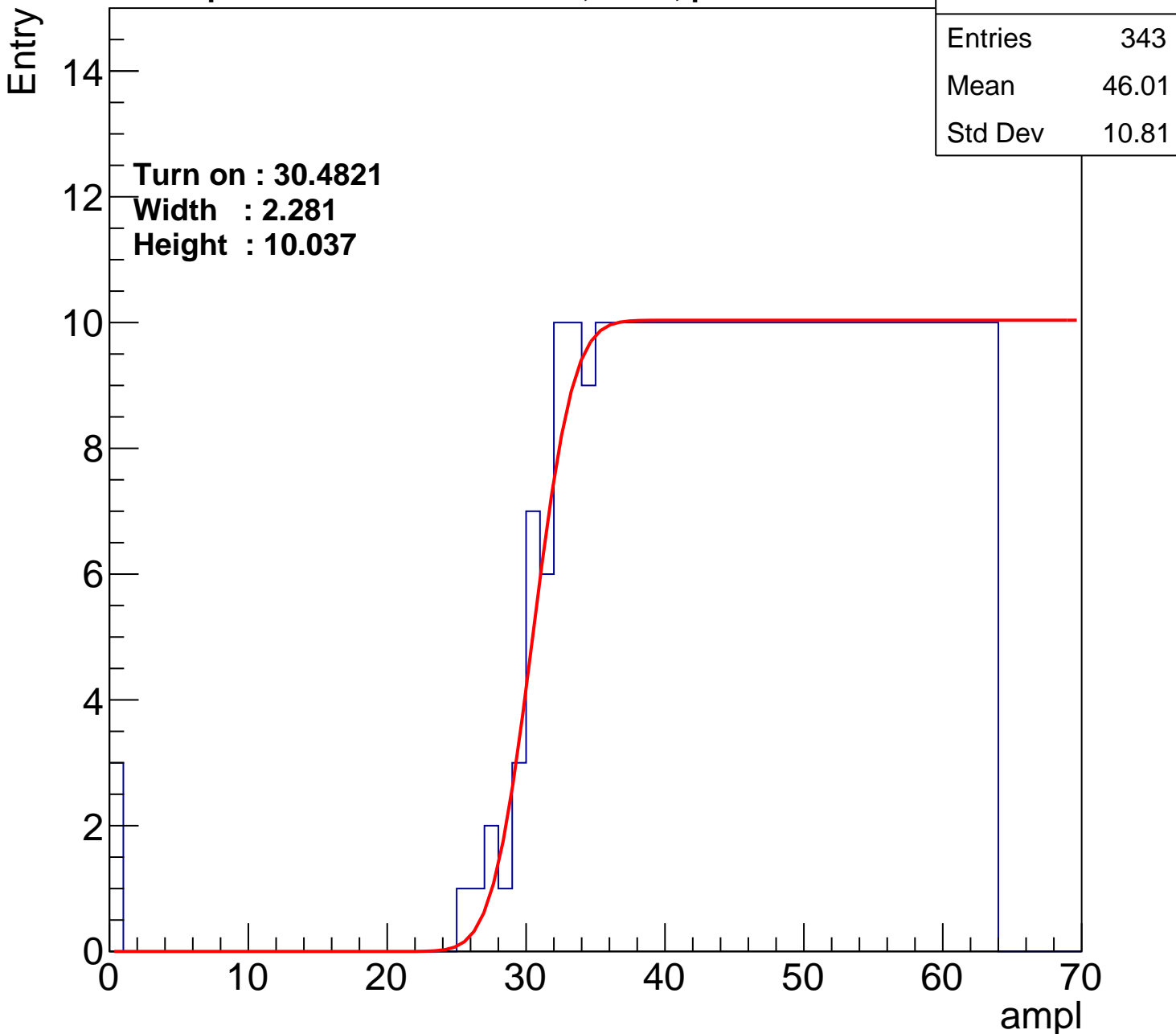
Mean	46.01
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Std Dev	10.81
---------	-------

Turn on : 30.4821

Width : 2.281

Height : 10.037



B0L000S, U11-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	44.02
Std Dev	11.55

Turn on : 25.8796

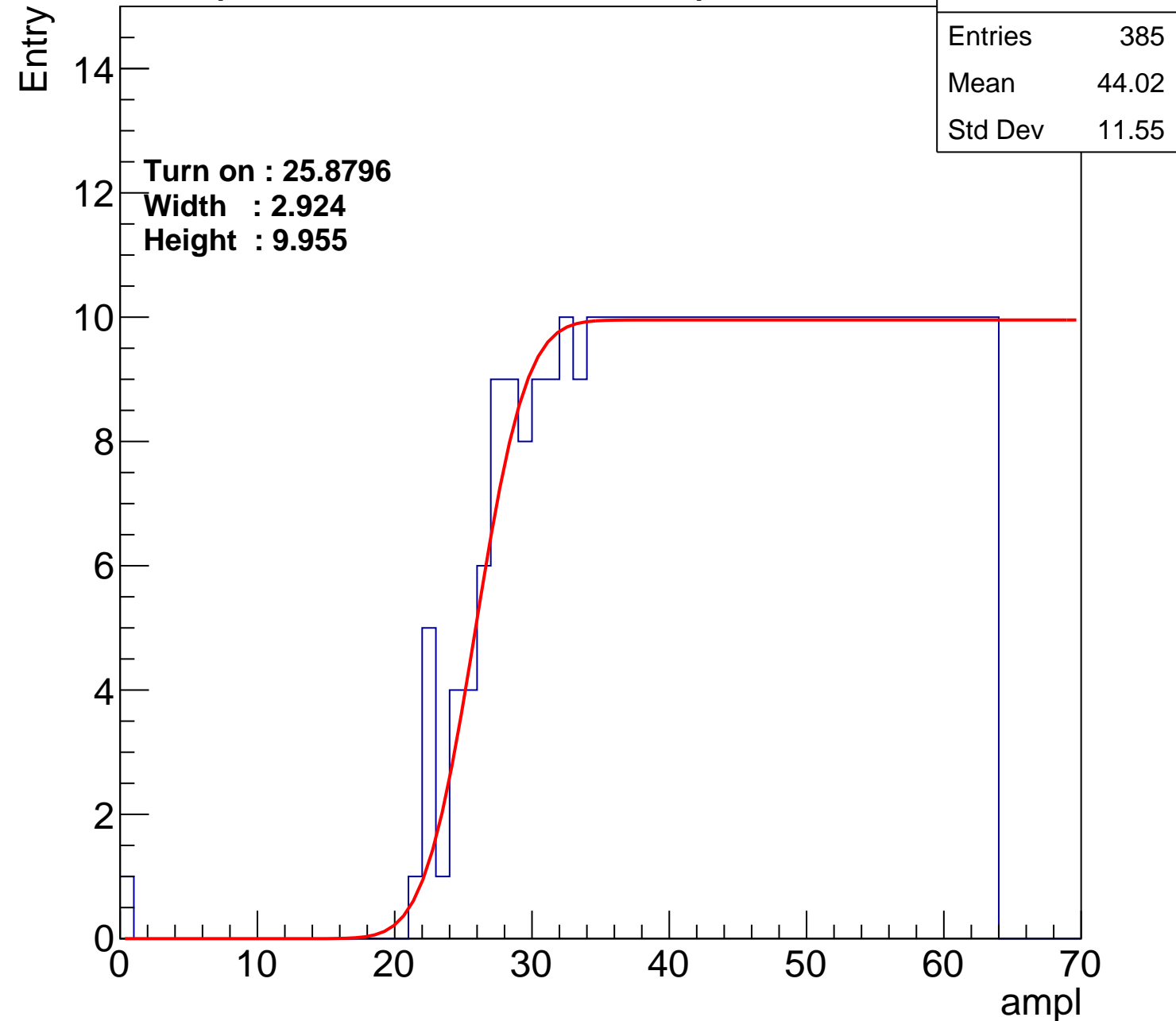
Width : 2.924

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch123

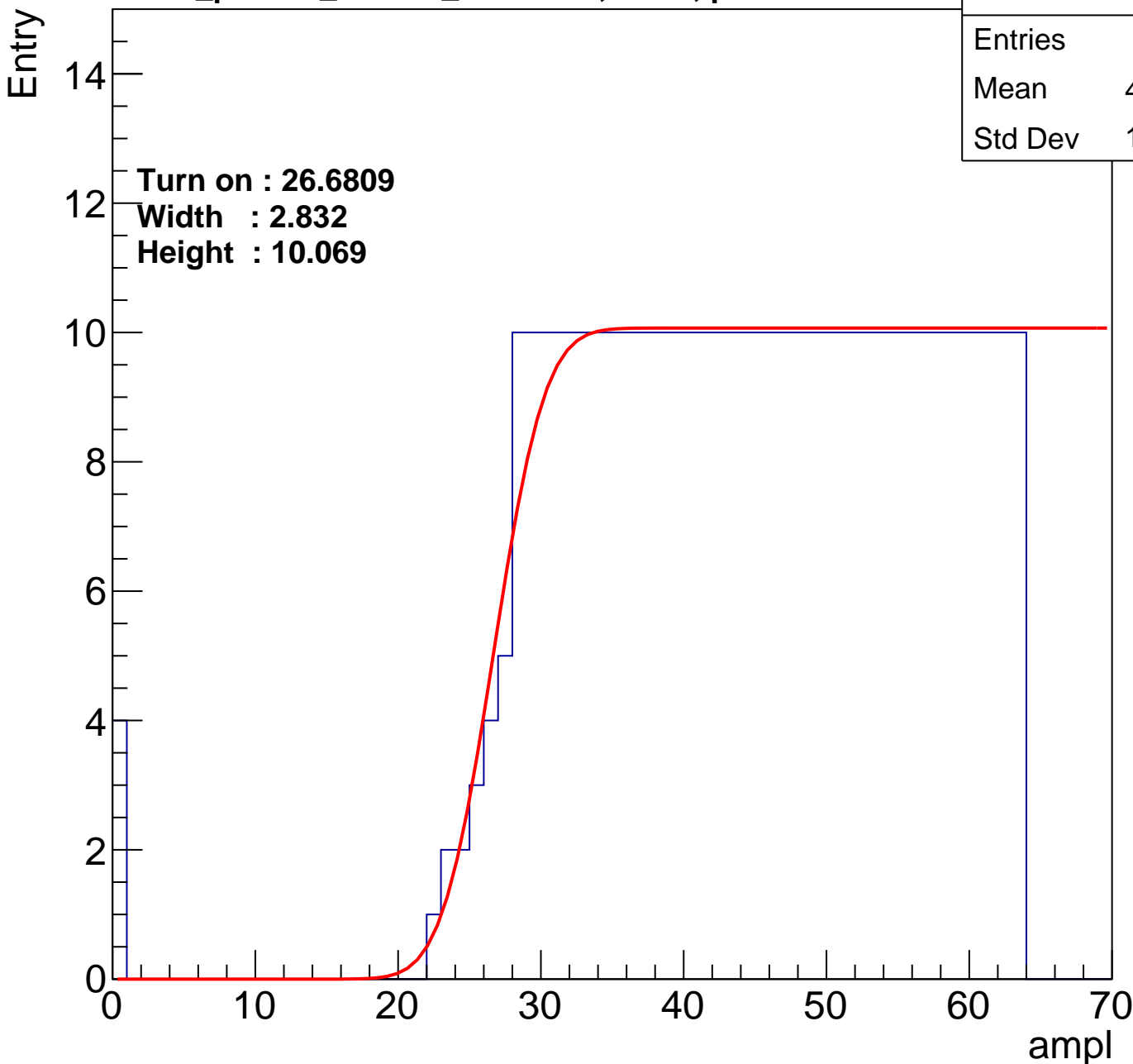
calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.12
Std Dev	11.84

Turn on : 26.6809

Width : 2.832

Height : 10.069



B0L000S, U11-ch124

calib_packv5_042523_0143.root, FC#5, port B1

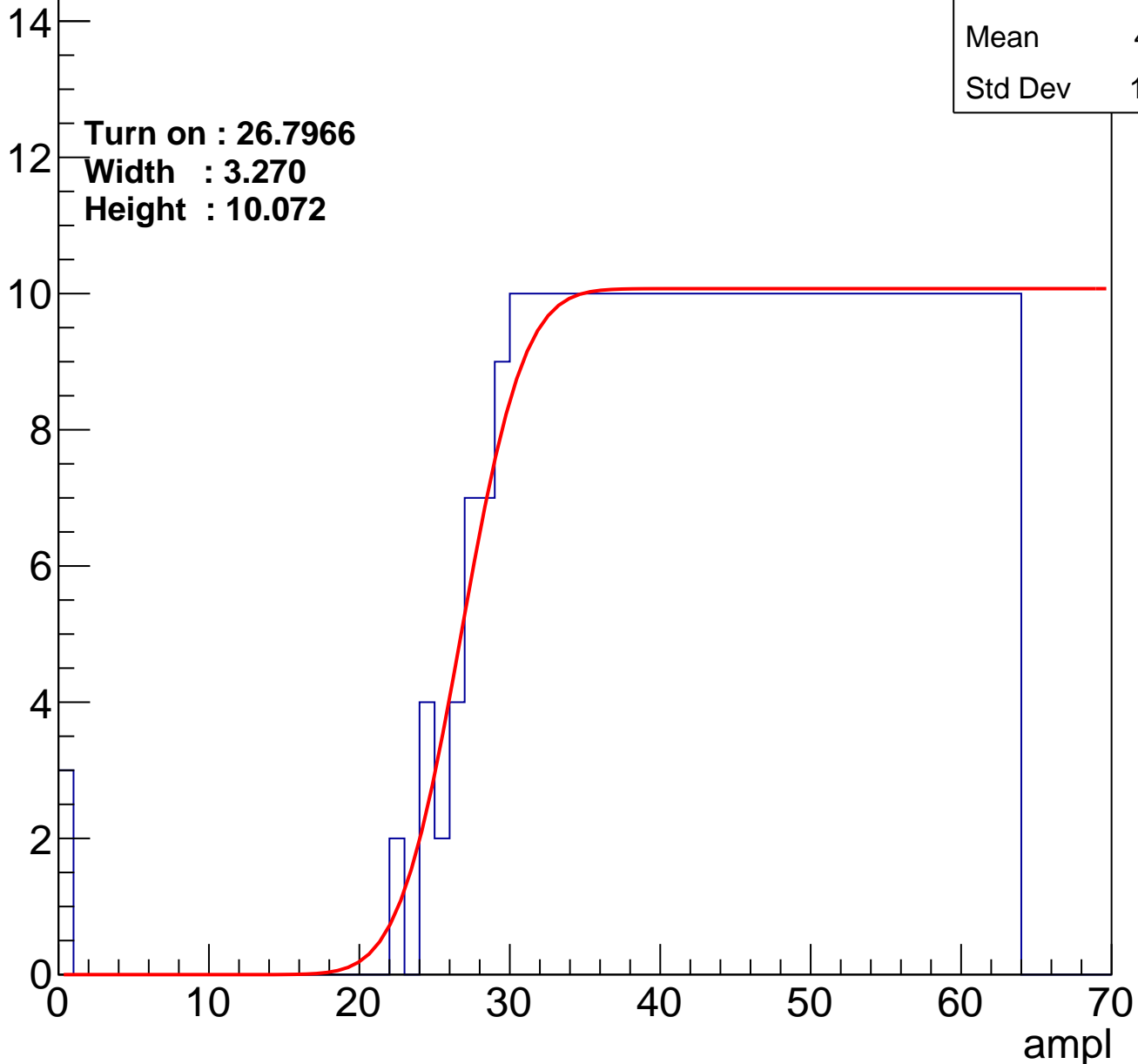
Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.7966

Width : 3.270

Height : 10.072

Entry



B0L000S, U11-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.33
Std Dev	10.98

Turn on : 28.4274

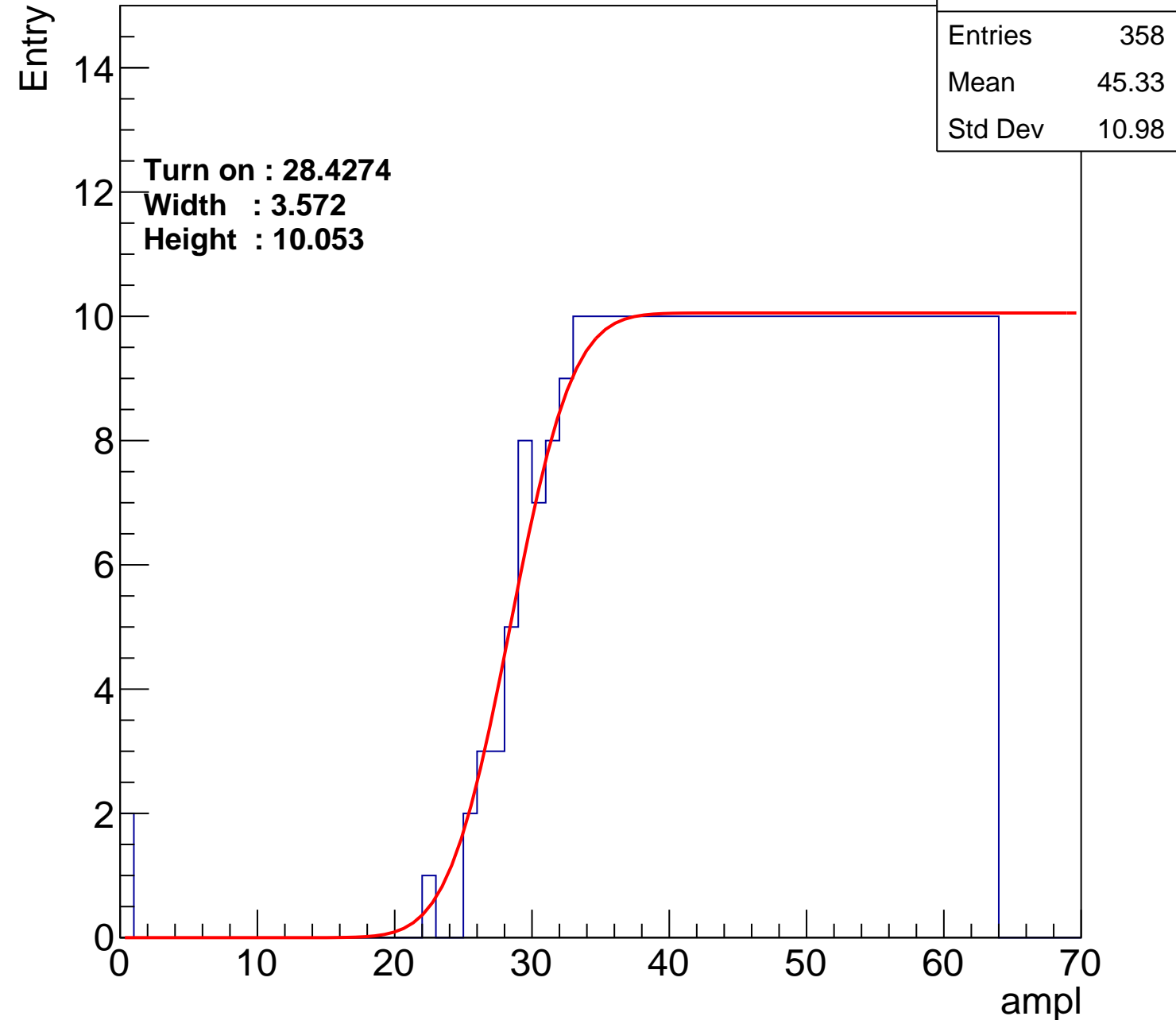
Width : 3.572

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch126

calib_packv5_042523_0143.root, FC#5, port B1

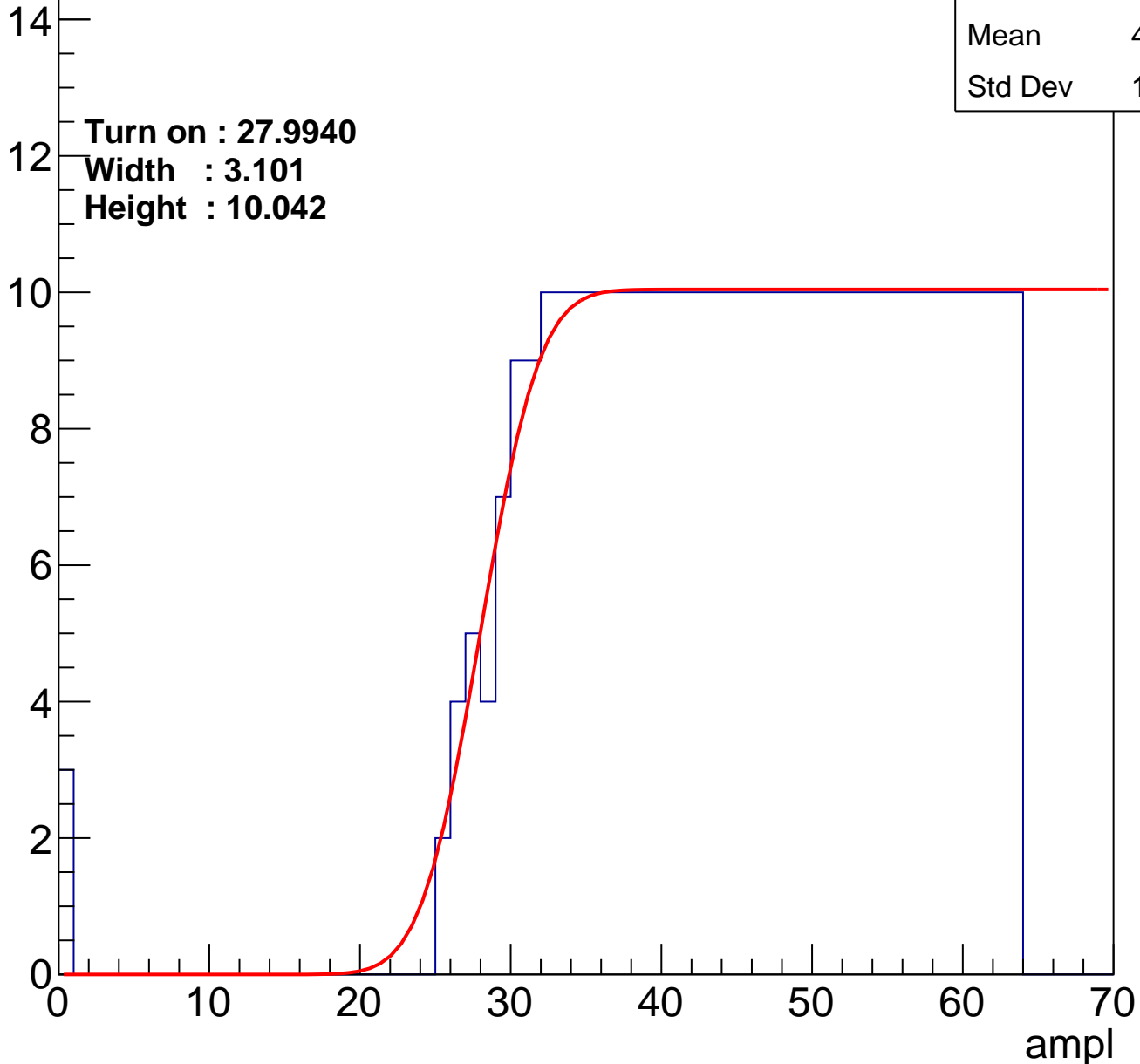
Entries	363
Mean	45.05
Std Dev	11.25

Turn on : 27.9940

Width : 3.101

Height : 10.042

Entry



B0L000S, U11-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 27.0290

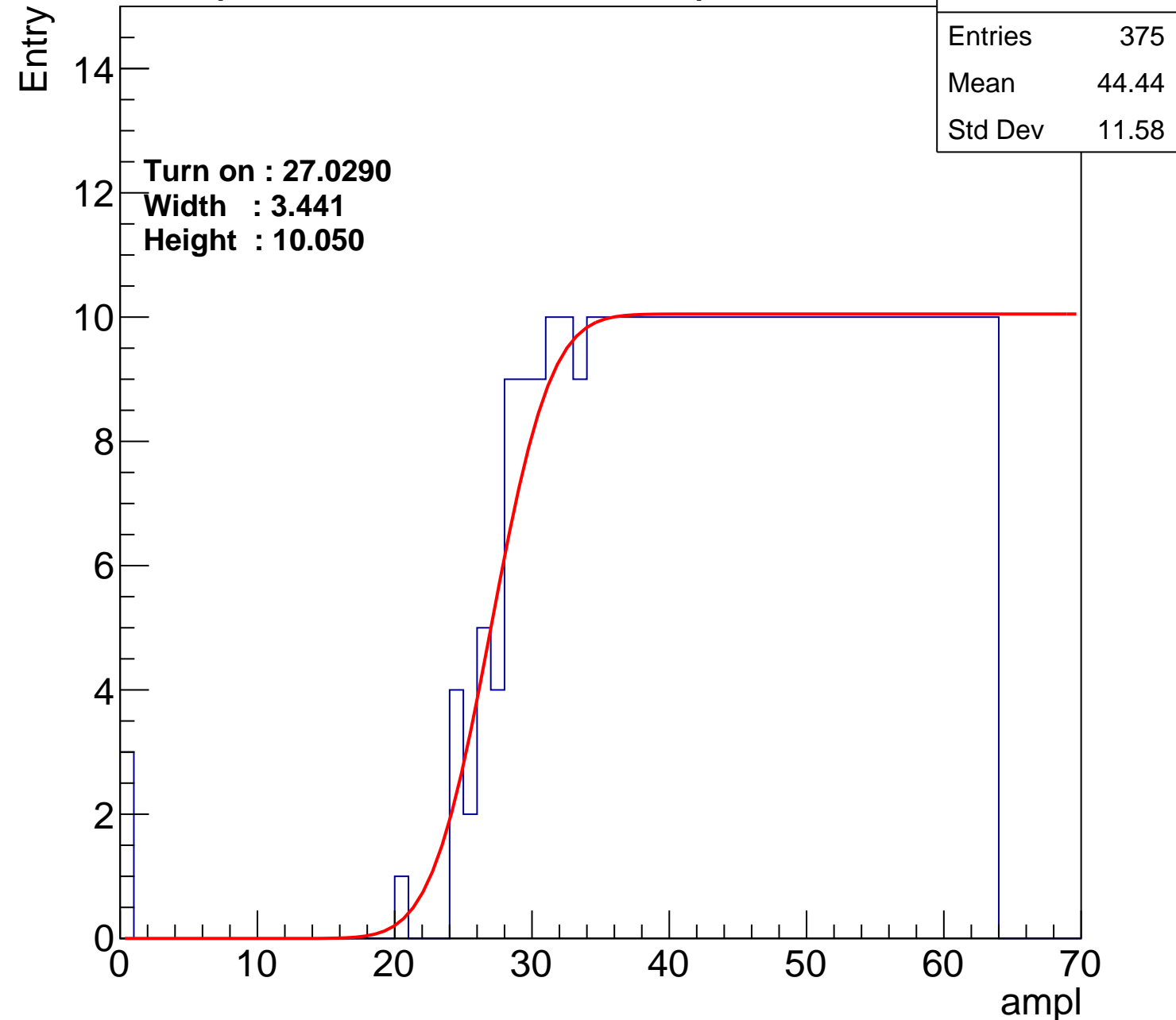
Width : 3.441

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U11-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 27.0290

Width : 3.441

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl

