

B0L000S, U23-ch0

calib_packv5_042523_0143.root, FC#5, port B1

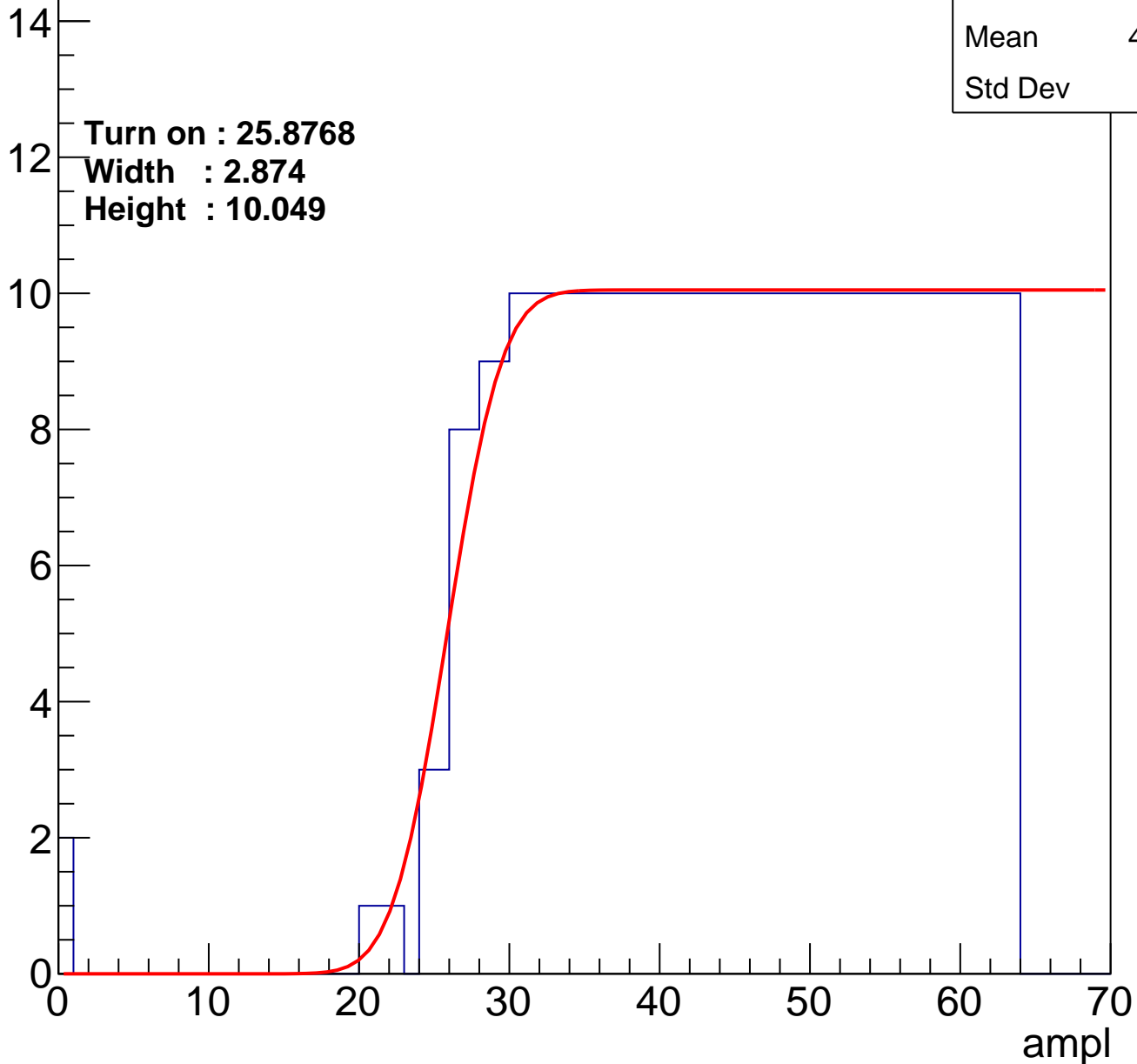
| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.04 |
| Std Dev | 11.6 |

Turn on : 25.8768

Width : 2.874

Height : 10.049

Entry



B0L000S, U23-ch1

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.43 |
| Std Dev | 11.41 |

Turn on : 26.9070

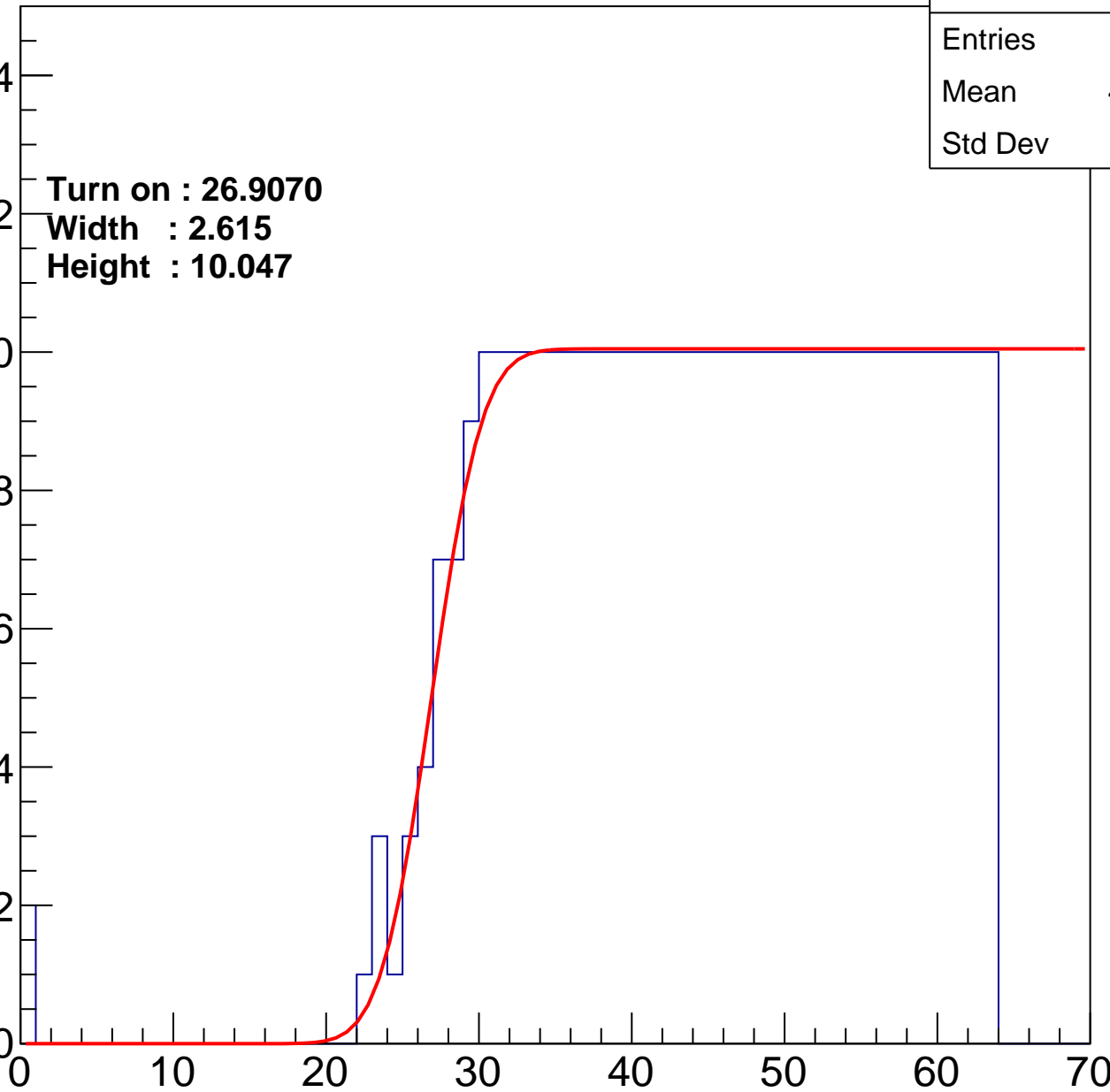
Width : 2.615

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch2

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 363 |
| Mean | 45.11 |
| Std Dev | 11.08 |

Turn on : 28.2756

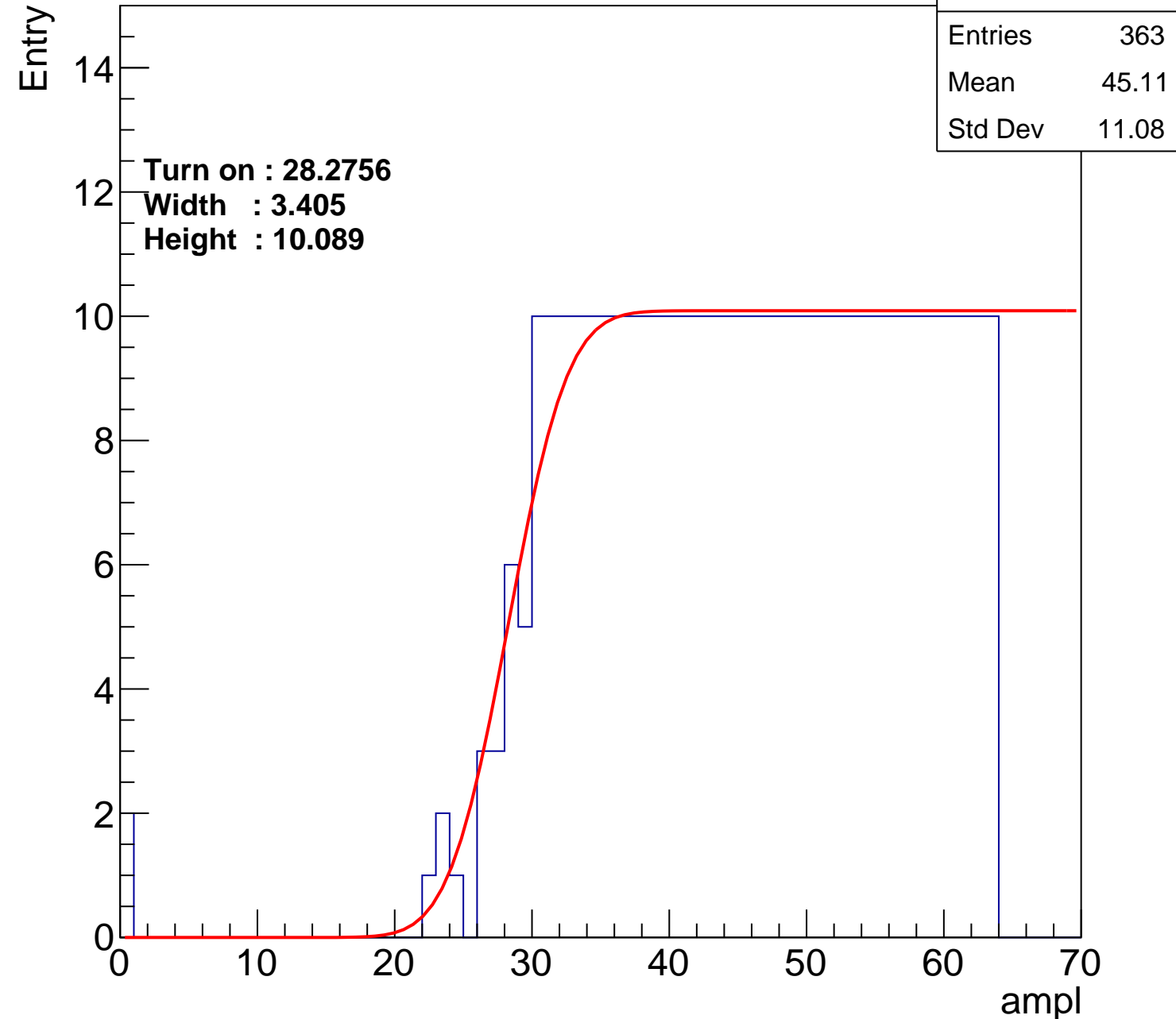
Width : 3.405

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch3

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 386 |
| Mean | 43.97 |
| Std Dev | 11.66 |

Turn on : 25.7803

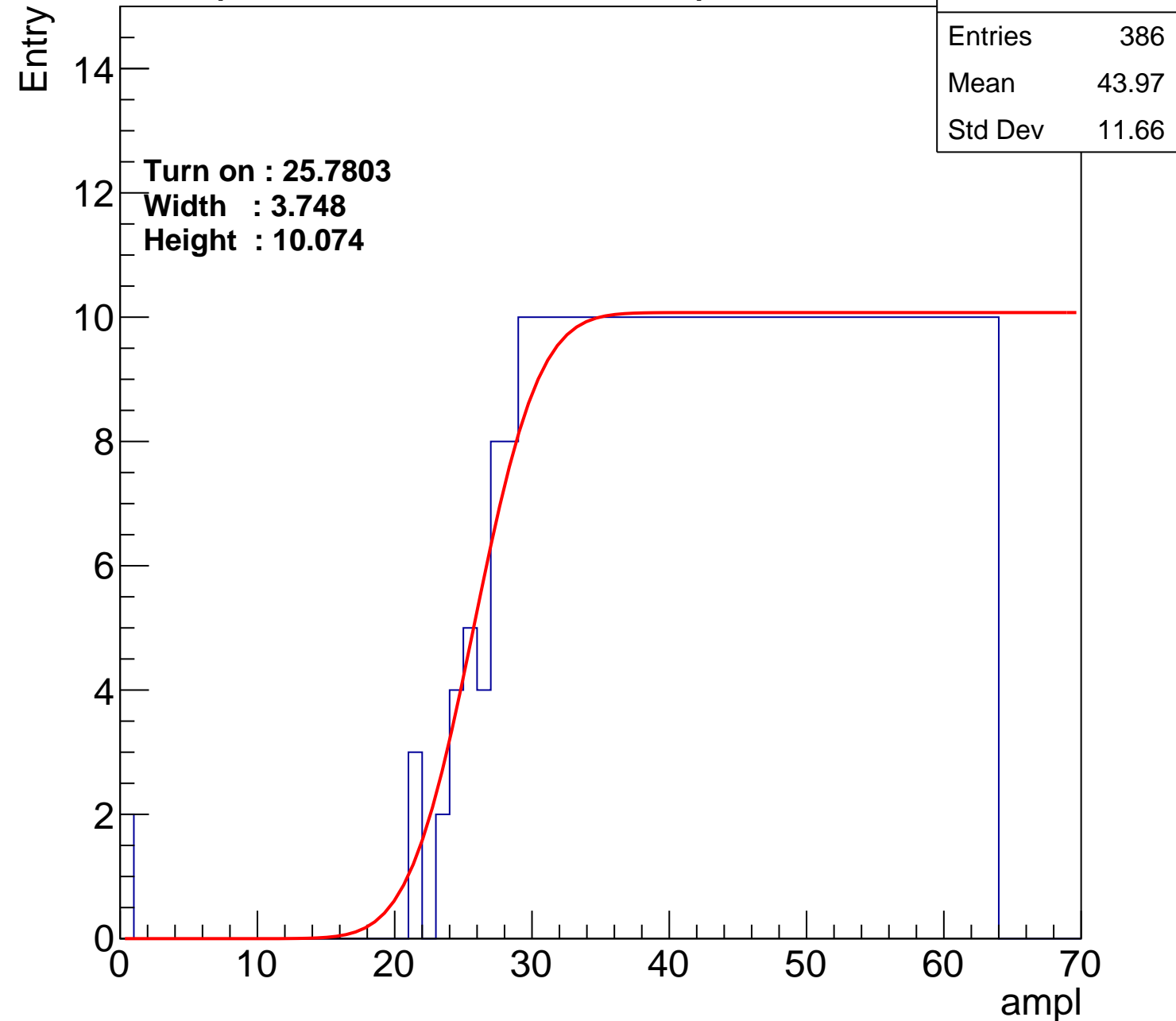
Width : 3.748

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch4

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.96 |
| Std Dev | 11.01 |

Turn on : 27.9505

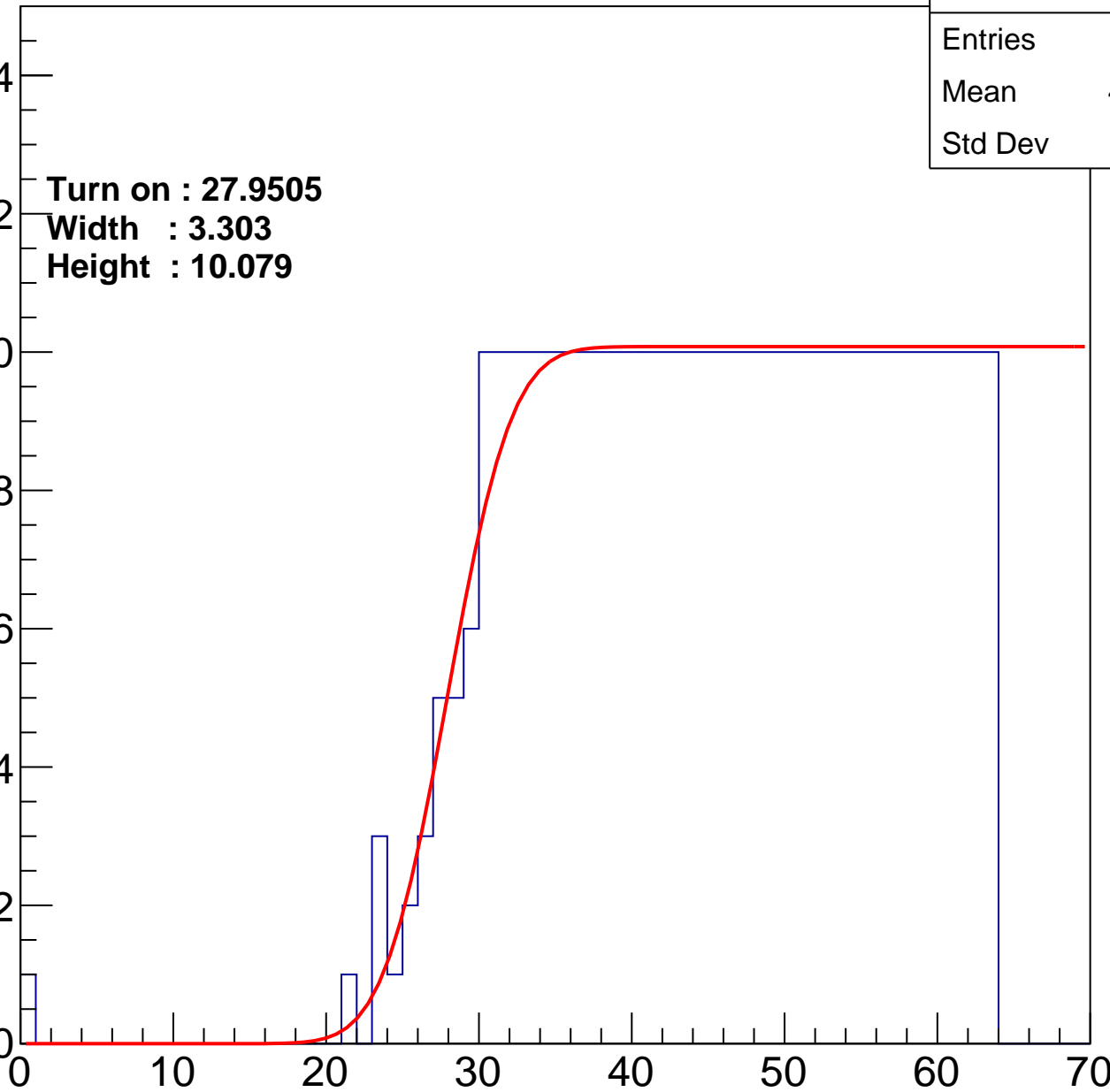
Width : 3.303

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch5

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.23 |
| Std Dev | 11.4 |

Turn on : 26.2136

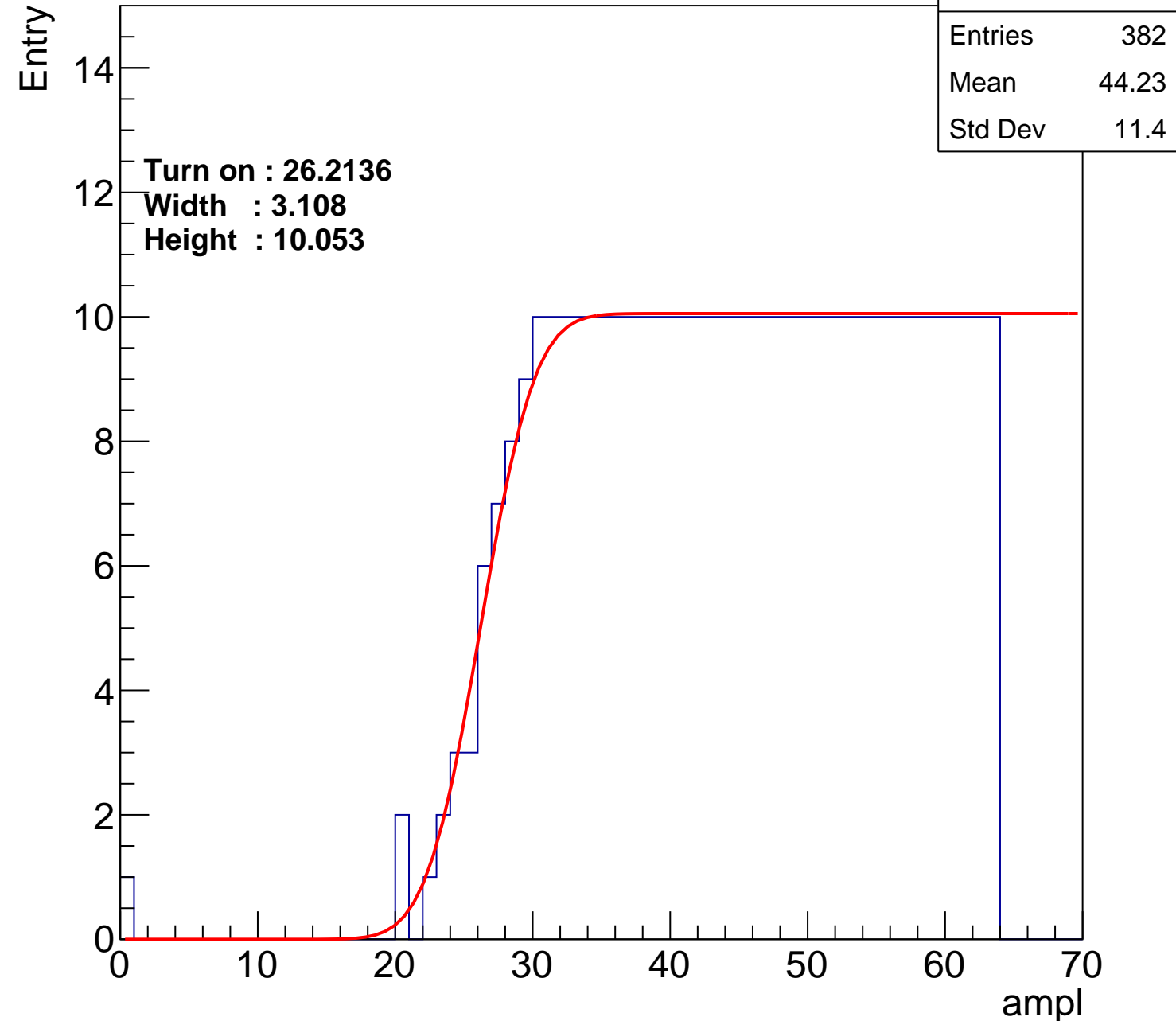
Width : 3.108

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch6

calib_packv5_042523_0143.root, FC#5, port B1

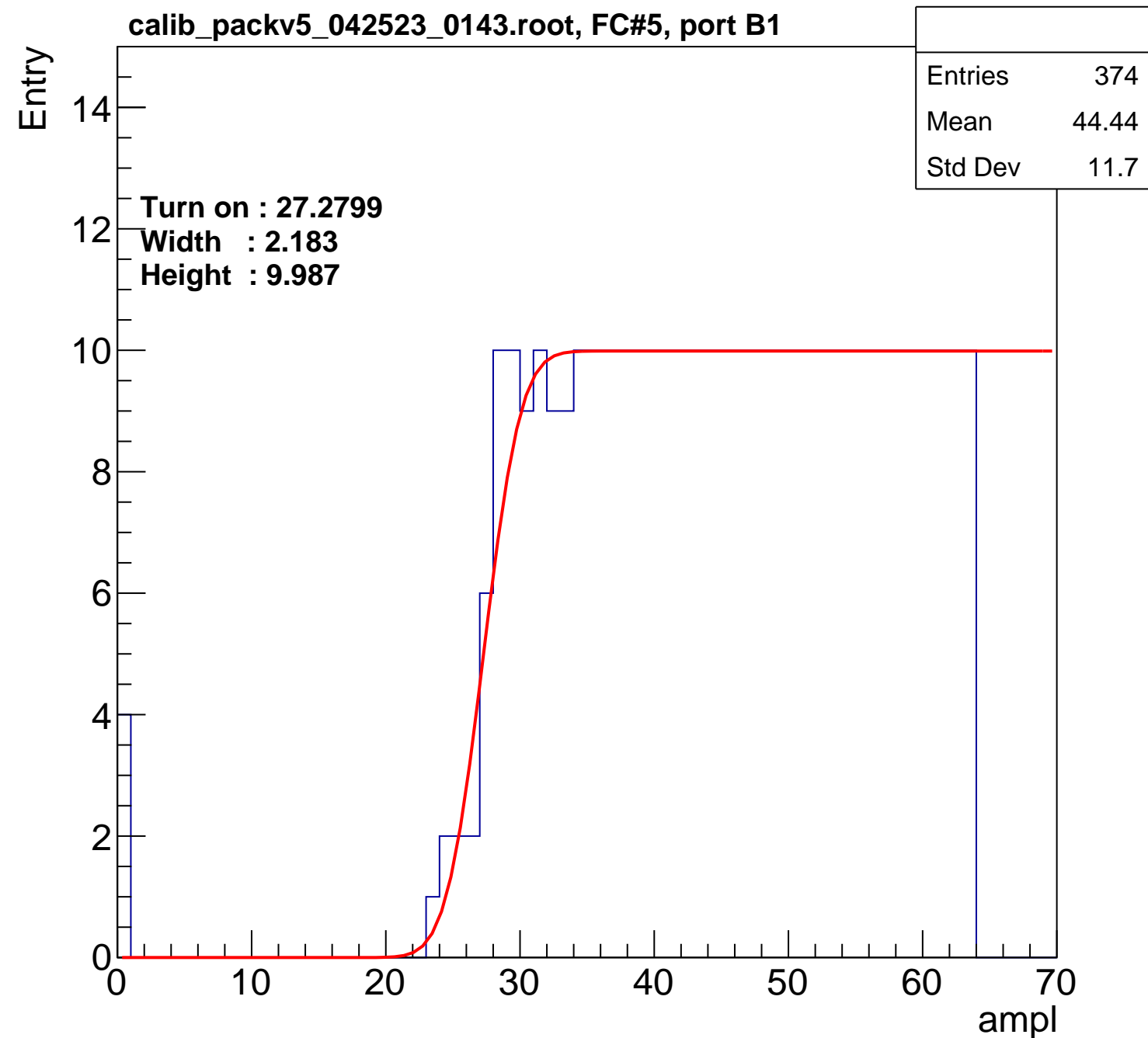
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2799
Width : 2.183
Height : 9.987

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.44 |
| Std Dev | 11.7 |

ampl



B0L000S, U23-ch7

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 400 |
| Mean | 43.34 |
| Std Dev | 11.87 |

Turn on : 23.9025

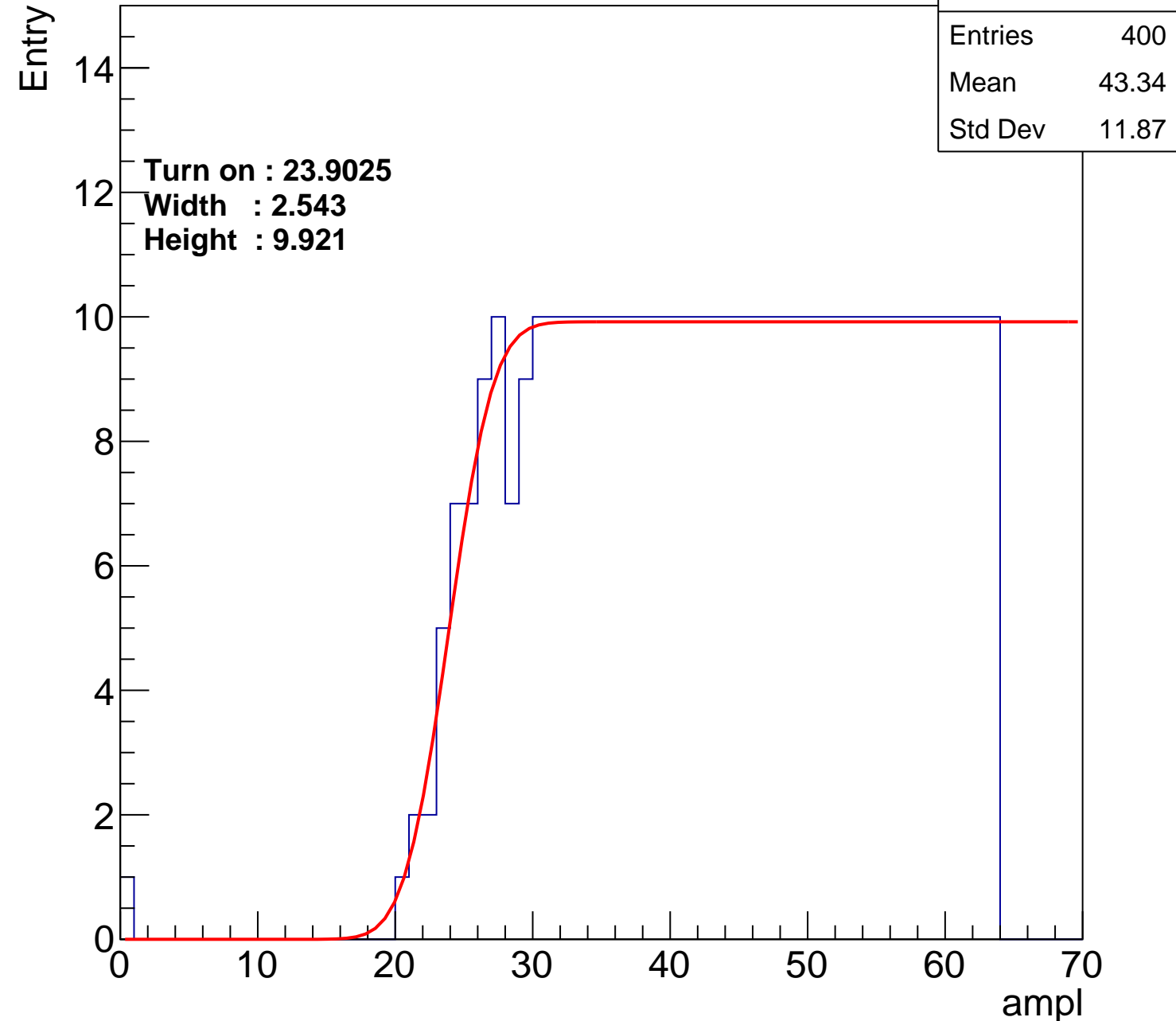
Width : 2.543

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch8

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.64 |
| Std Dev | 11.7 |

Turn on : 24.6200

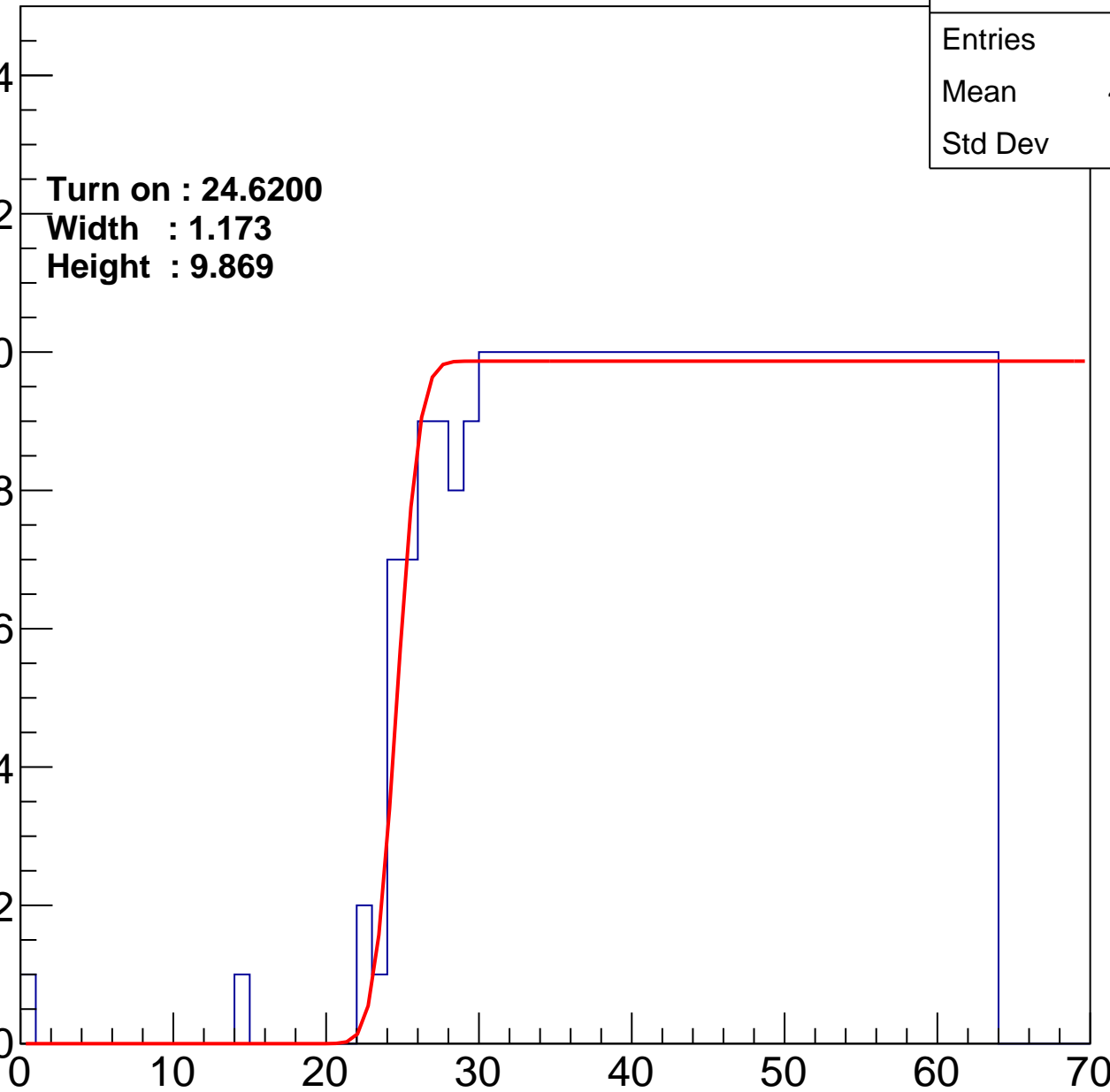
Width : 1.173

Height : 9.869

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch9

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.99 |
| Std Dev | 11.14 |

Turn on : 27.7010

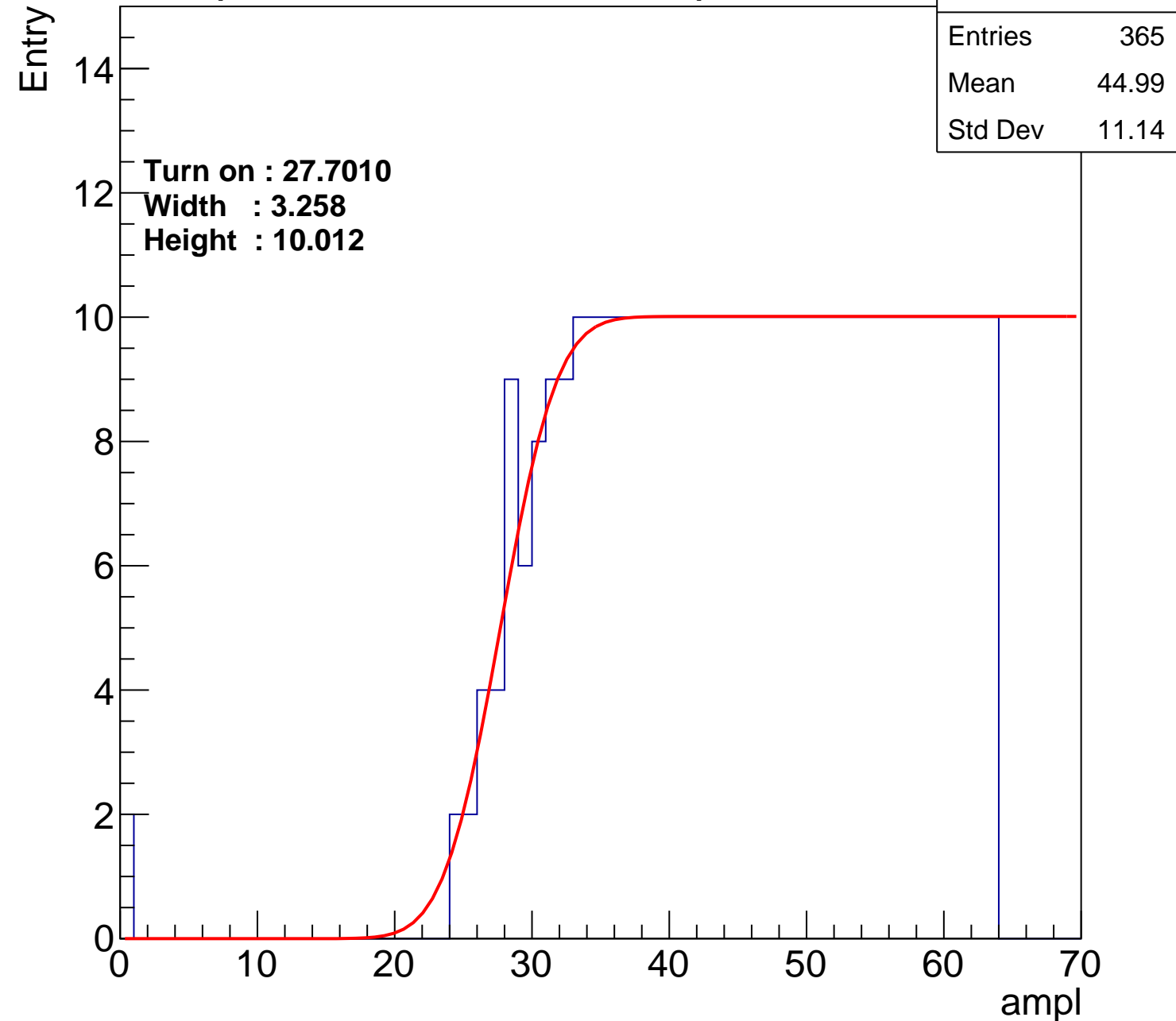
Width : 3.258

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch10

calib_packv5_042523_0143.root, FC#5, port B1

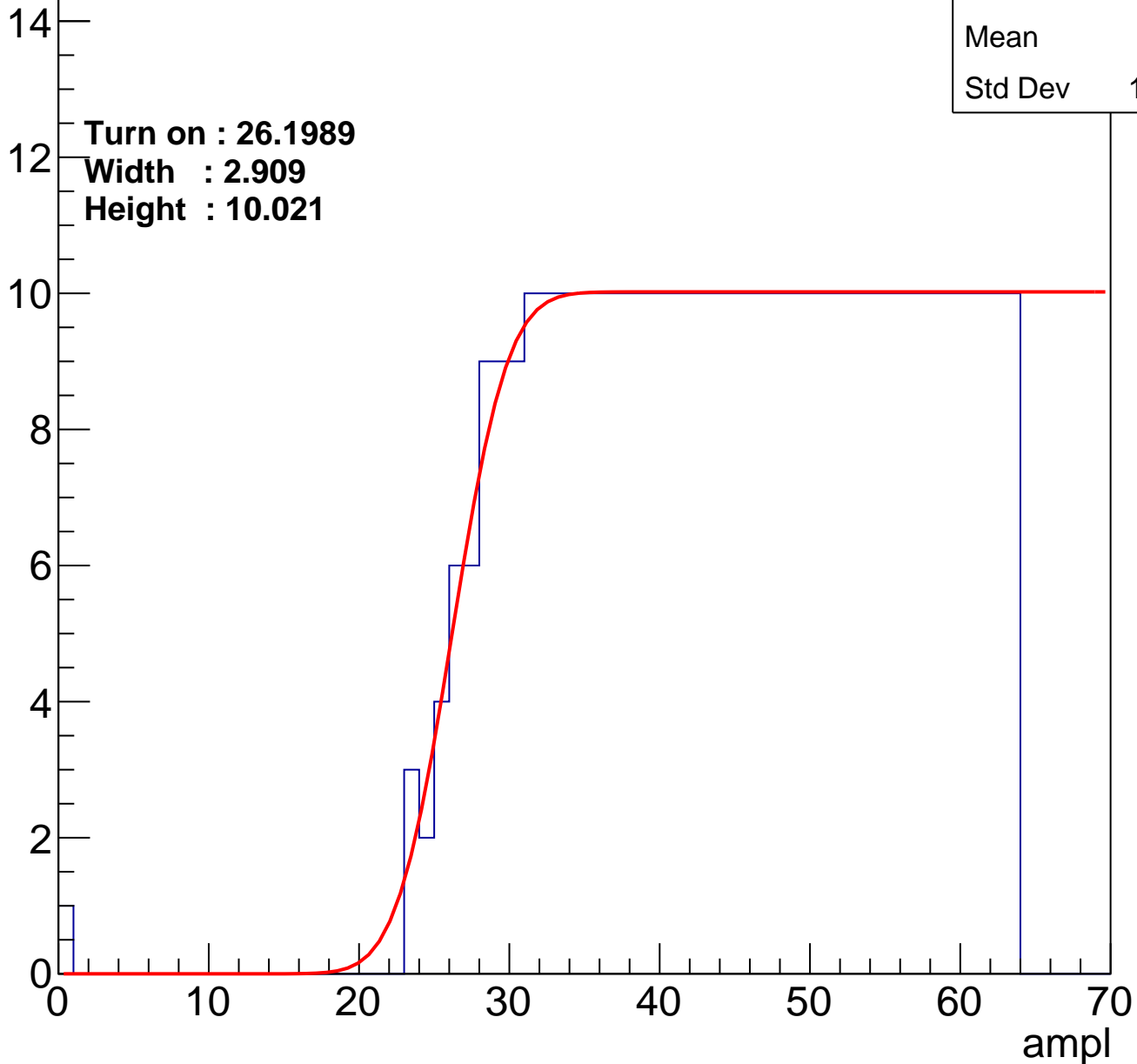
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.4 |
| Std Dev | 11.27 |

Turn on : 26.1989

Width : 2.909

Height : 10.021

Entry



B0L000S, U23-ch11

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.37 |
| Std Dev | 11.44 |

Turn on : 26.1874

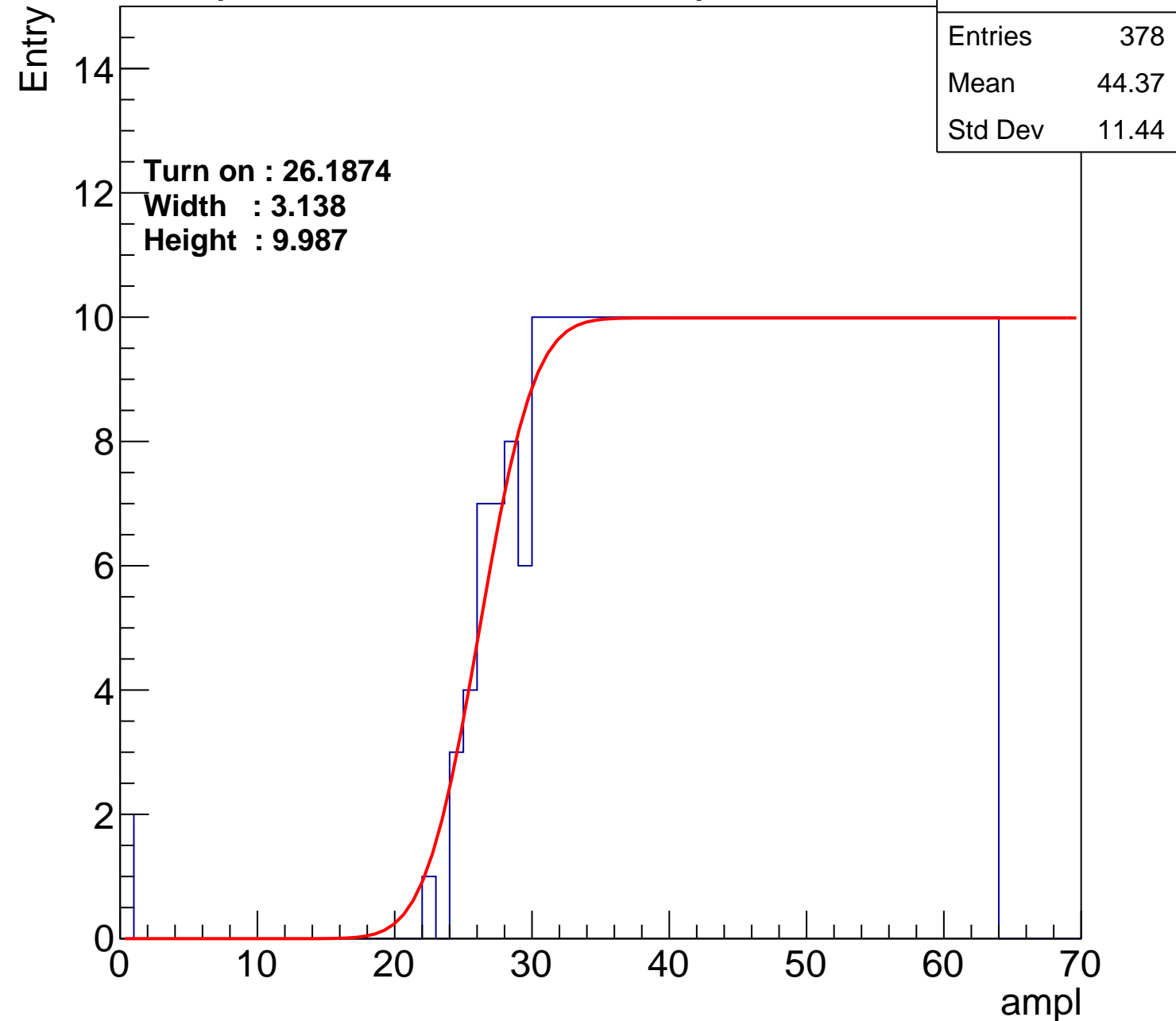
Width : 3.138

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch12

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.57 |
| Std Dev | 11.34 |

Turn on : 26.9866

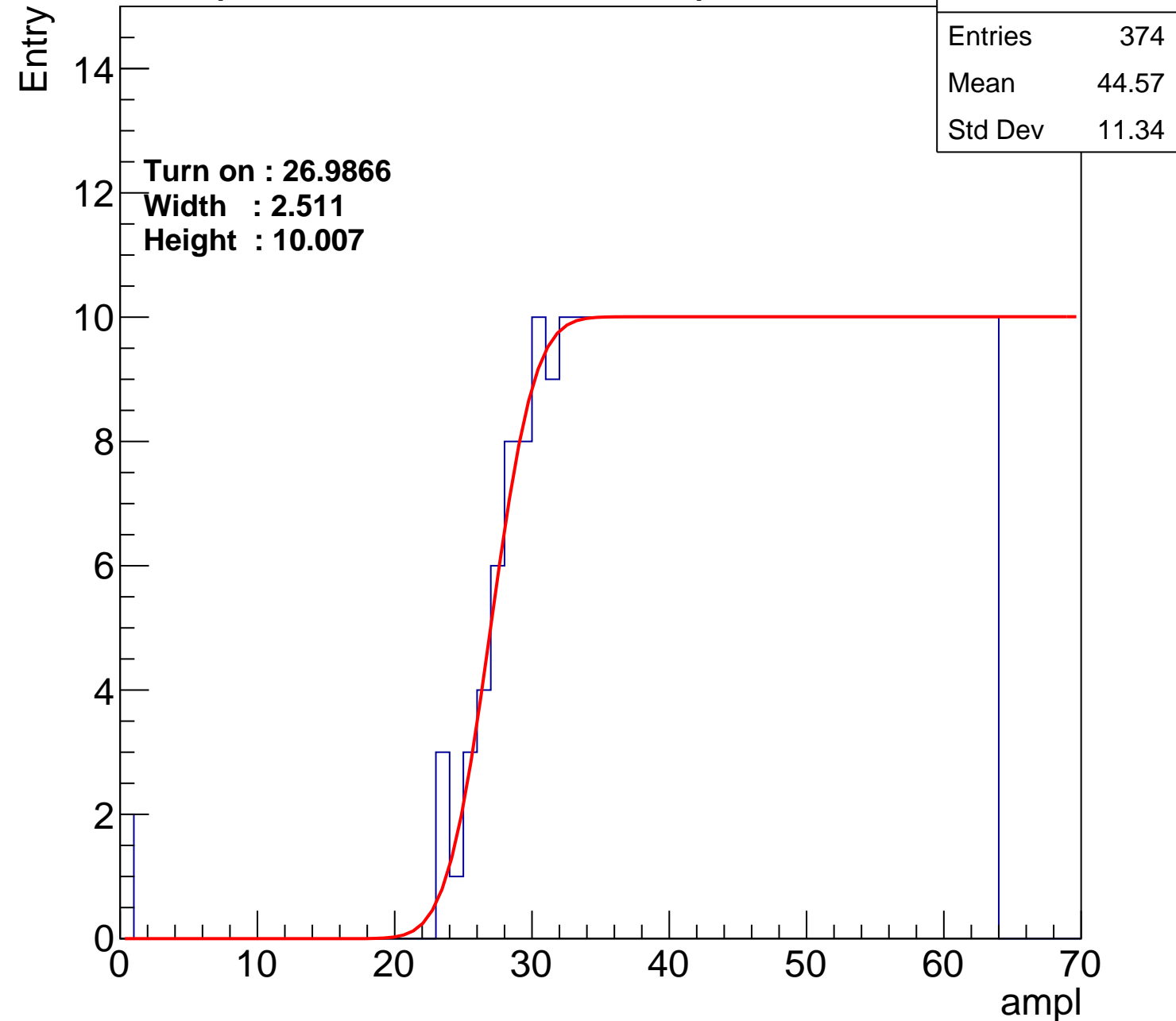
Width : 2.511

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch13

calib_packv5_042523_0143.root, FC#5, port B1

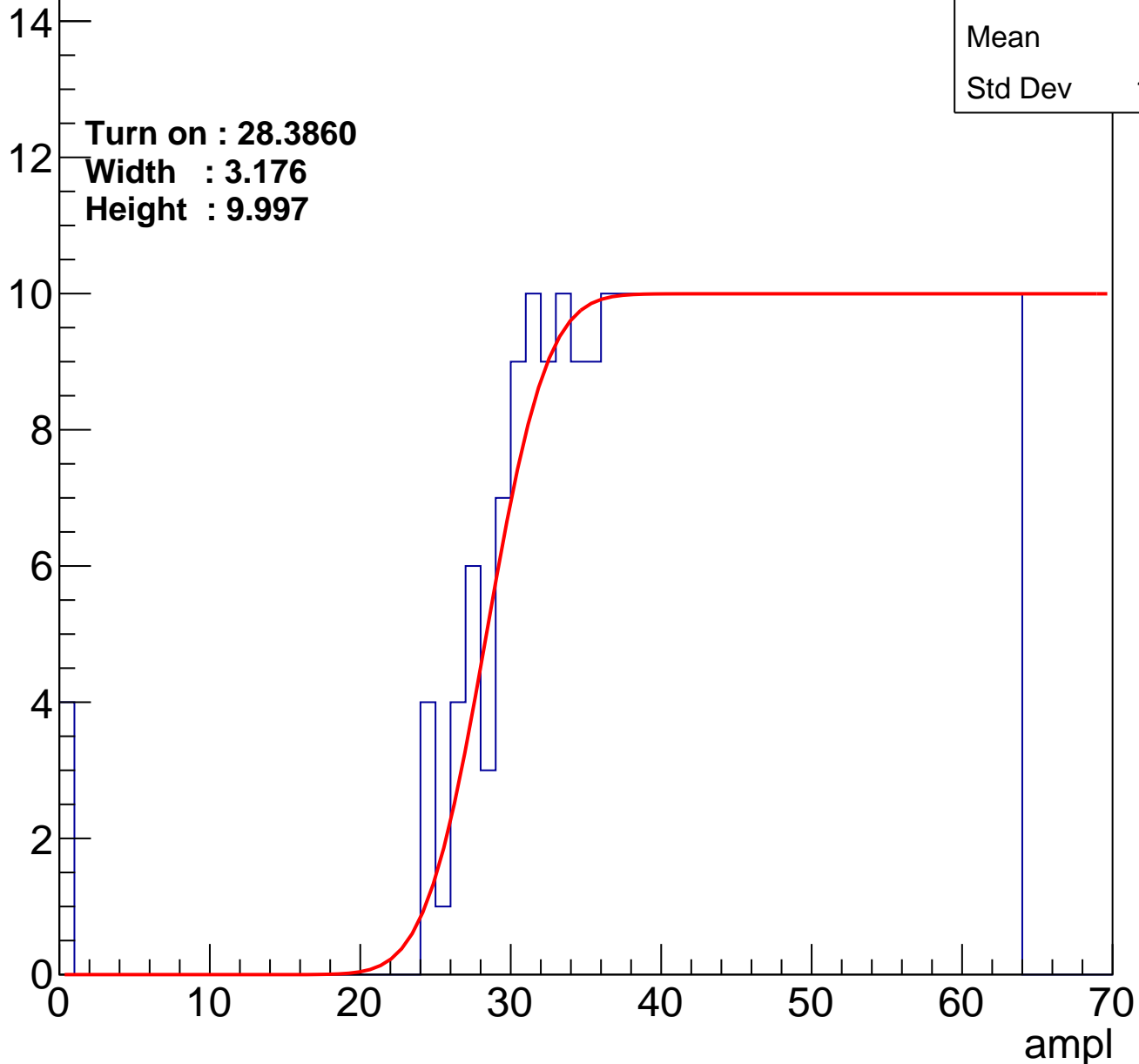
| | |
|---------|-------|
| Entries | 365 |
| Mean | 44.8 |
| Std Dev | 11.61 |

Turn on : 28.3860

Width : 3.176

Height : 9.997

Entry



B0L000S, U23-ch14

calib_packv5_042523_0143.root, FC#5, port B1

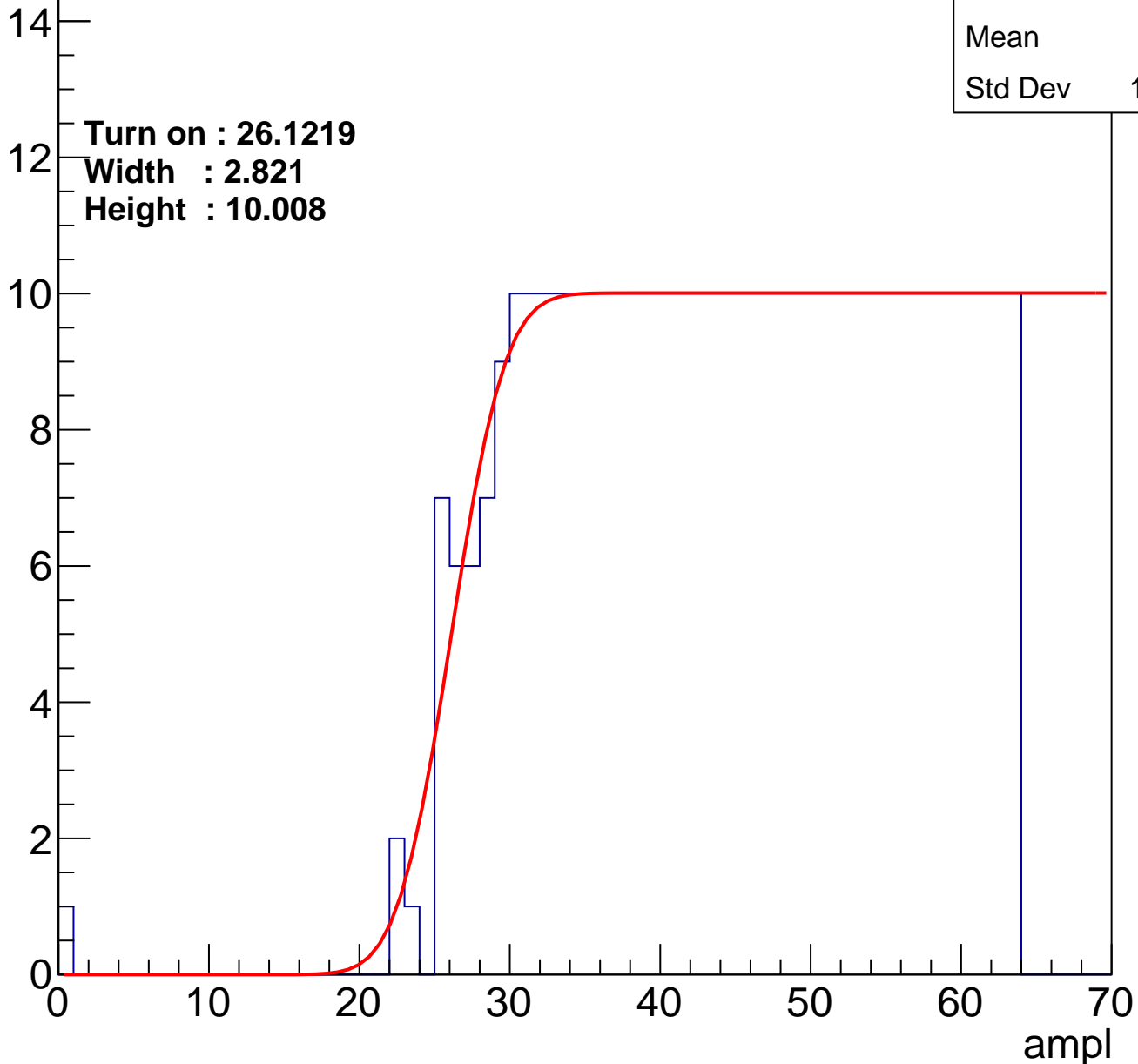
| | |
|---------|-------|
| Entries | 379 |
| Mean | 44.4 |
| Std Dev | 11.27 |

Turn on : 26.1219

Width : 2.821

Height : 10.008

Entry



B0L000S, U23-ch15

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.41 |
| Std Dev | 11.29 |

Turn on : 25.8988

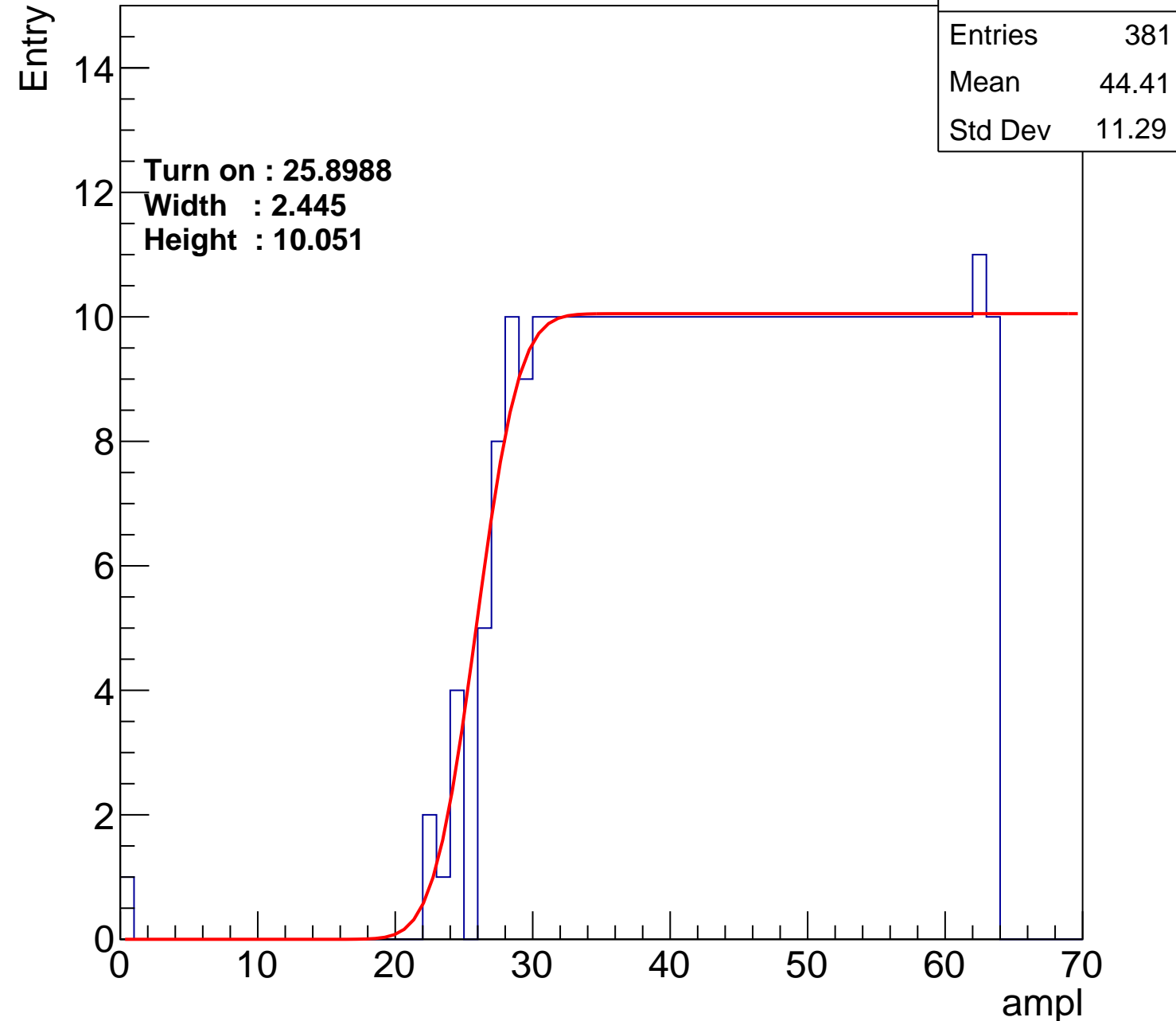
Width : 2.445

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch16

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.58 |
| Std Dev | 11.38 |

Turn on : 27.0310

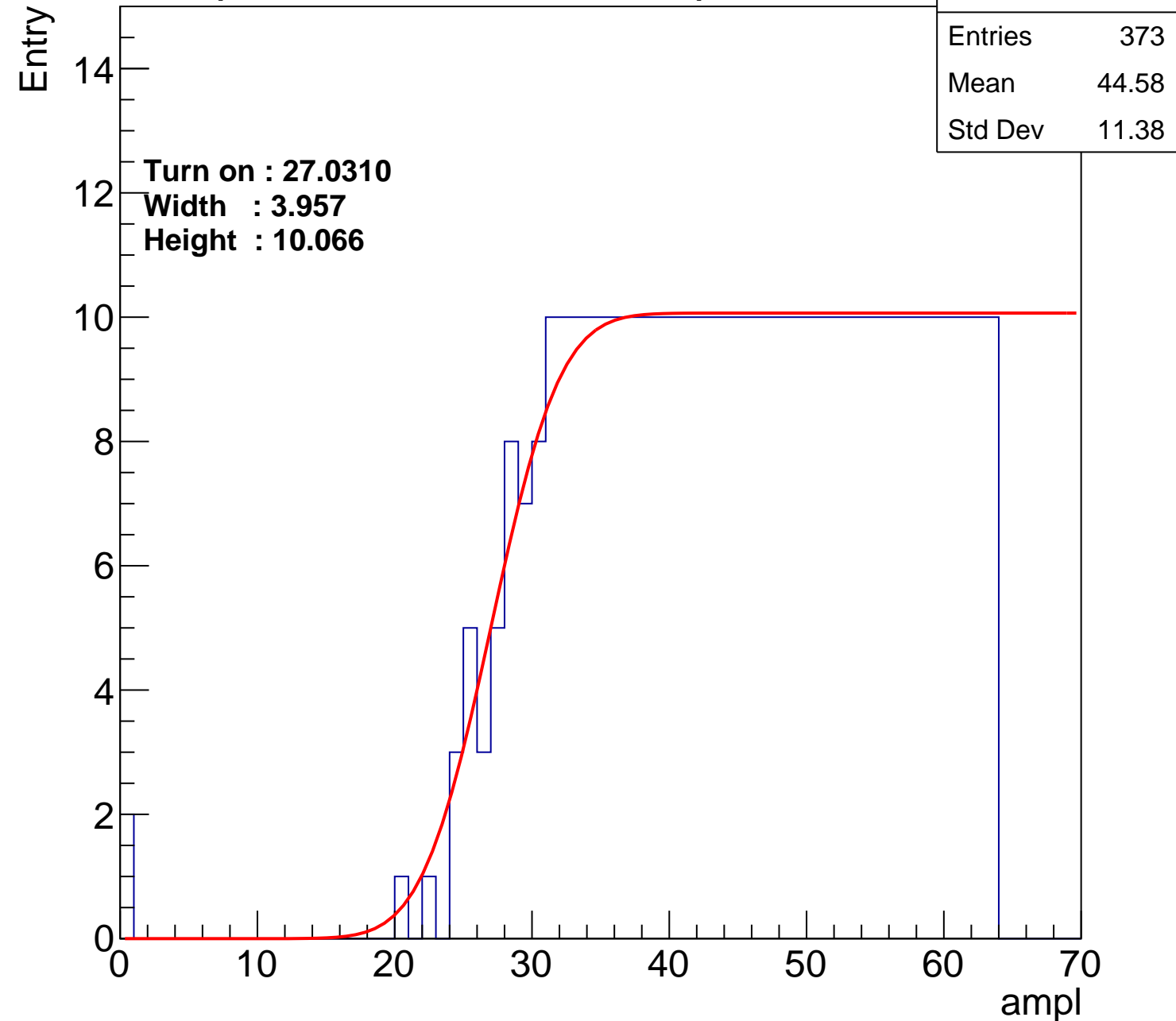
Width : 3.957

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch17

calib_packv5_042523_0143.root, FC#5, port B1

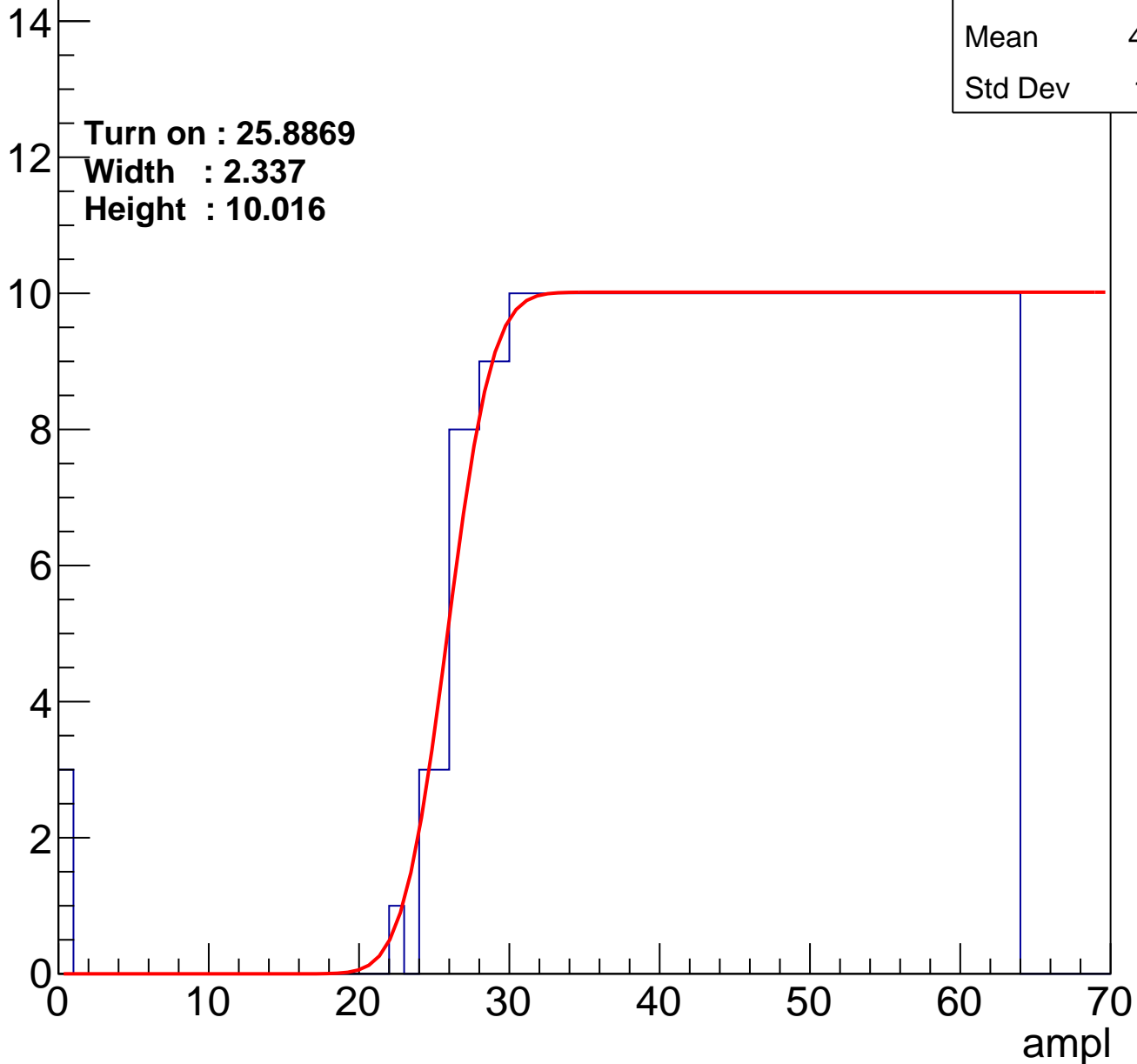
| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.05 |
| Std Dev | 11.71 |

Turn on : 25.8869

Width : 2.337

Height : 10.016

Entry



B0L000S, U23-ch18

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.98 |
| Std Dev | 10.96 |

Turn on : 27.6031

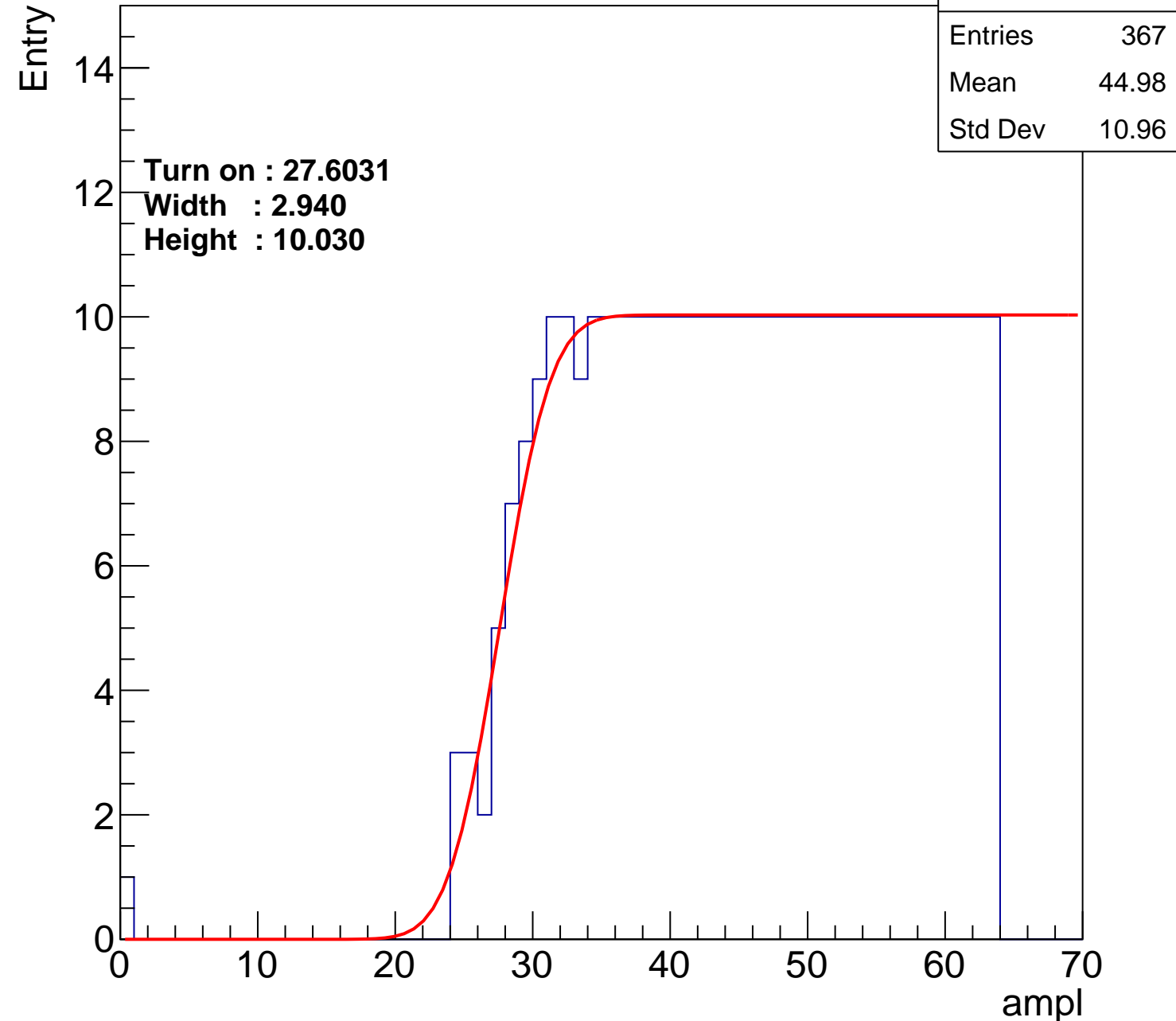
Width : 2.940

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 24.9798
Width : 2.796
Height : 10.011



B0L000S, U23-ch20

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 365 |
| Mean | 45.03 |
| Std Dev | 11.09 |

Turn on : 27.7494

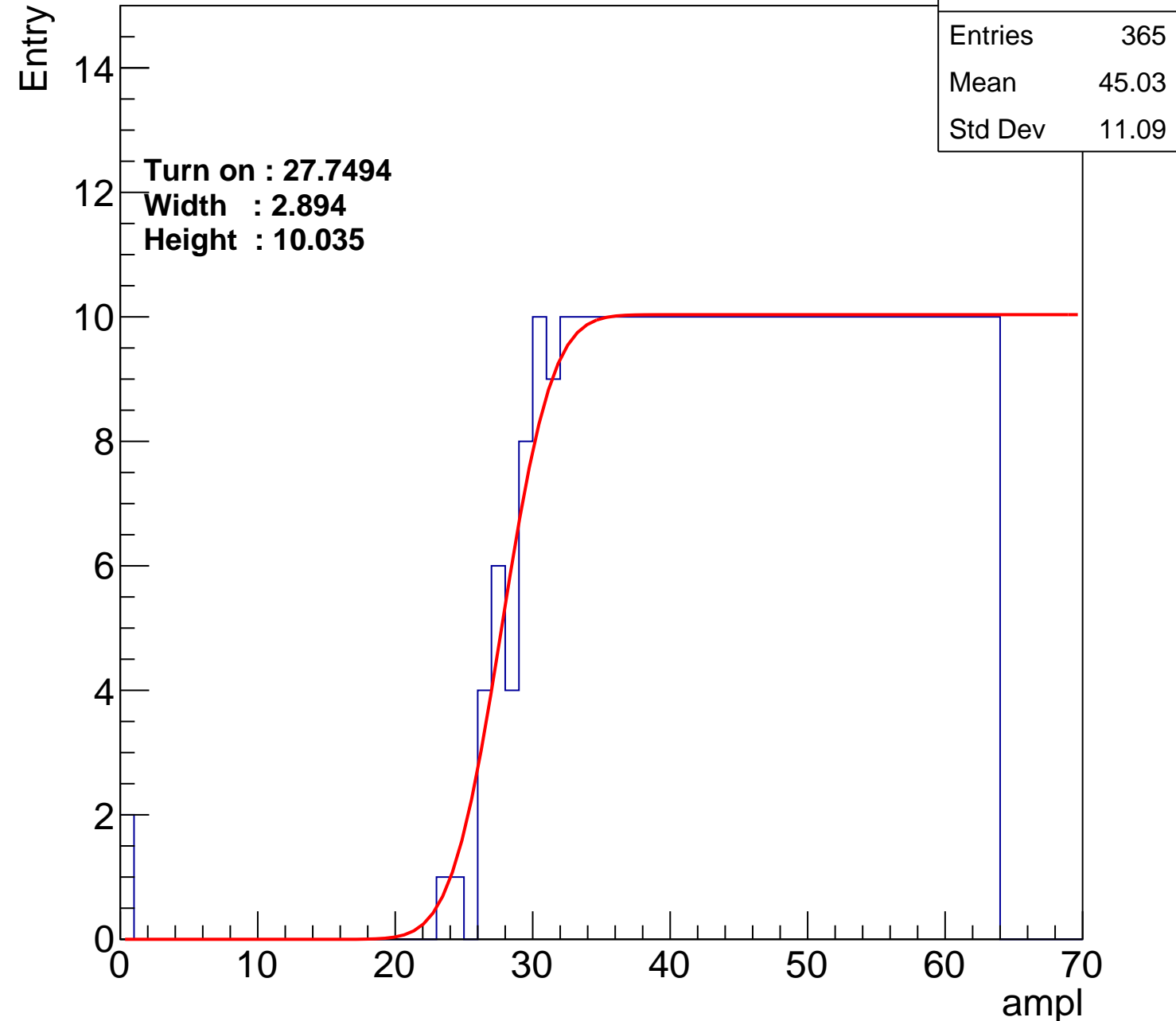
Width : 2.894

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch21

calib_packv5_042523_0143.root, FC#5, port B1

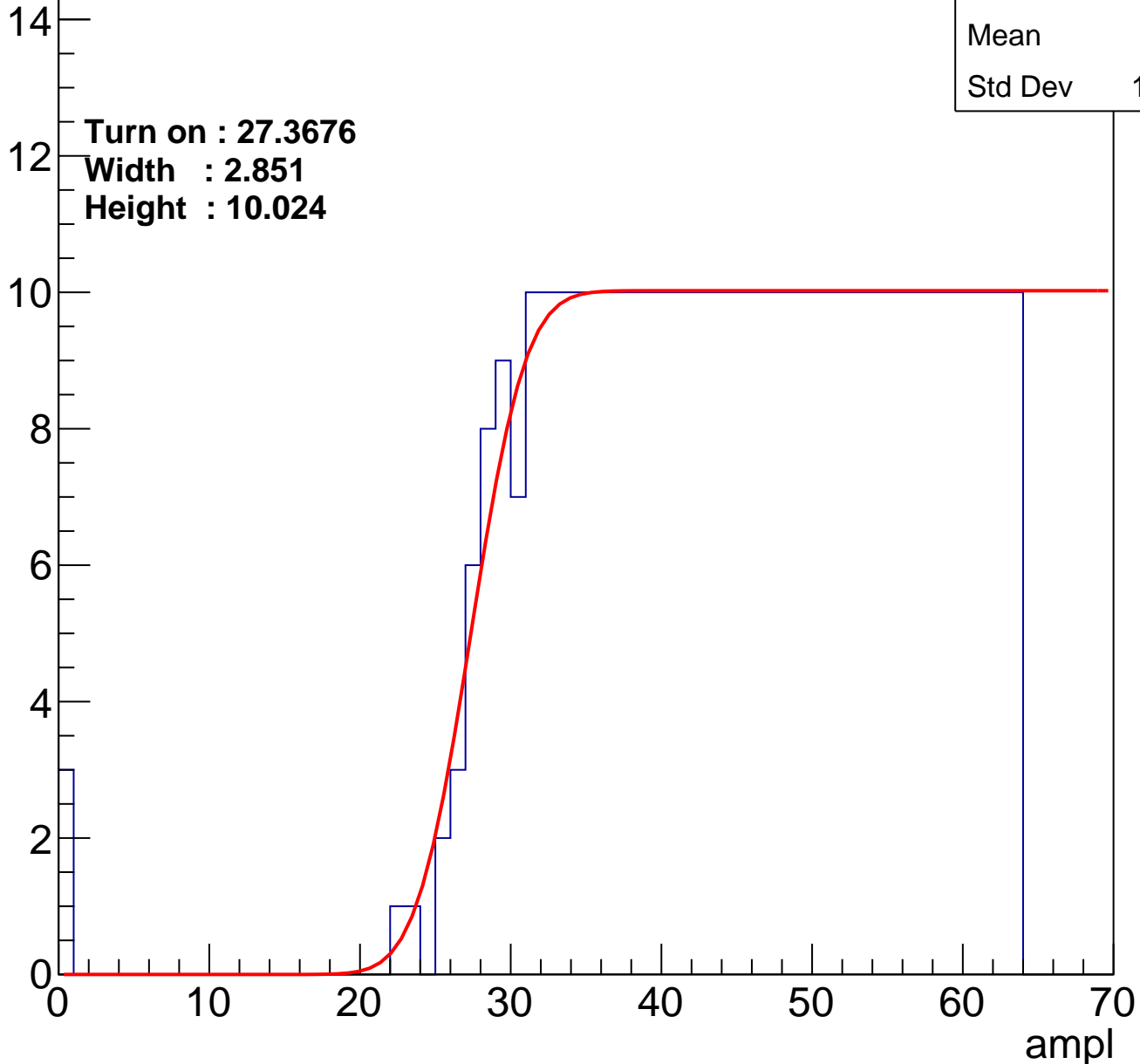
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.7 |
| Std Dev | 11.43 |

Turn on : 27.3676

Width : 2.851

Height : 10.024

Entry



B0L000S, U23-ch22

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.05 |
| Std Dev | 11.65 |

Turn on : 26.2228

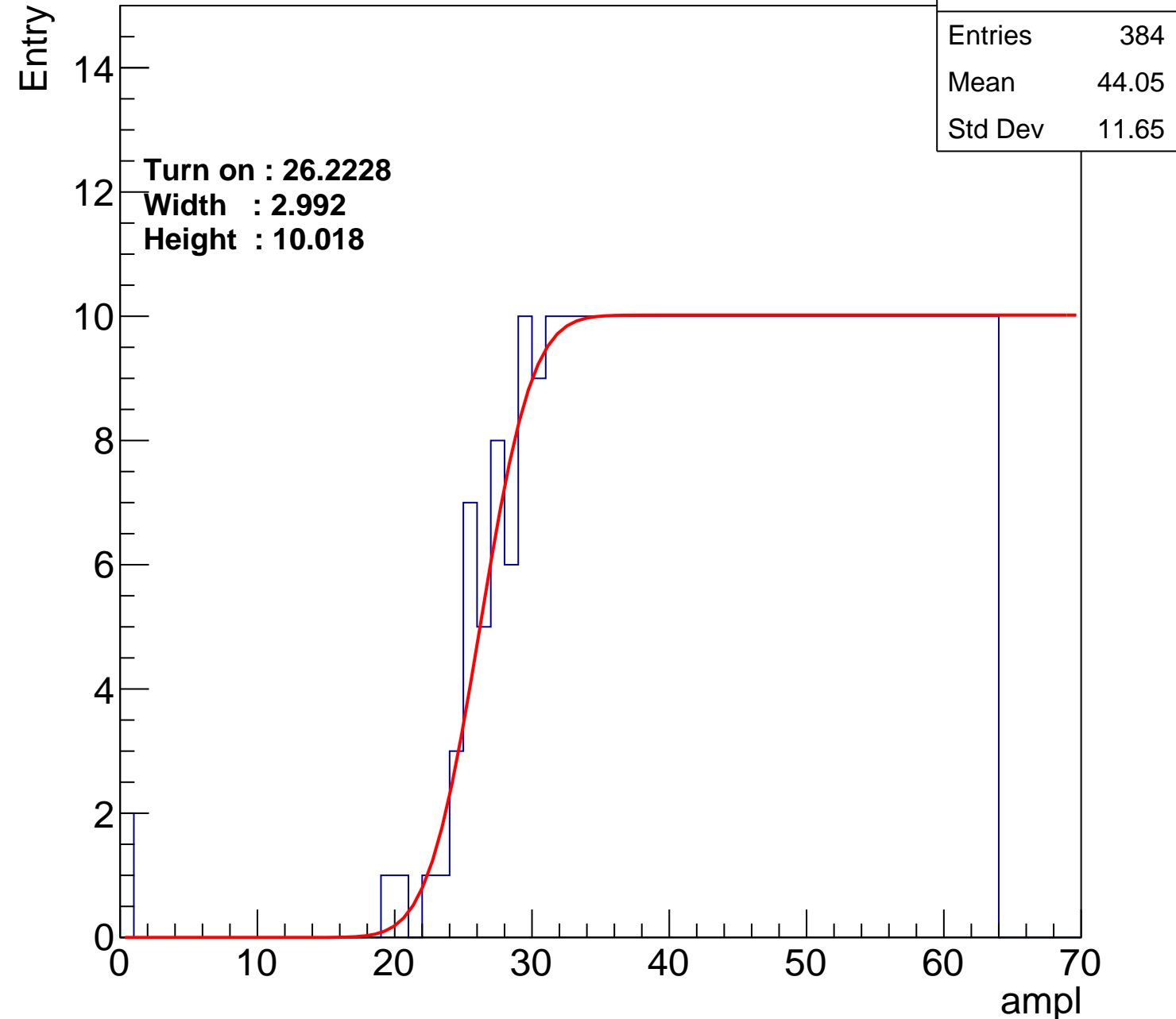
Width : 2.992

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch23

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.82 |
| Std Dev | 11.86 |

Turn on : 25.6305

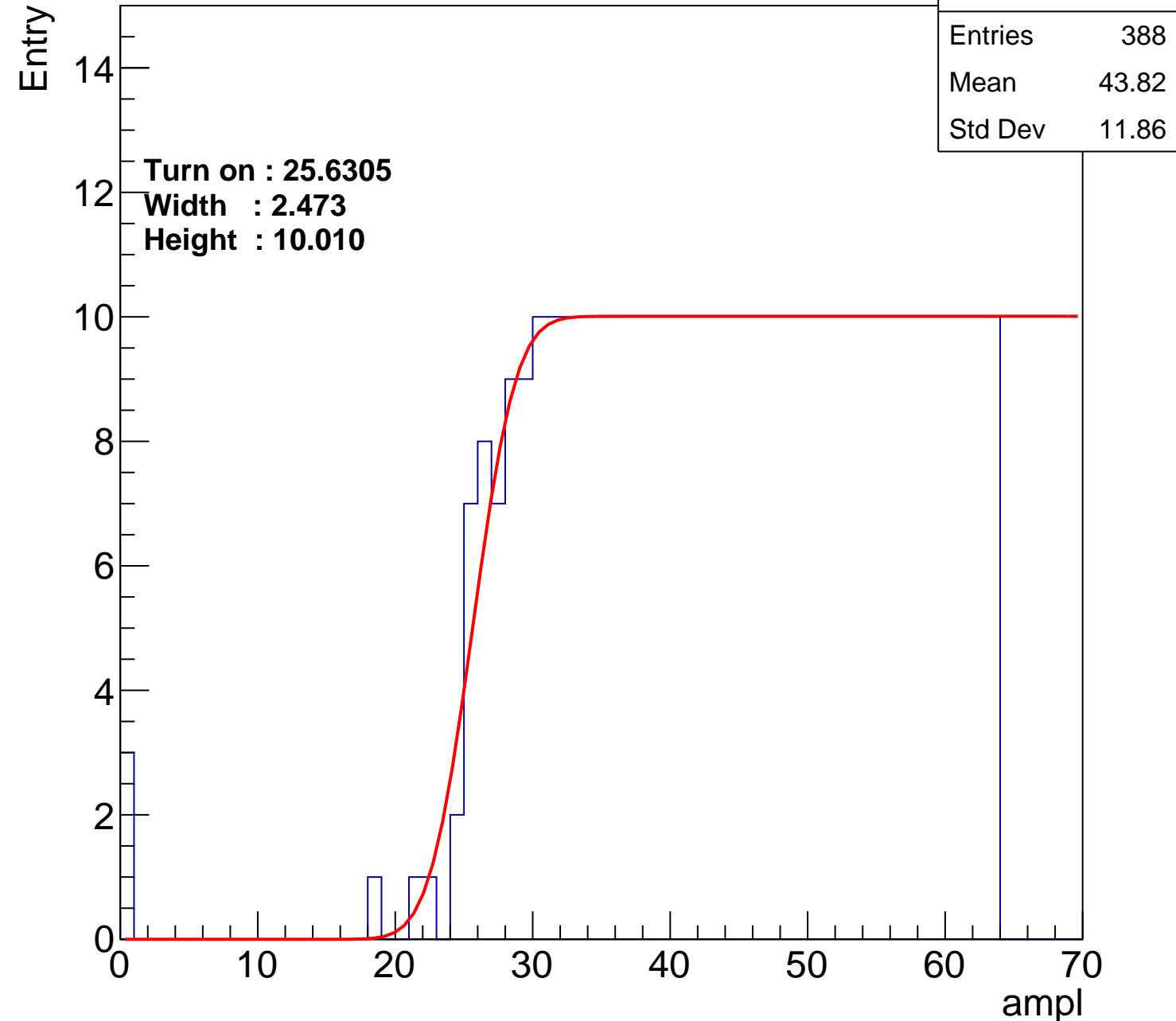
Width : 2.473

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 25.5794
Width : 2.901
Height : 10.003



B0L000S, U23-ch25

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.57 |
| Std Dev | 11.33 |

Turn on : 26.5922

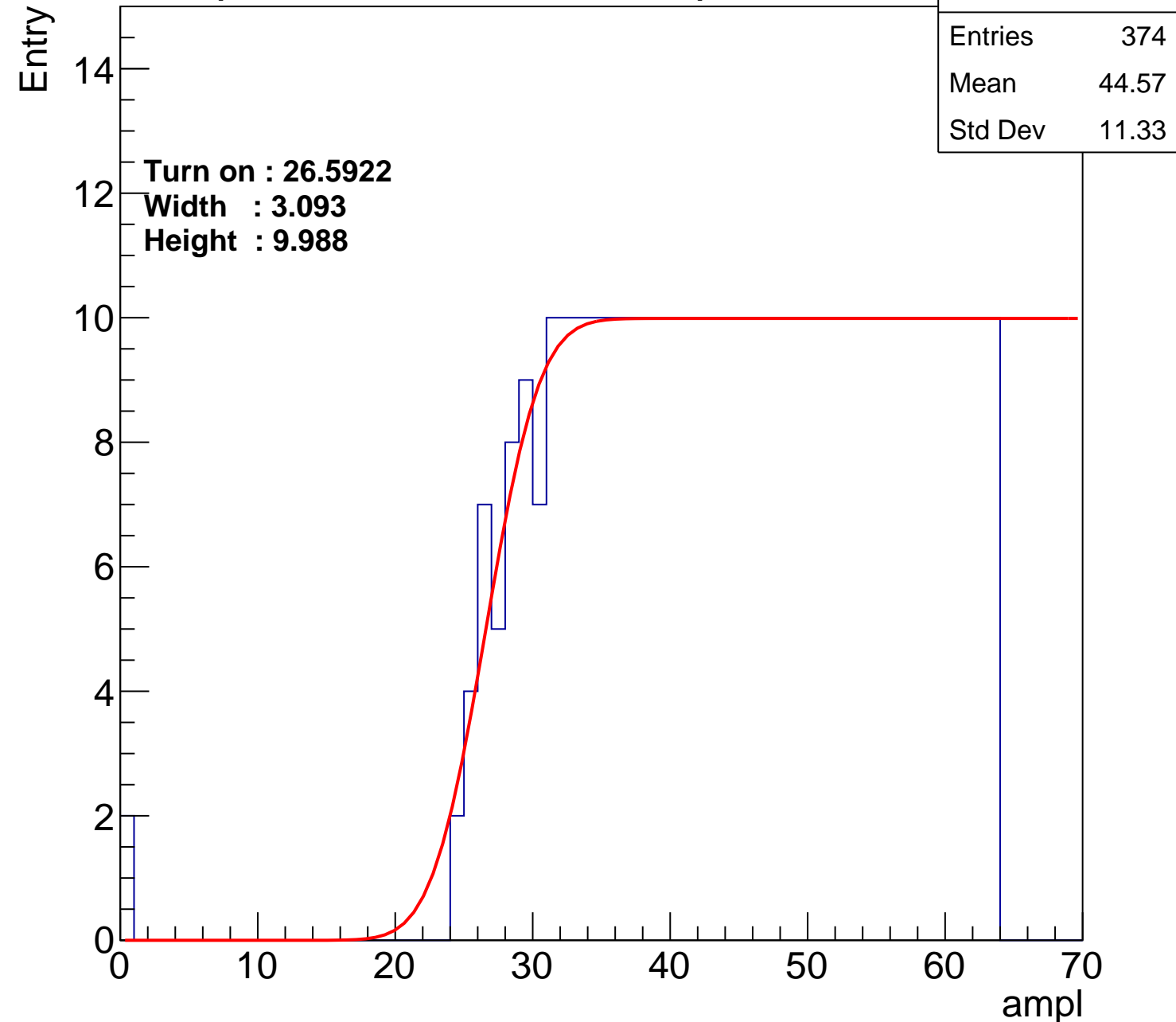
Width : 3.093

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch26

calib_packv5_042523_0143.root, FC#5, port B1

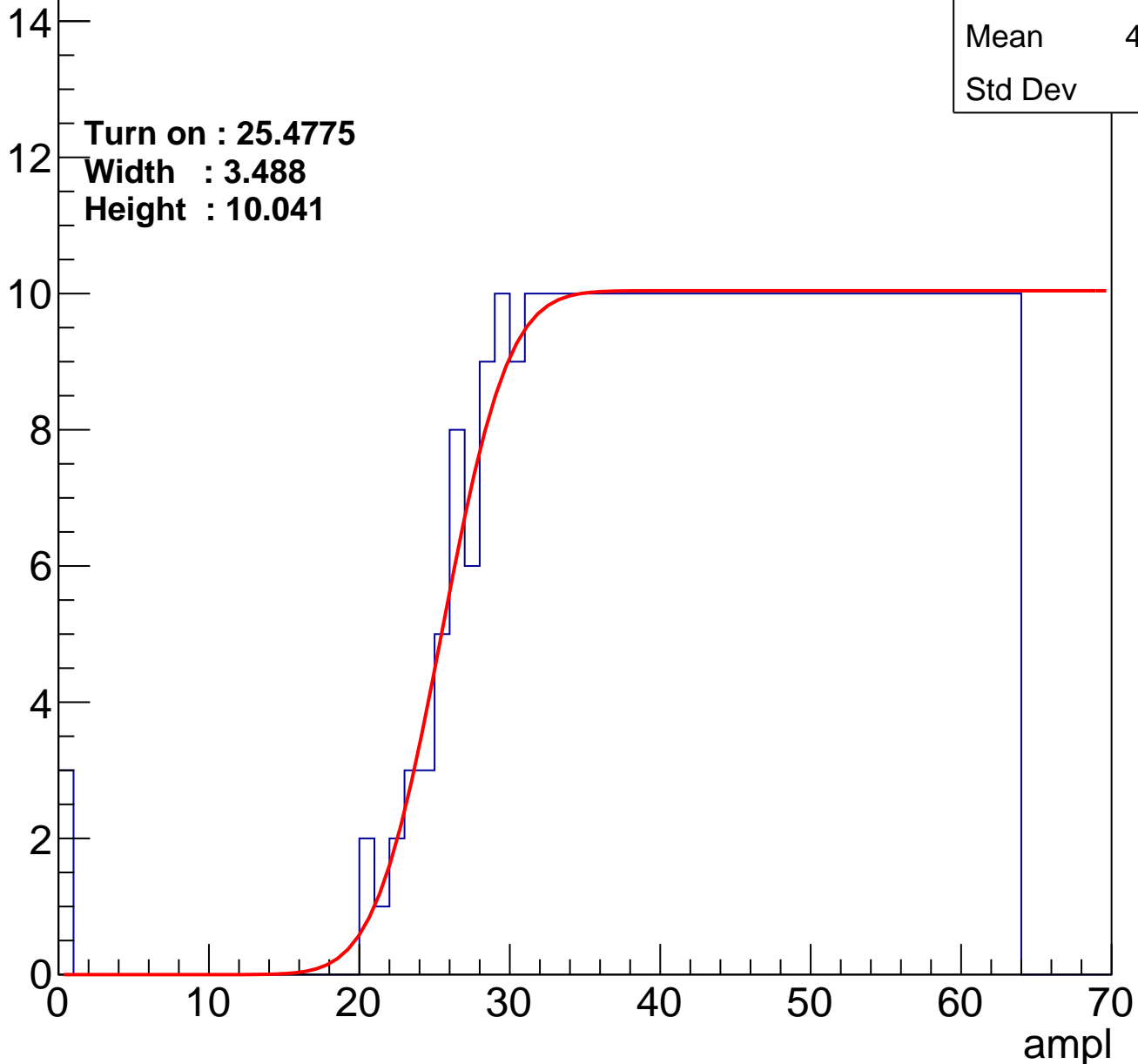
| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.64 |
| Std Dev | 12 |

Turn on : 25.4775

Width : 3.488

Height : 10.041

Entry



B0L000S, U23-ch27

calib_packv5_042523_0143.root, FC#5, port B1

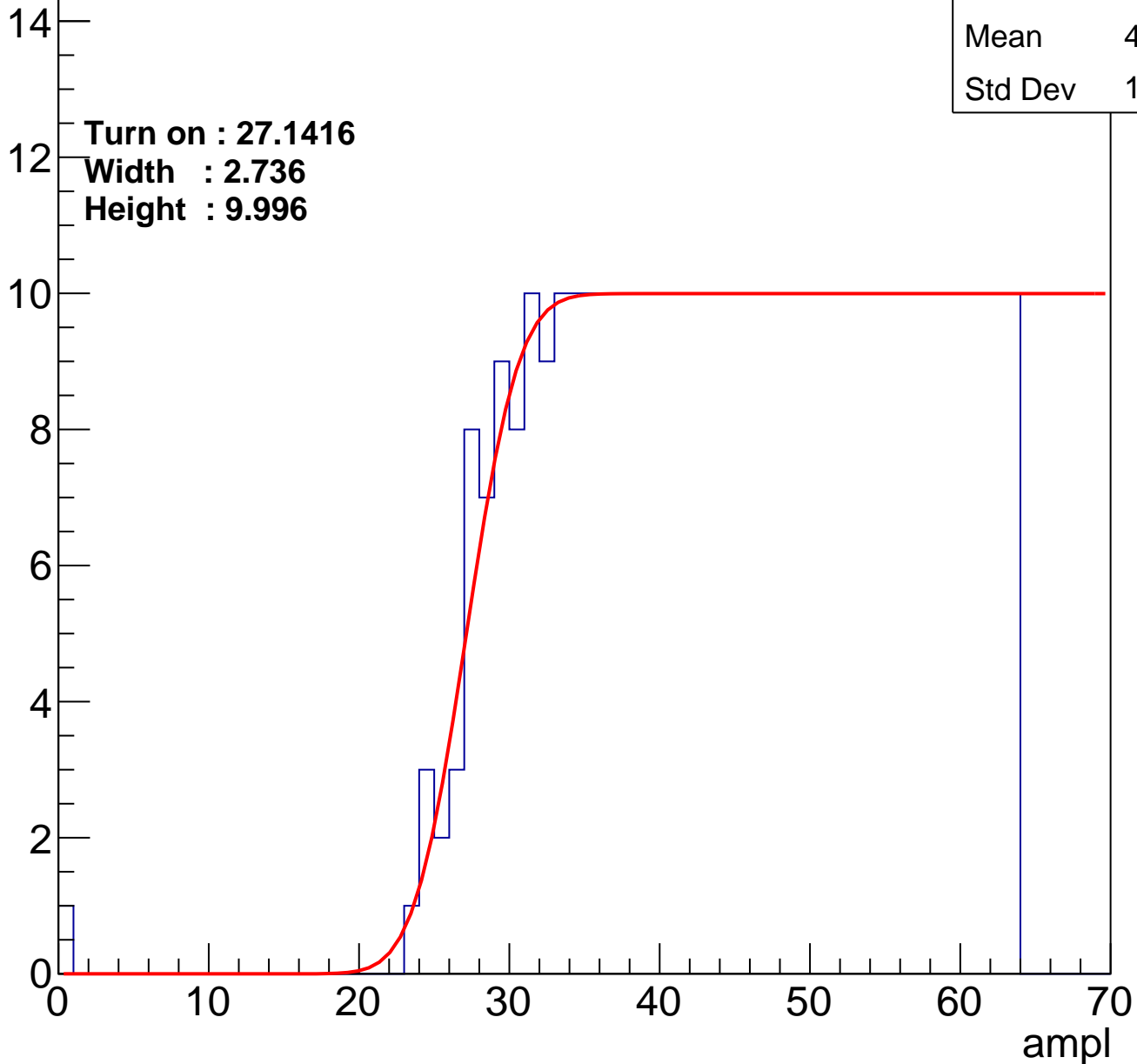
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.78 |
| Std Dev | 11.07 |

Turn on : 27.1416

Width : 2.736

Height : 9.996

Entry



B0L000S, U23-ch28

calib_packv5_042523_0143.root, FC#5, port B1

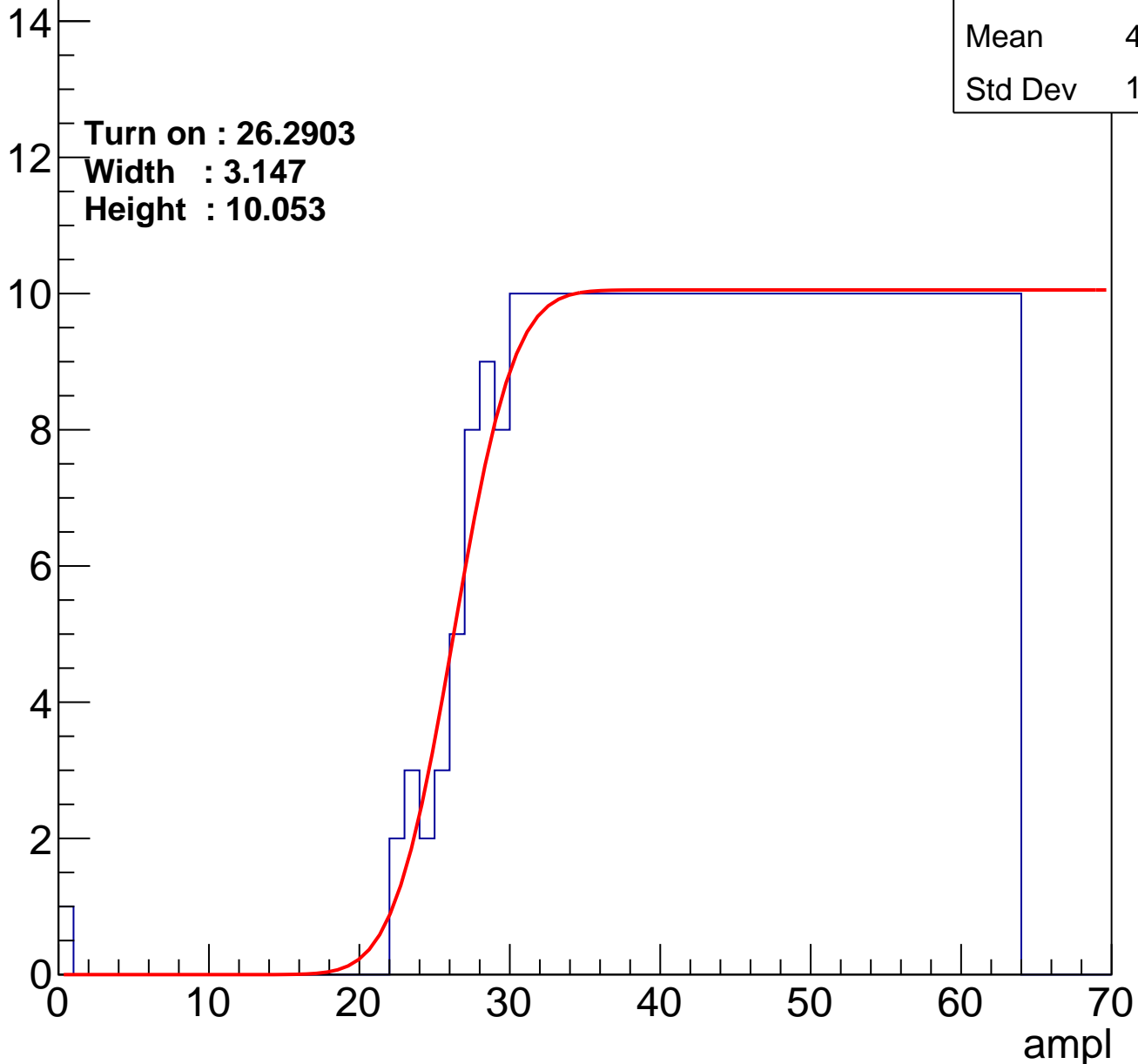
| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.29 |
| Std Dev | 11.34 |

Turn on : 26.2903

Width : 3.147

Height : 10.053

Entry



B0L000S, U23-ch29

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.3 |
| Std Dev | 10.82 |

Turn on : 28.6029

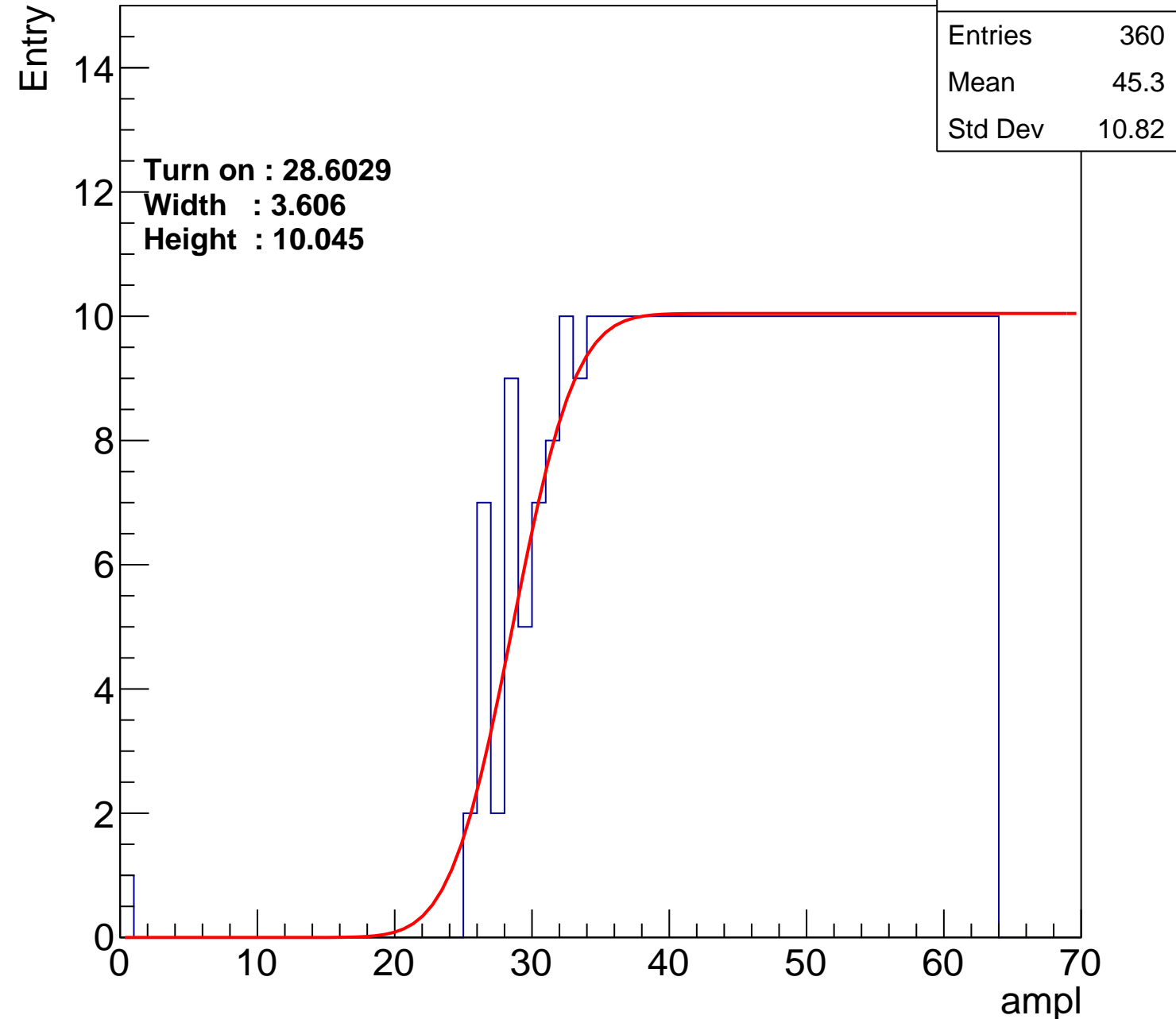
Width : 3.606

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch30

calib_packv5_042523_0143.root, FC#5, port B1

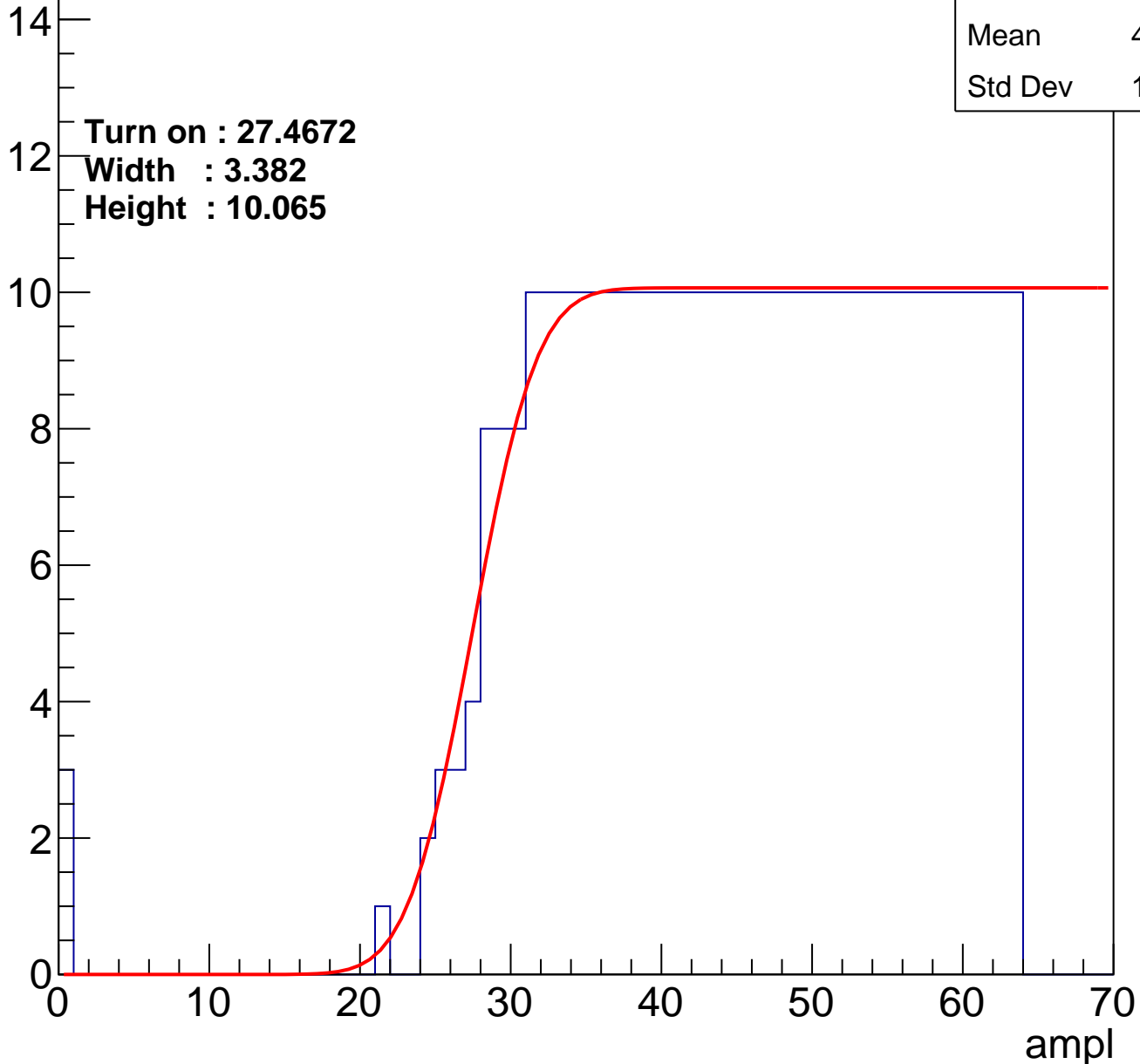
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.69 |
| Std Dev | 11.45 |

Turn on : 27.4672

Width : 3.382

Height : 10.065

Entry



B0L000S, U23-ch31

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.23 |
| Std Dev | 11.44 |

Turn on : 26.3160

Width : 3.582

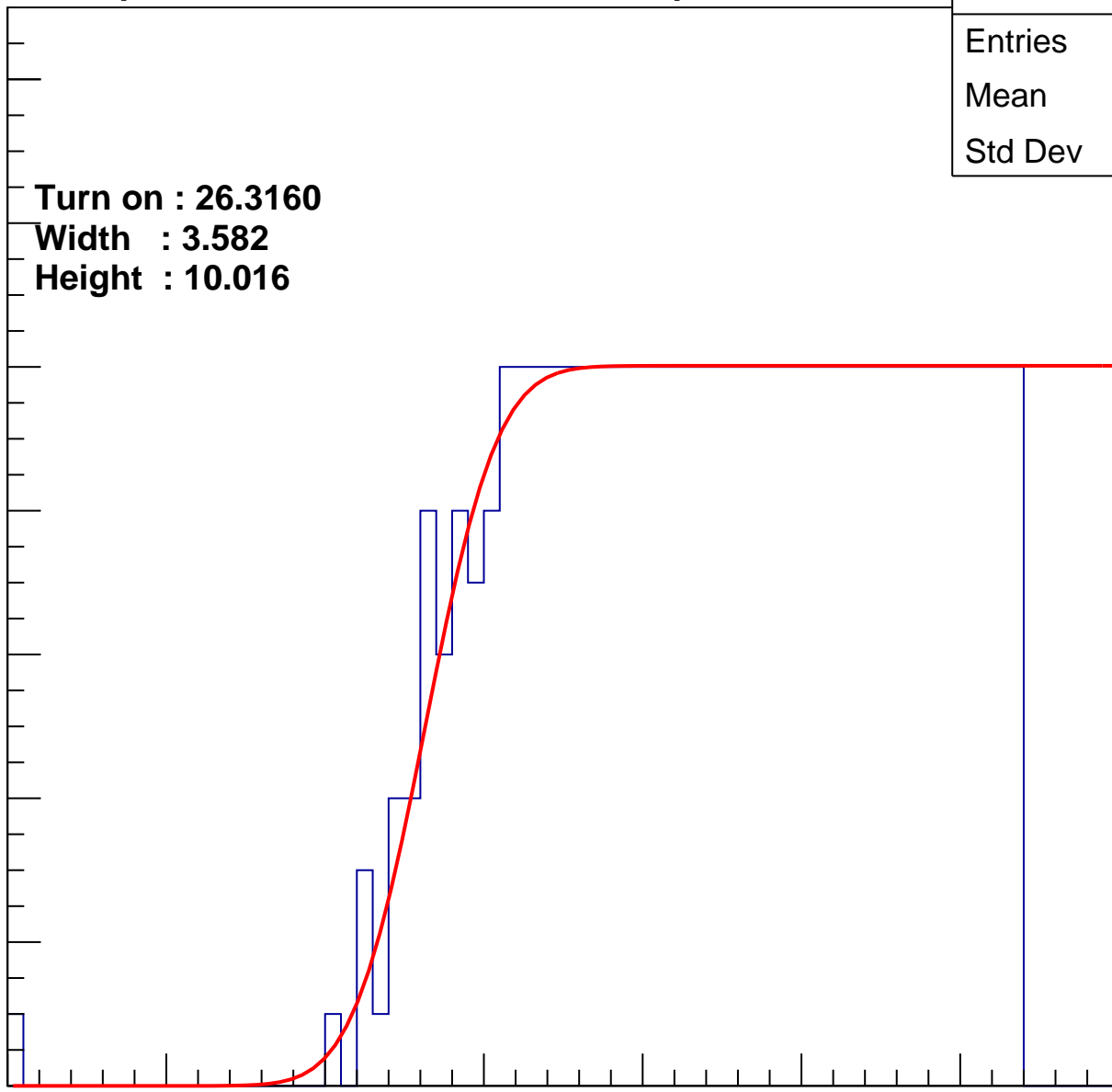
Height : 10.016

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L000S, U23-ch32

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.65 |
| Std Dev | 11.27 |

Turn on : 26.8684

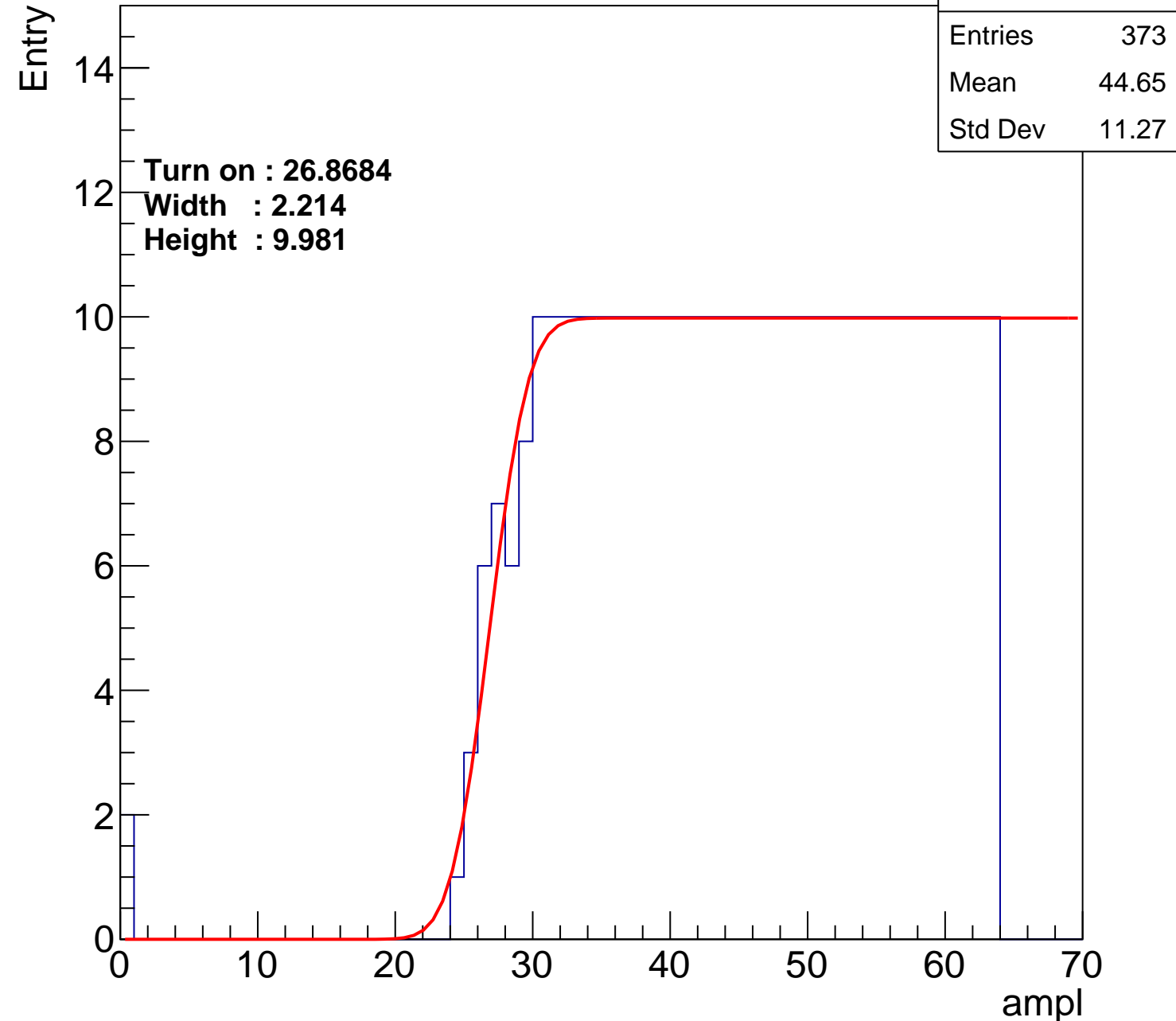
Width : 2.214

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch33

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.99 |
| Std Dev | 10.95 |

Turn on : 27.7114

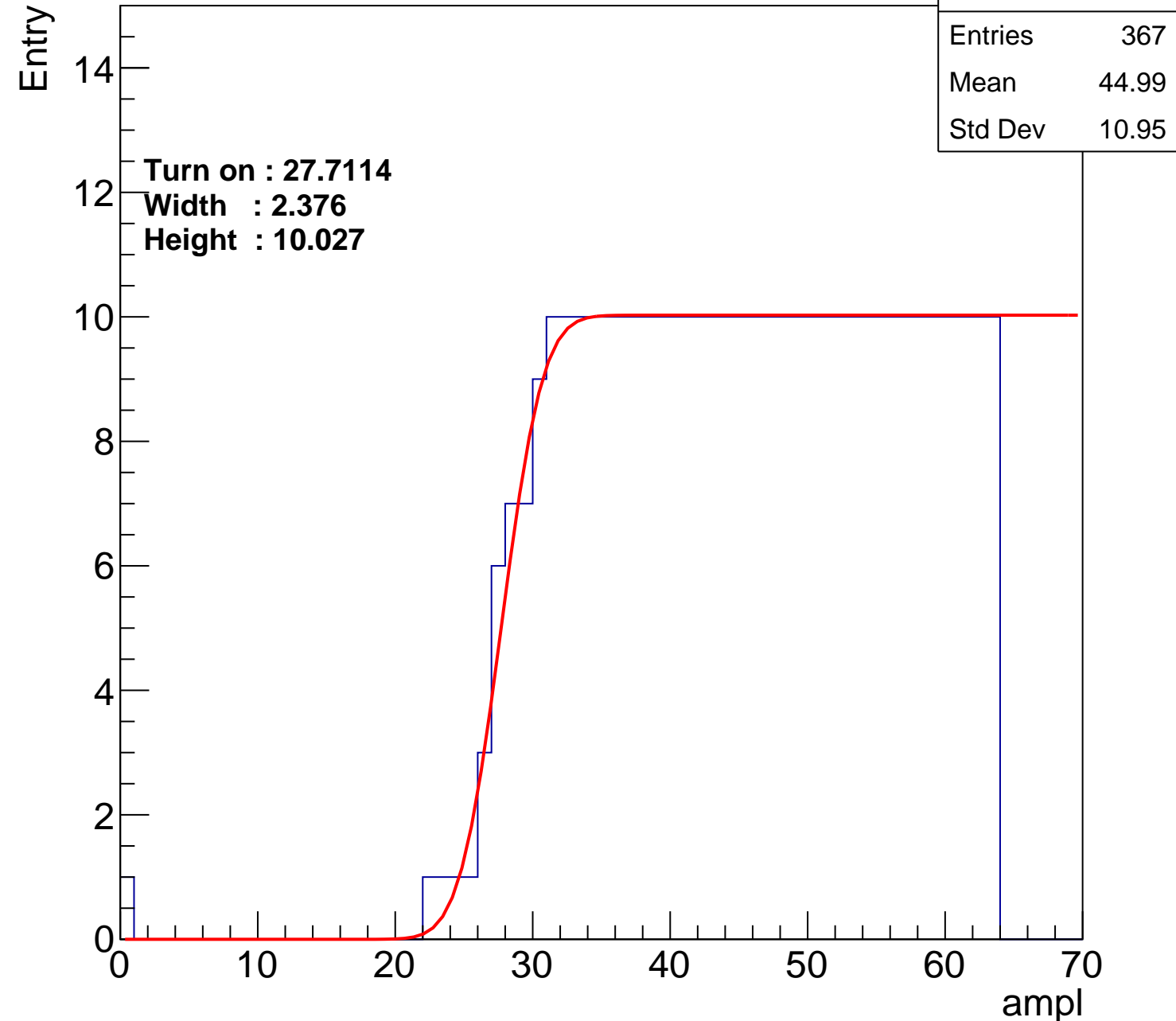
Width : 2.376

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 28.4954
Width : 3.522
Height : 10.039



B0L000S, U23-ch35

calib_packv5_042523_0143.root, FC#5, port B1

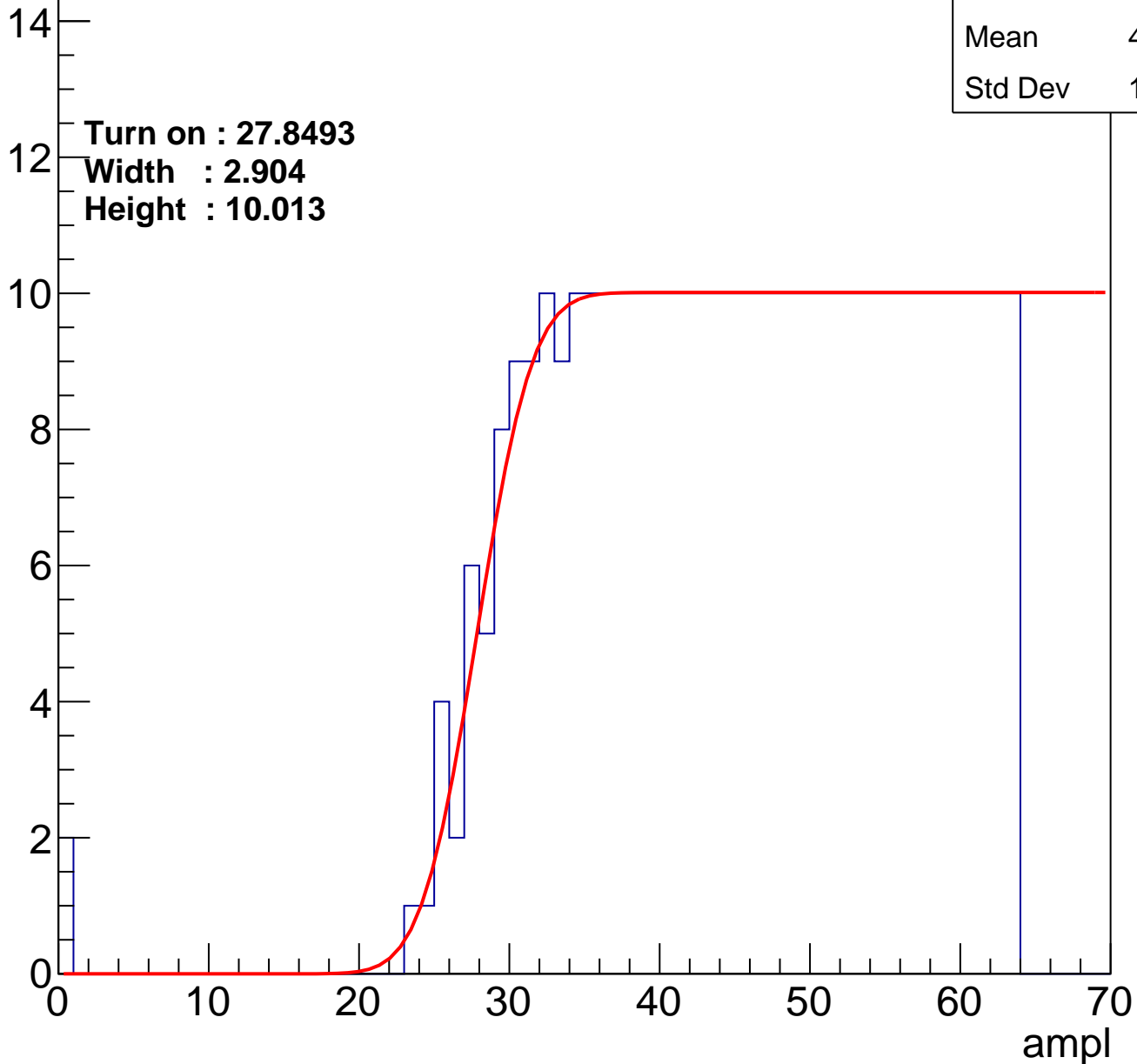
| | |
|---------|-------|
| Entries | 366 |
| Mean | 44.94 |
| Std Dev | 11.17 |

Turn on : 27.8493

Width : 2.904

Height : 10.013

Entry



B0L000S, U23-ch36

calib_packv5_042523_0143.root, FC#5, port B1

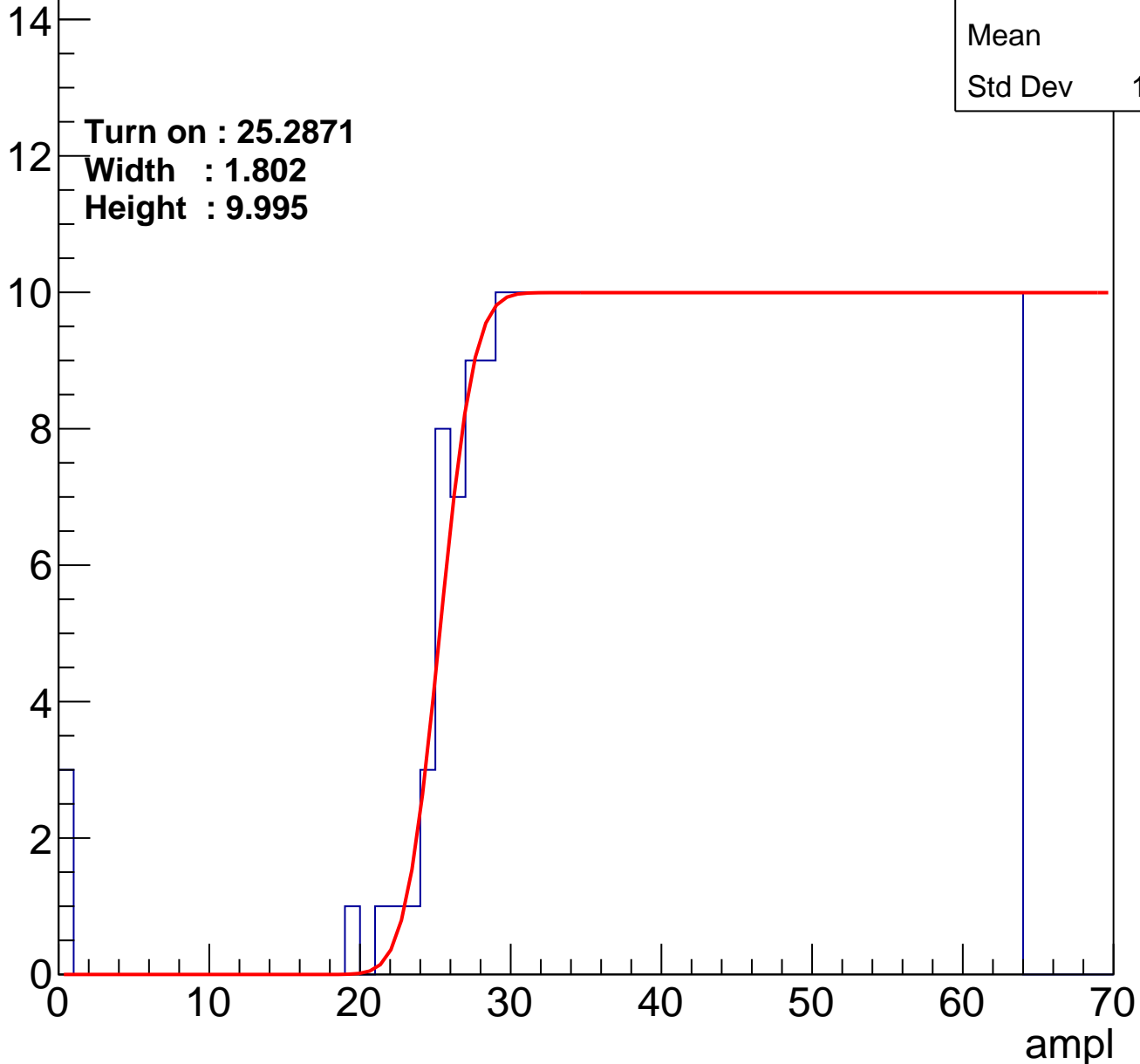
| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.6 |
| Std Dev | 11.96 |

Turn on : 25.2871

Width : 1.802

Height : 9.995

Entry



B0L000S, U23-ch37

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.97 |
| Std Dev | 11.78 |

Turn on : 25.7127

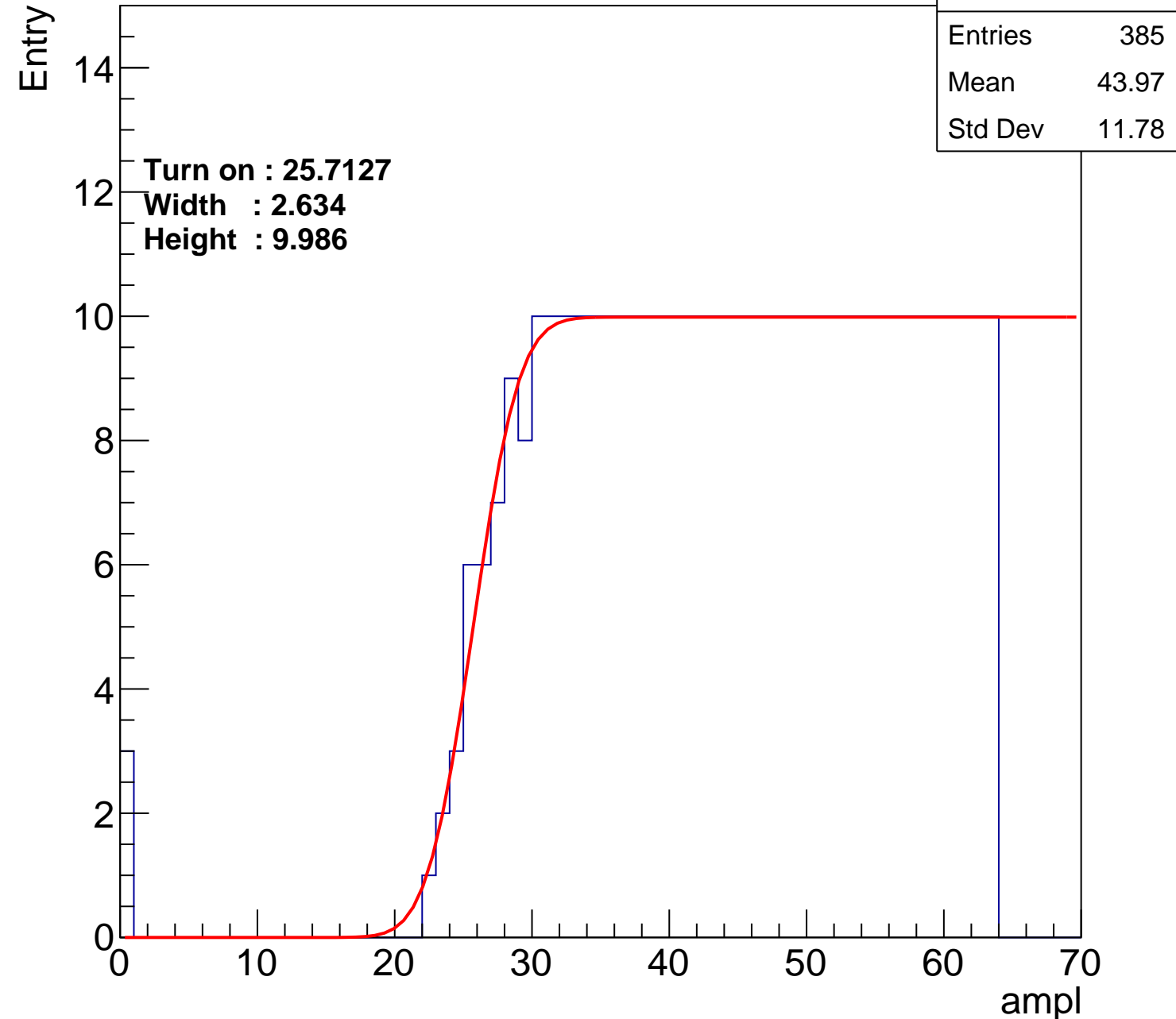
Width : 2.634

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch38

calib_packv5_042523_0143.root, FC#5, port B1

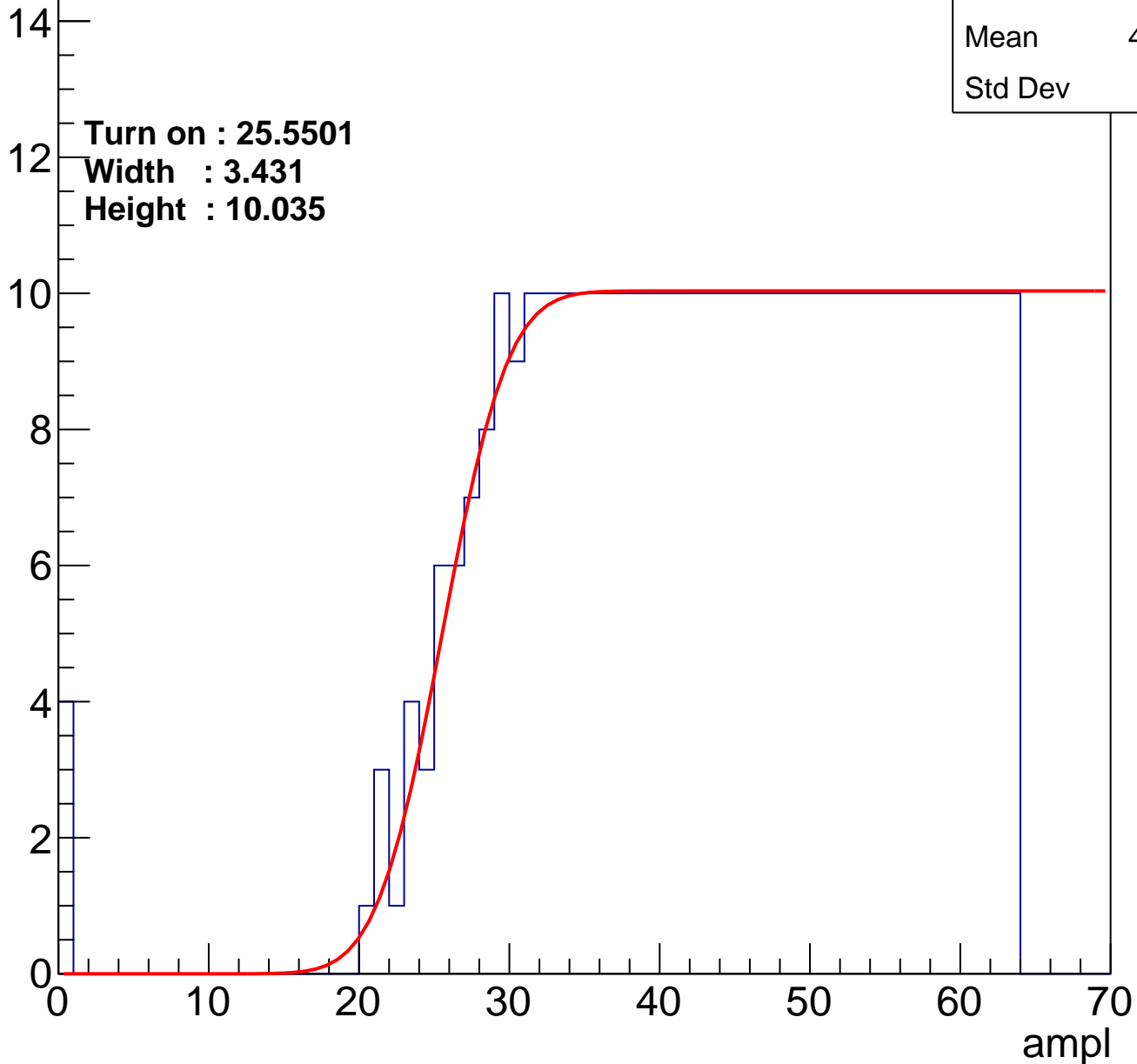
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.52 |
| Std Dev | 12.2 |

Turn on : 25.5501

Width : 3.431

Height : 10.035

Entry



B0L000S, U23-ch39

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.34 |
| Std Dev | 11.76 |

Turn on : 27.2239

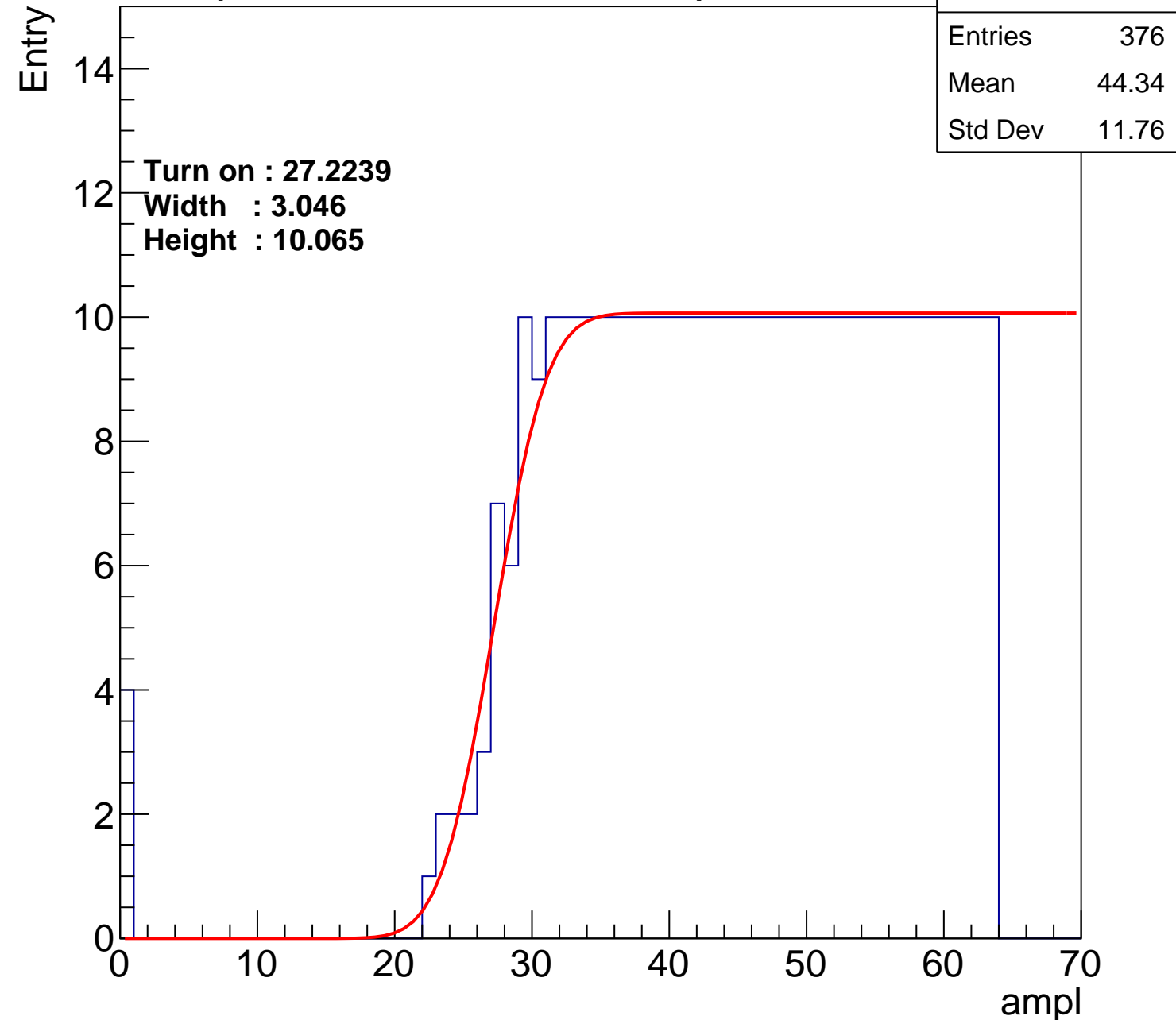
Width : 3.046

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch40

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 385 |
| Mean | 43.97 |
| Std Dev | 11.79 |

Turn on : 25.8588

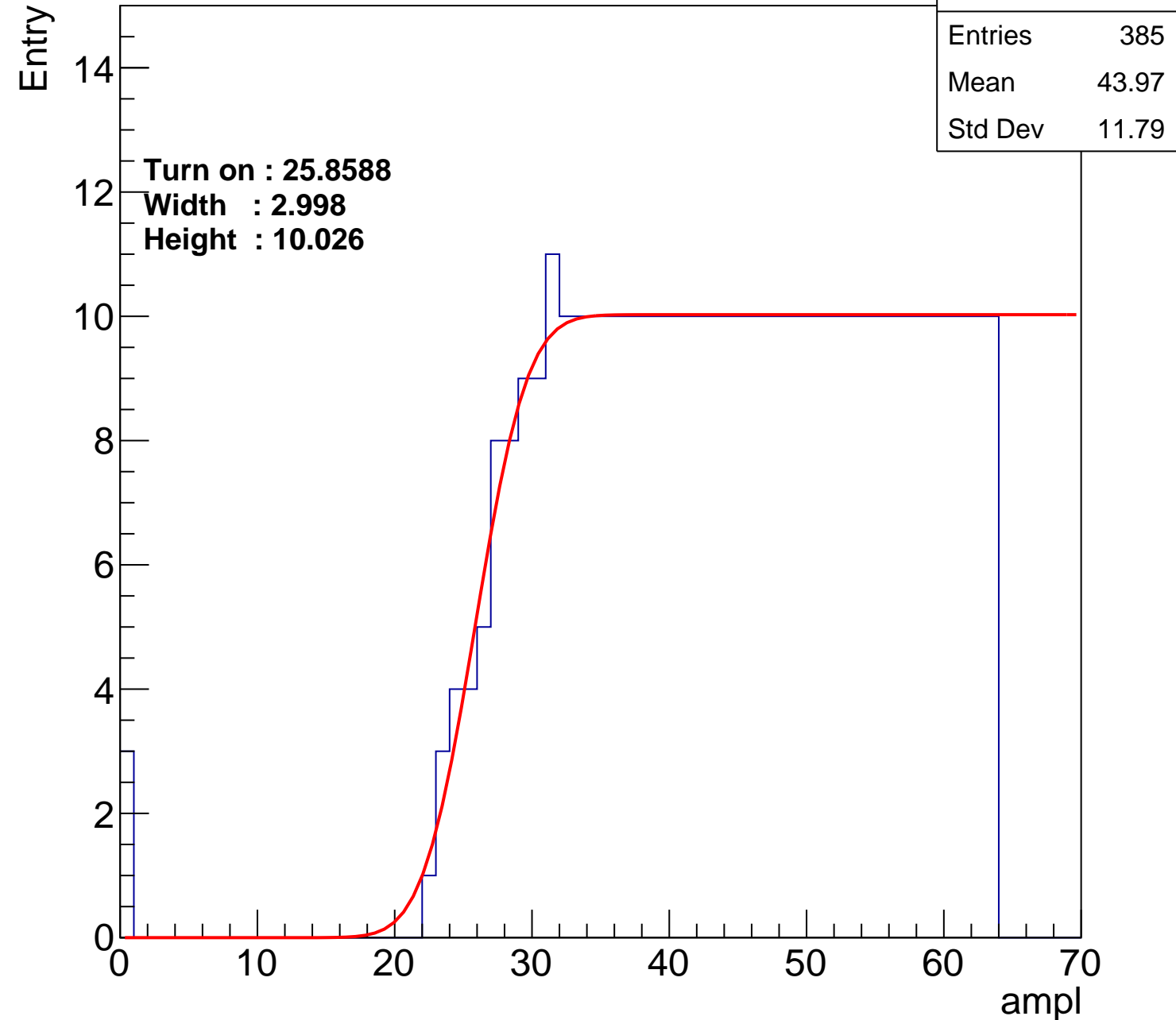
Width : 2.998

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch41

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.66 |
| Std Dev | 11.12 |

Turn on : 26.8560

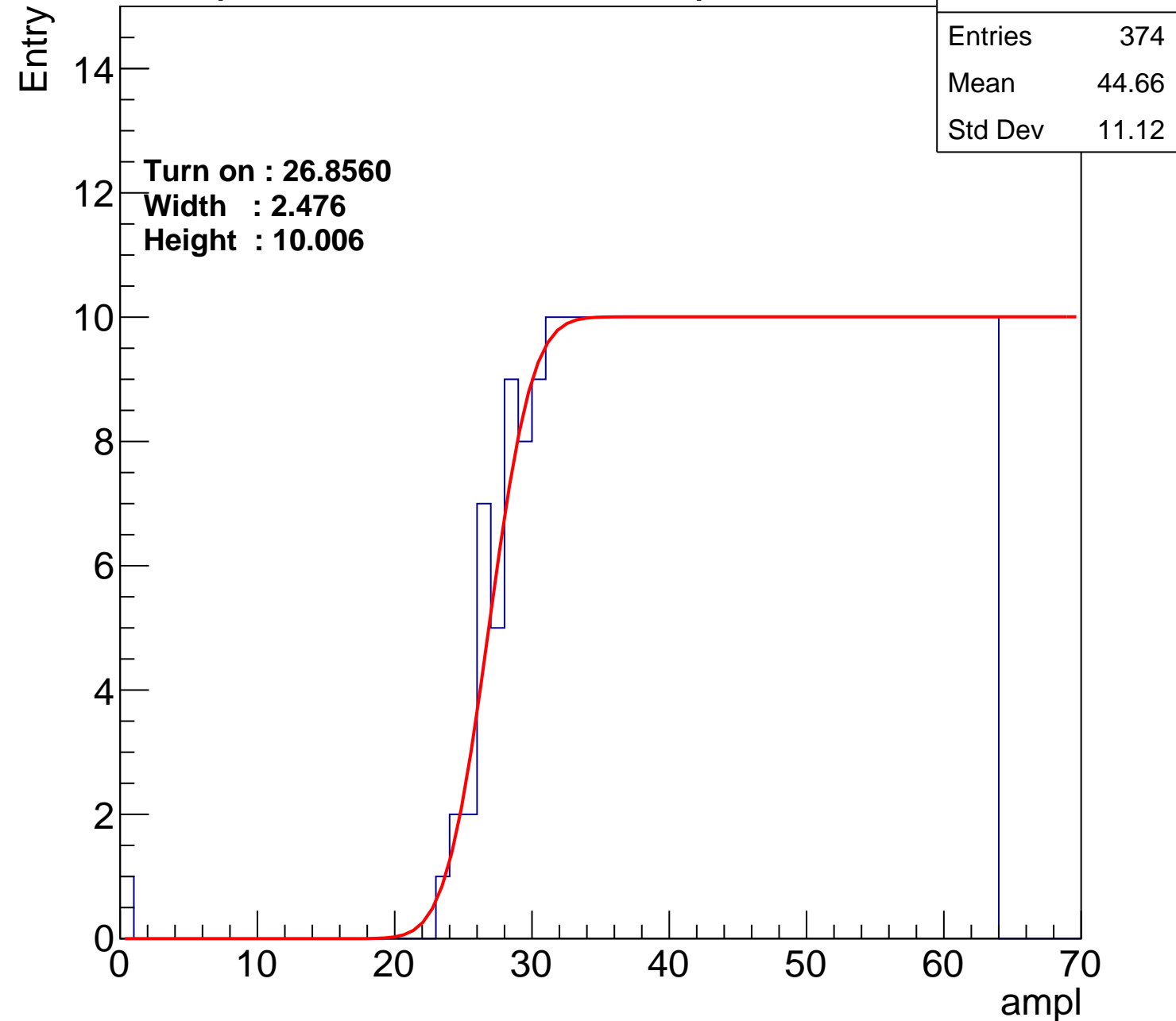
Width : 2.476

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch42

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 397 |
| Mean | 43.46 |
| Std Dev | 11.9 |

Turn on : 24.4647

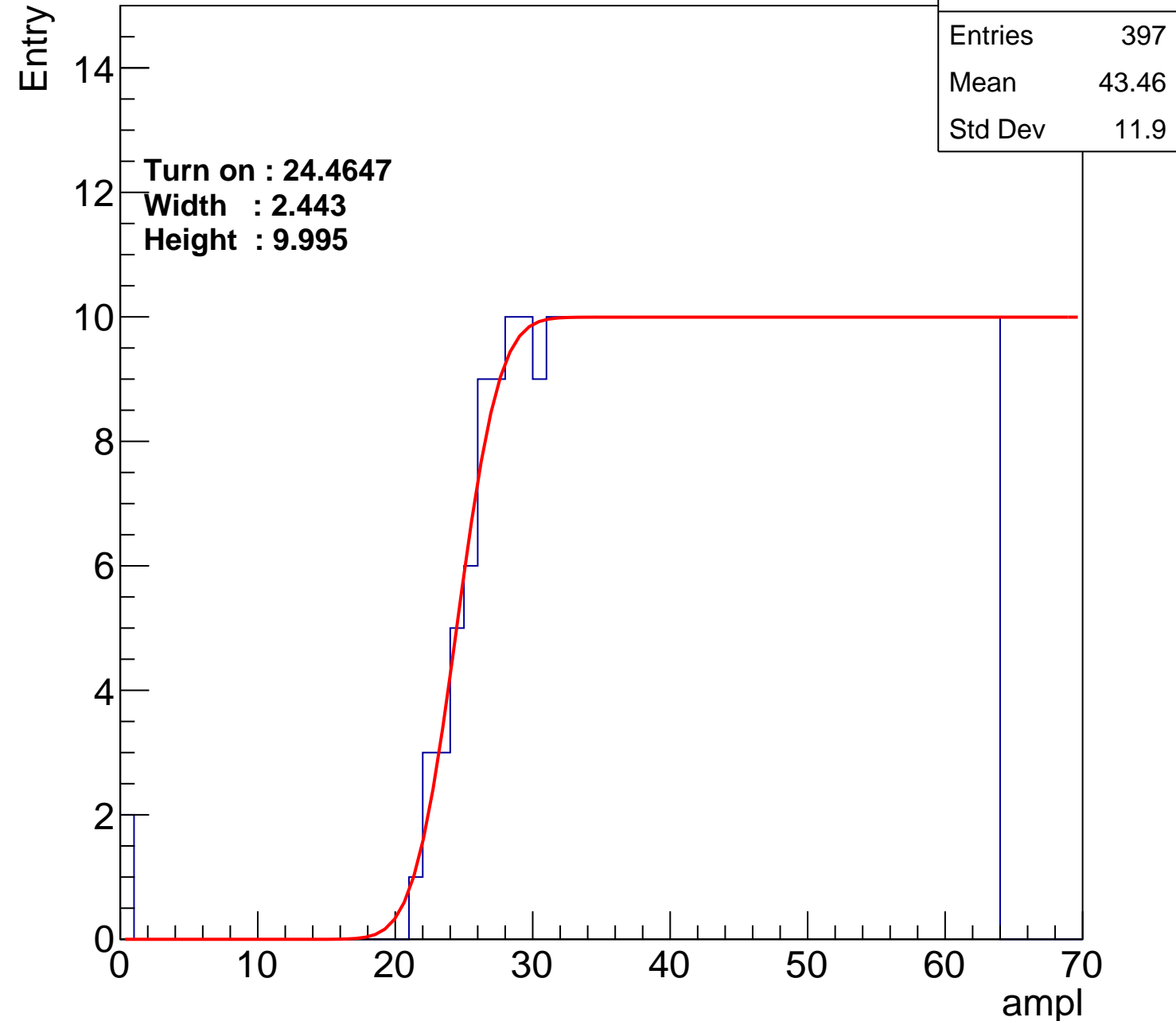
Width : 2.443

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch43

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.11 |
| Std Dev | 11.59 |

Turn on : 25.5941

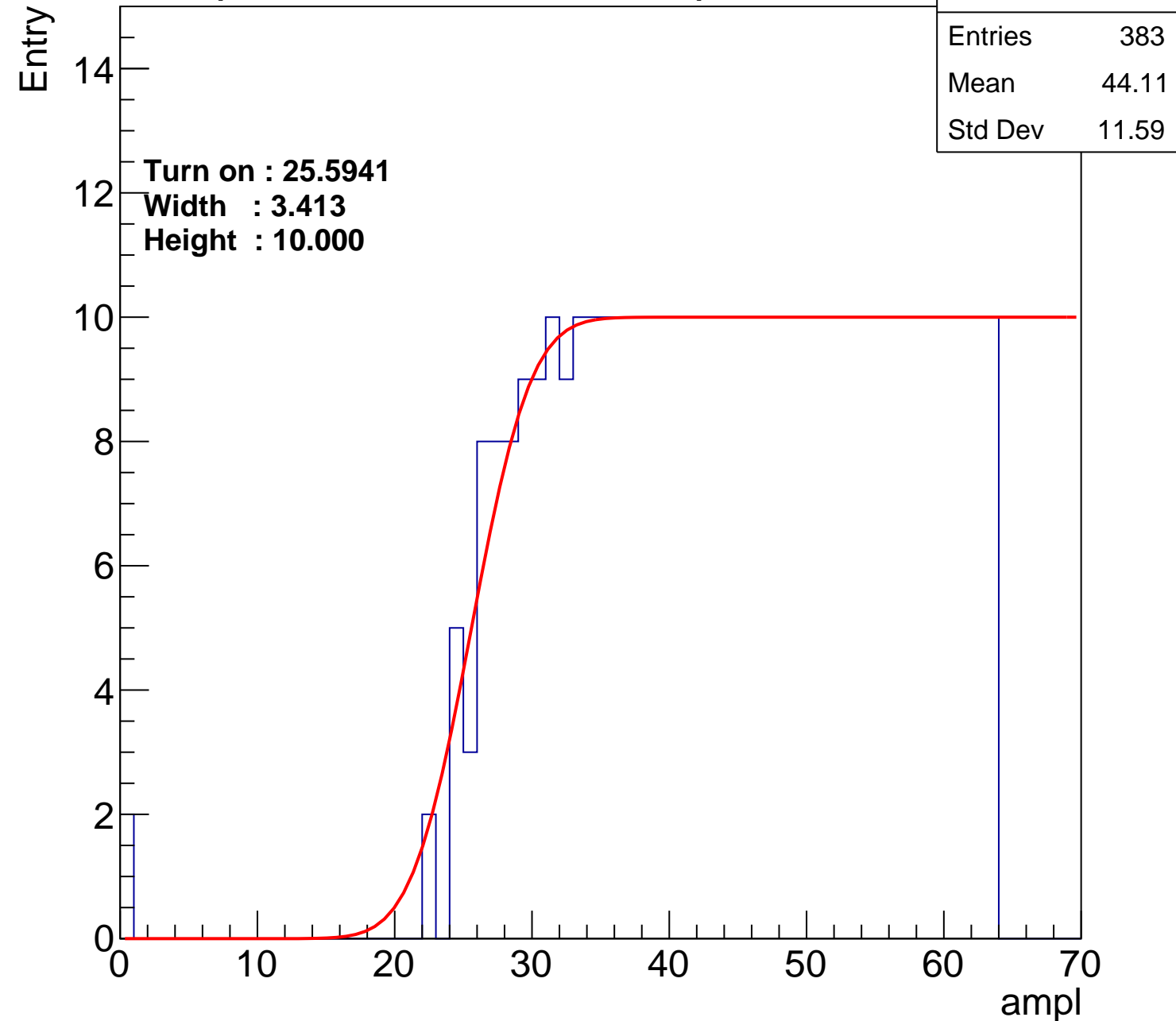
Width : 3.413

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch44

calib_packv5_042523_0143.root, FC#5, port B1

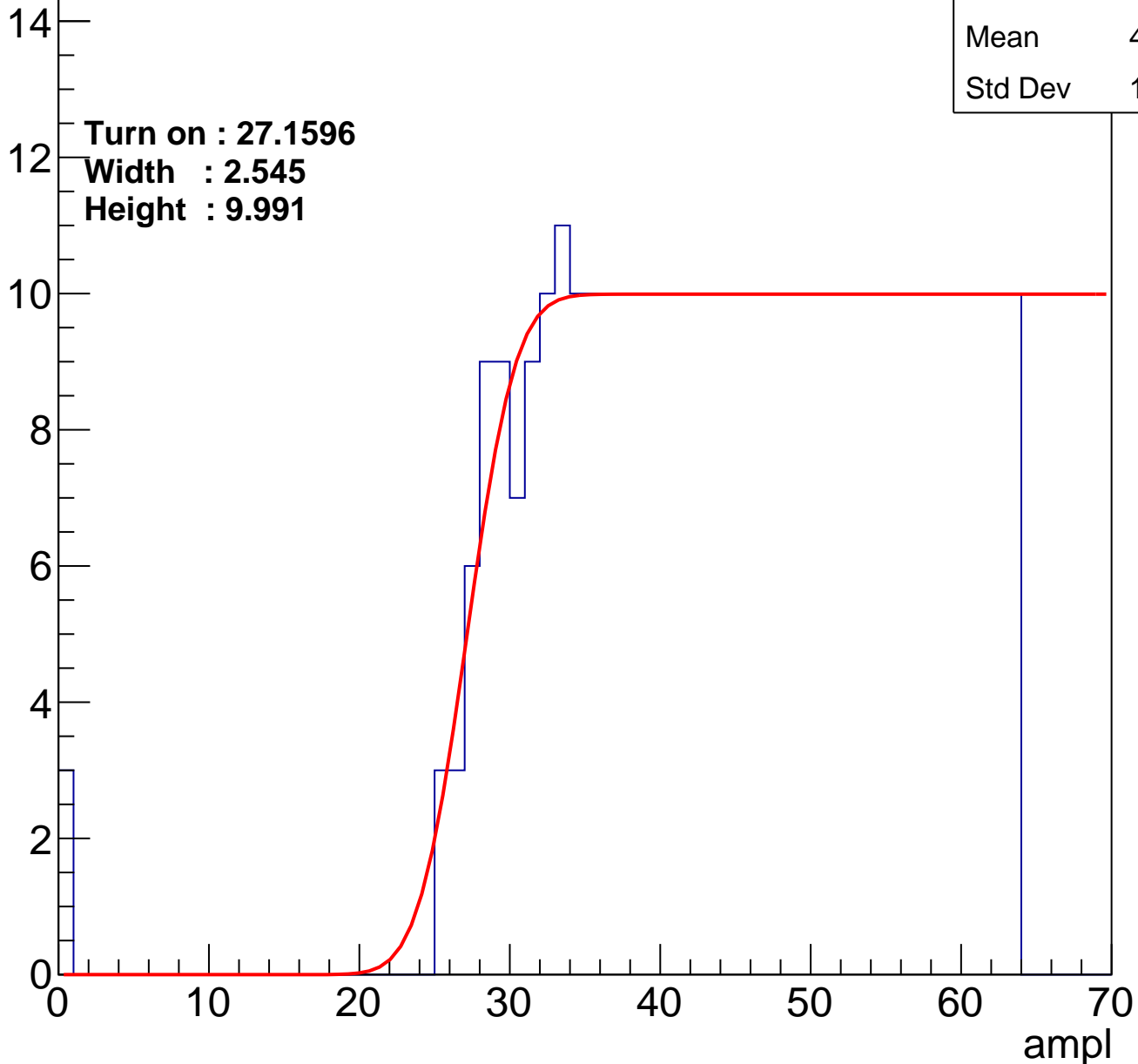
| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.73 |
| Std Dev | 11.38 |

Turn on : 27.1596

Width : 2.545

Height : 9.991

Entry



B0L000S, U23-ch45

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.31 |
| Std Dev | 11.8 |

Turn on : 27.1884

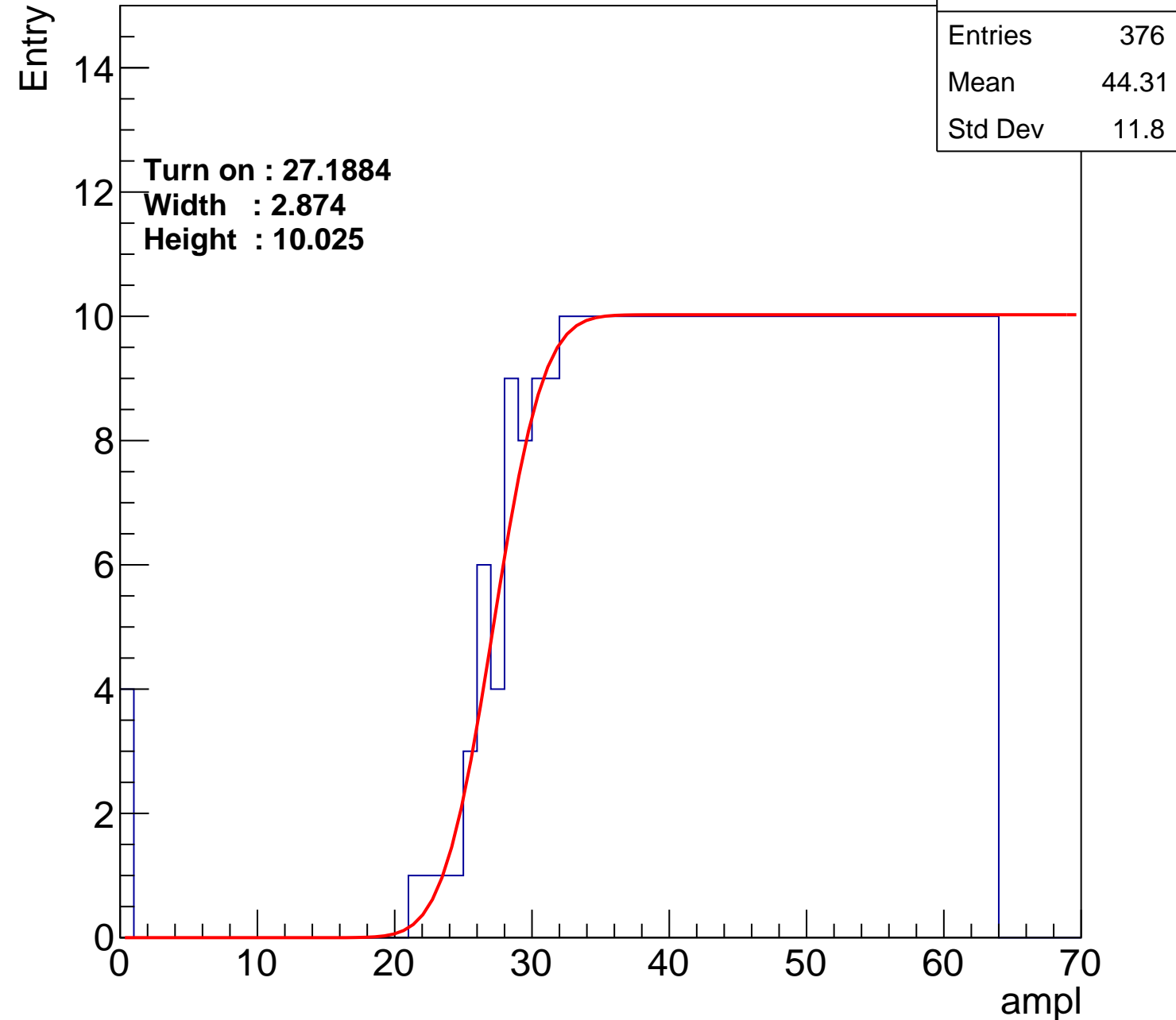
Width : 2.874

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch46

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.57 |
| Std Dev | 12.55 |

Turn on : 25.7435

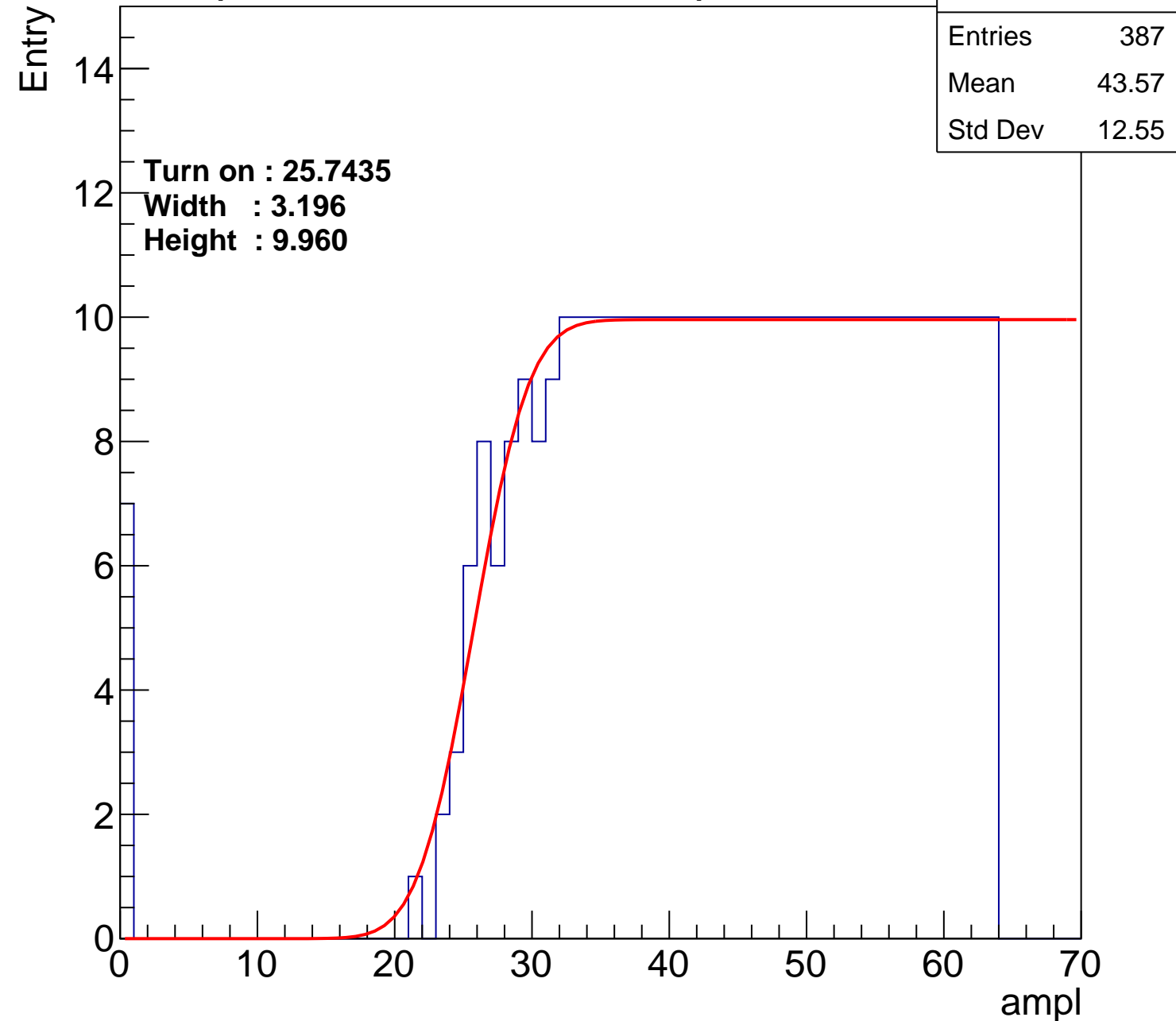
Width : 3.196

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch47

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 354 |
| Mean | 45.65 |
| Std Dev | 10.58 |

Turn on : 28.2476

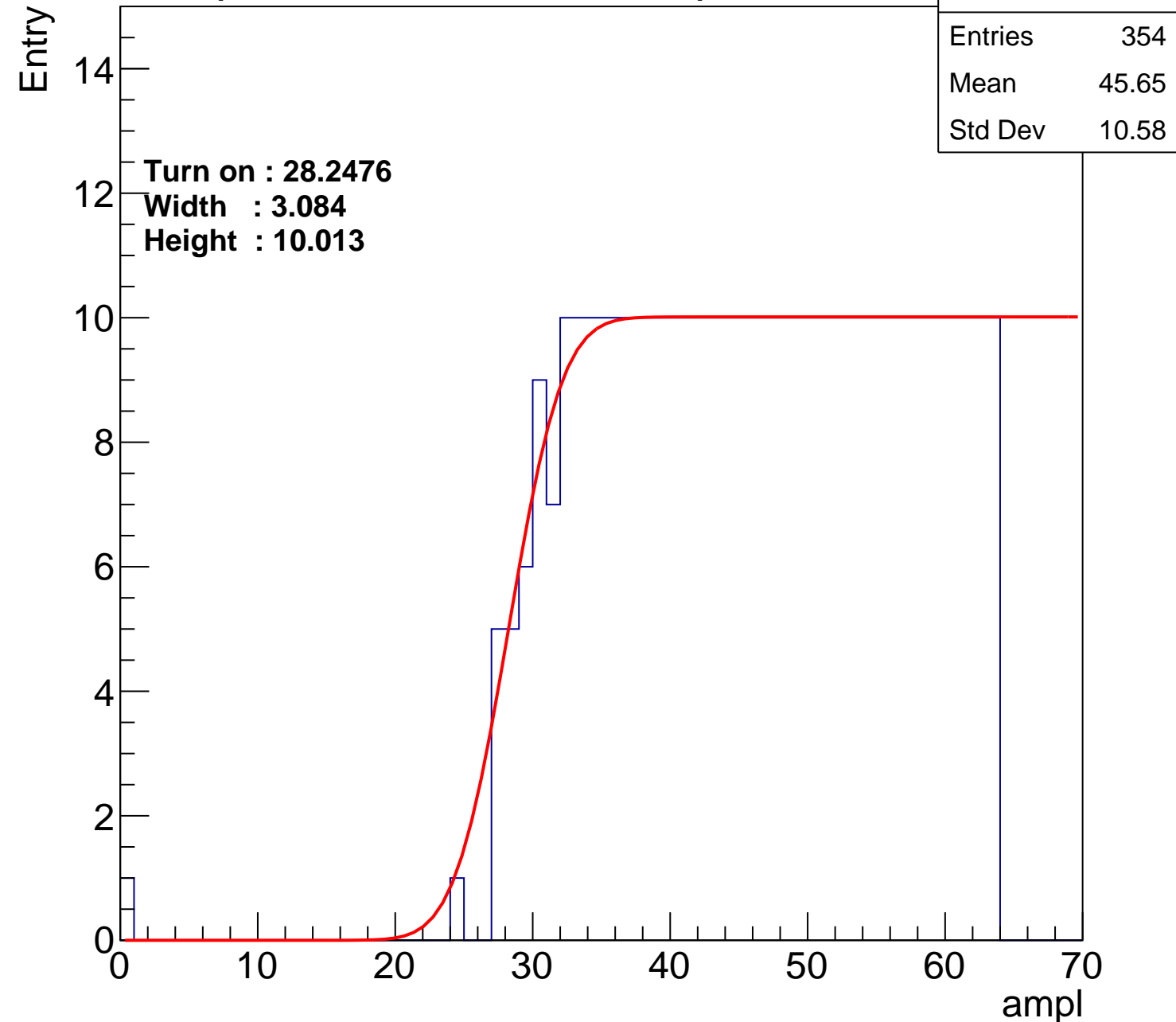
Width : 3.084

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch48

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.46 |
| Std Dev | 11.6 |

Turn on : 27.5616

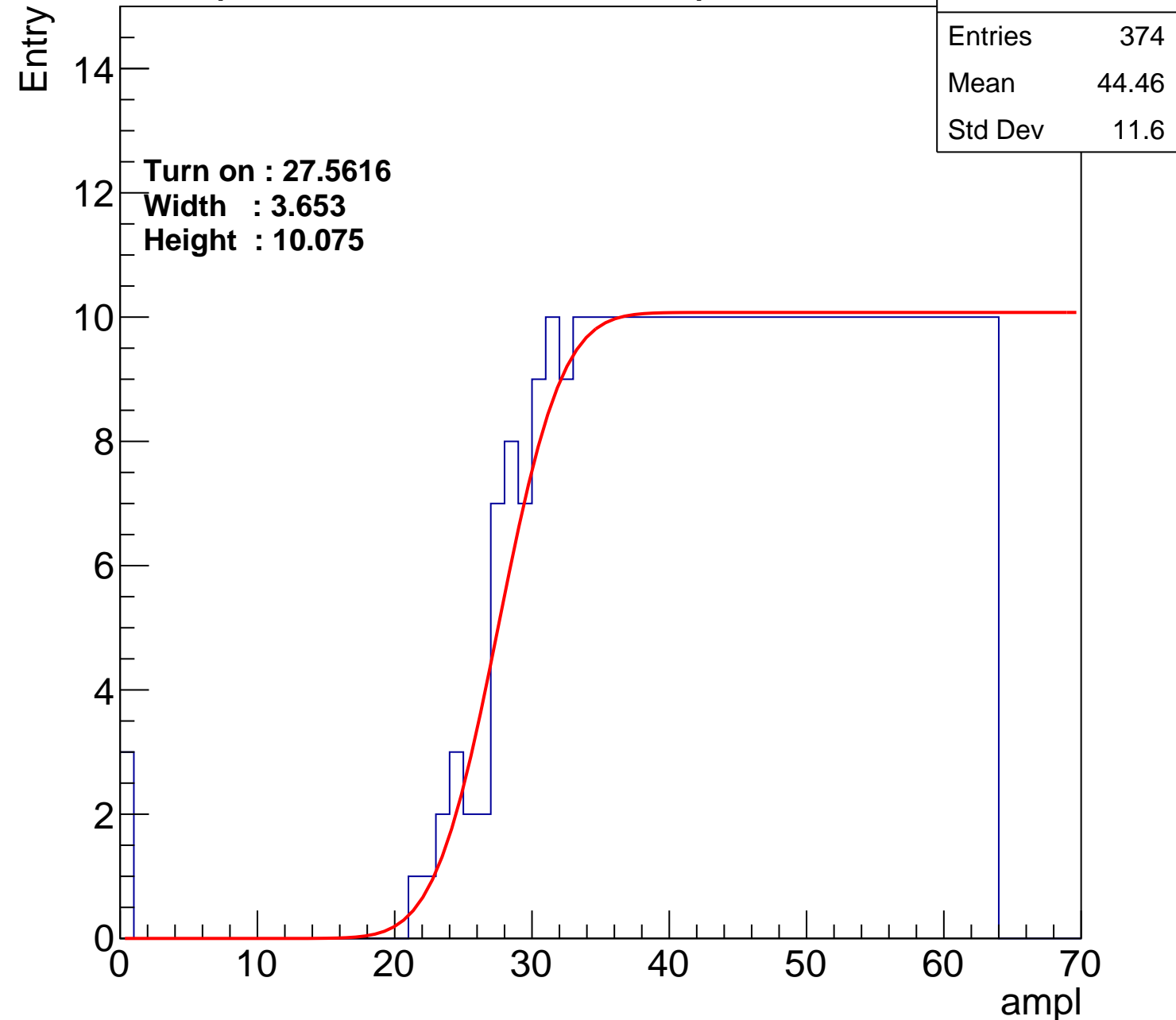
Width : 3.653

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch49

calib_packv5_042523_0143.root, FC#5, port B1

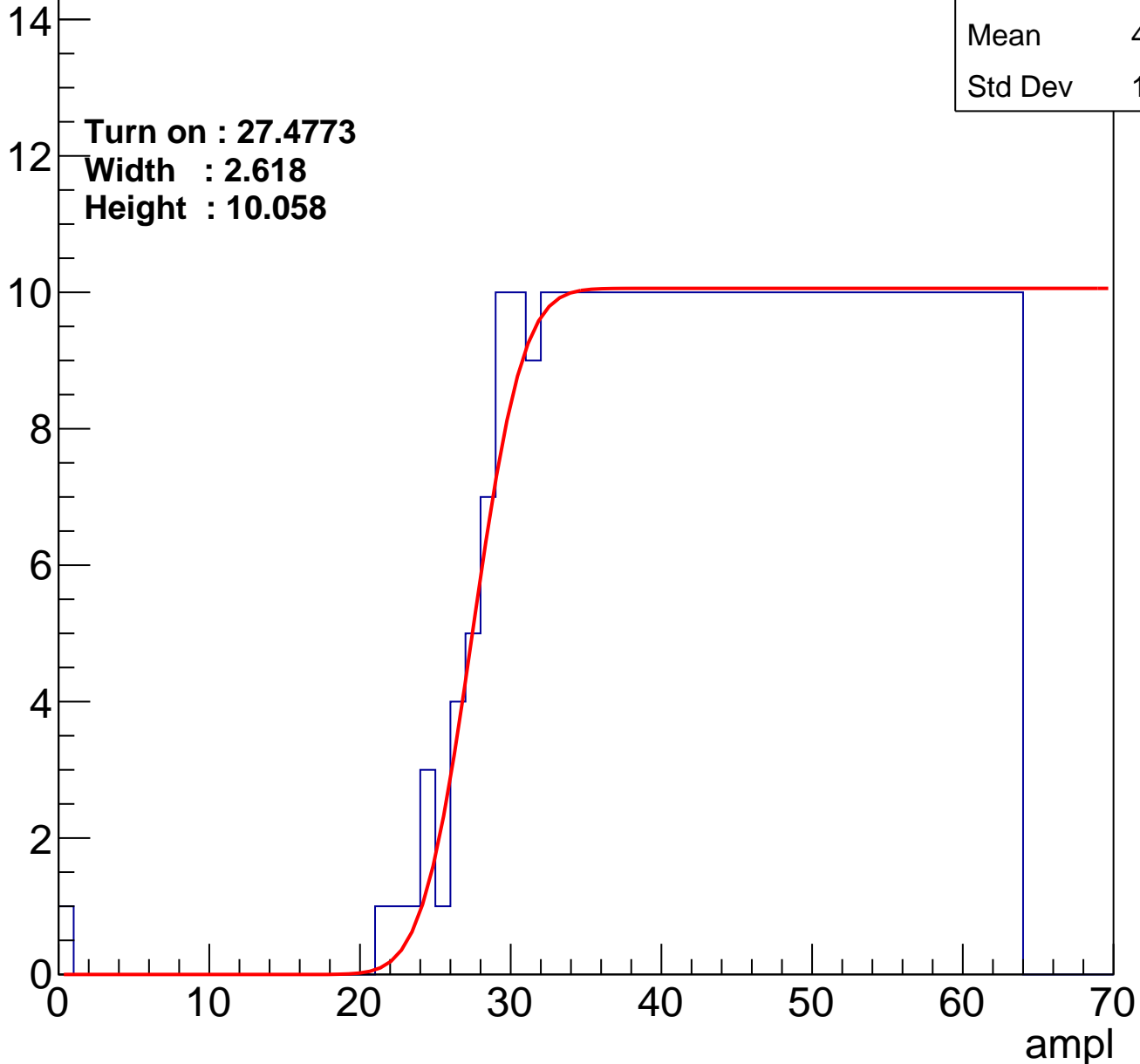
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.68 |
| Std Dev | 11.13 |

Turn on : 27.4773

Width : 2.618

Height : 10.058

Entry



B0L000S, U23-ch50

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 381 |
| Mean | 44.17 |
| Std Dev | 11.69 |

Turn on : 26.4738

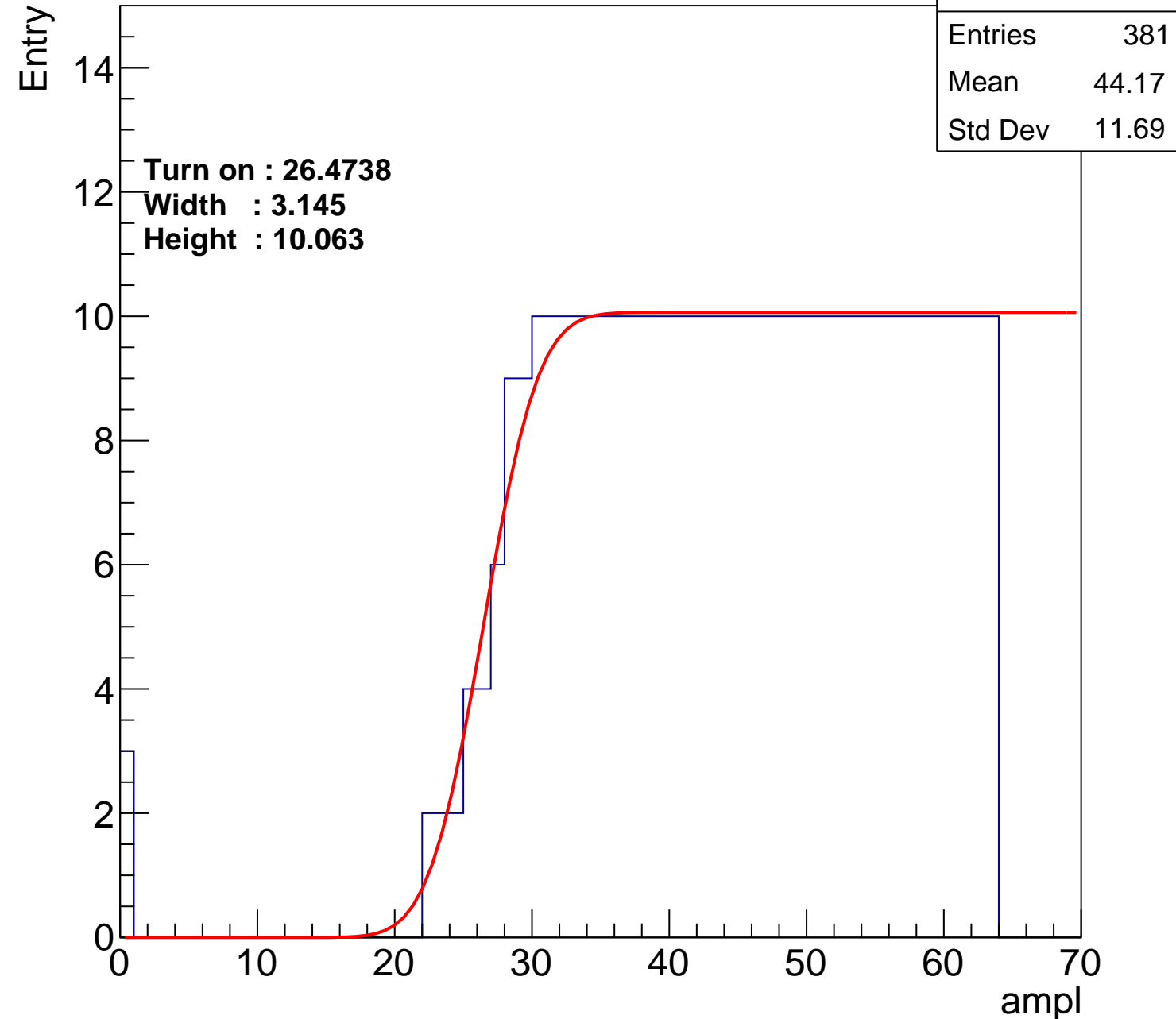
Width : 3.145

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch51

calib_packv5_042523_0143.root, FC#5, port B1

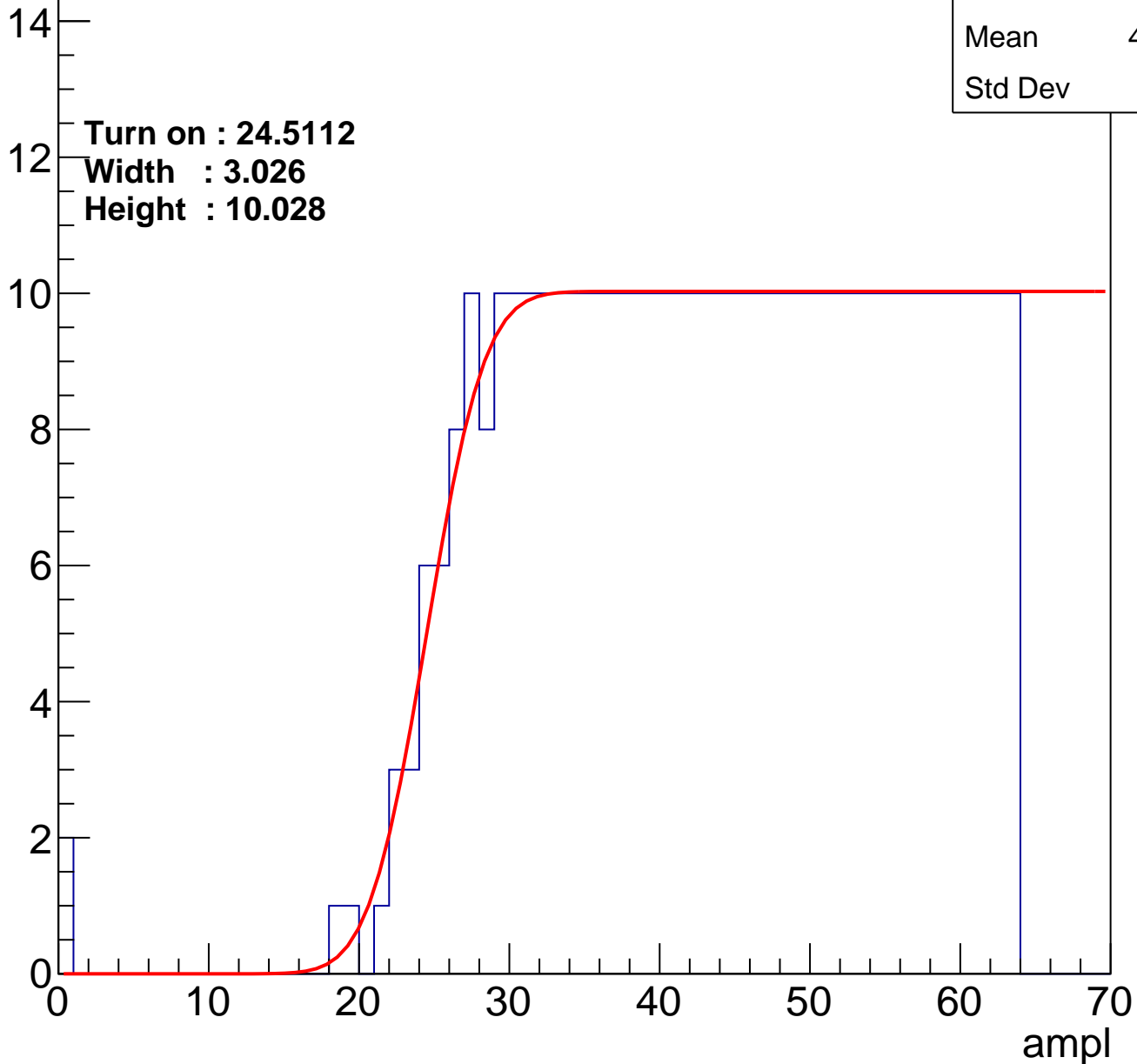
| | |
|---------|-------|
| Entries | 399 |
| Mean | 43.33 |
| Std Dev | 12 |

Turn on : 24.5112

Width : 3.026

Height : 10.028

Entry



B0L000S, U23-ch52

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 45.04 |
| Std Dev | 11.13 |

Turn on : 27.9475

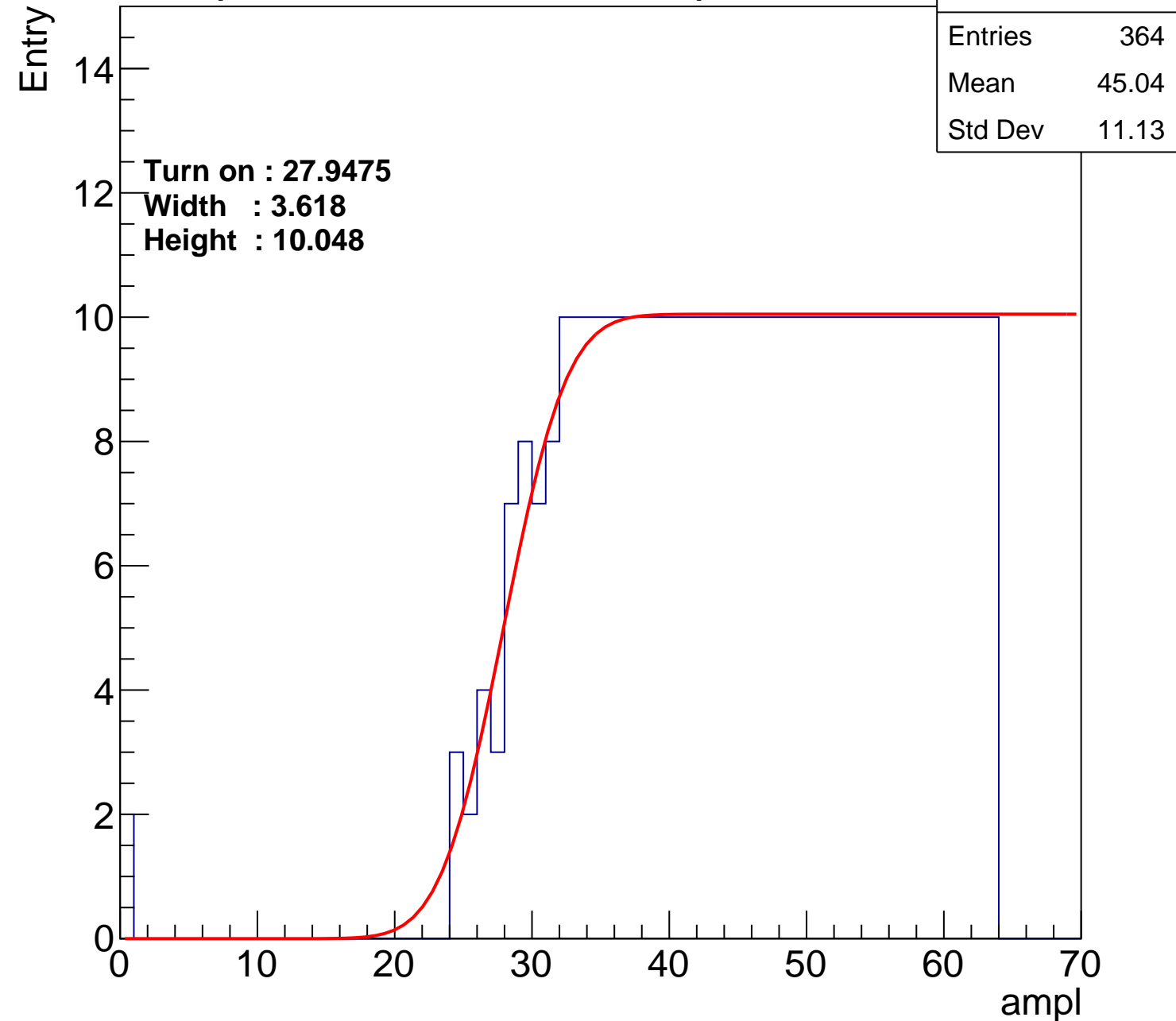
Width : 3.618

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch53

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 43.91 |
| Std Dev | 12.06 |

Turn on : 26.1814

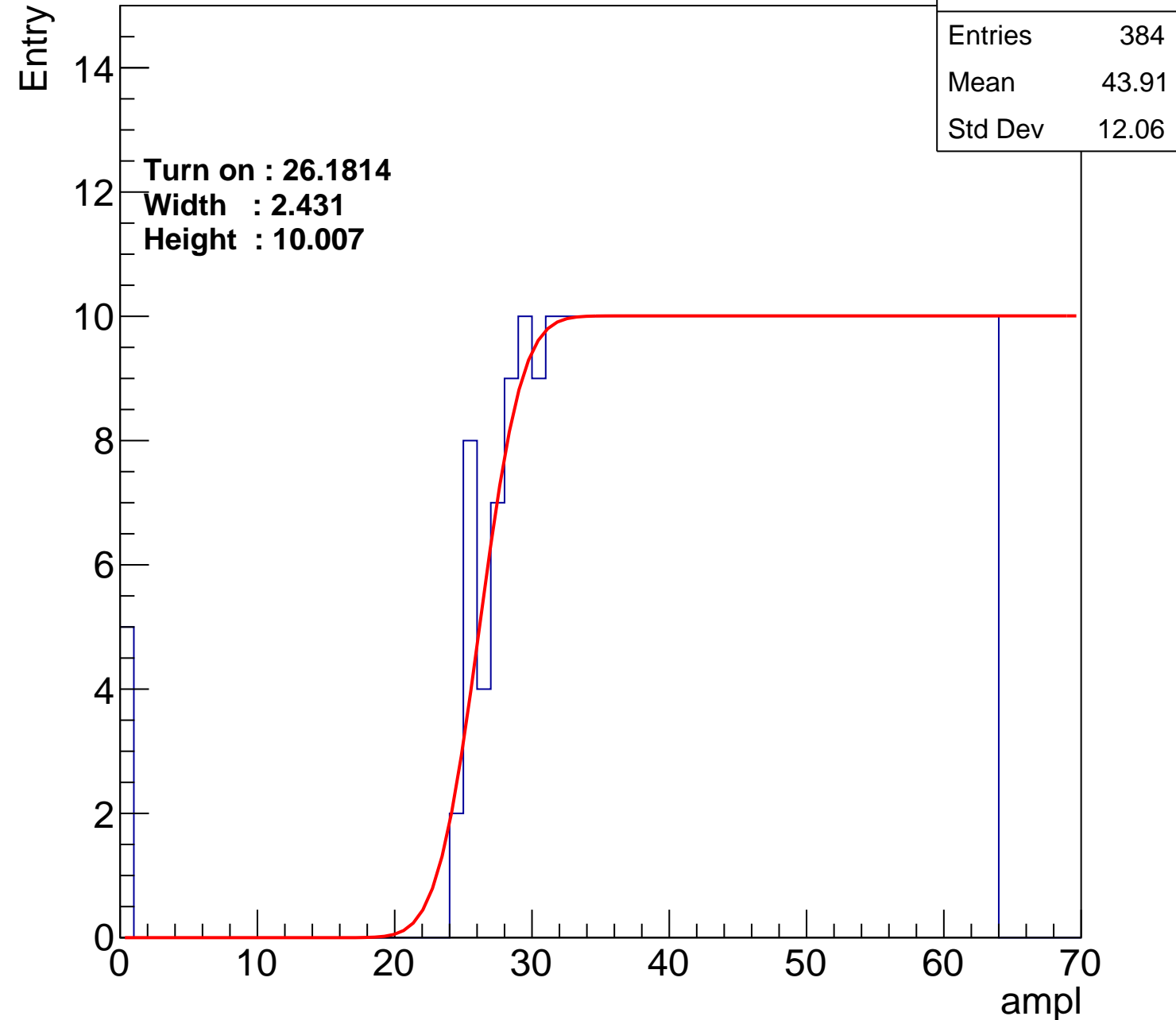
Width : 2.431

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.4334
Width : 3.498
Height : 10.088



B0L000S, U23-ch55

calib_packv5_042523_0143.root, FC#5, port B1

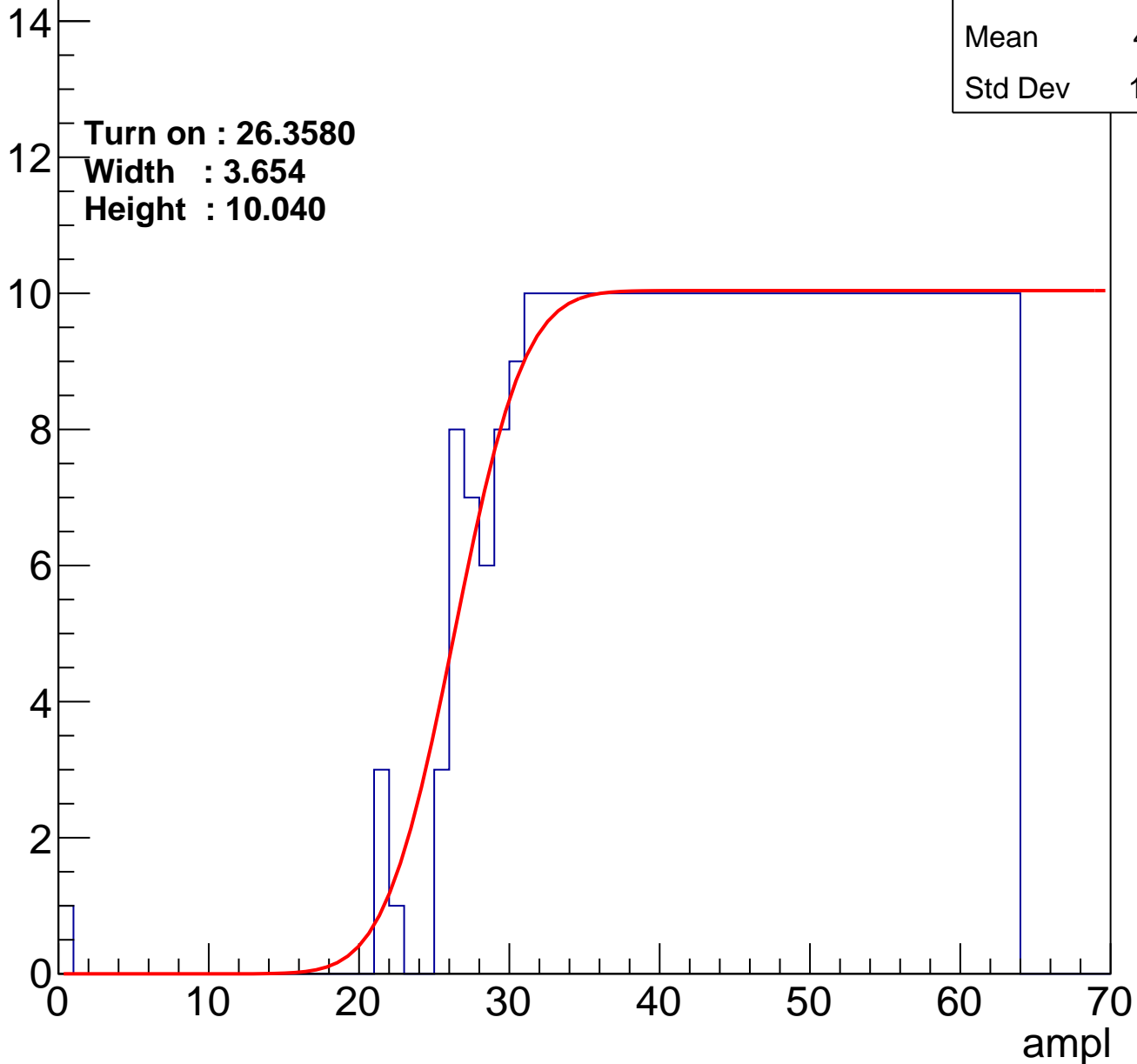
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.51 |
| Std Dev | 11.25 |

Turn on : 26.3580

Width : 3.654

Height : 10.040

Entry



B0L000S, U23-ch56

calib_packv5_042523_0143.root, FC#5, port B1

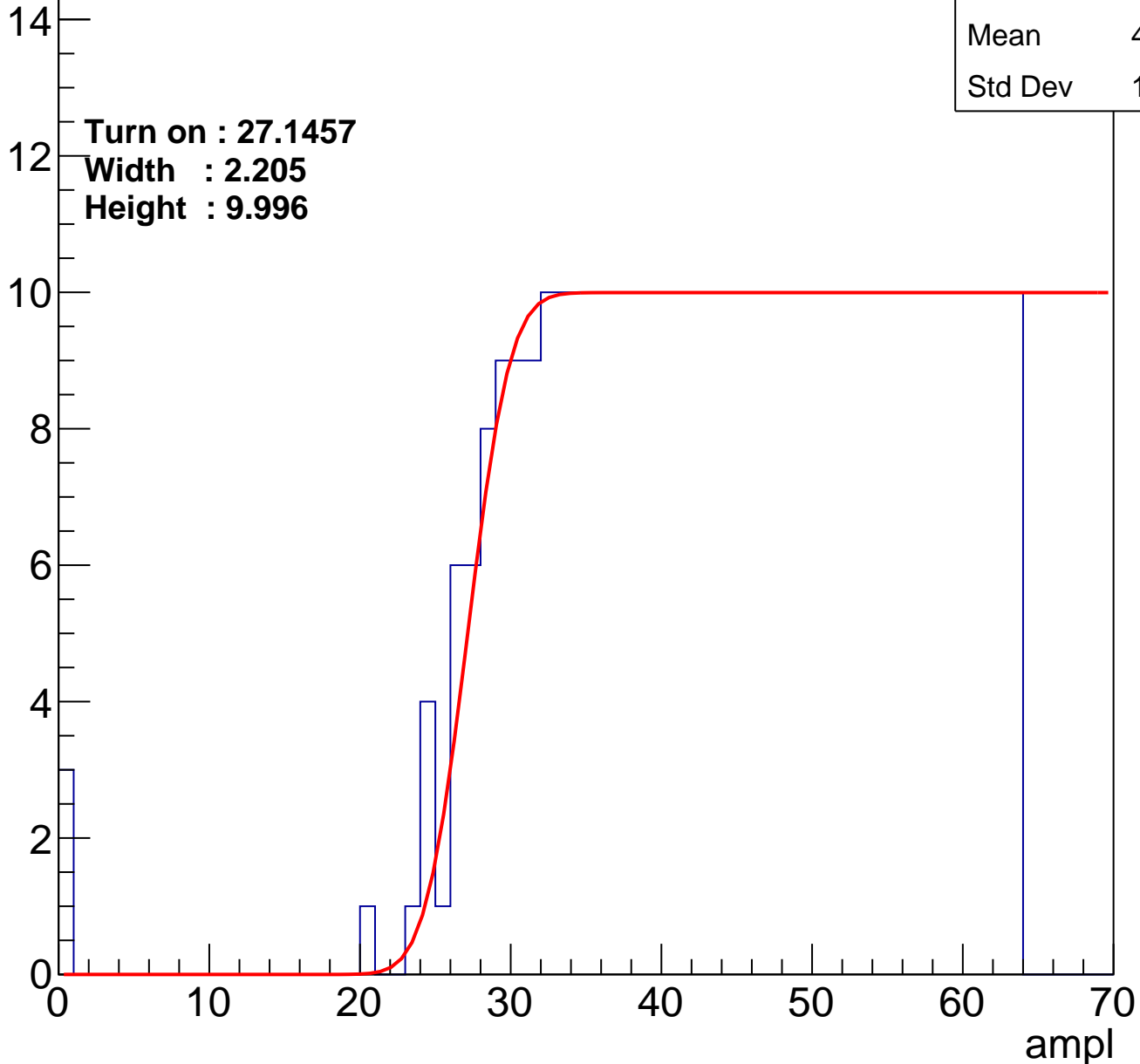
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.34 |
| Std Dev | 11.63 |

Turn on : 27.1457

Width : 2.205

Height : 9.996

Entry



B0L000S, U23-ch57

calib_packv5_042523_0143.root, FC#5, port B1

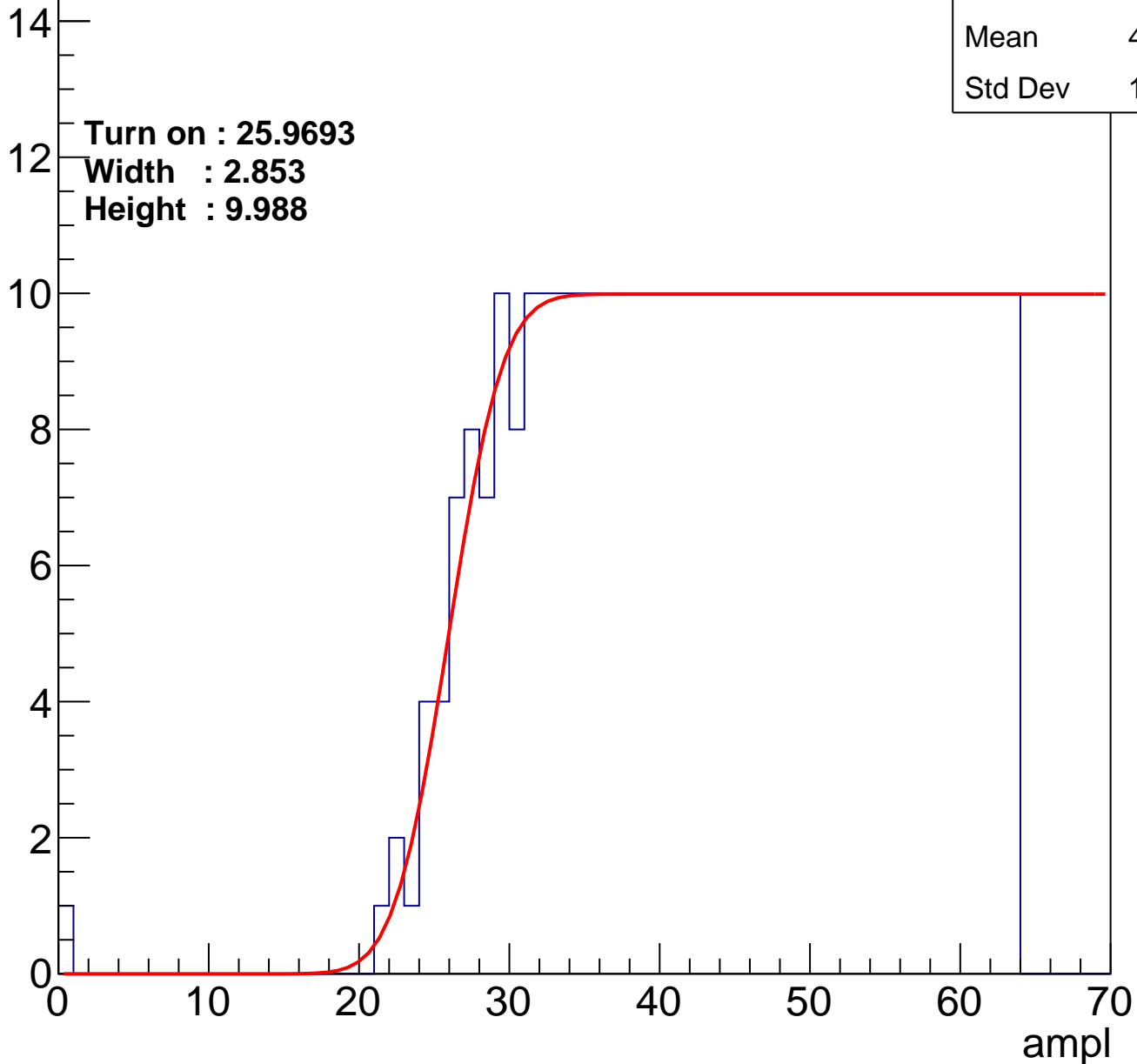
| | |
|---------|-------|
| Entries | 383 |
| Mean | 44.17 |
| Std Dev | 11.42 |

Turn on : 25.9693

Width : 2.853

Height : 9.988

Entry



B0L000S, U23-ch58

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.33 |
| Std Dev | 11.6 |

Turn on : 26.4081

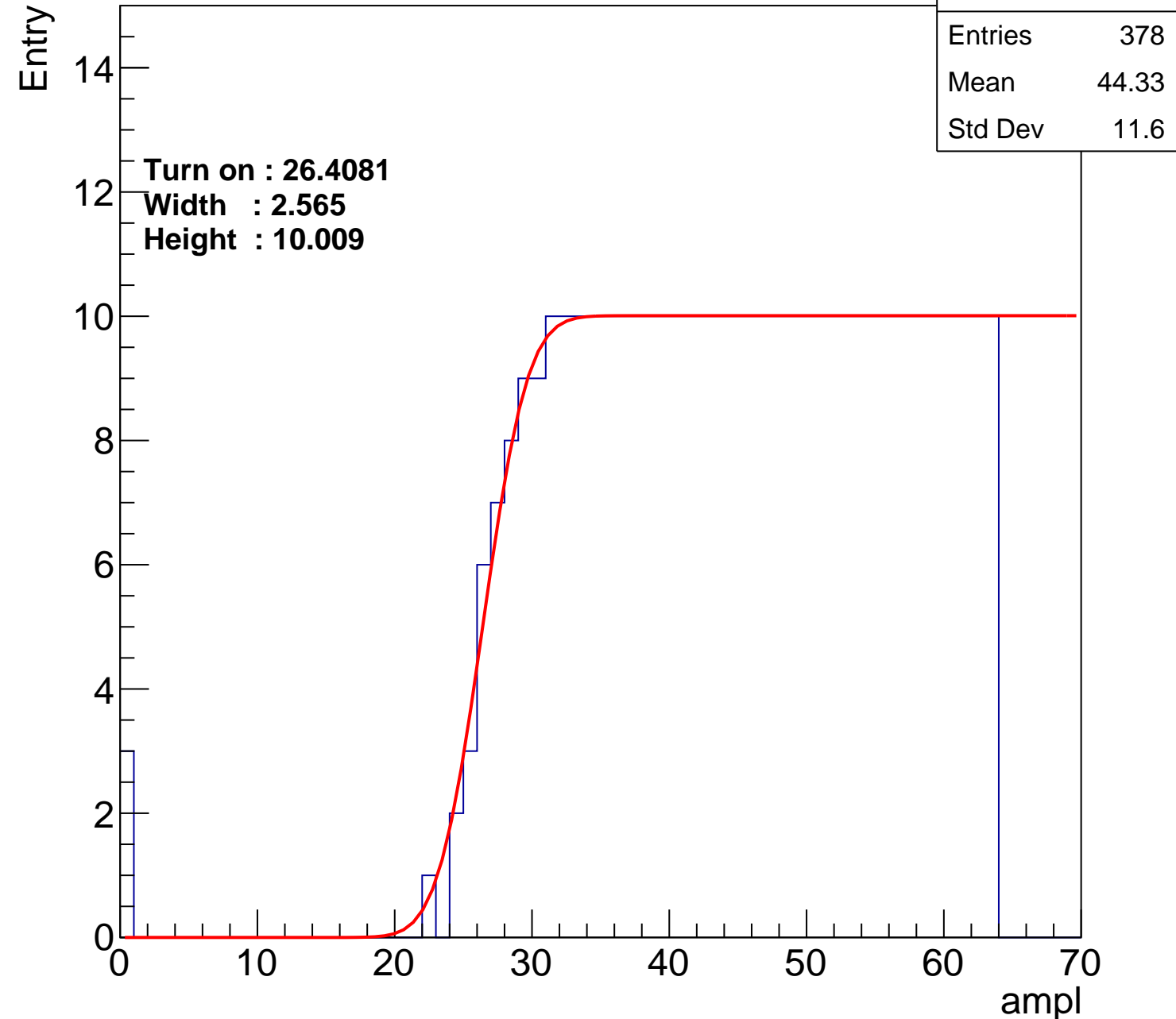
Width : 2.565

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch59

calib_packv5_042523_0143.root, FC#5, port B1

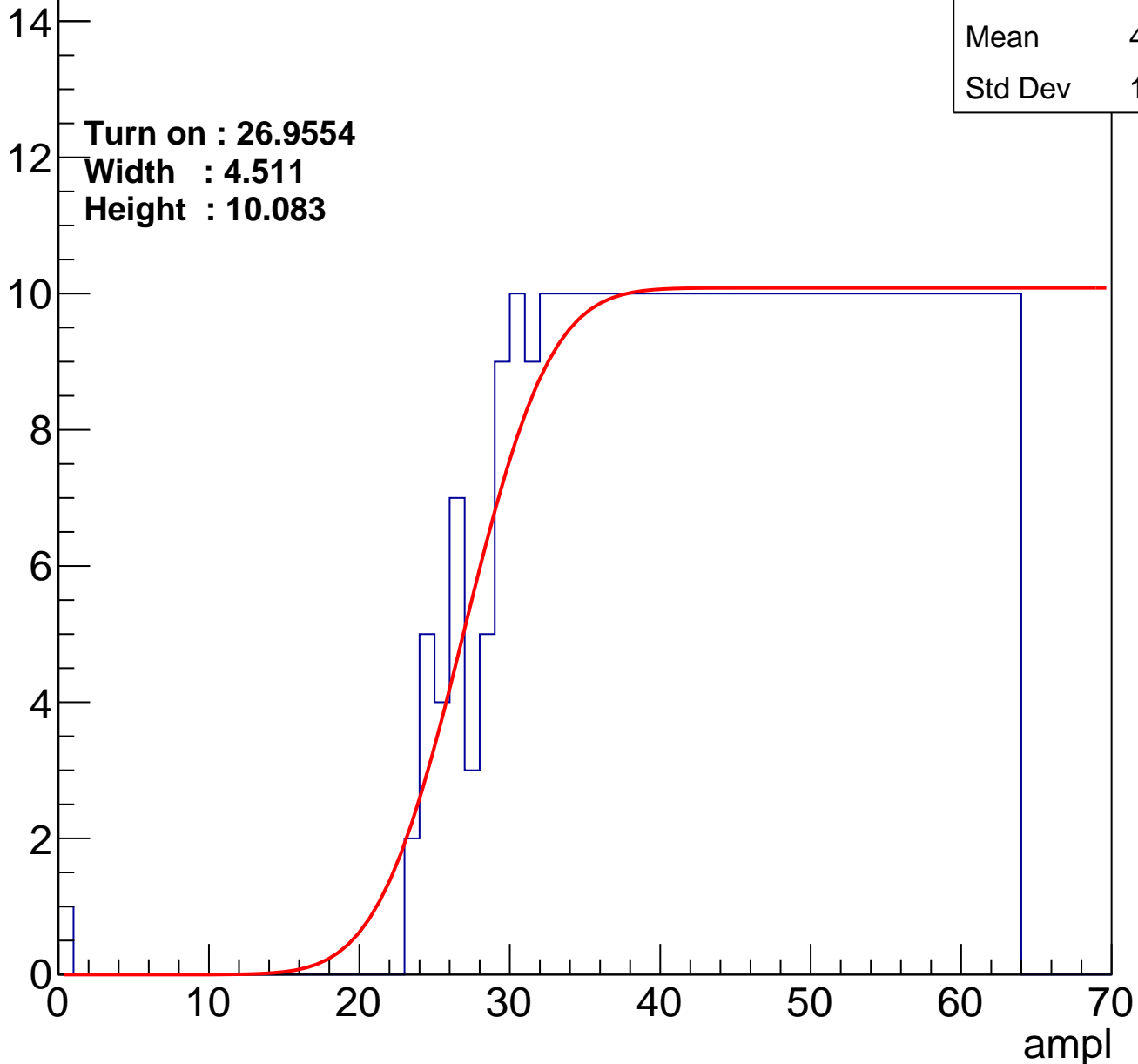
| | |
|---------|-------|
| Entries | 375 |
| Mean | 44.56 |
| Std Dev | 11.23 |

Turn on : 26.9554

Width : 4.511

Height : 10.083

Entry



B0L000S, U23-ch60

calib_packv5_042523_0143.root, FC#5, port B1

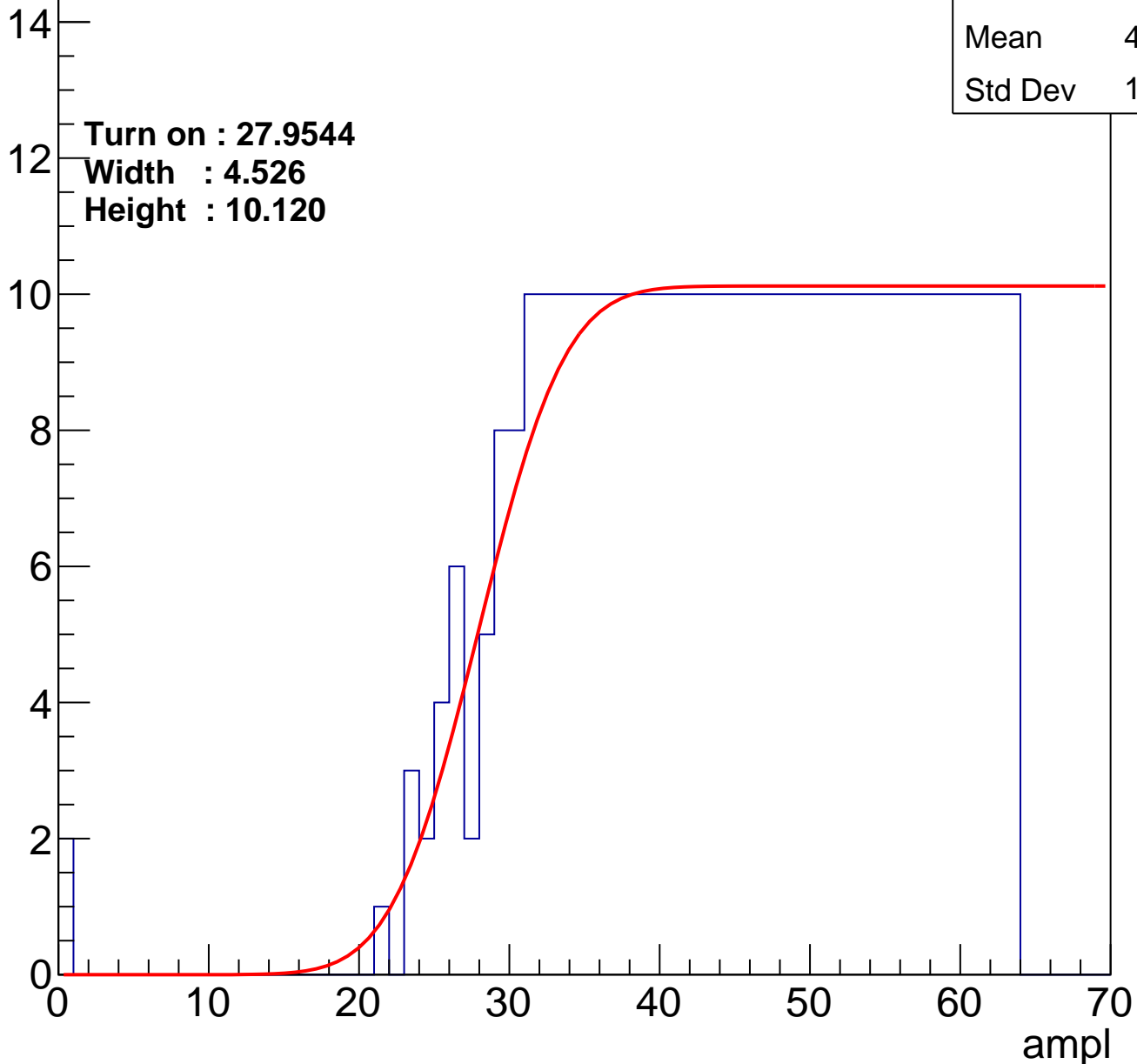
| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.66 |
| Std Dev | 11.36 |

Turn on : 27.9544

Width : 4.526

Height : 10.120

Entry



B0L000S, U23-ch61

calib_packv5_042523_0143.root, FC#5, port B1

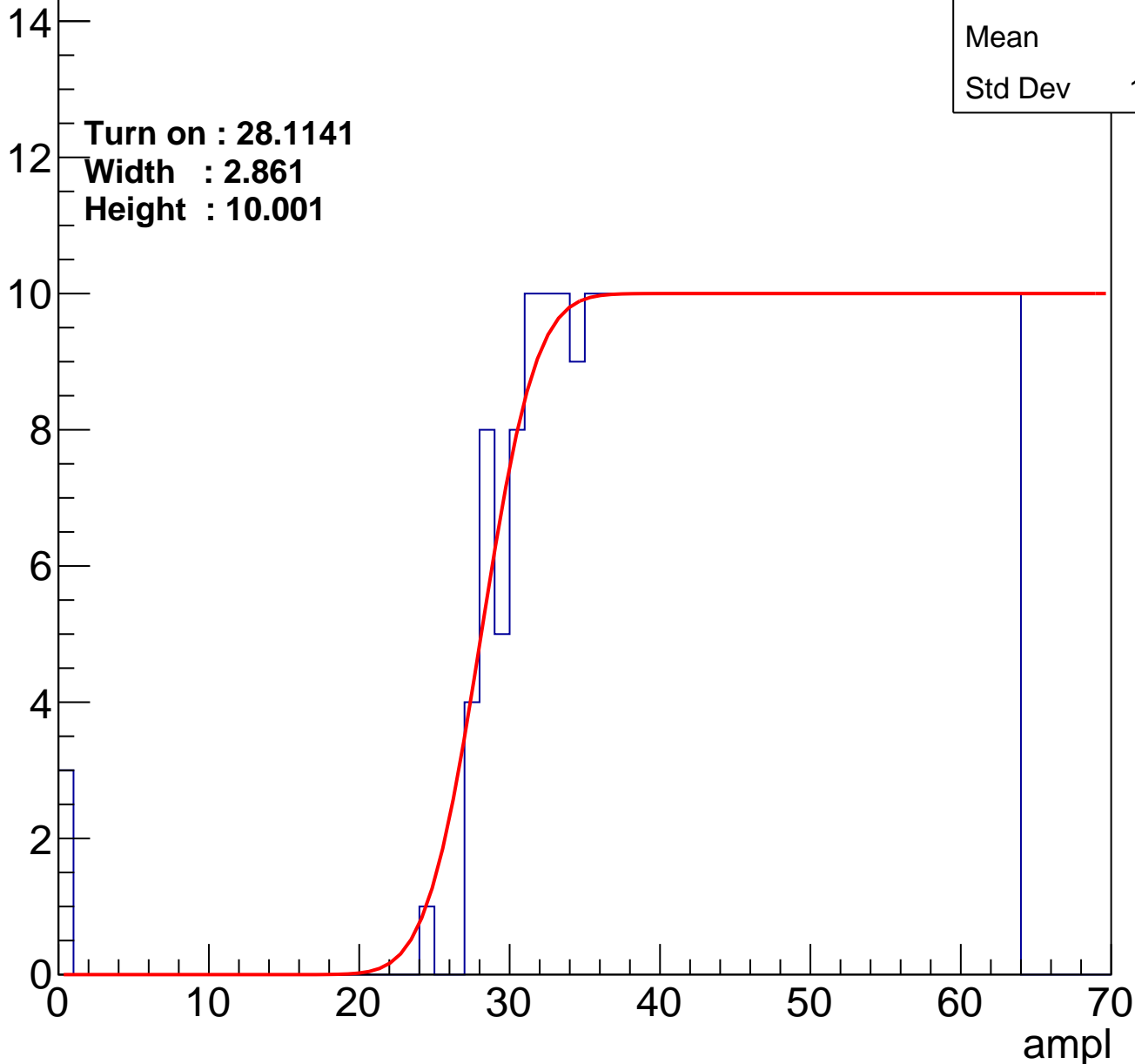
| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.3 |
| Std Dev | 11.12 |

Turn on : 28.1141

Width : 2.861

Height : 10.001

Entry



B0L000S, U23-ch62

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.69 |
| Std Dev | 11.74 |

Turn on : 24.8799

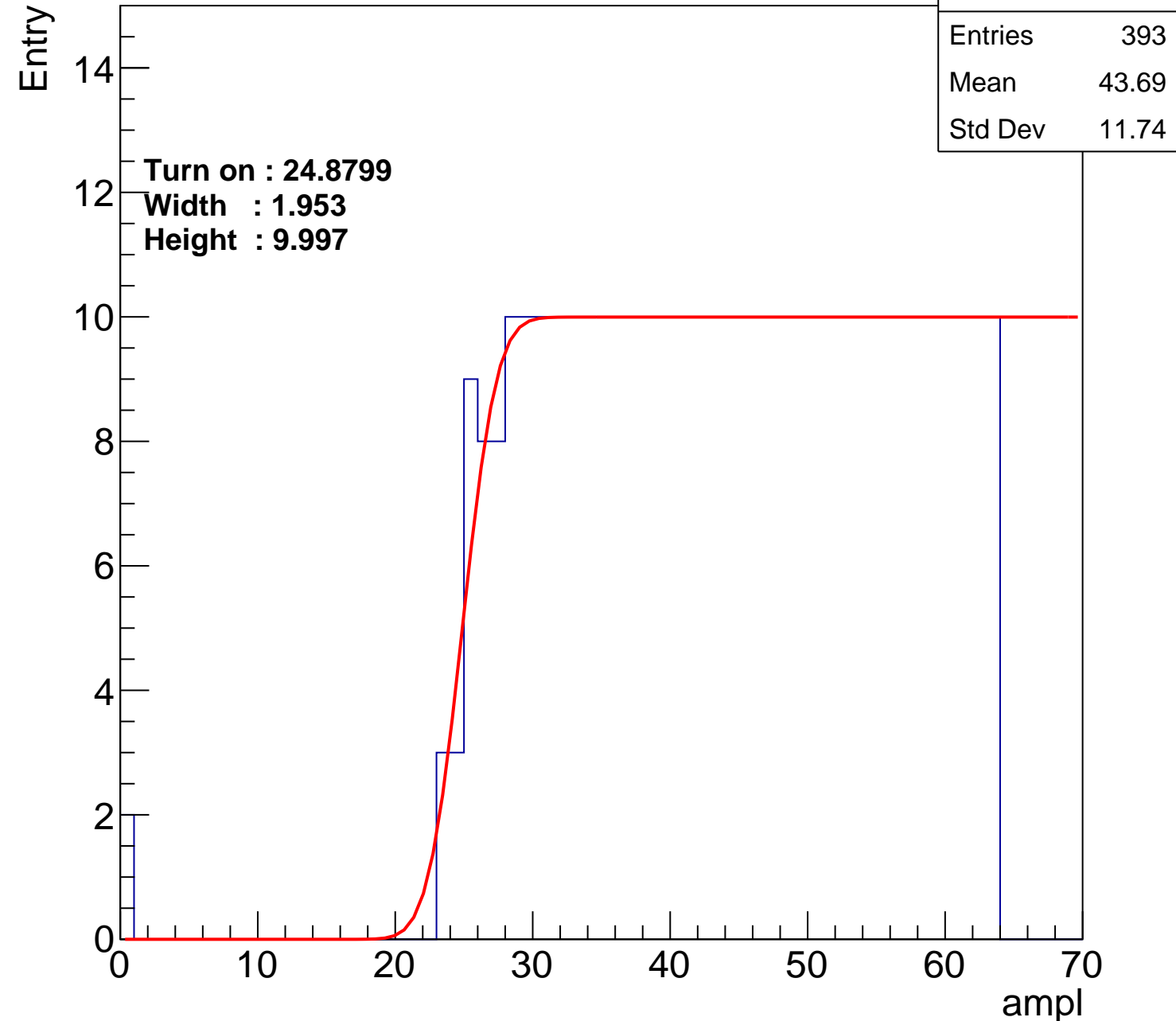
Width : 1.953

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch63

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.71 |
| Std Dev | 11.28 |

Turn on : 27.0644

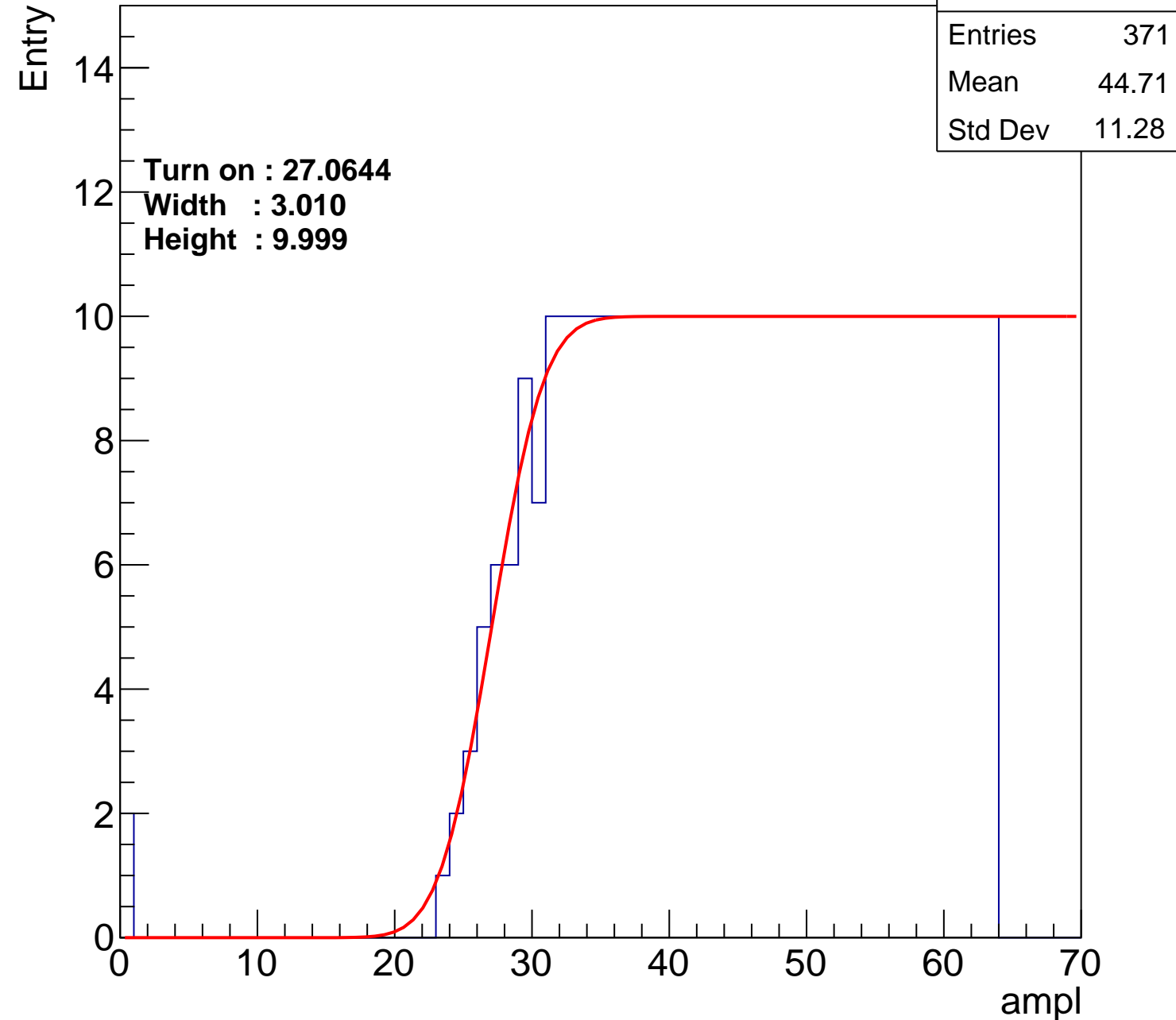
Width : 3.010

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch64

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.89 |
| Std Dev | 11.15 |

Turn on : 27.6440

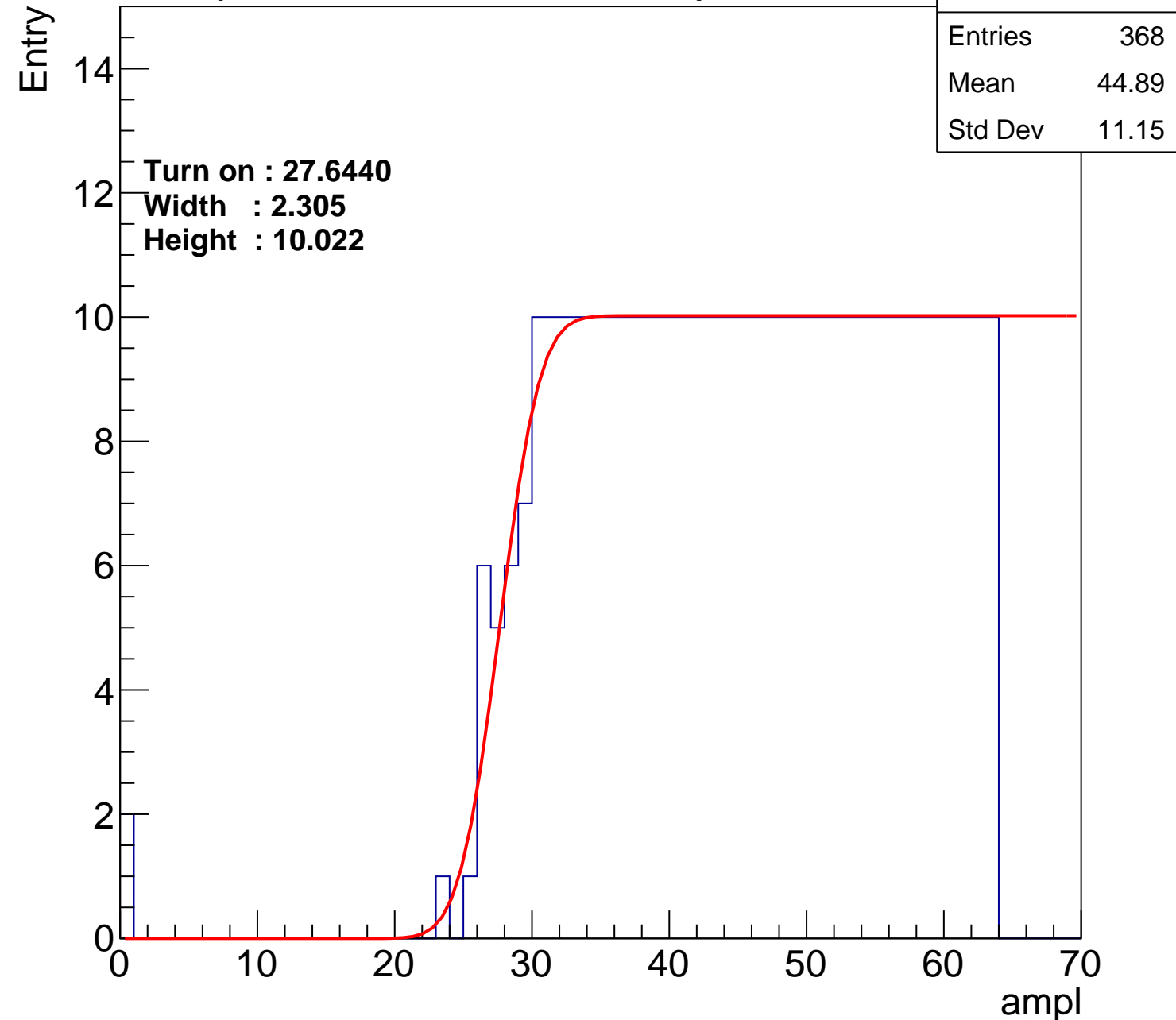
Width : 2.305

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch65

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.64 |
| Std Dev | 12 |

Turn on : 25.4704

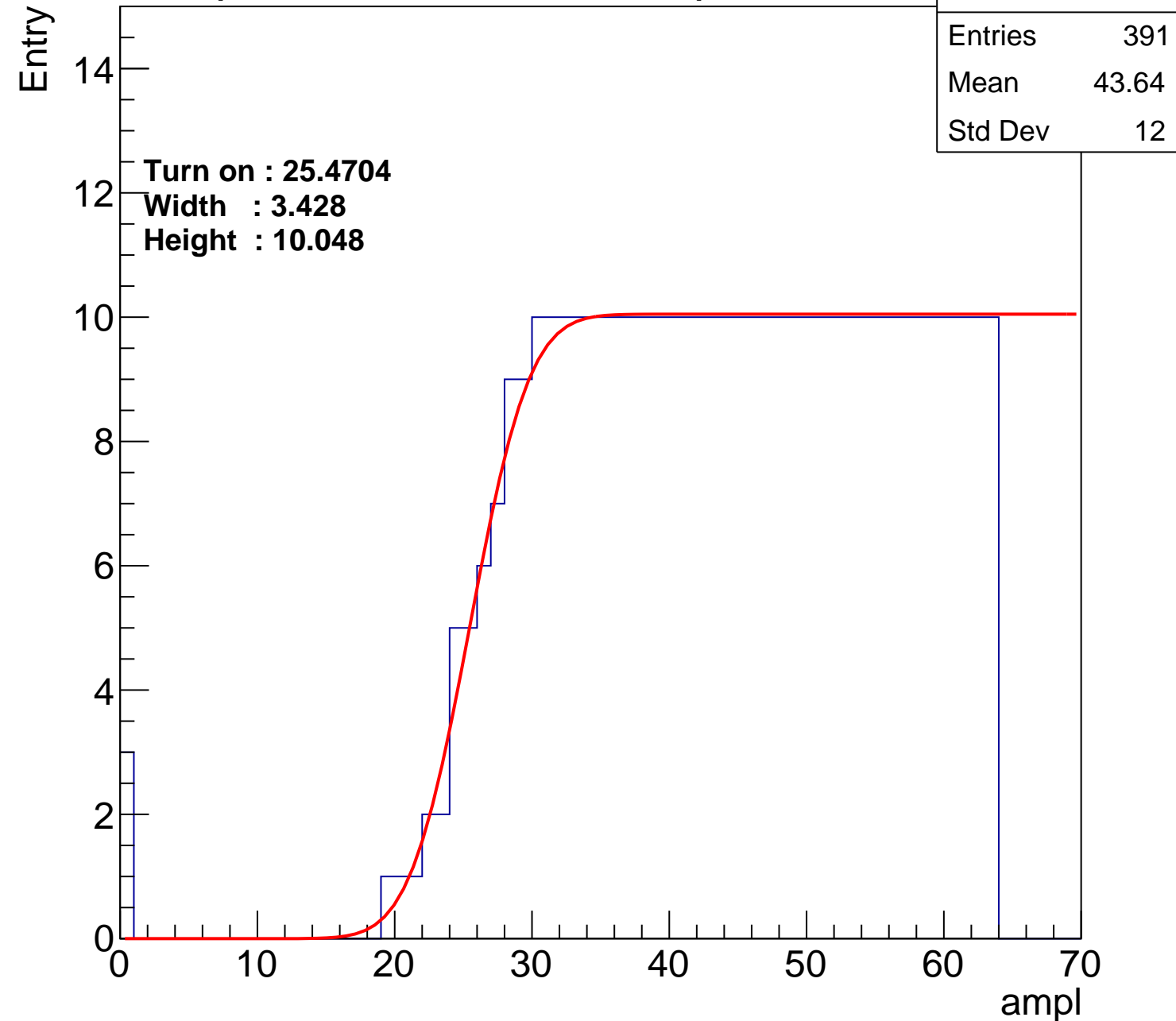
Width : 3.428

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch66

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.36 |
| Std Dev | 11.29 |

Turn on : 26.7012

Width : 2.579

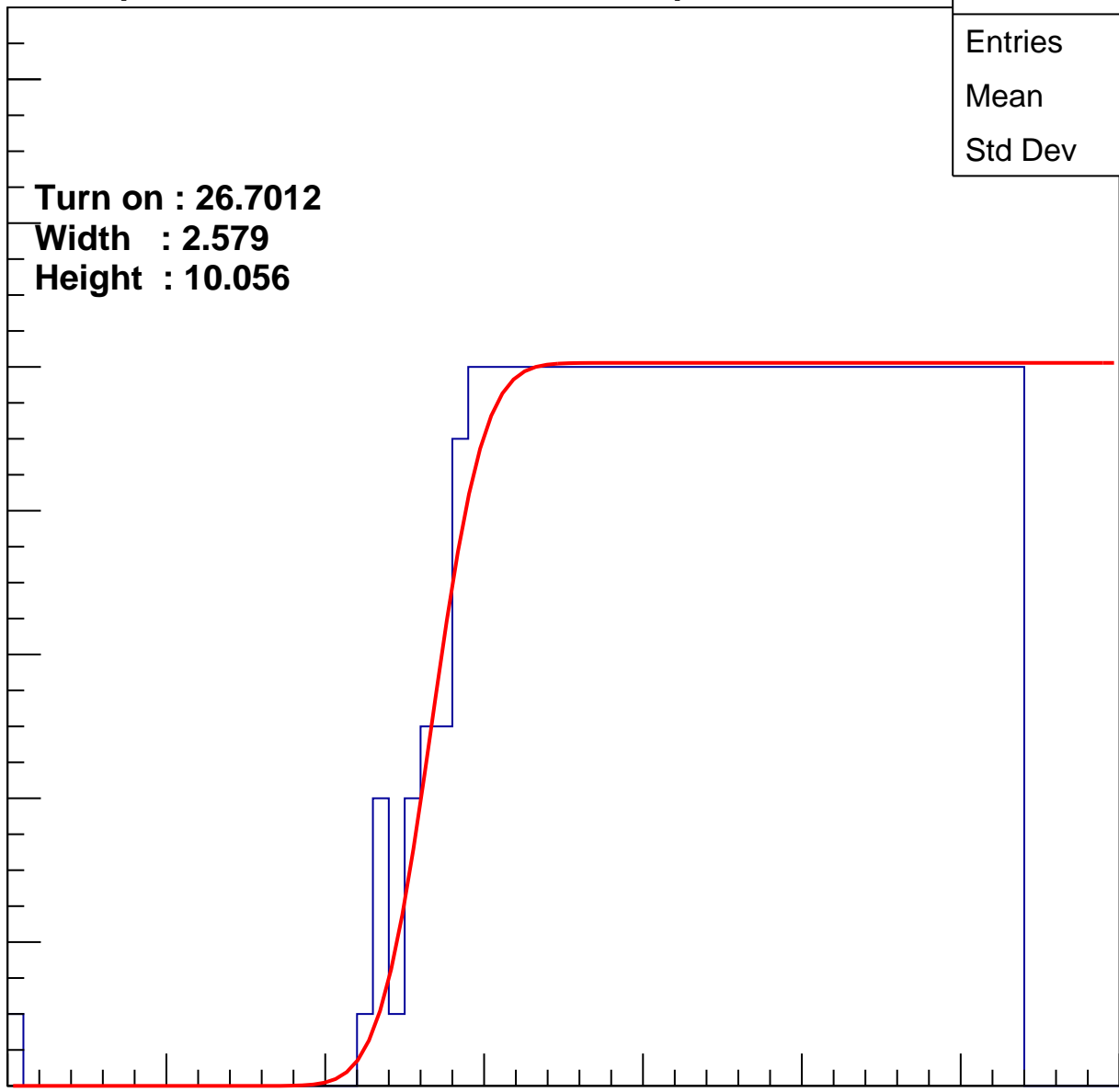
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U23-ch67

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 360 |
| Mean | 45.26 |
| Std Dev | 10.99 |

Turn on : 28.9220

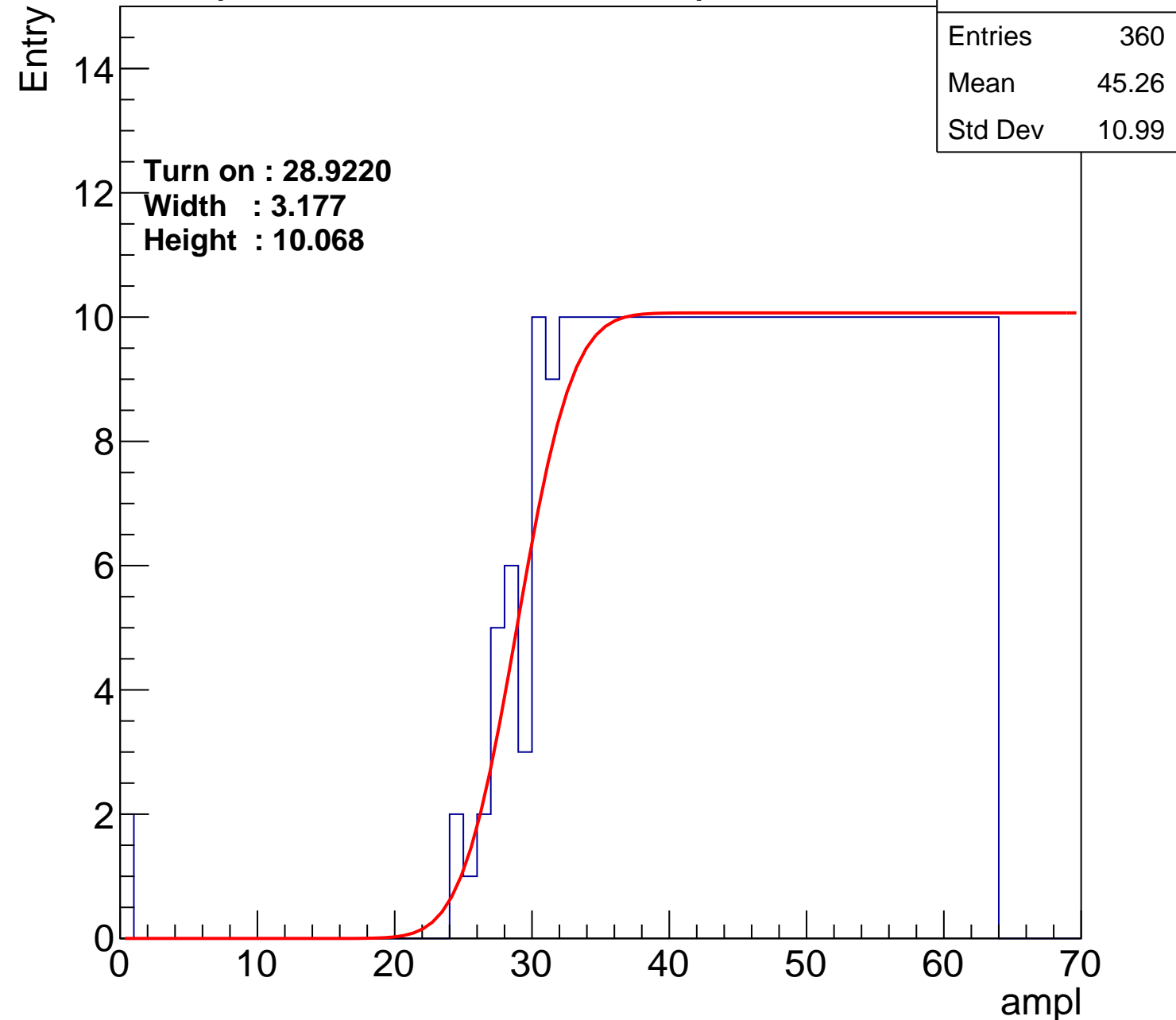
Width : 3.177

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch68

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.67 |
| Std Dev | 12.01 |

Turn on : 25.5691

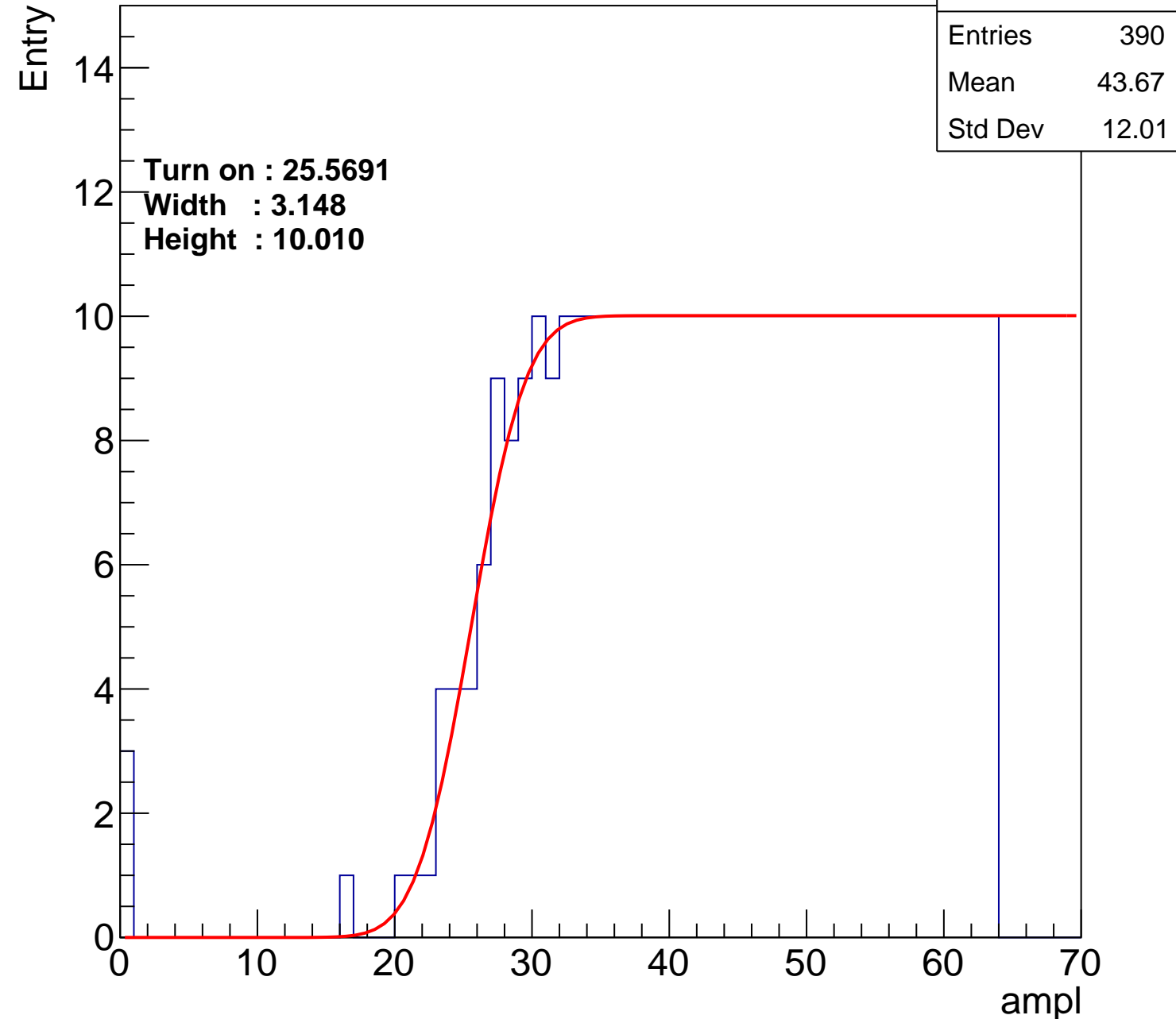
Width : 3.148

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch69

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.57 |
| Std Dev | 11.35 |

Turn on : 26.9492

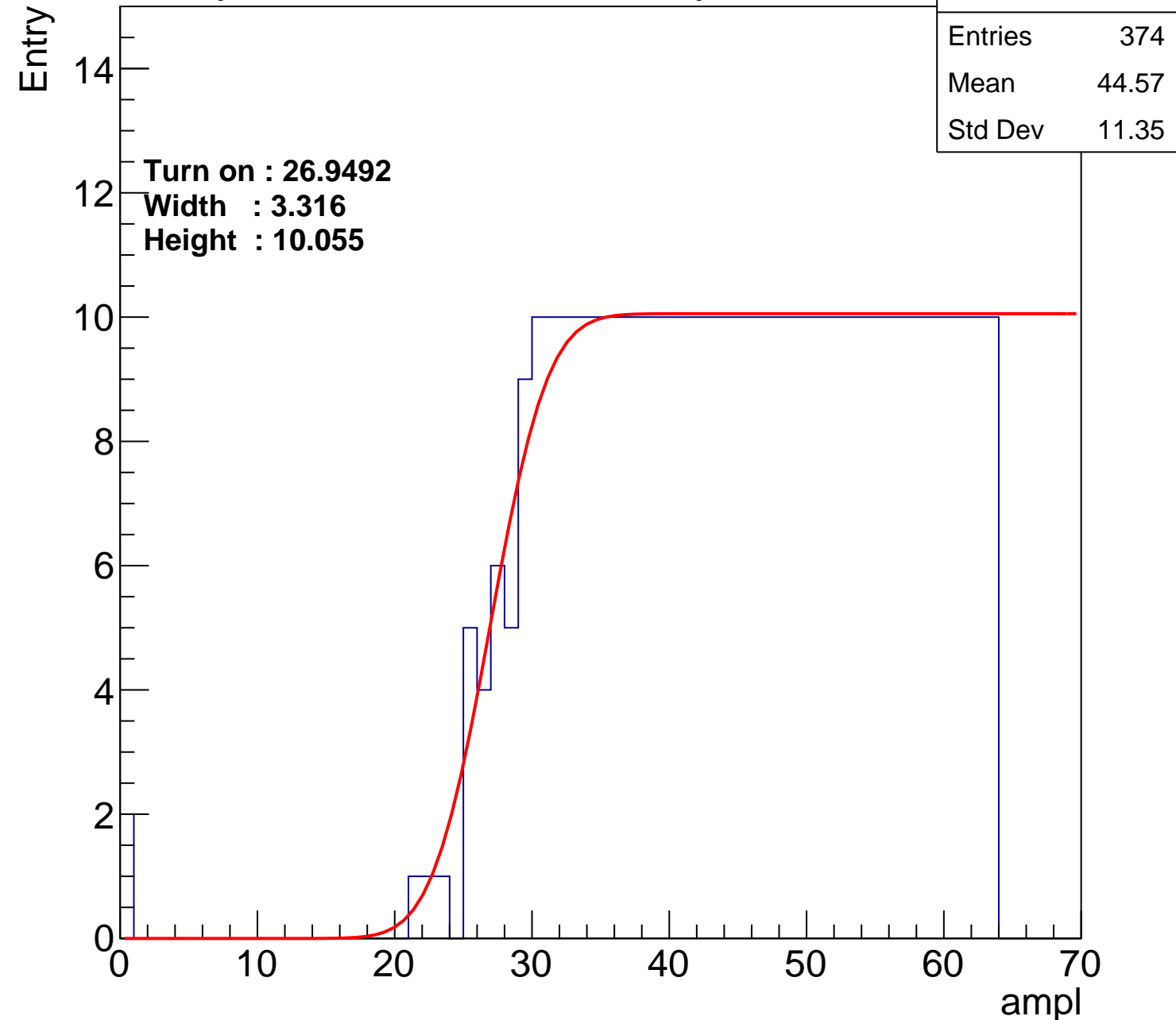
Width : 3.316

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch70

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.16 |
| Std Dev | 11.39 |

Turn on : 26.4180

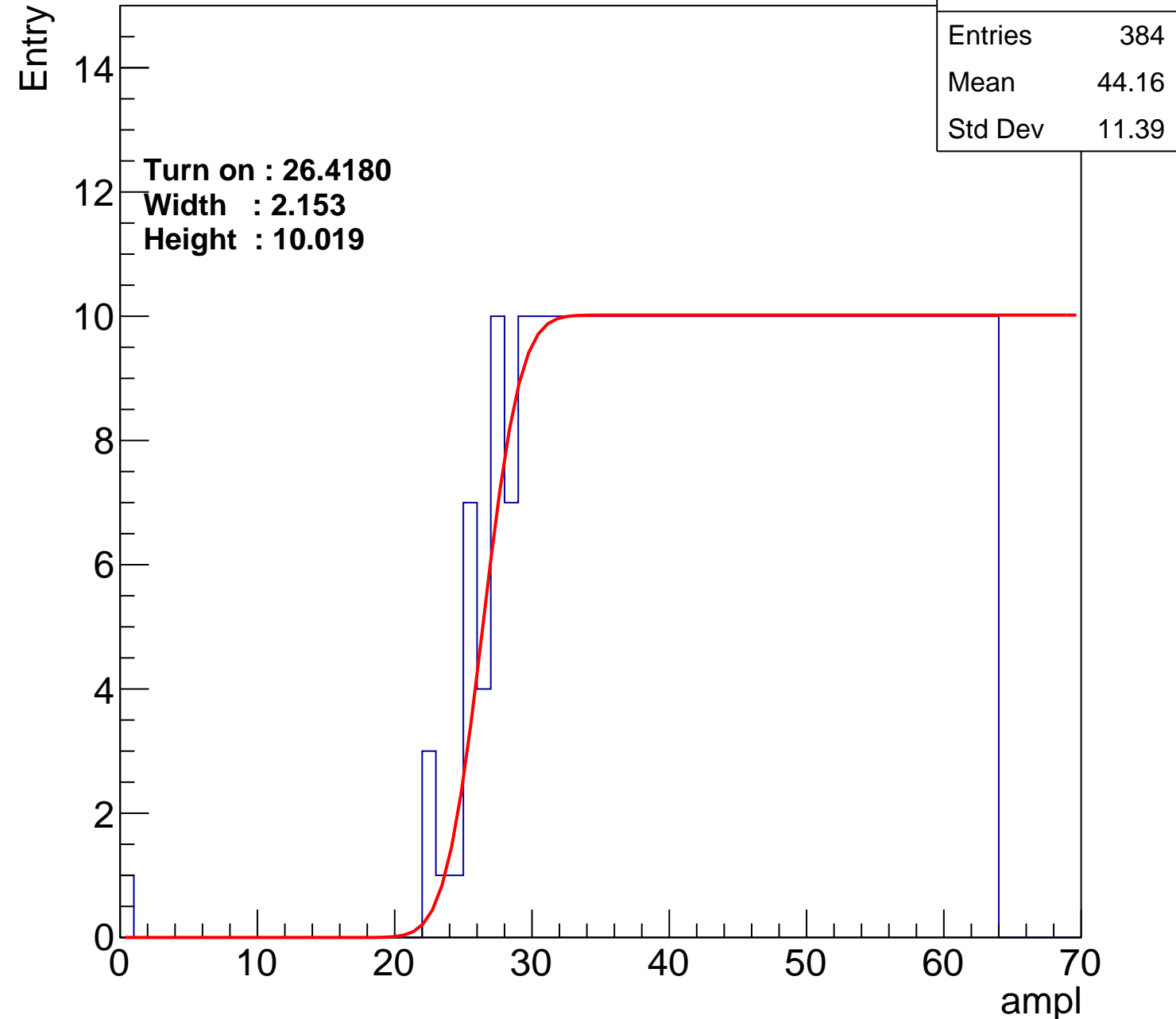
Width : 2.153

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch71

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-----|
| Entries | 381 |
|---------|-----|

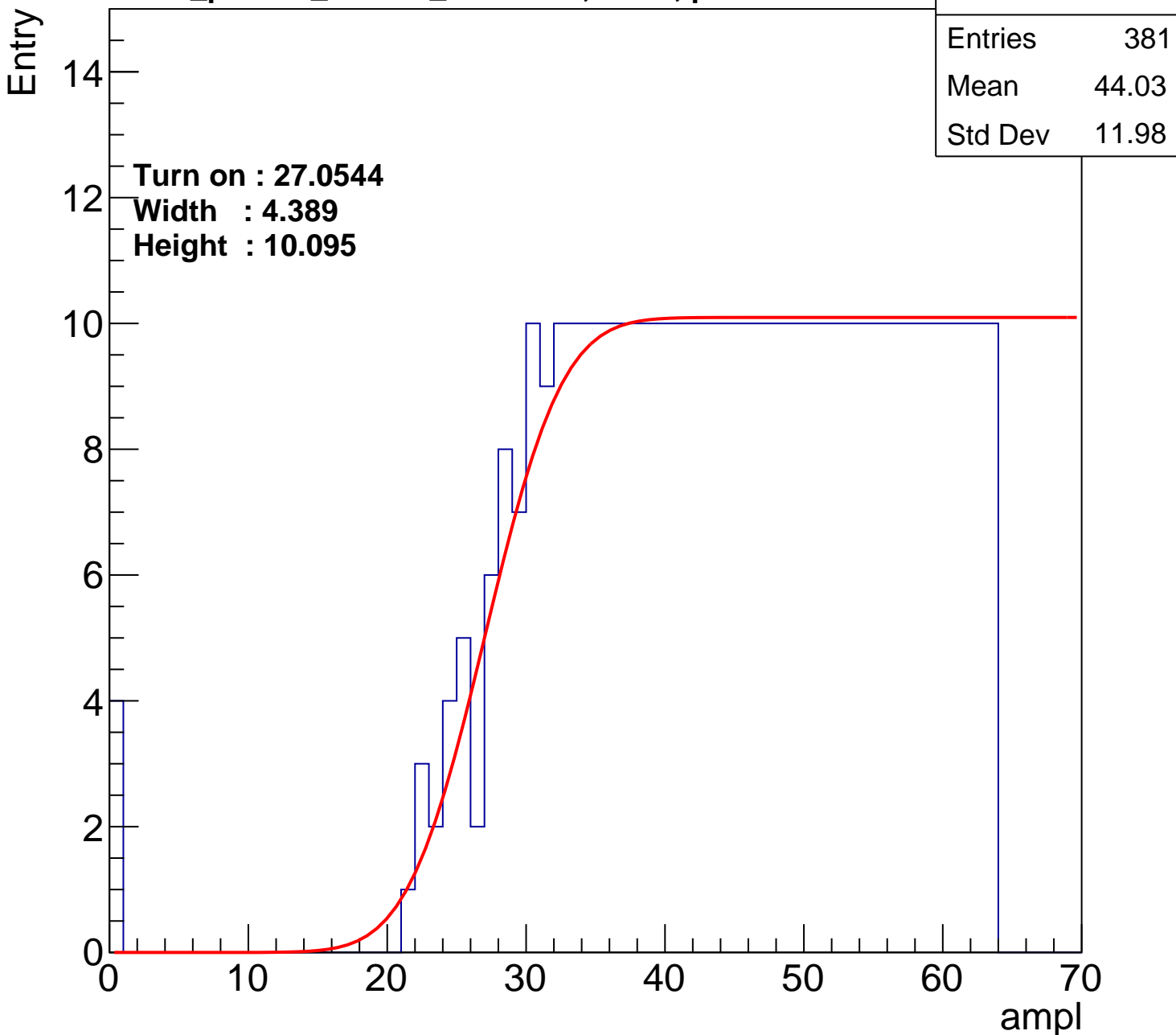
| | |
|------|-------|
| Mean | 44.03 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 11.98 |
|---------|-------|

Turn on : 27.0544

Width : 4.389

Height : 10.095



B0L000S, U23-ch72

calib_packv5_042523_0143.root, FC#5, port B1

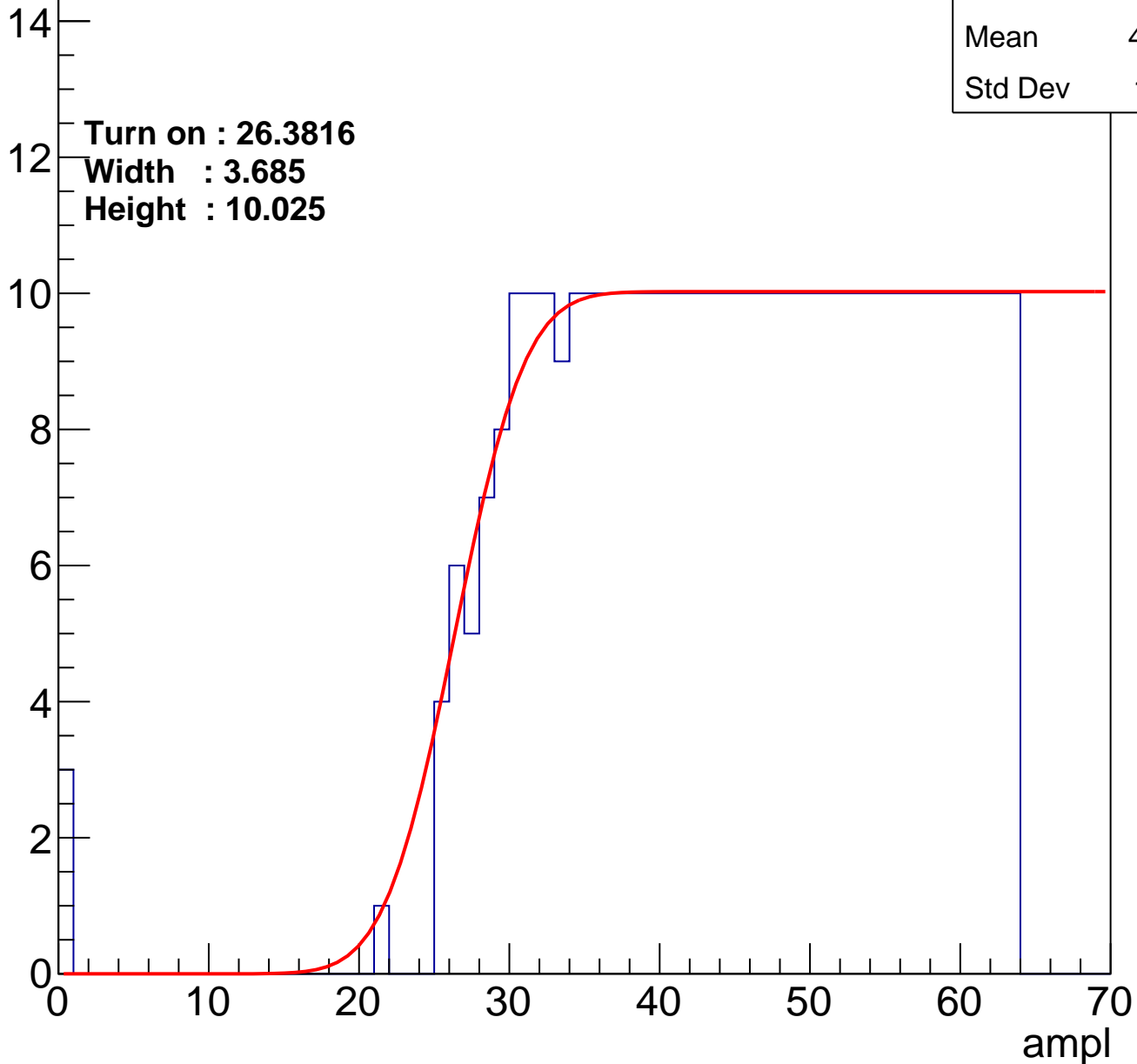
| | |
|---------|-------|
| Entries | 373 |
| Mean | 44.55 |
| Std Dev | 11.51 |

Turn on : 26.3816

Width : 3.685

Height : 10.025

Entry



B0L000S, U23-ch73

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|------|
| Entries | 379 |
| Mean | 44.3 |
| Std Dev | 11.5 |

Turn on : 26.1837

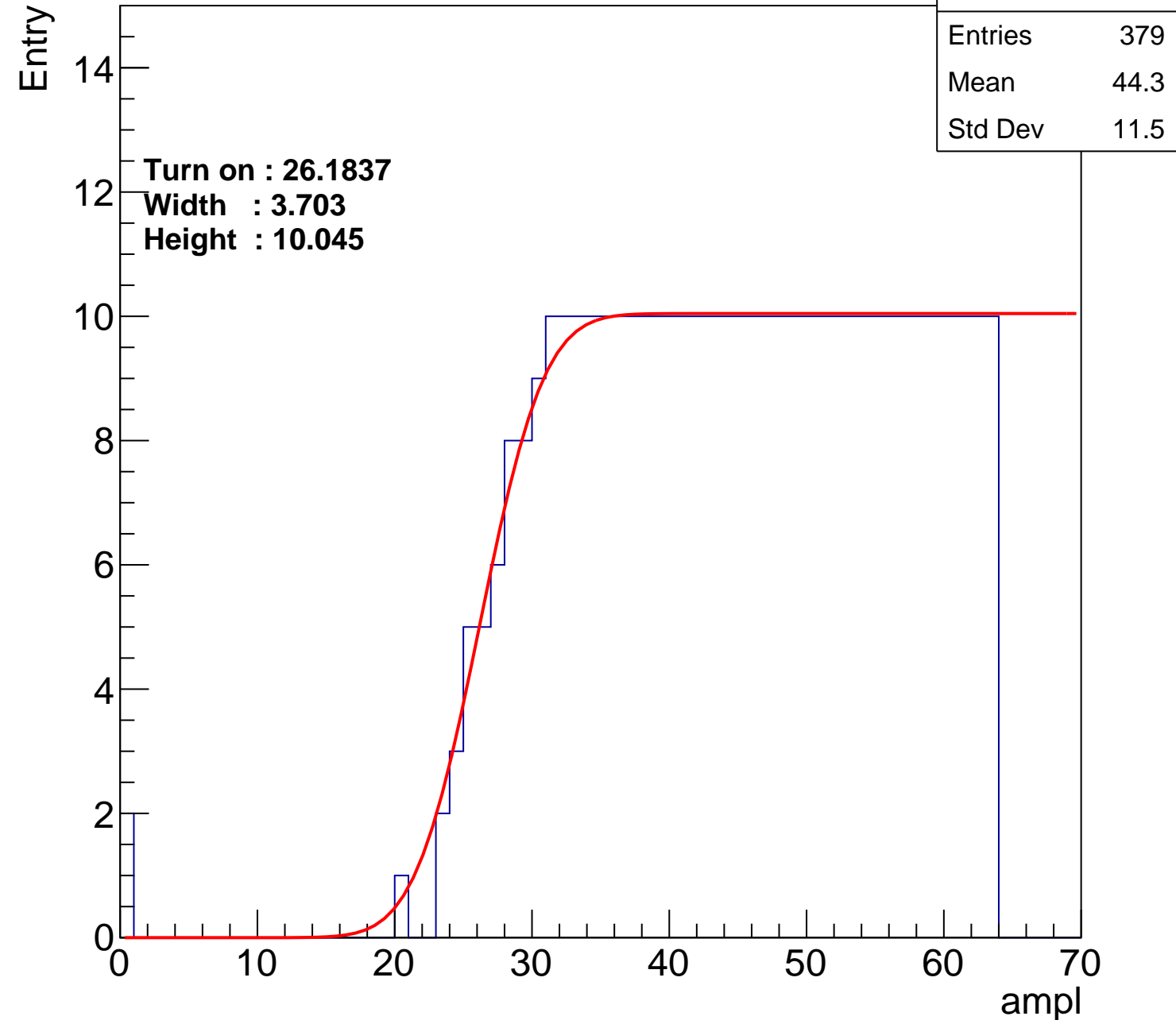
Width : 3.703

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch74

calib_packv5_042523_0143.root, FC#5, port B1

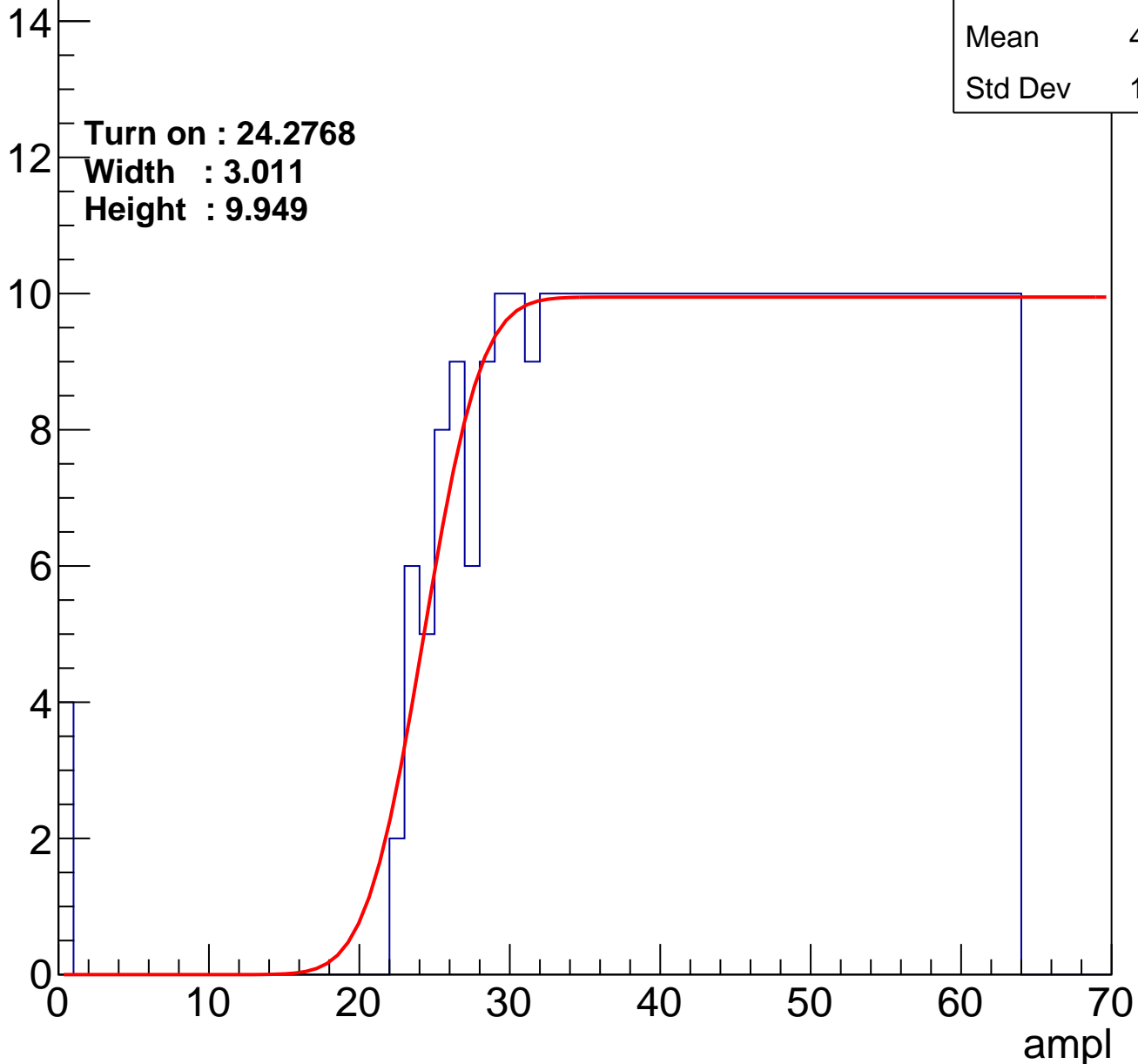
| | |
|---------|-------|
| Entries | 398 |
| Mean | 43.26 |
| Std Dev | 12.27 |

Turn on : 24.2768

Width : 3.011

Height : 9.949

Entry



B0L000S, U23-ch75

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.97 |
| Std Dev | 11.49 |

Turn on : 25.7048

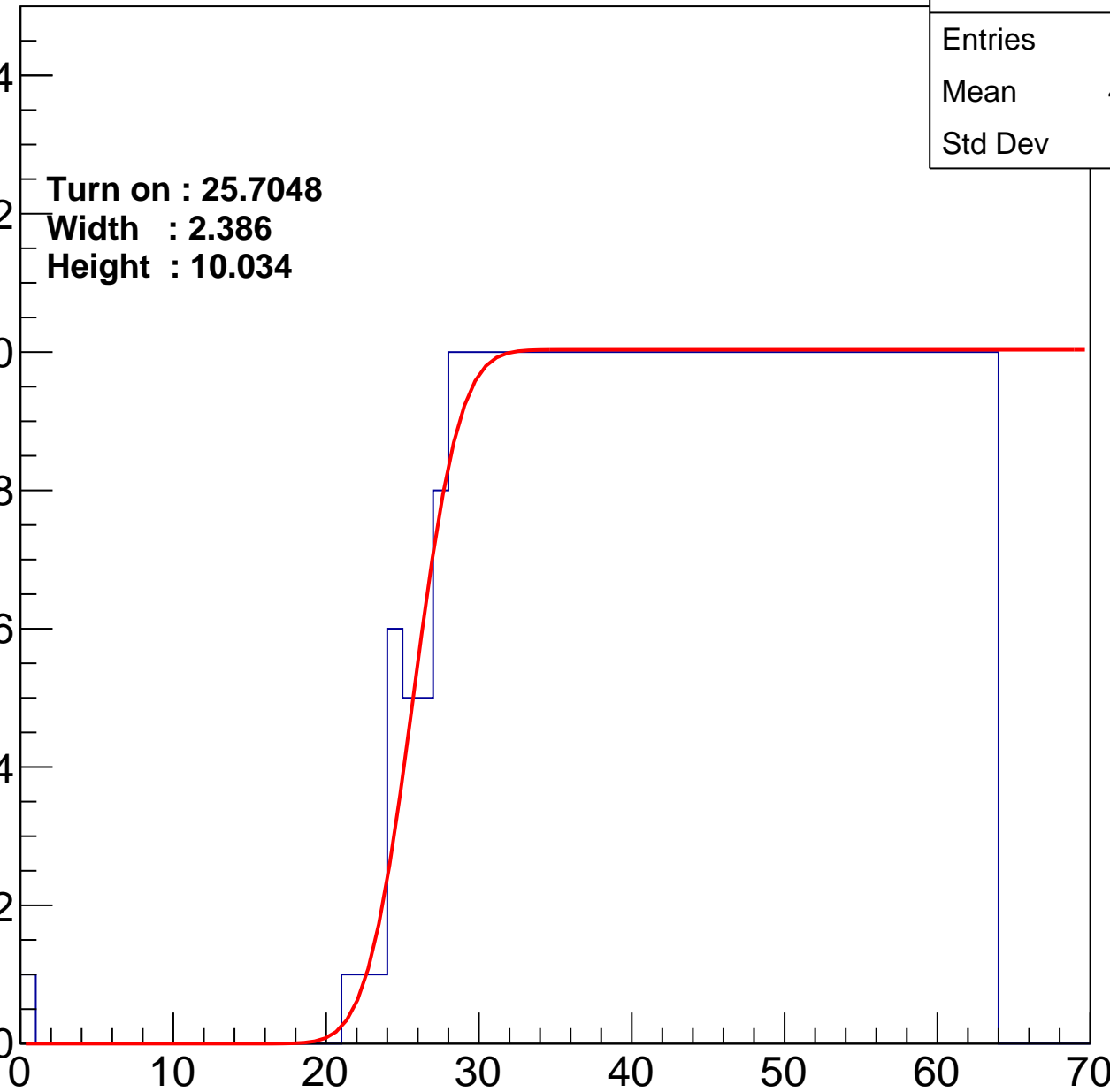
Width : 2.386

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch76

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.73 |
| Std Dev | 11.42 |

Turn on : 27.5164

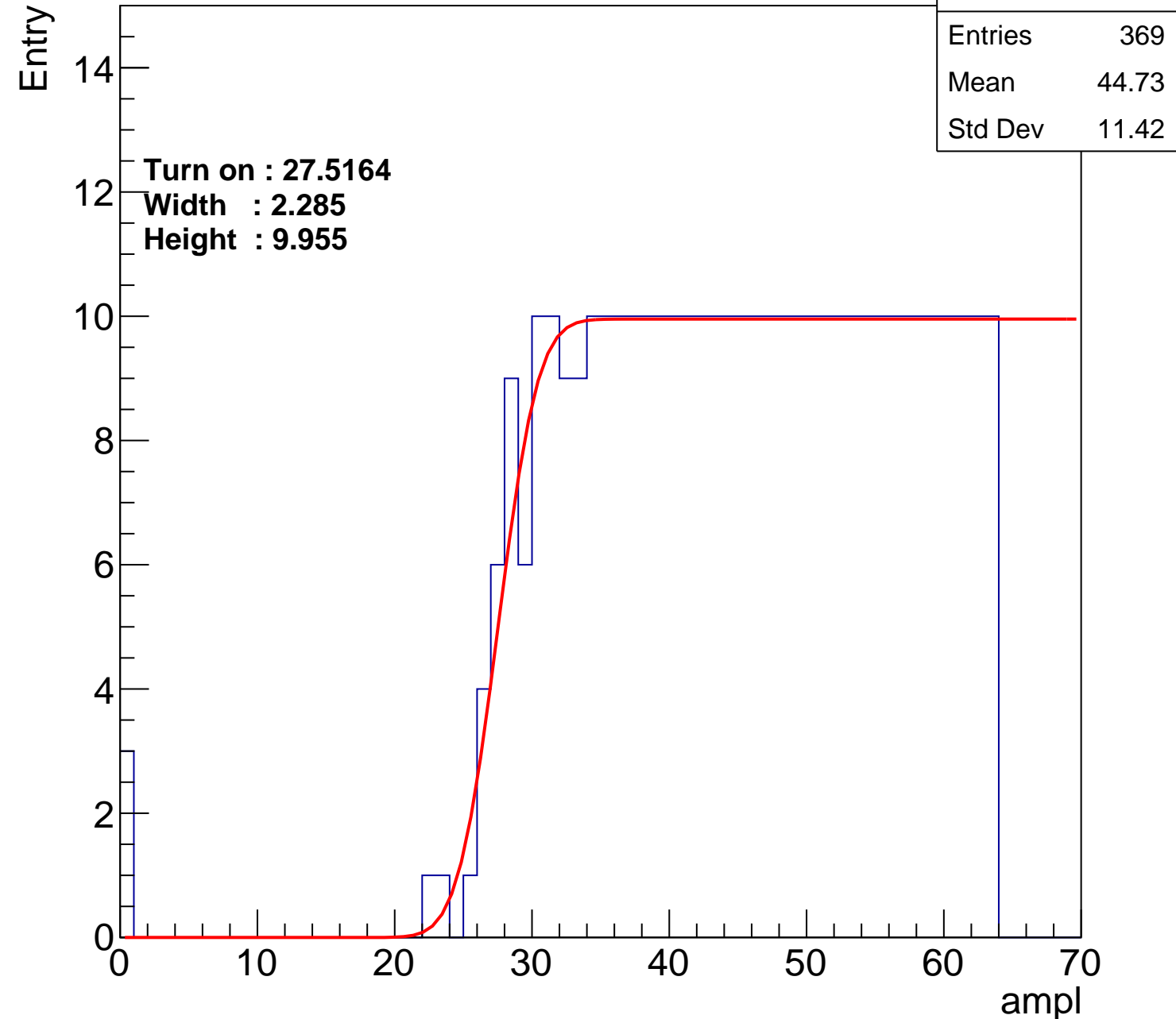
Width : 2.285

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch77

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.43 |
| Std Dev | 12.22 |

Turn on : 24.8904

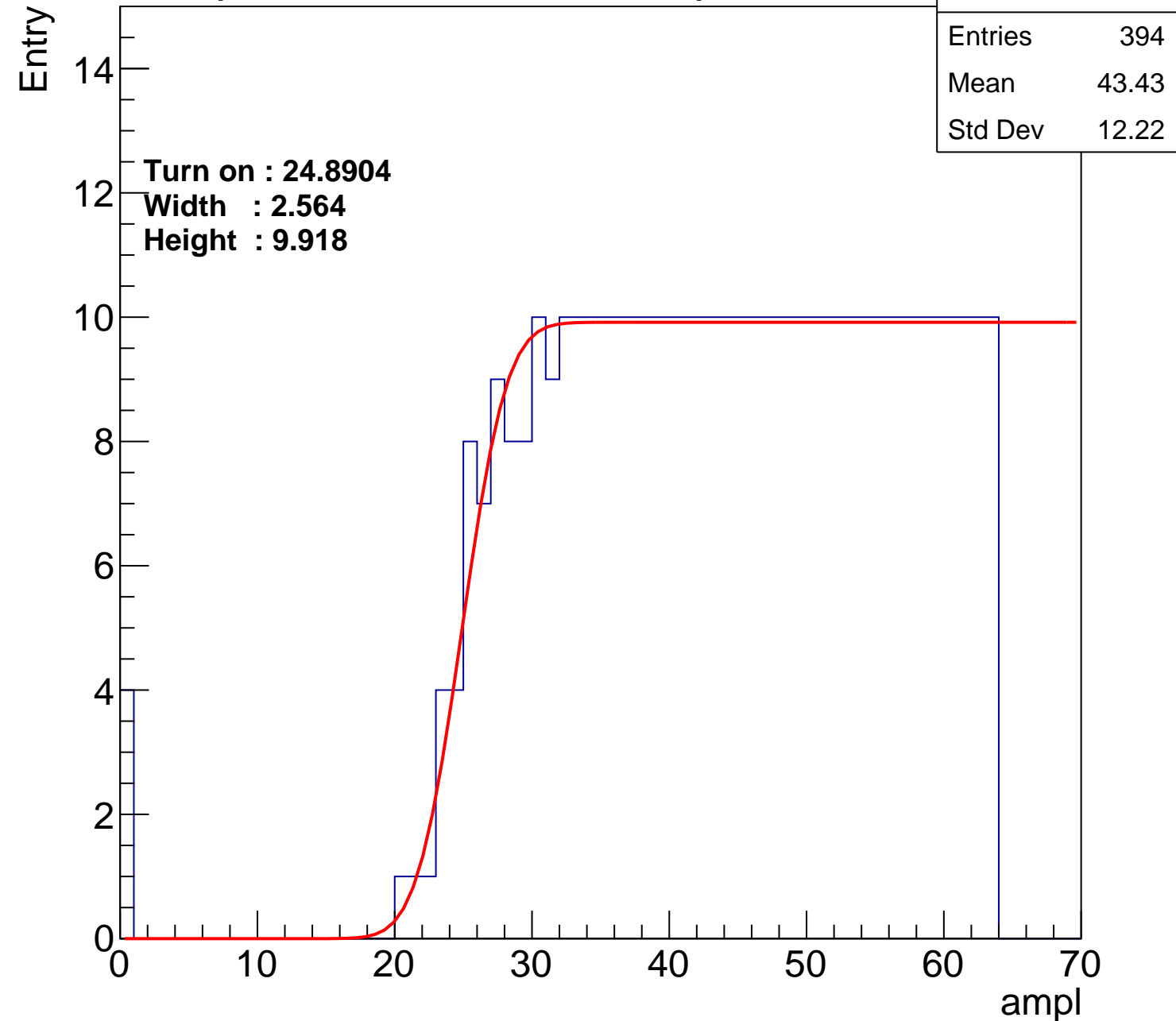
Width : 2.564

Height : 9.918

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch78

calib_packv5_042523_0143.root, FC#5, port B1

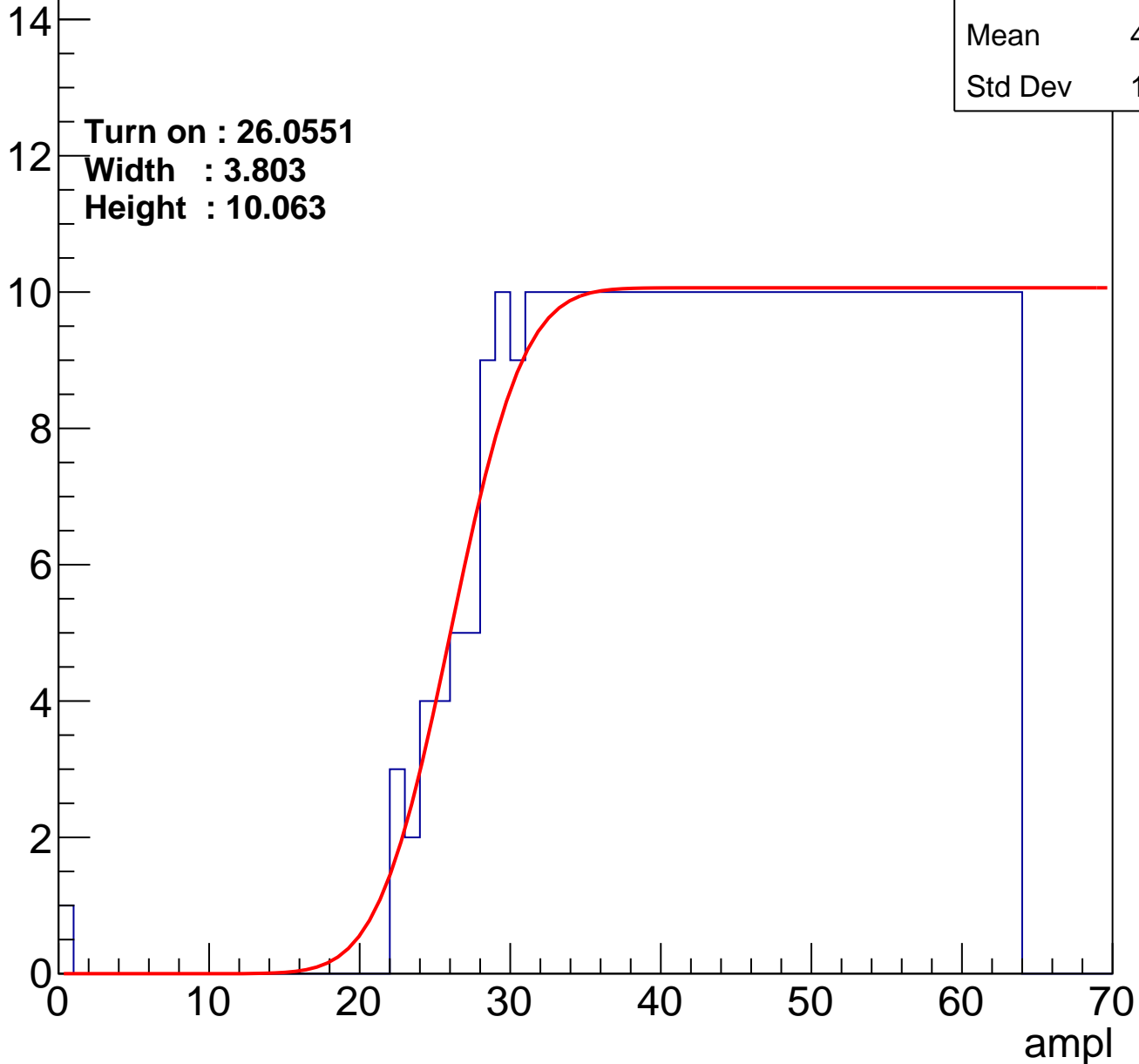
| | |
|---------|-------|
| Entries | 382 |
| Mean | 44.23 |
| Std Dev | 11.39 |

Turn on : 26.0551

Width : 3.803

Height : 10.063

Entry



B0L000S, U23-ch79

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 346 |
| Mean | 45.83 |
| Std Dev | 10.94 |

Turn on : 29.7424

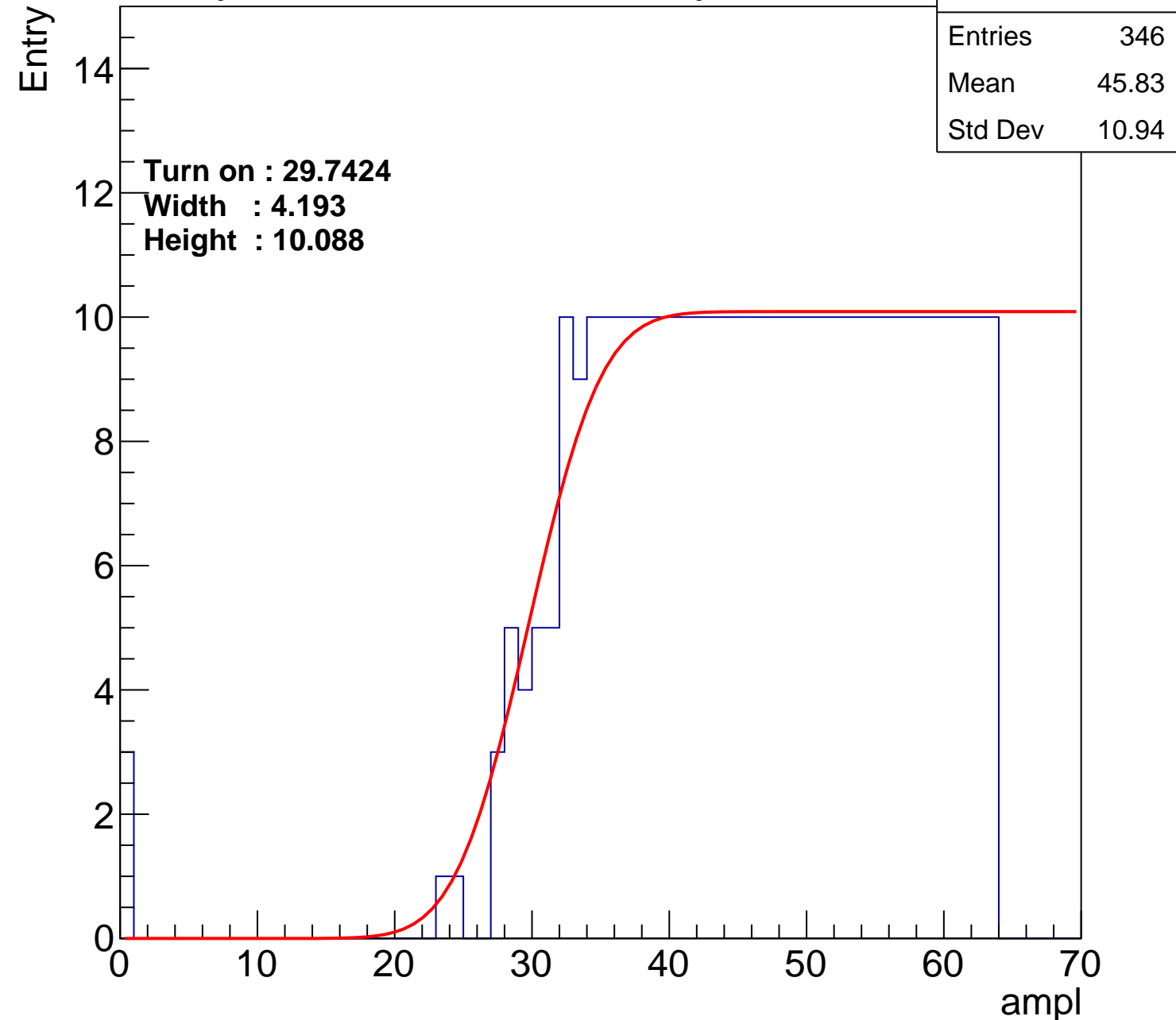
Width : 4.193

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch80

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.95 |
| Std Dev | 11.64 |

Turn on : 25.3665

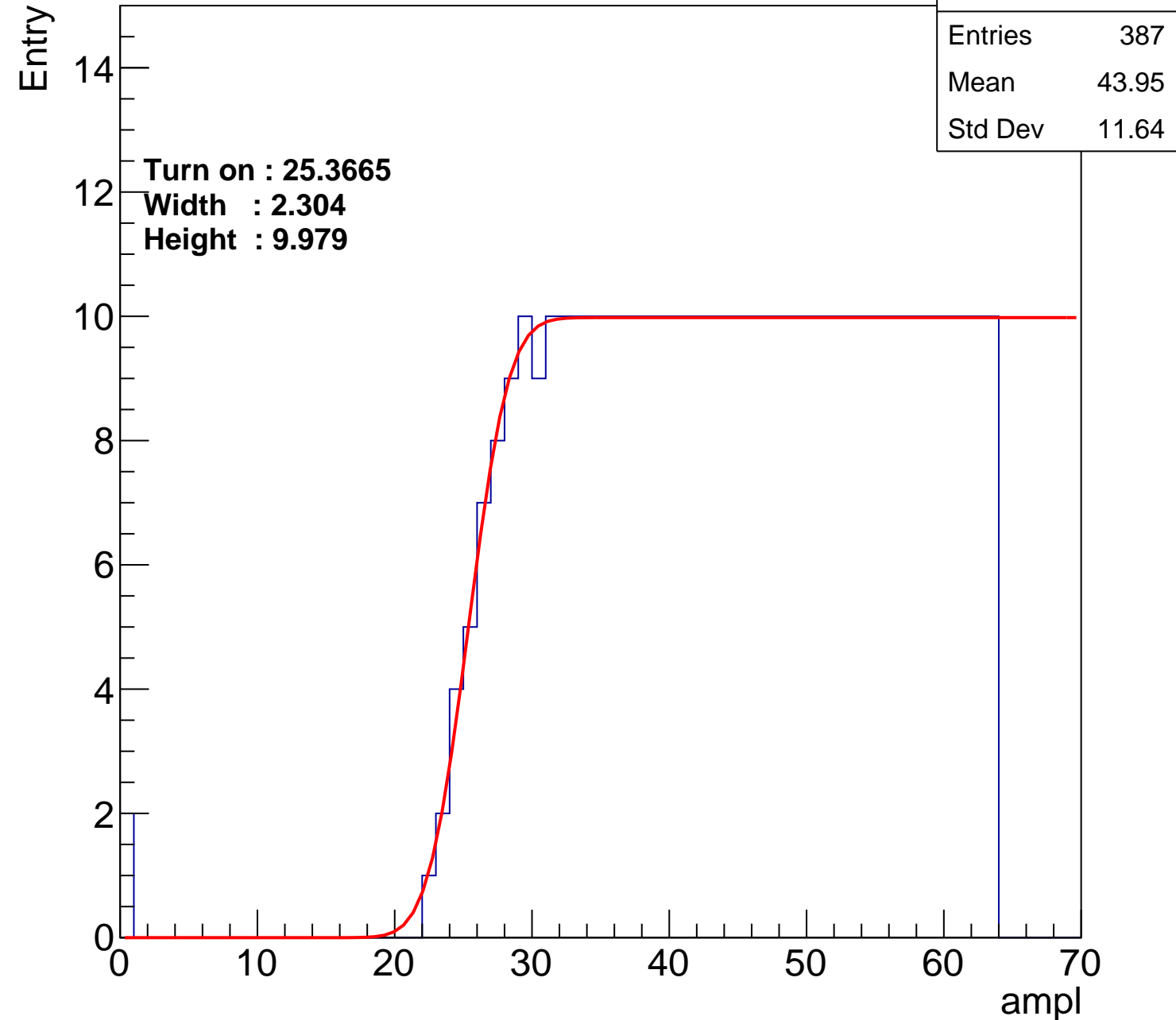
Width : 2.304

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch81

calib_packv5_042523_0143.root, FC#5, port B1

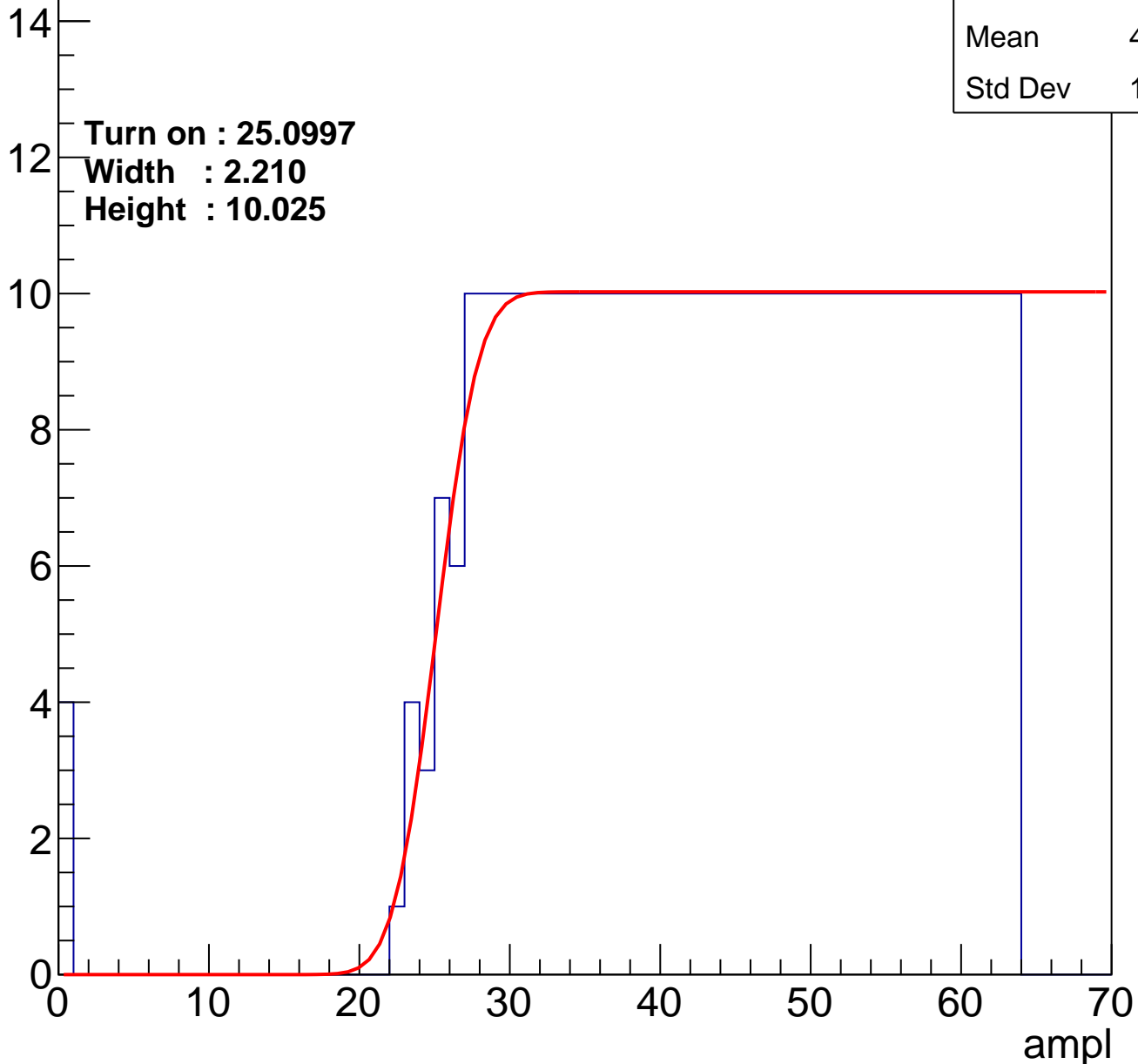
| | |
|---------|-------|
| Entries | 395 |
| Mean | 43.46 |
| Std Dev | 12.13 |

Turn on : 25.0997

Width : 2.210

Height : 10.025

Entry



B0L000S, U23-ch82

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.28 |
| Std Dev | 11.48 |

Turn on : 26.3460

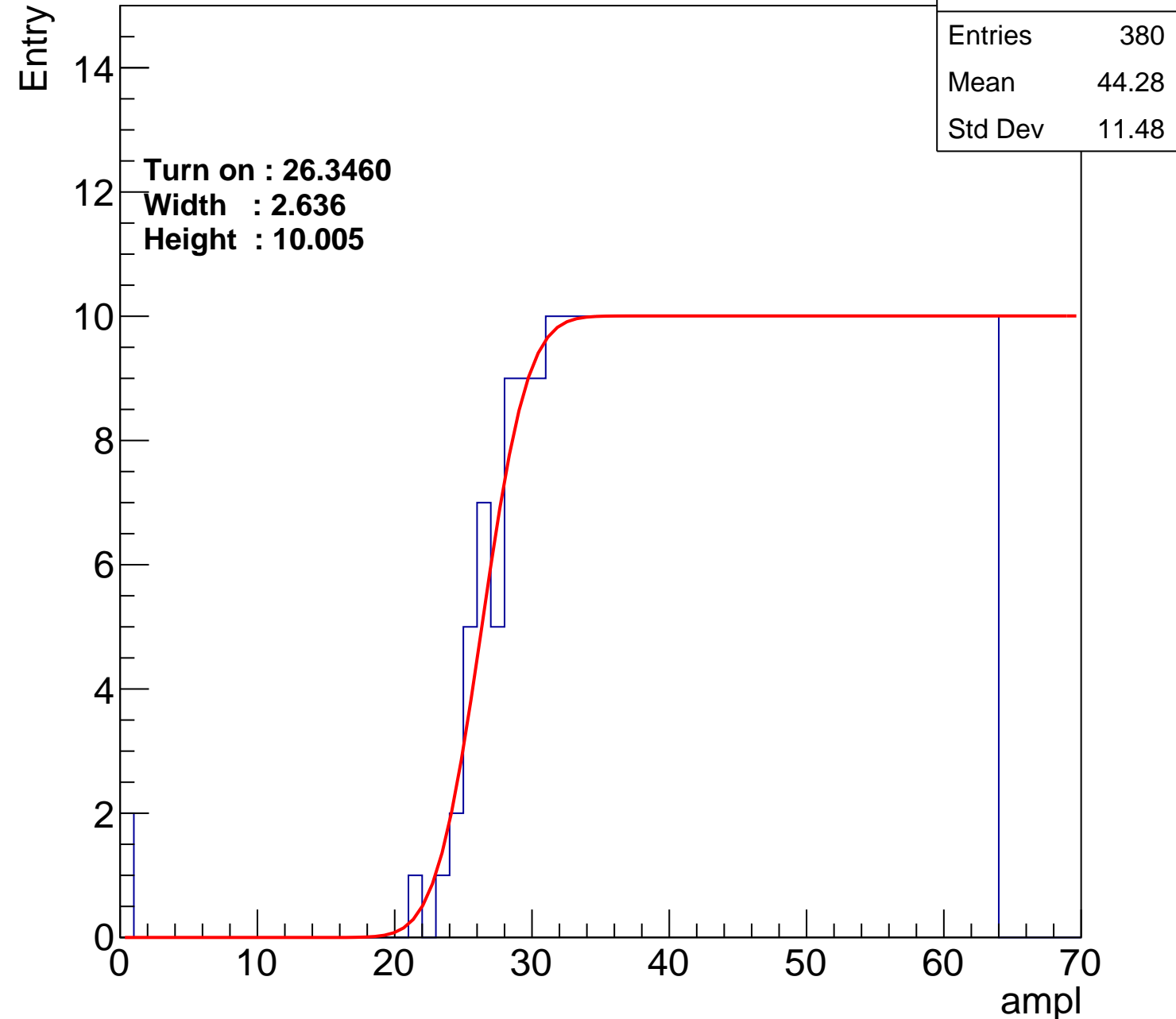
Width : 2.636

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch83

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.96 |
| Std Dev | 11 |

Turn on : 27.5748

Width : 2.501

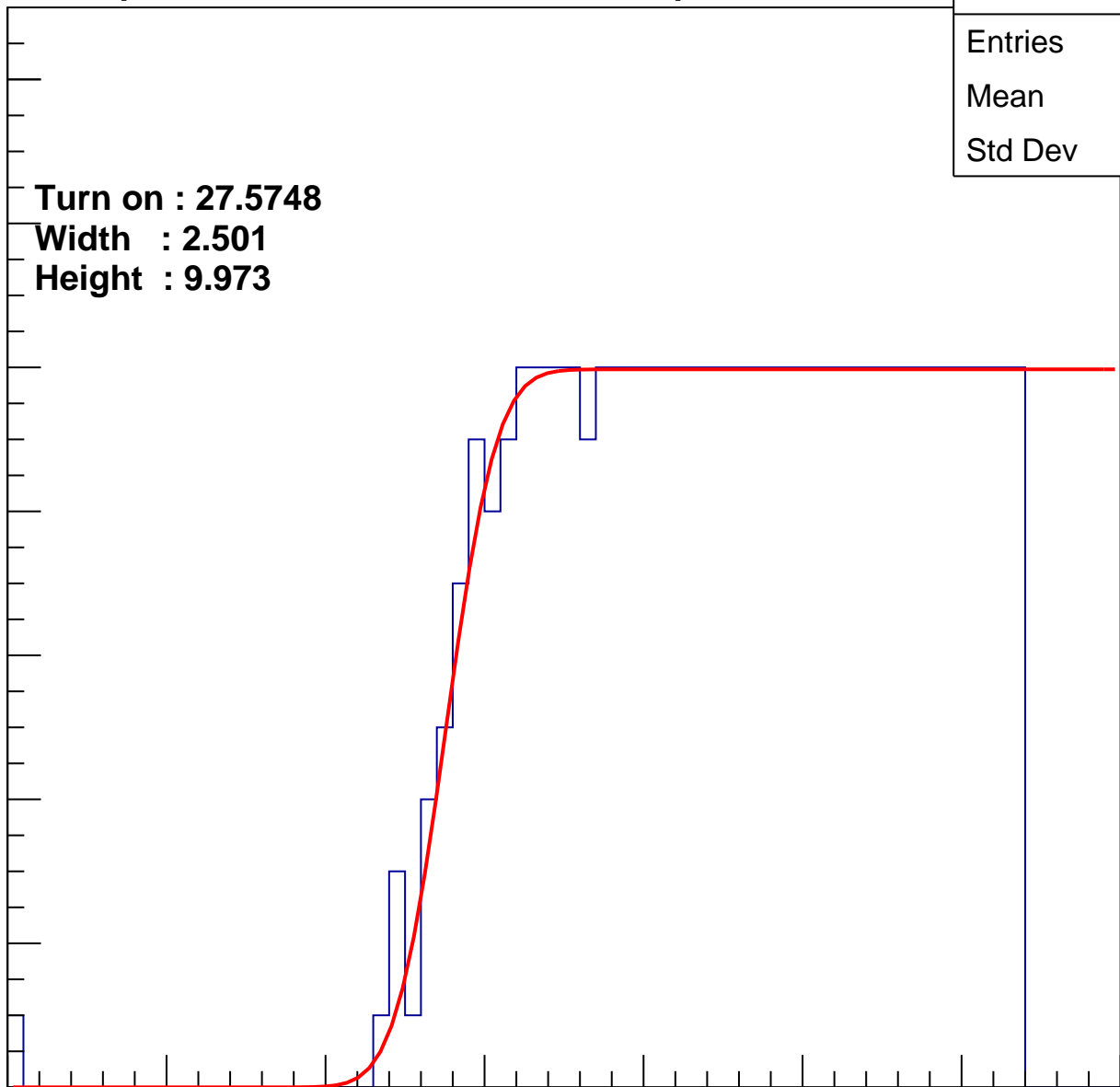
Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U23-ch84

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 390 |
| Mean | 43.72 |
| Std Dev | 11.91 |

Turn on : 25.5927

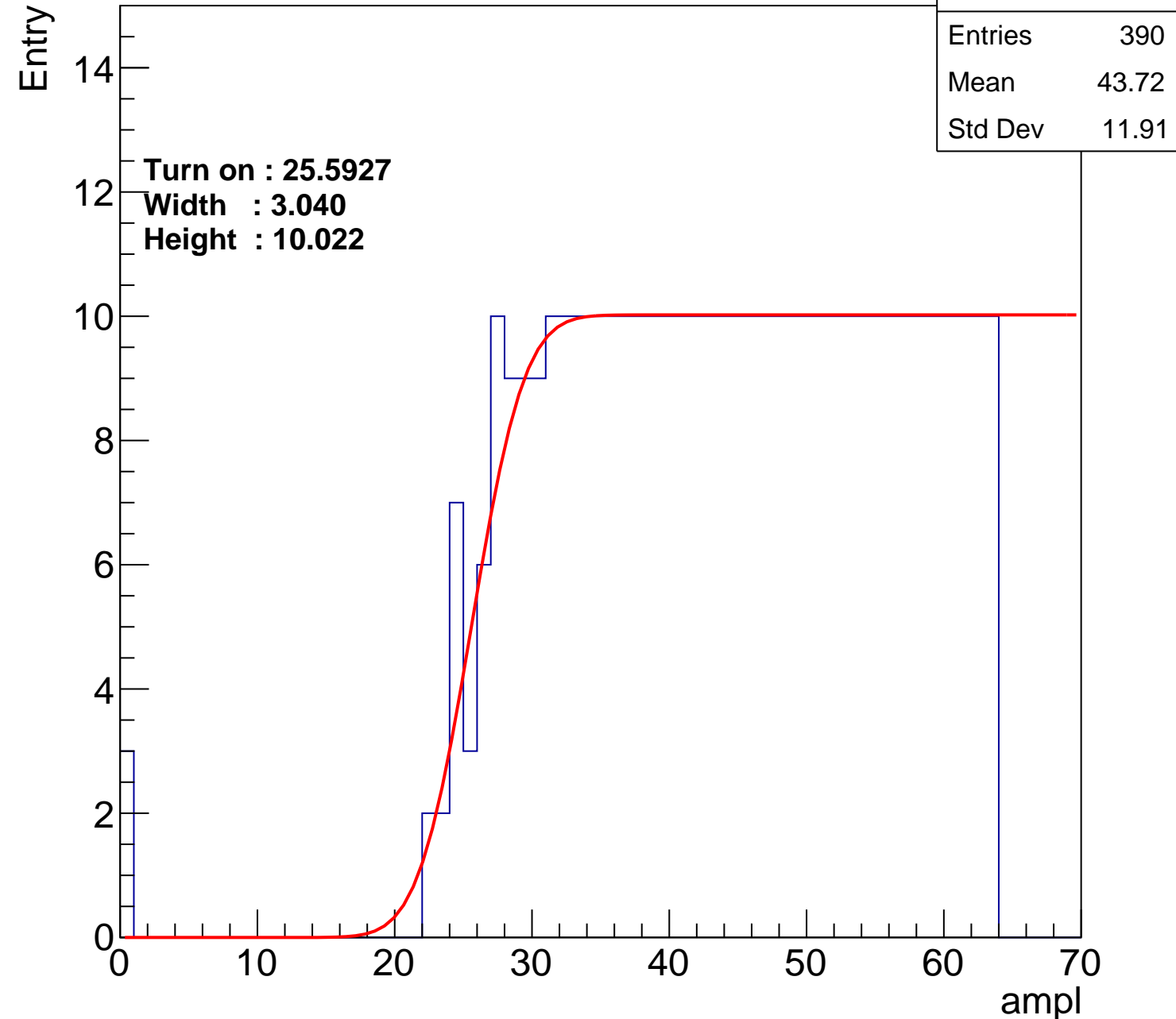
Width : 3.040

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch85

calib_packv5_042523_0143.root, FC#5, port B1

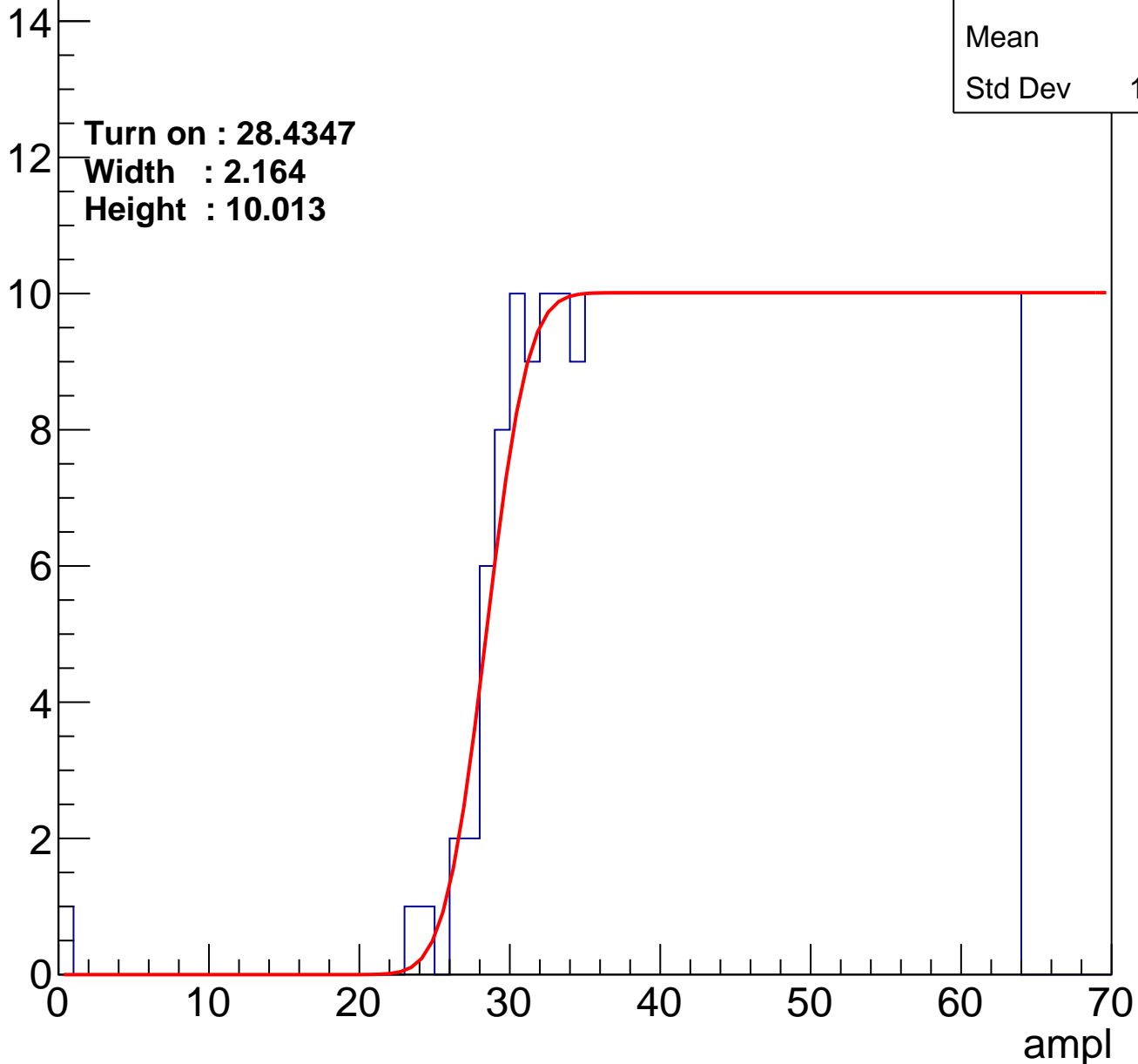
| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.4 |
| Std Dev | 10.72 |

Turn on : 28.4347

Width : 2.164

Height : 10.013

Entry



B0L000S, U23-ch86

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.86 |
| Std Dev | 11.85 |

Turn on : 25.3371

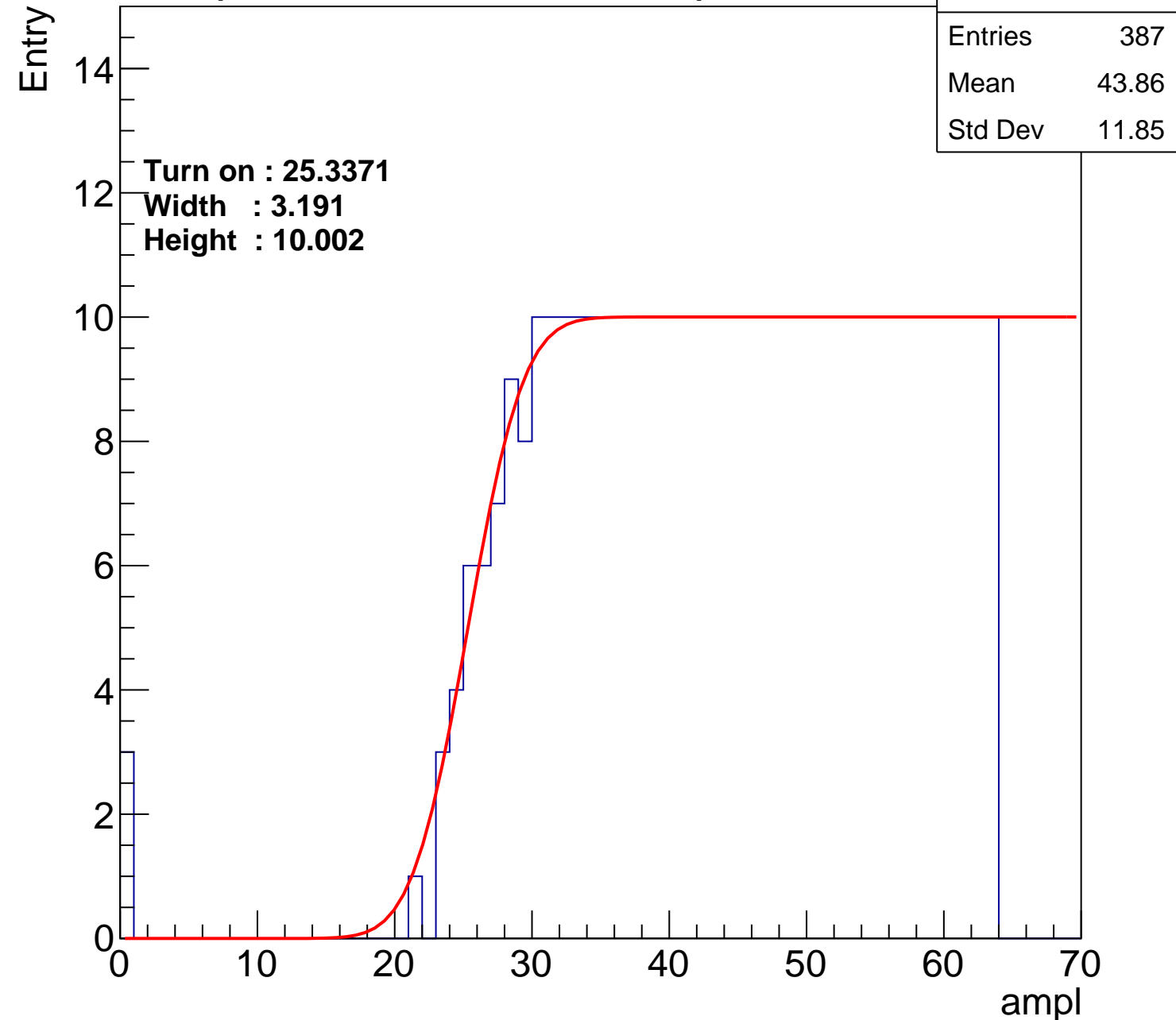
Width : 3.191

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch87

calib_packv5_042523_0143.root, FC#5, port B1

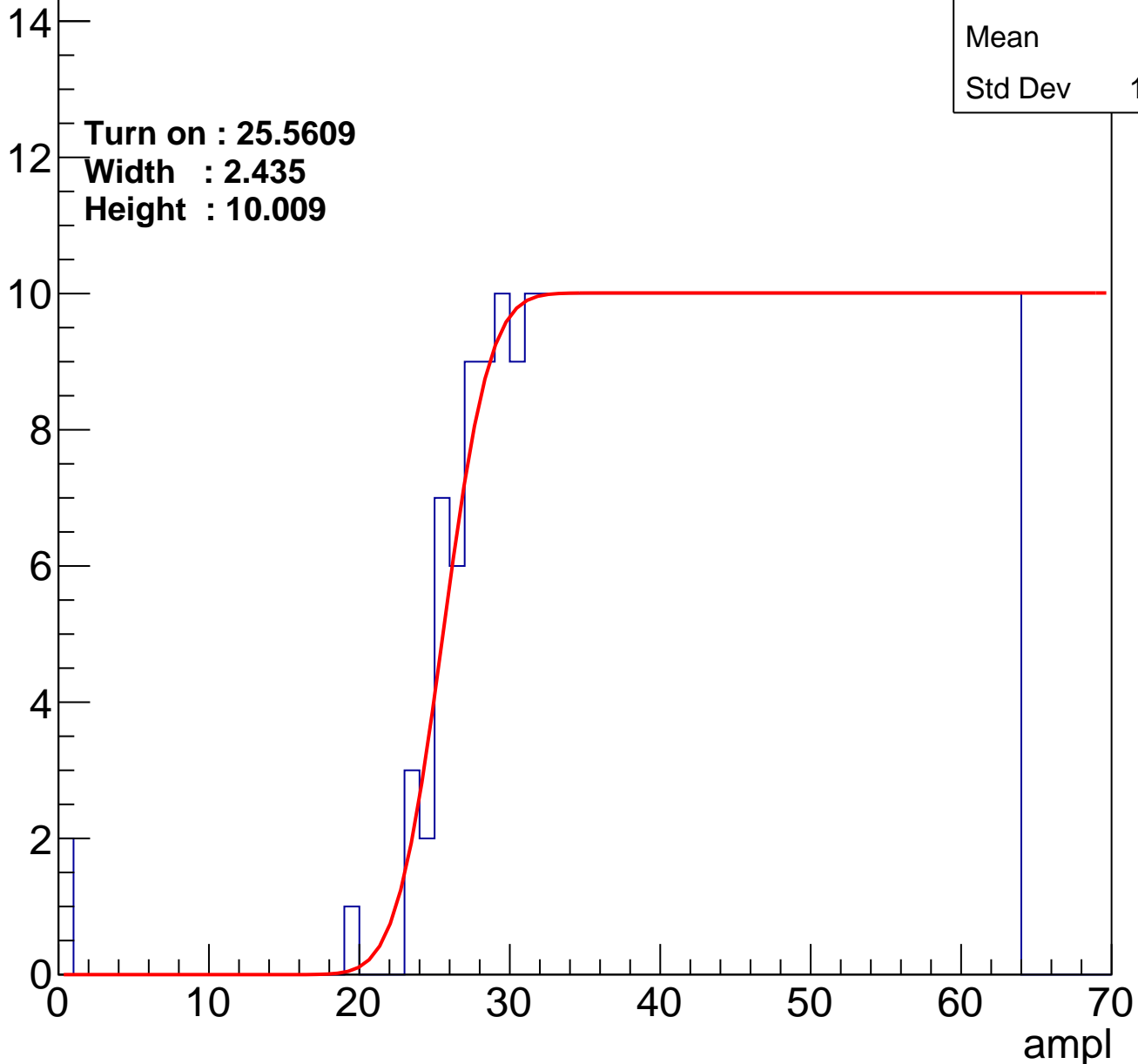
| | |
|---------|-------|
| Entries | 388 |
| Mean | 43.9 |
| Std Dev | 11.68 |

Turn on : 25.5609

Width : 2.435

Height : 10.009

Entry



B0L000S, U23-ch88

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.28 |
| Std Dev | 11 |

Turn on : 28.3005

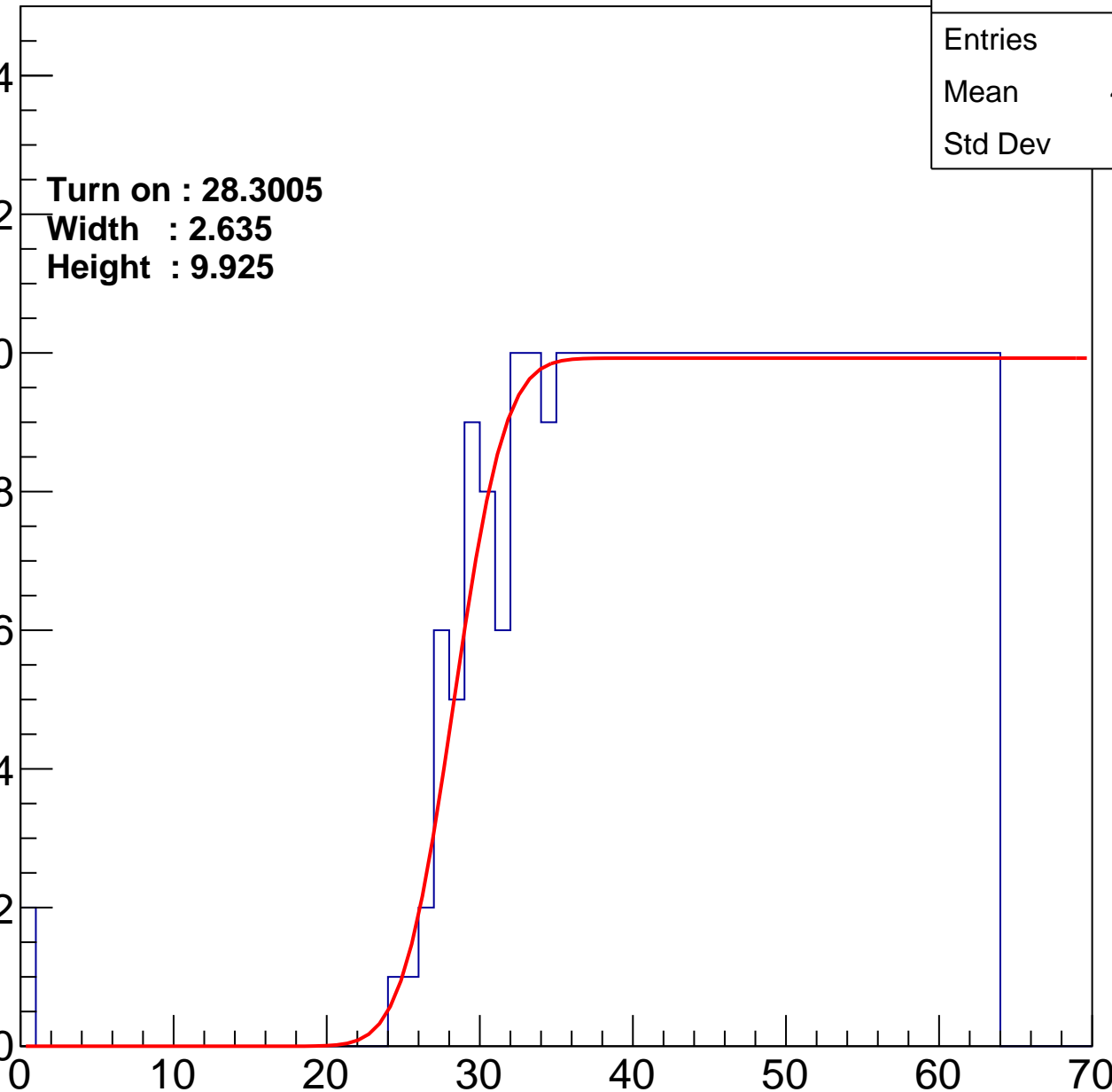
Width : 2.635

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch89

calib_packv5_042523_0143.root, FC#5, port B1

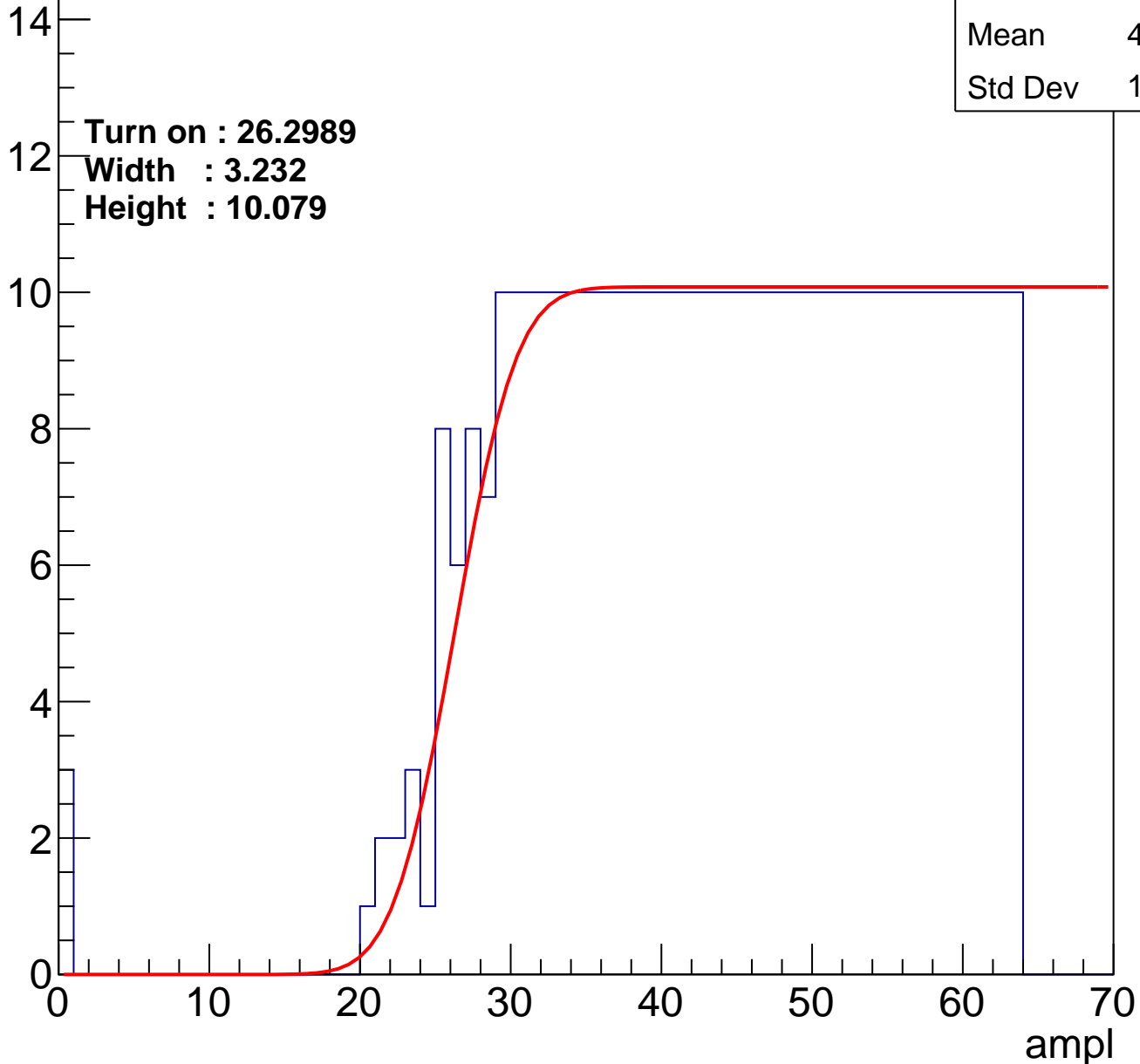
| | |
|---------|-------|
| Entries | 391 |
| Mean | 43.65 |
| Std Dev | 11.98 |

Turn on : 26.2989

Width : 3.232

Height : 10.079

Entry



B0L000S, U23-ch90

calib_packv5_042523_0143.root, FC#5, port B1

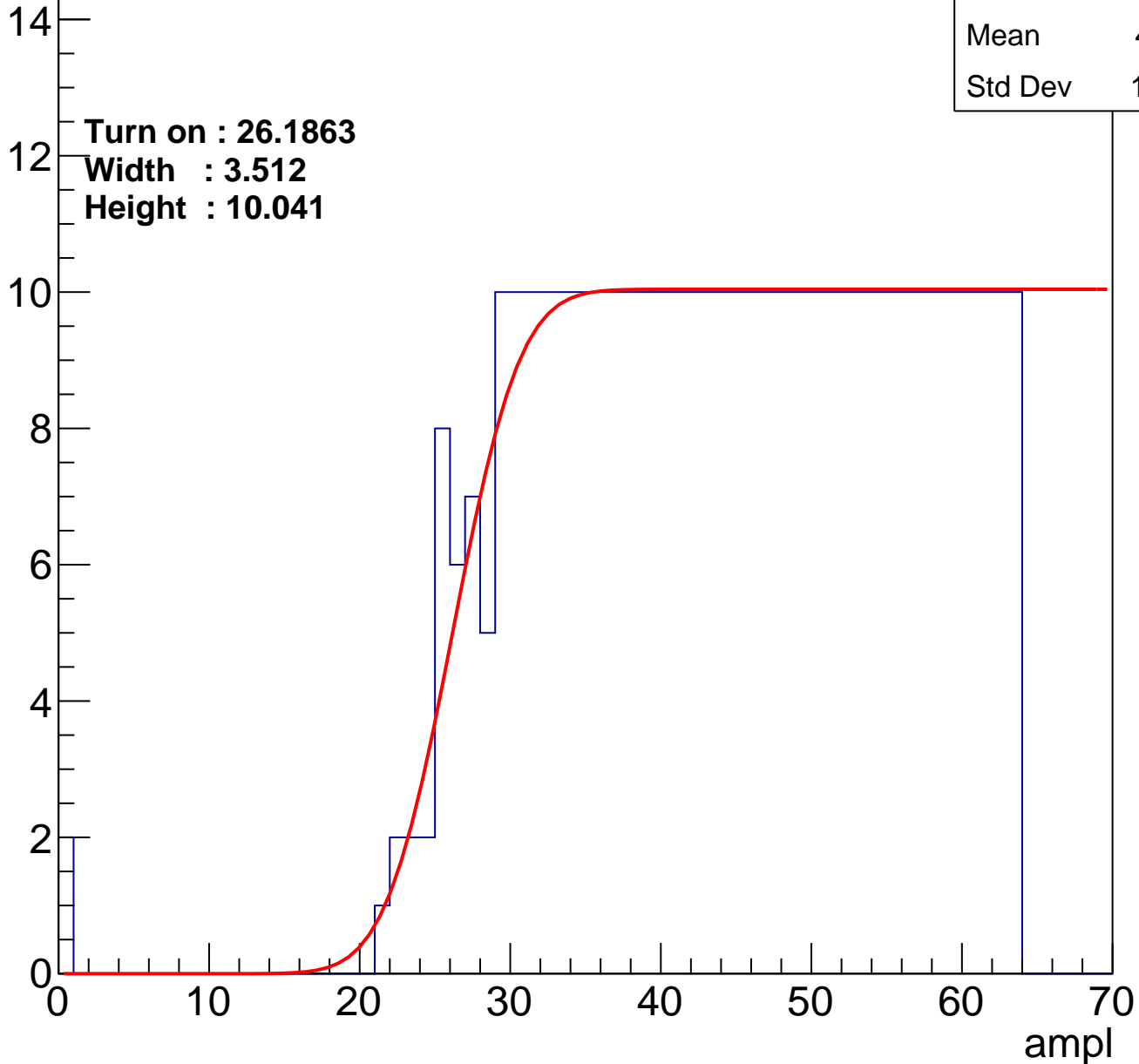
| | |
|---------|-------|
| Entries | 385 |
| Mean | 44.01 |
| Std Dev | 11.65 |

Turn on : 26.1863

Width : 3.512

Height : 10.041

Entry



B0L000S, U23-ch91

calib_packv5_042523_0143.root, FC#5, port B1

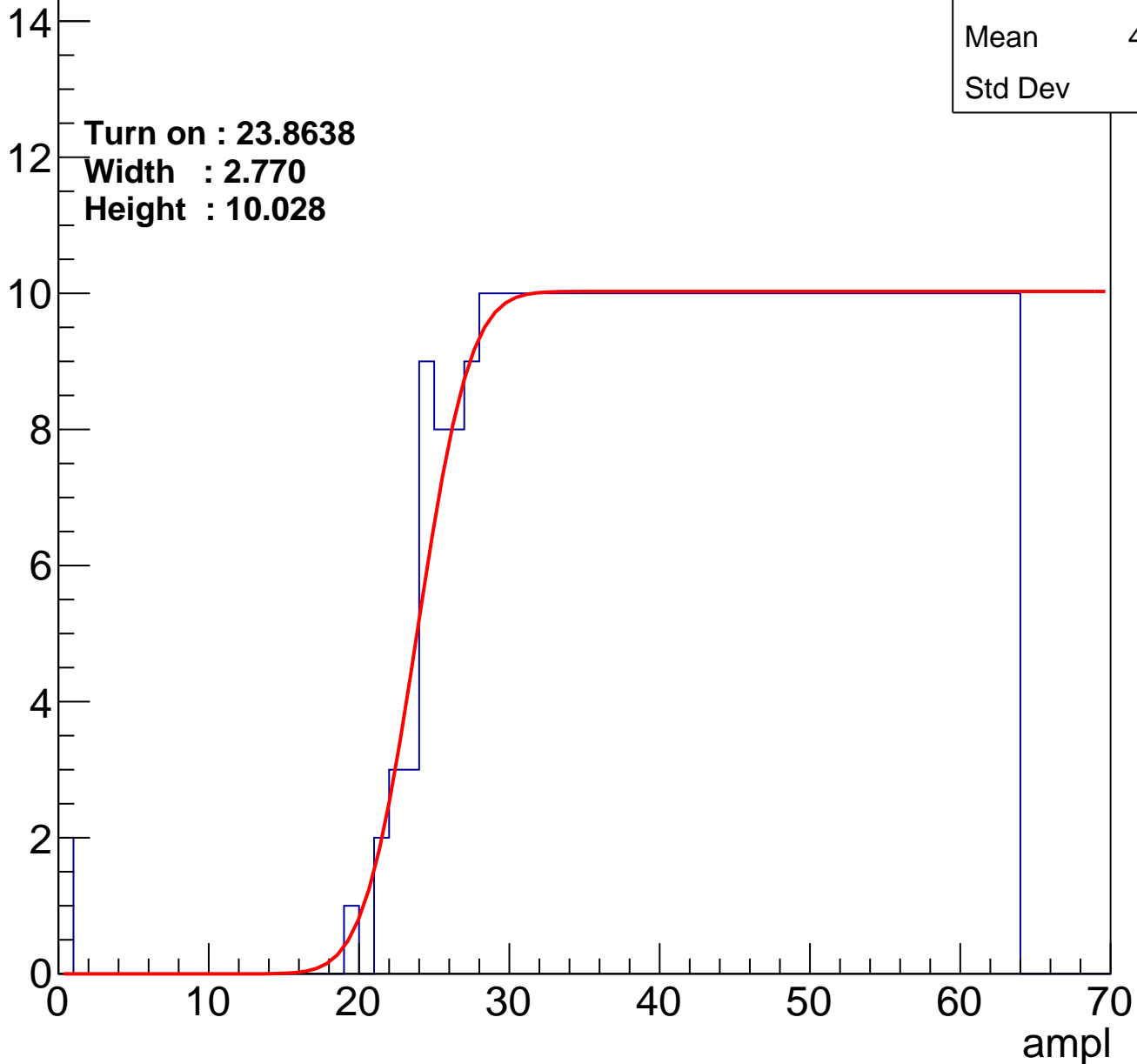
| | |
|---------|-------|
| Entries | 405 |
| Mean | 43.07 |
| Std Dev | 12.1 |

Turn on : 23.8638

Width : 2.770

Height : 10.028

Entry



B0L000S, U23-ch92

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.31 |
| Std Dev | 11.01 |

Turn on : 28.9565

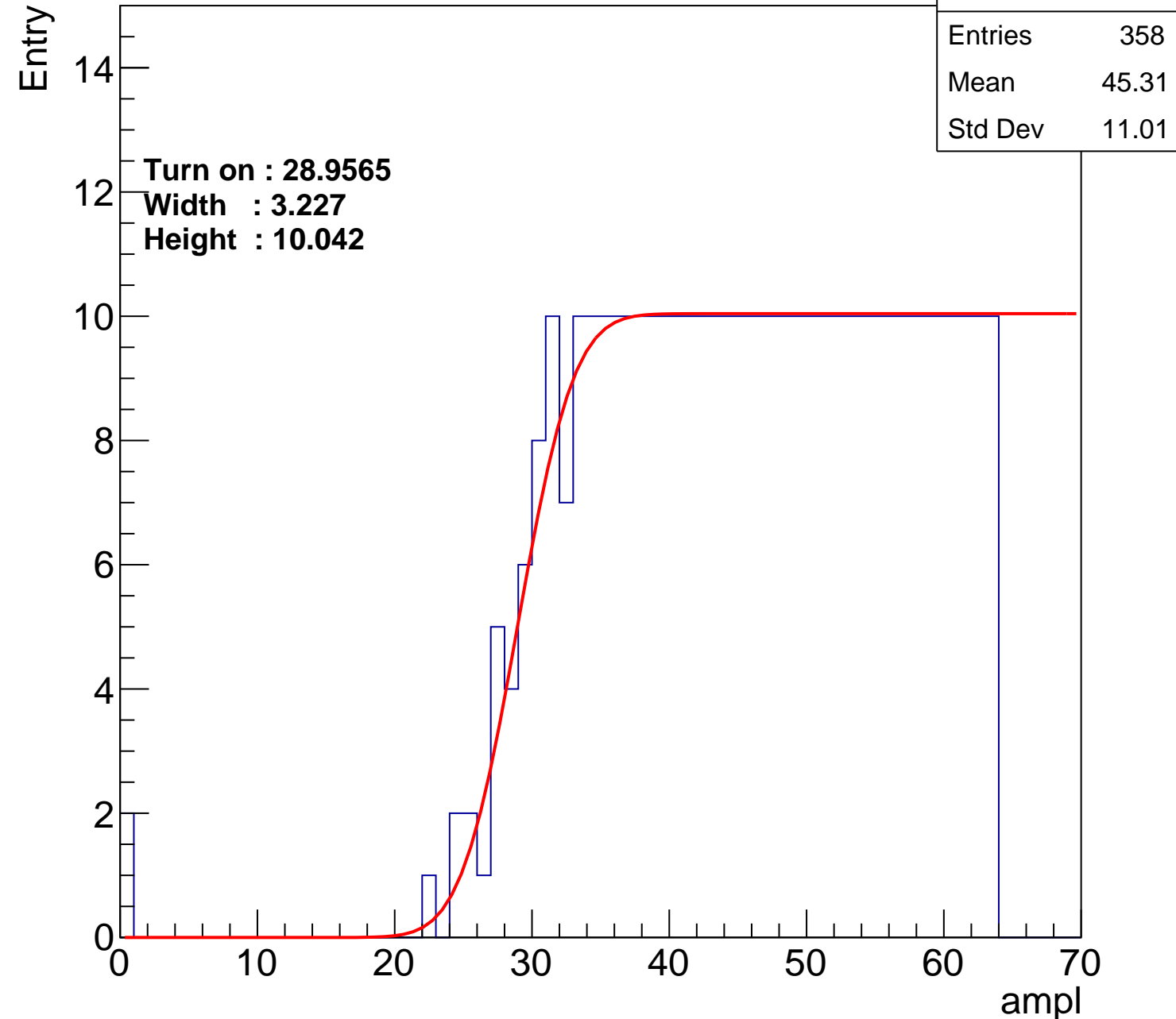
Width : 3.227

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch93

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.71 |
| Std Dev | 12.22 |

Turn on : 25.5518

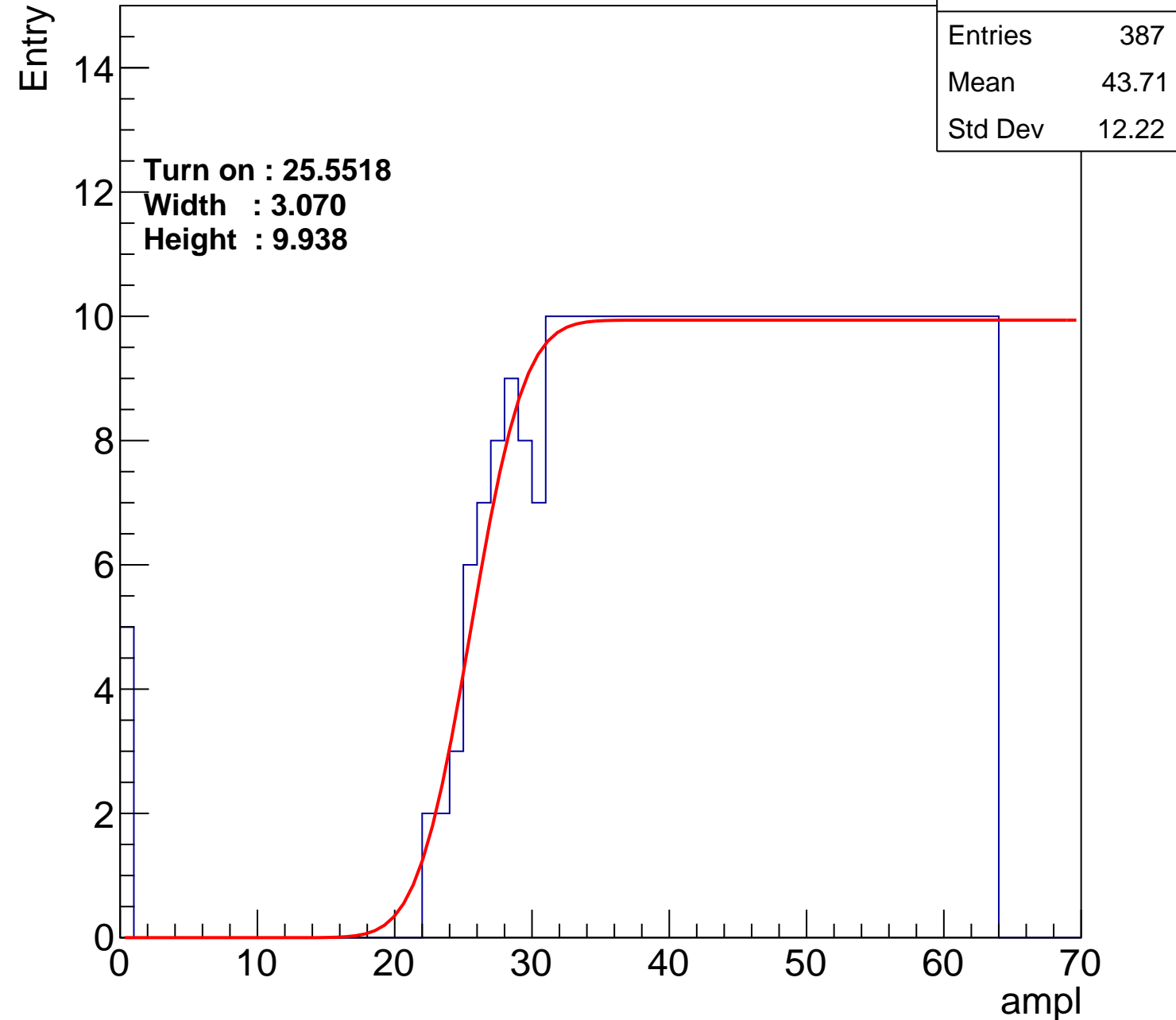
Width : 3.070

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch94

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 364 |
| Mean | 44.87 |
| Std Dev | 11.65 |

Turn on : 27.8198

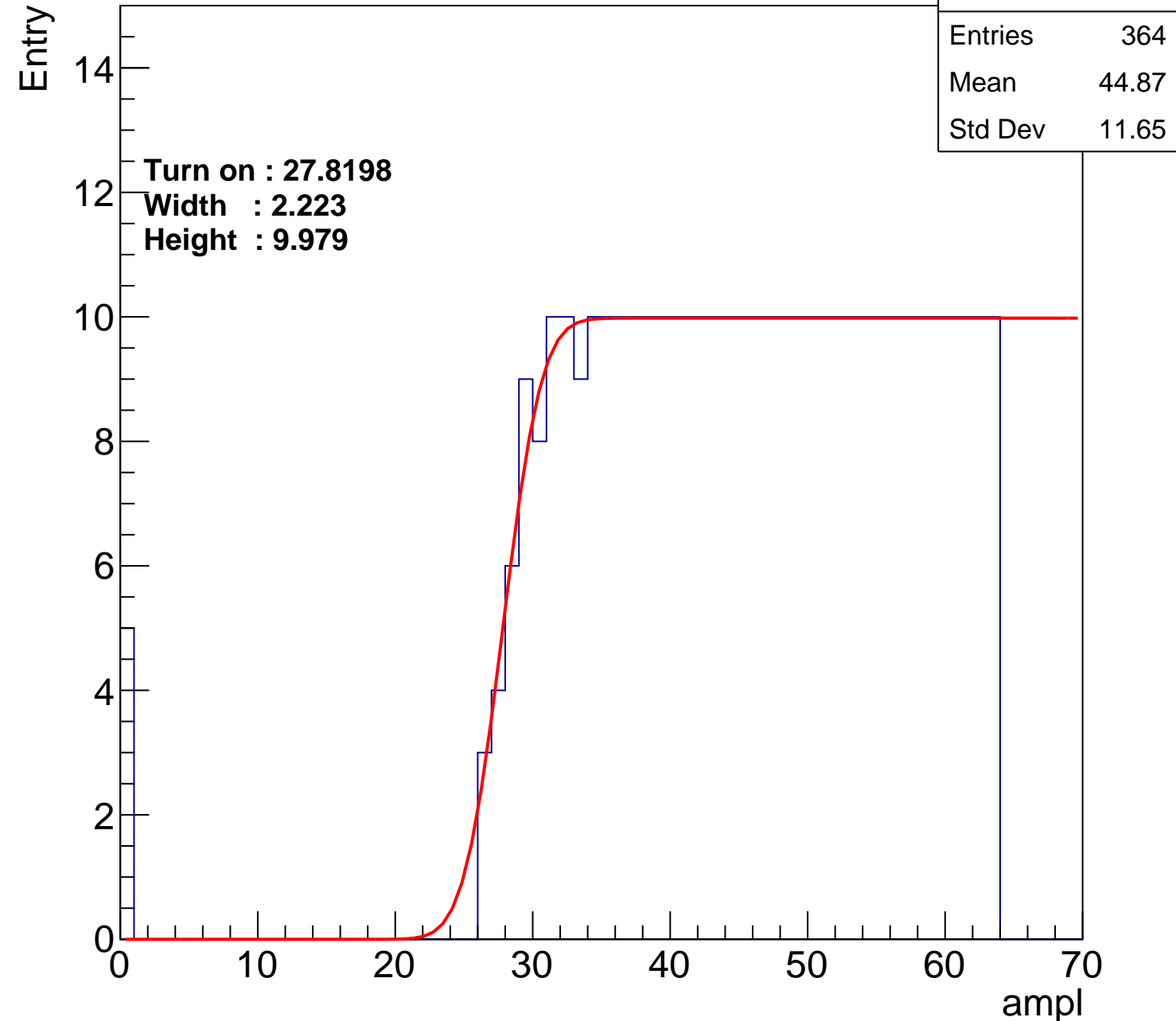
Width : 2.223

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch95

calib_packv5_042523_0143.root, FC#5, port B1

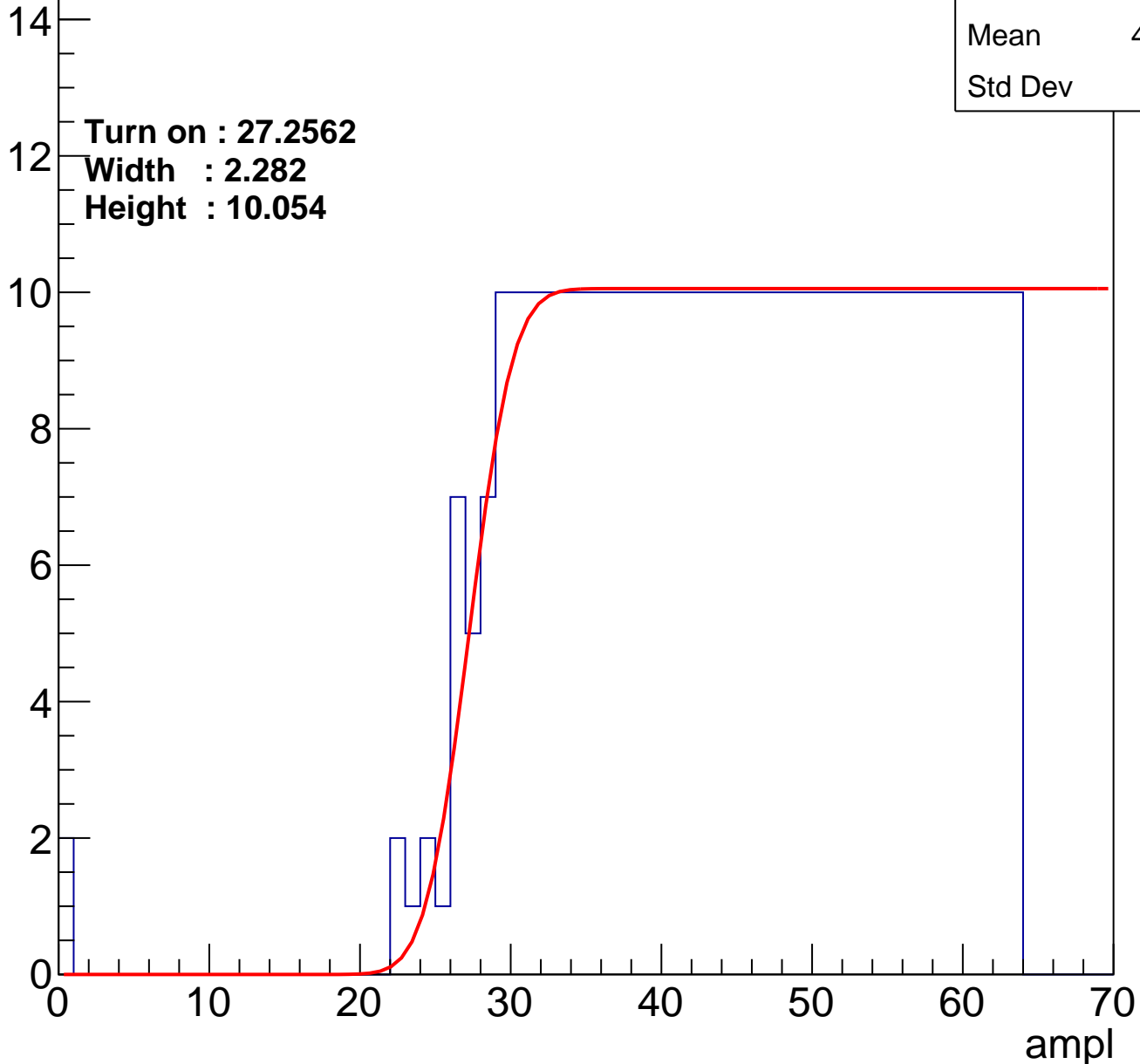
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.44 |
| Std Dev | 11.4 |

Turn on : 27.2562

Width : 2.282

Height : 10.054

Entry



B0L000S, U23-ch96

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.44 |
| Std Dev | 11.25 |

Turn on : 26.6881

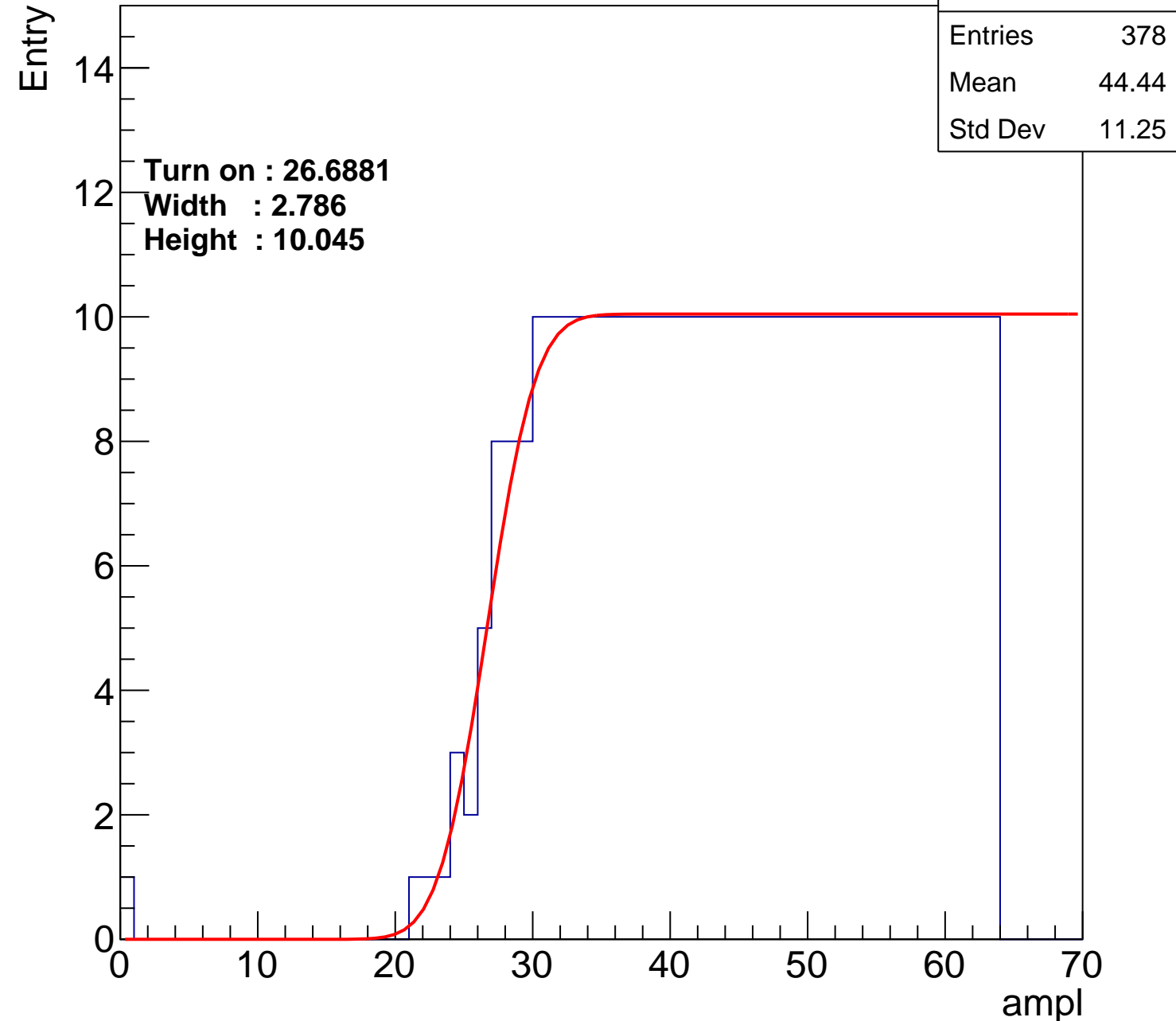
Width : 2.786

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch97

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 370 |
| Mean | 44.75 |
| Std Dev | 11.26 |

Turn on : 26.8969

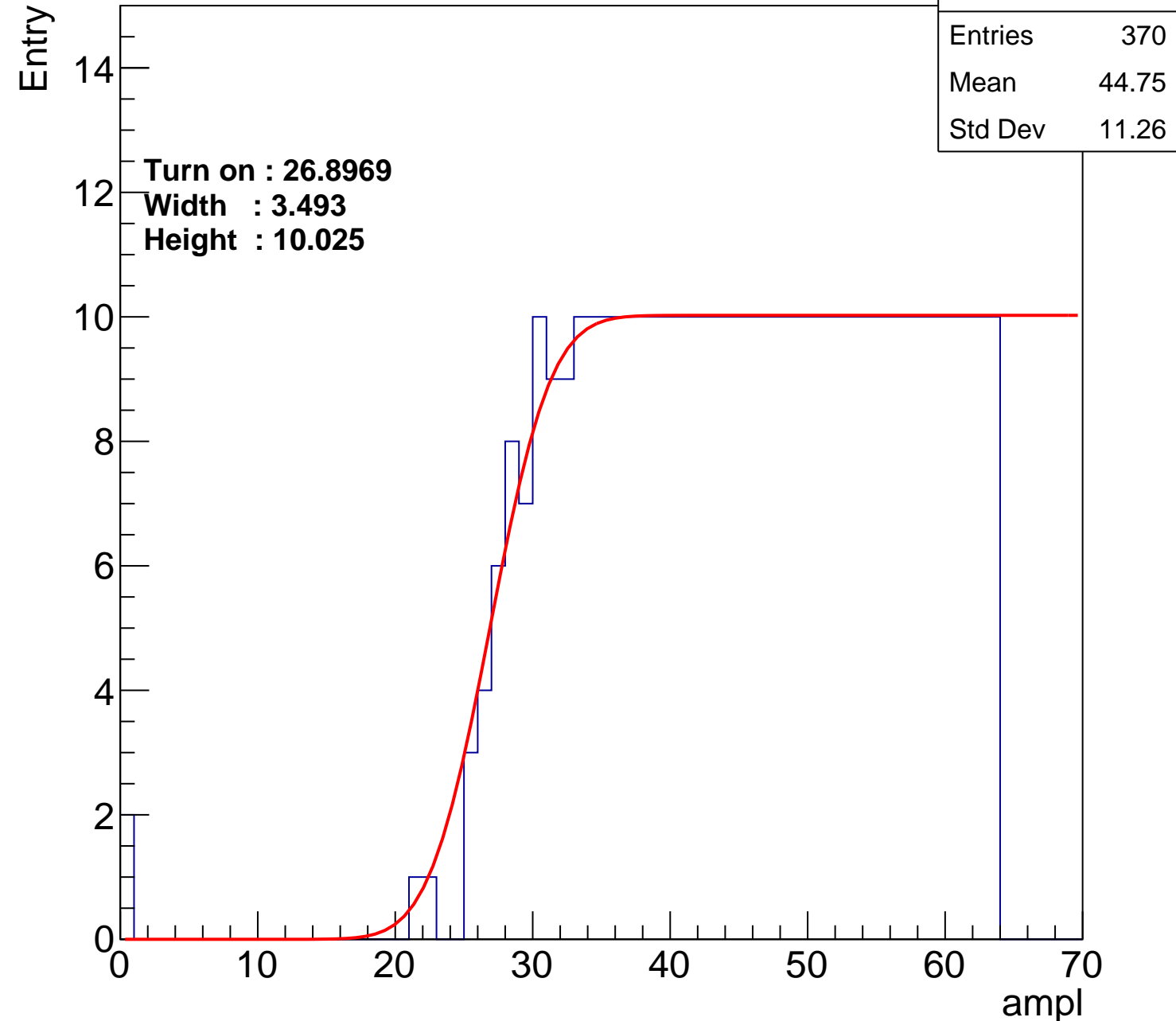
Width : 3.493

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch98

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.99 |
| Std Dev | 10.95 |

Turn on : 27.4720

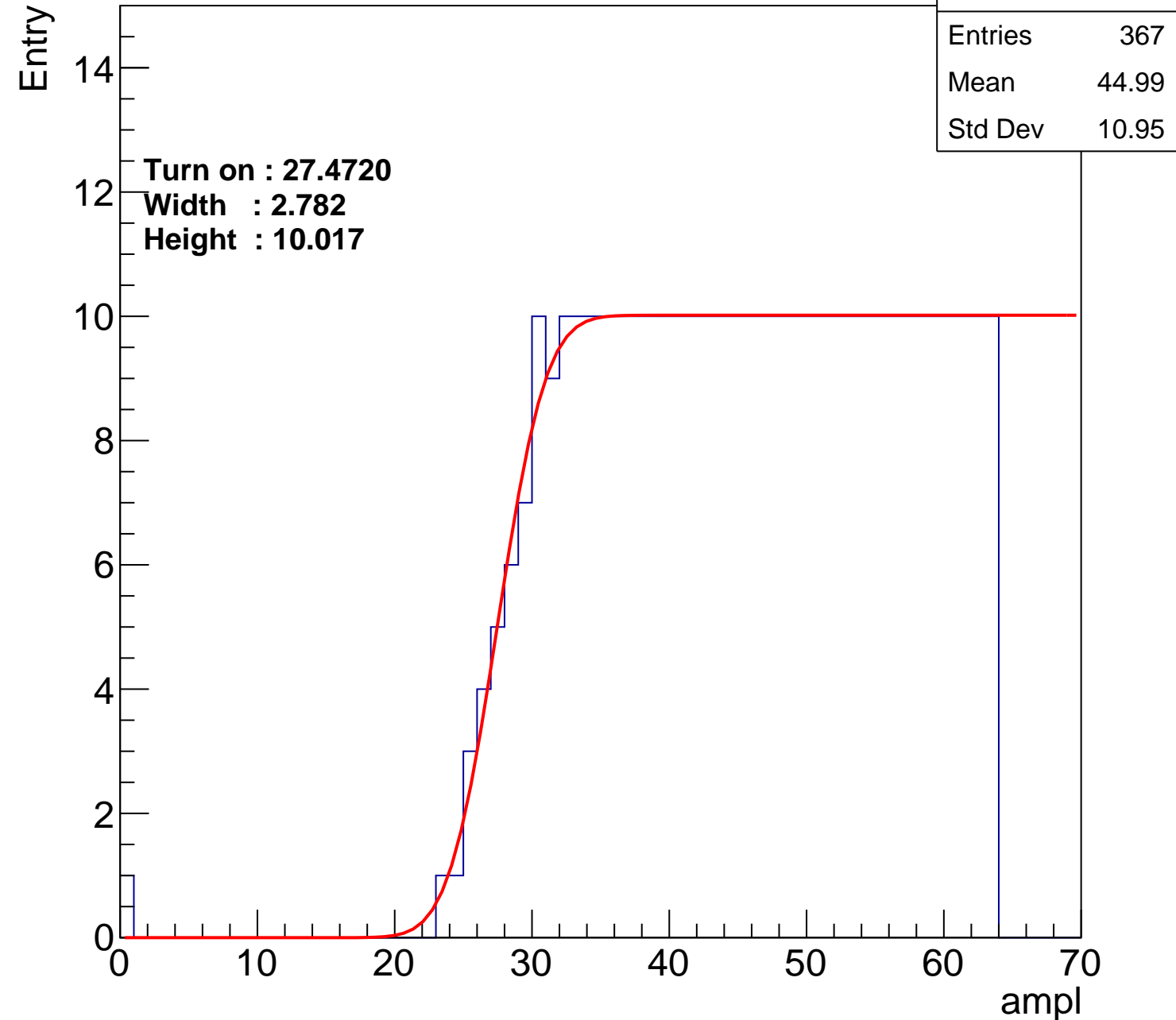
Width : 2.782

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch99

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.89 |
| Std Dev | 11.01 |

Turn on : 27.2706

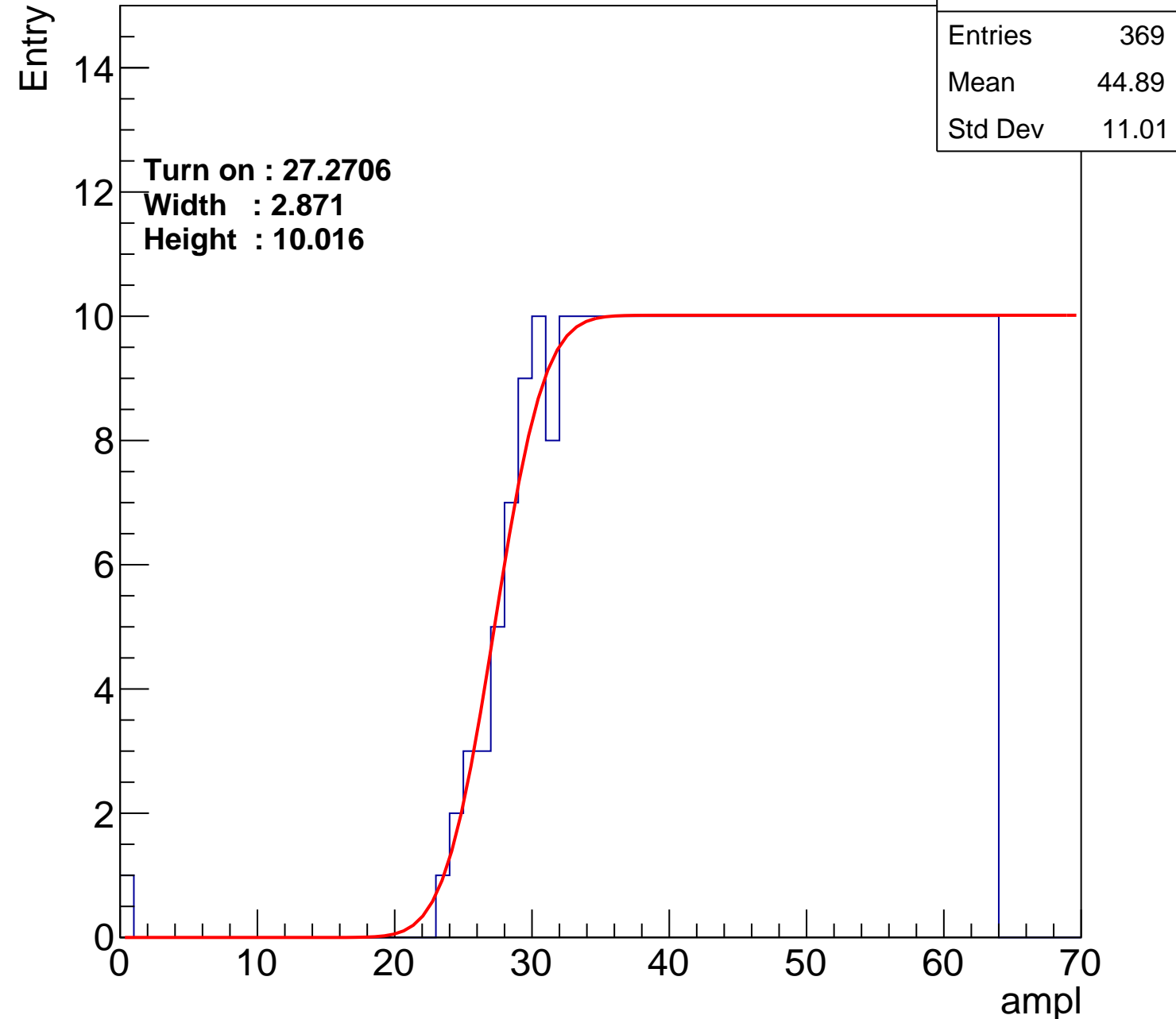
Width : 2.871

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch100

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 368 |
| Mean | 44.77 |
| Std Dev | 11.43 |

Turn on : 27.7211

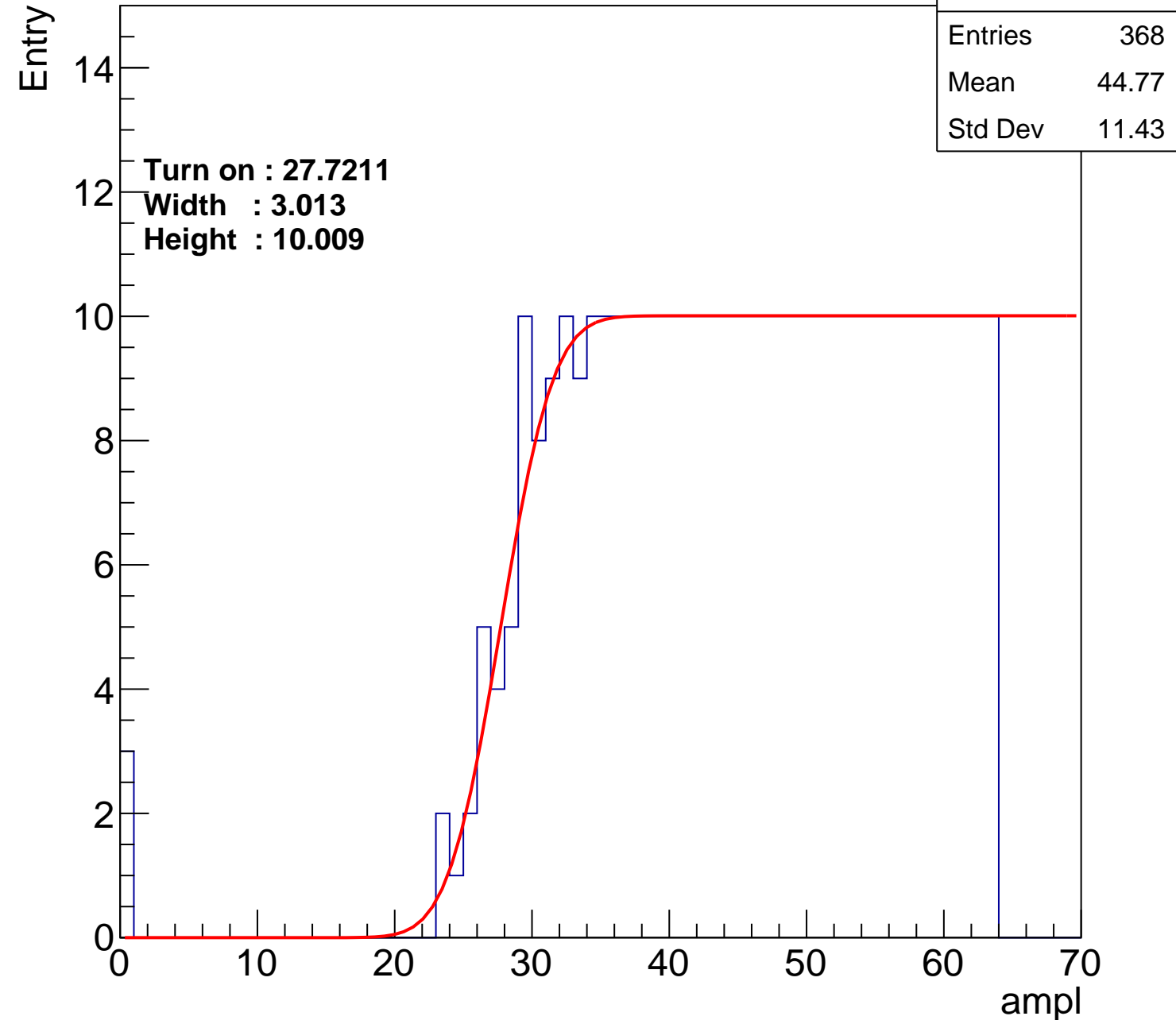
Width : 3.013

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch101

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 362 |
| Mean | 45.23 |
| Std Dev | 10.83 |

Turn on : 28.2903

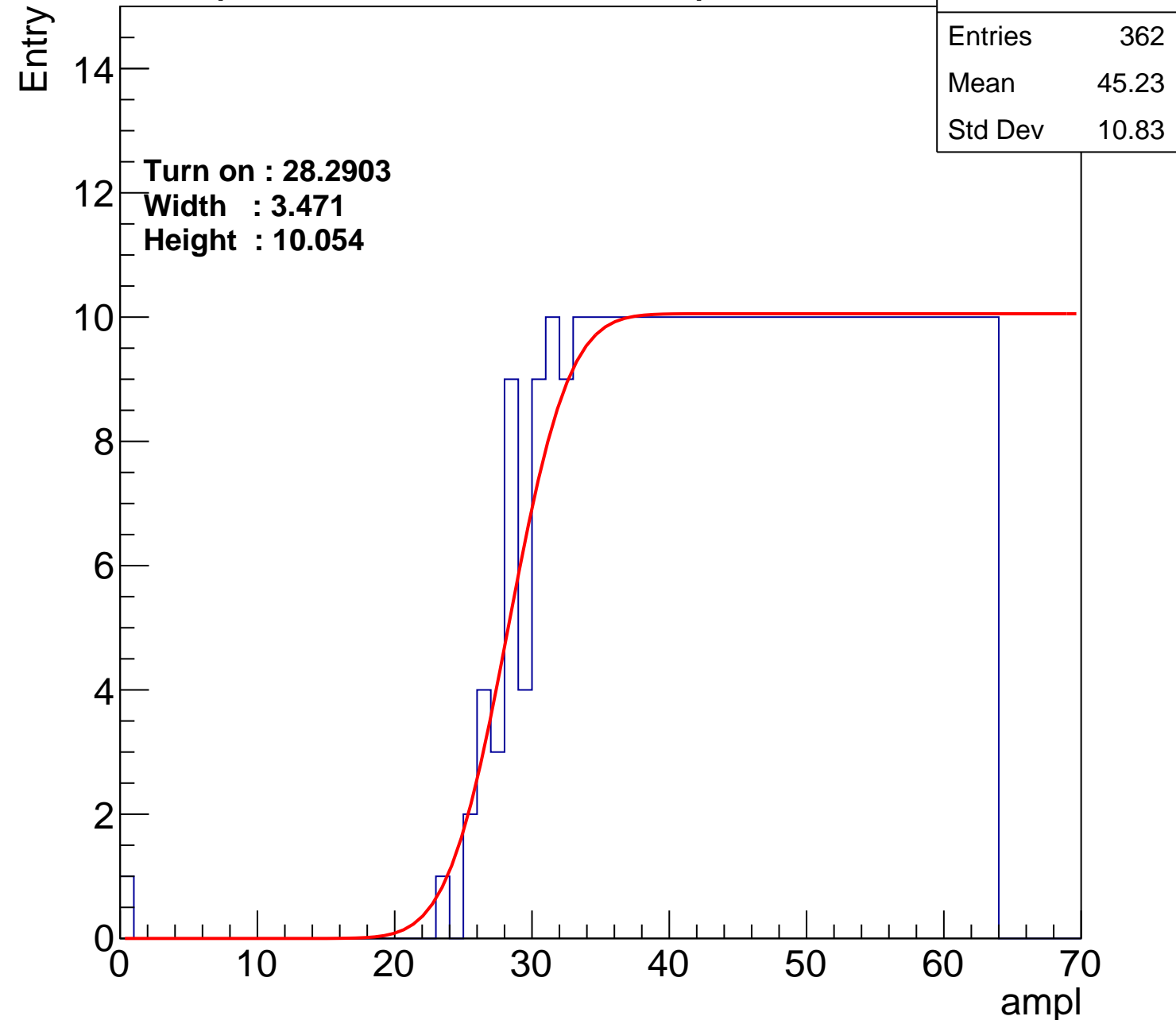
Width : 3.471

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch102

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.37 |
| Std Dev | 11.45 |

Turn on : 26.9787

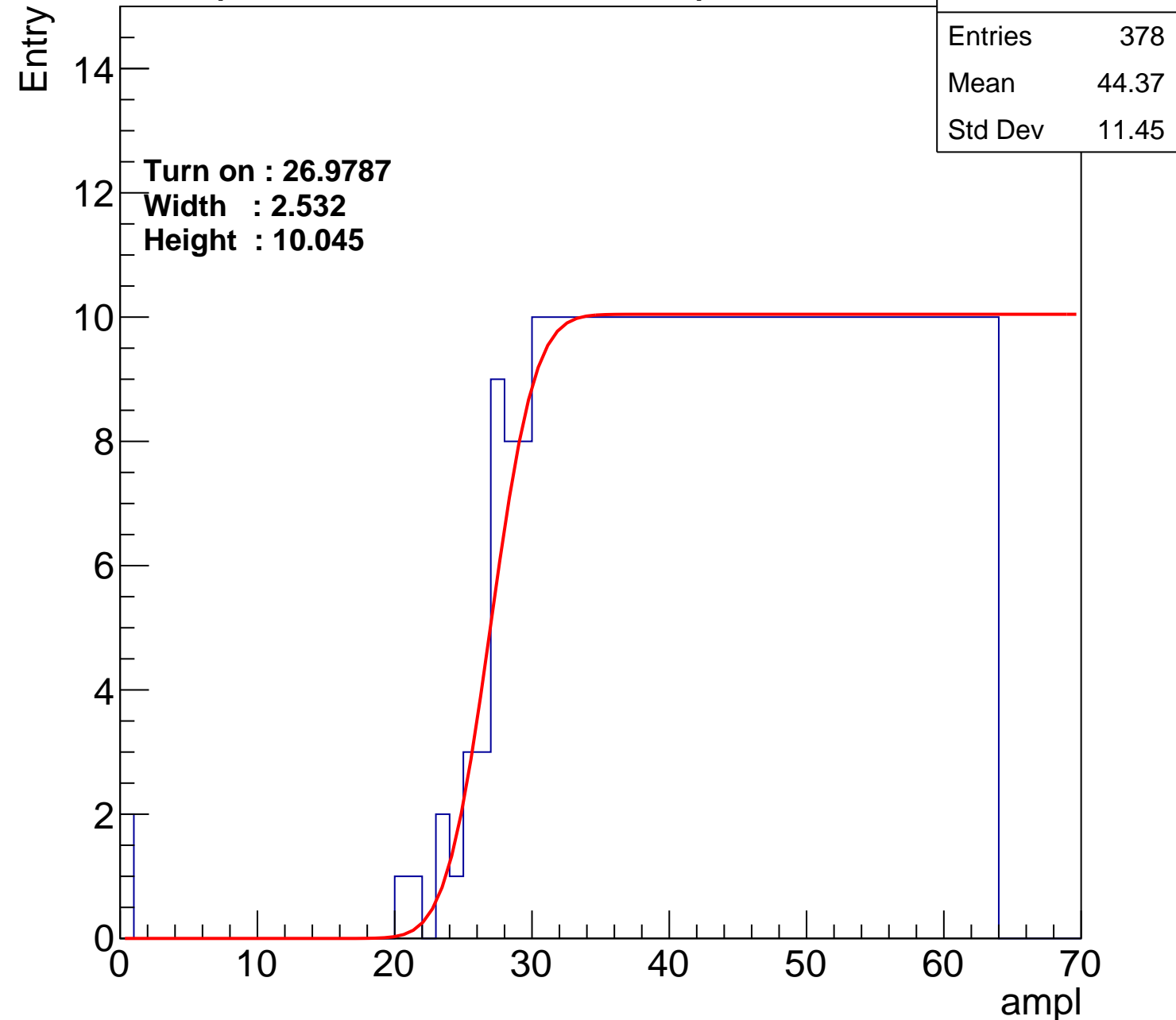
Width : 2.532

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch103

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 393 |
| Mean | 43.58 |
| Std Dev | 11.99 |

Turn on : 25.6410

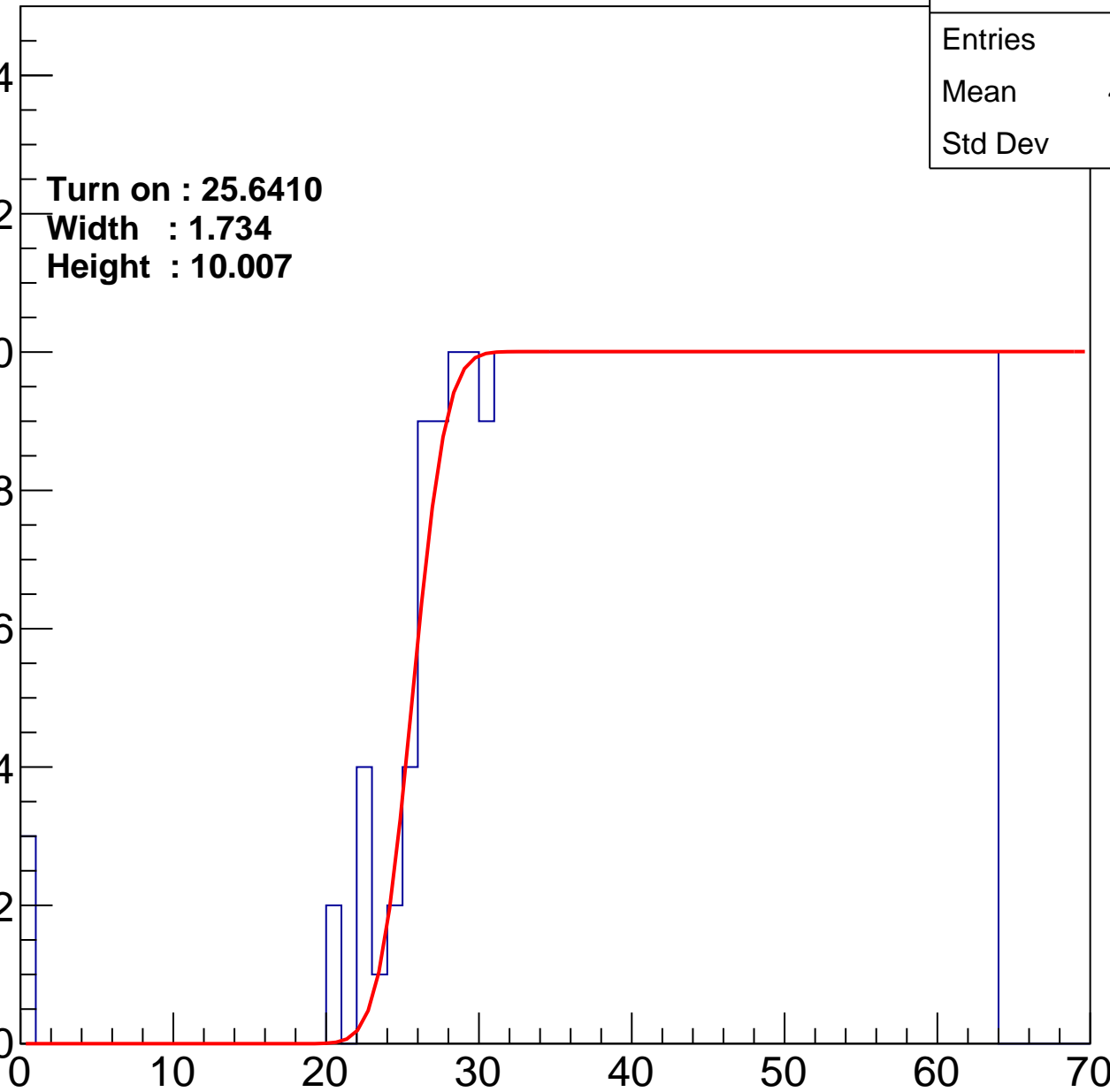
Width : 1.734

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch104

calib_packv5_042523_0143.root, FC#5, port B1

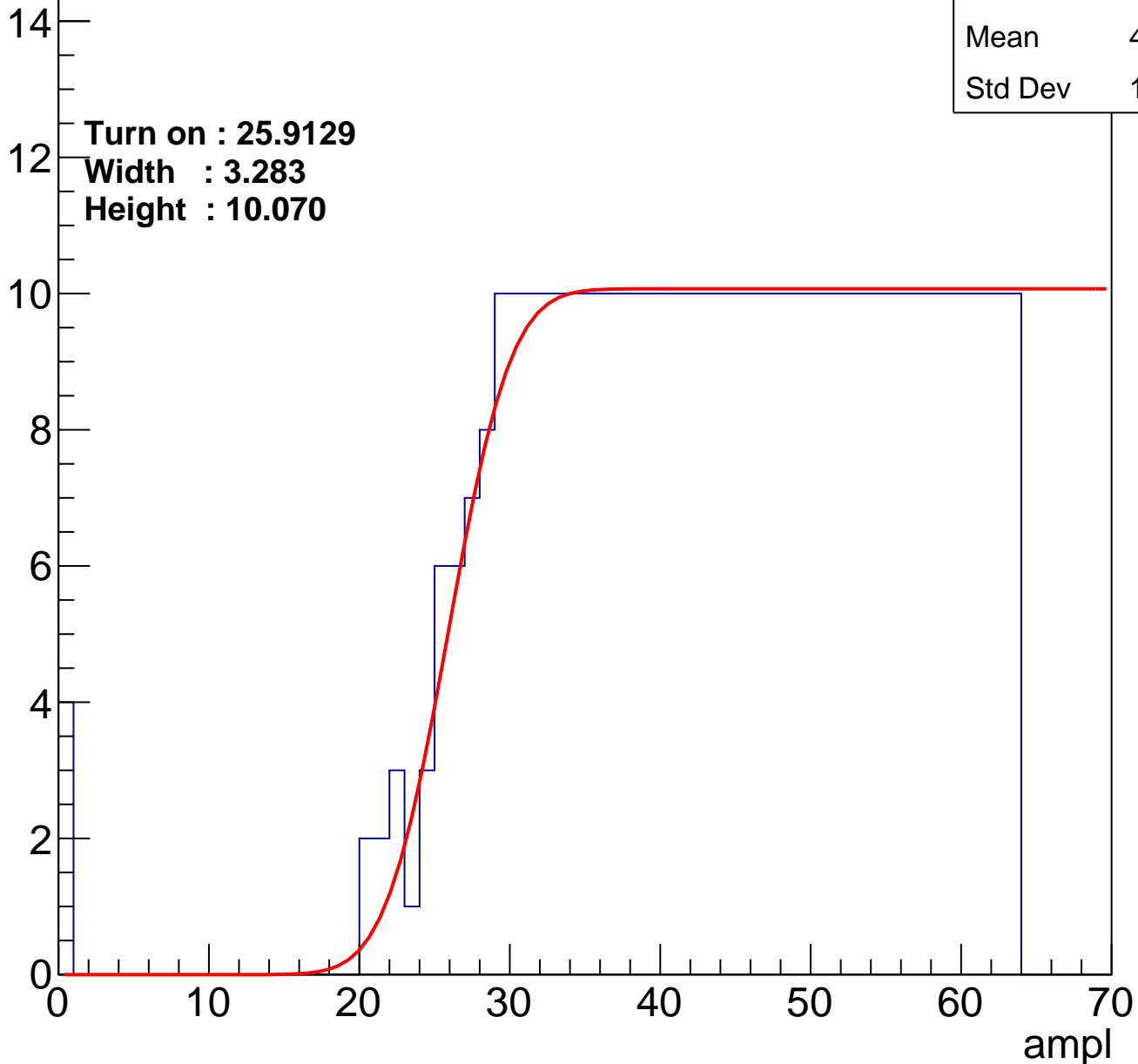
| | |
|---------|-------|
| Entries | 392 |
| Mean | 43.53 |
| Std Dev | 12.19 |

Turn on : 25.9129

Width : 3.283

Height : 10.070

Entry



B0L000S, U23-ch105

calib_packv5_042523_0143.root, FC#5, port B1

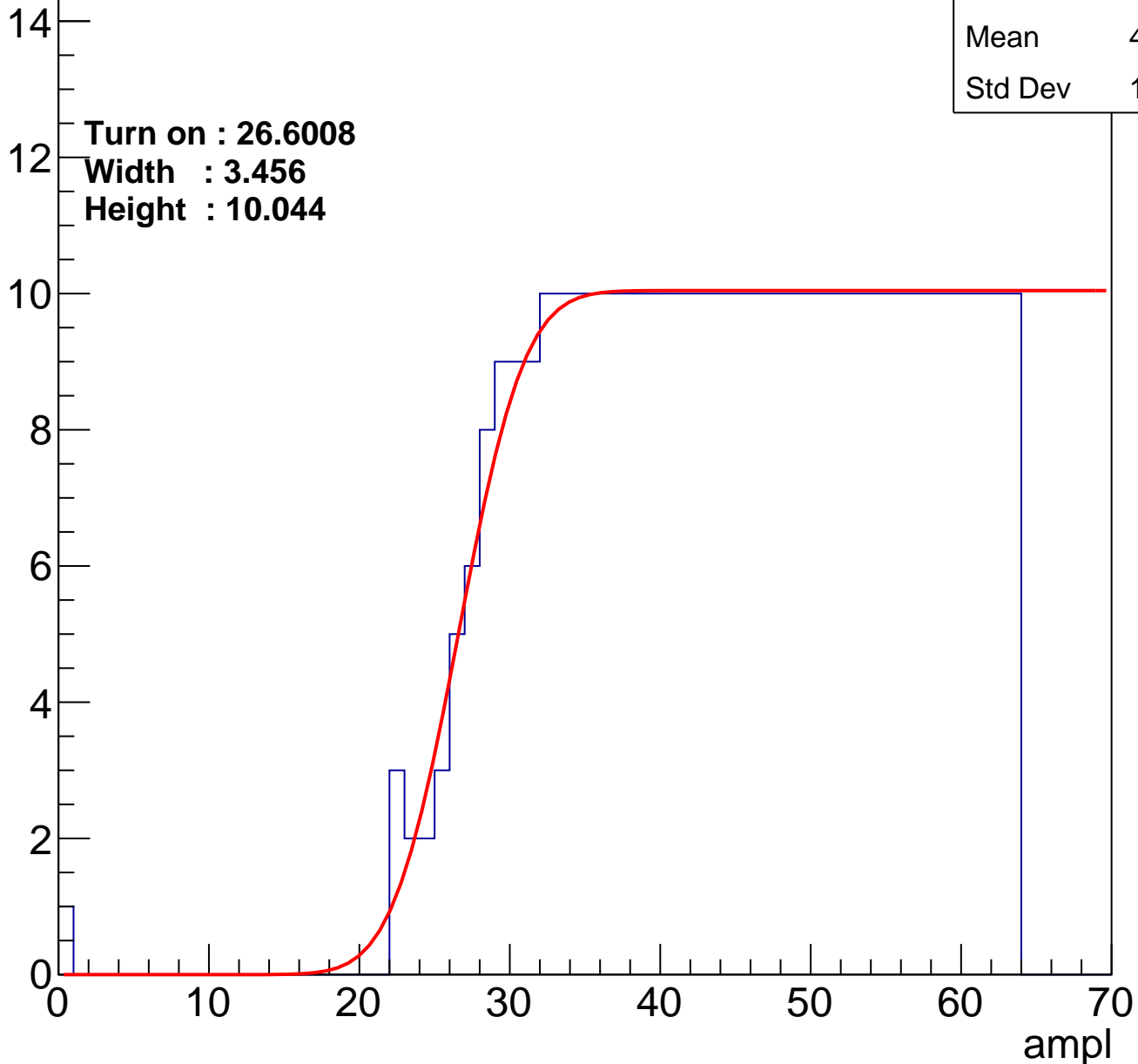
| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.46 |
| Std Dev | 11.28 |

Turn on : 26.6008

Width : 3.456

Height : 10.044

Entry



B0L000S, U23-ch106

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.8 |
| Std Dev | 11.44 |

Turn on : 28.1336

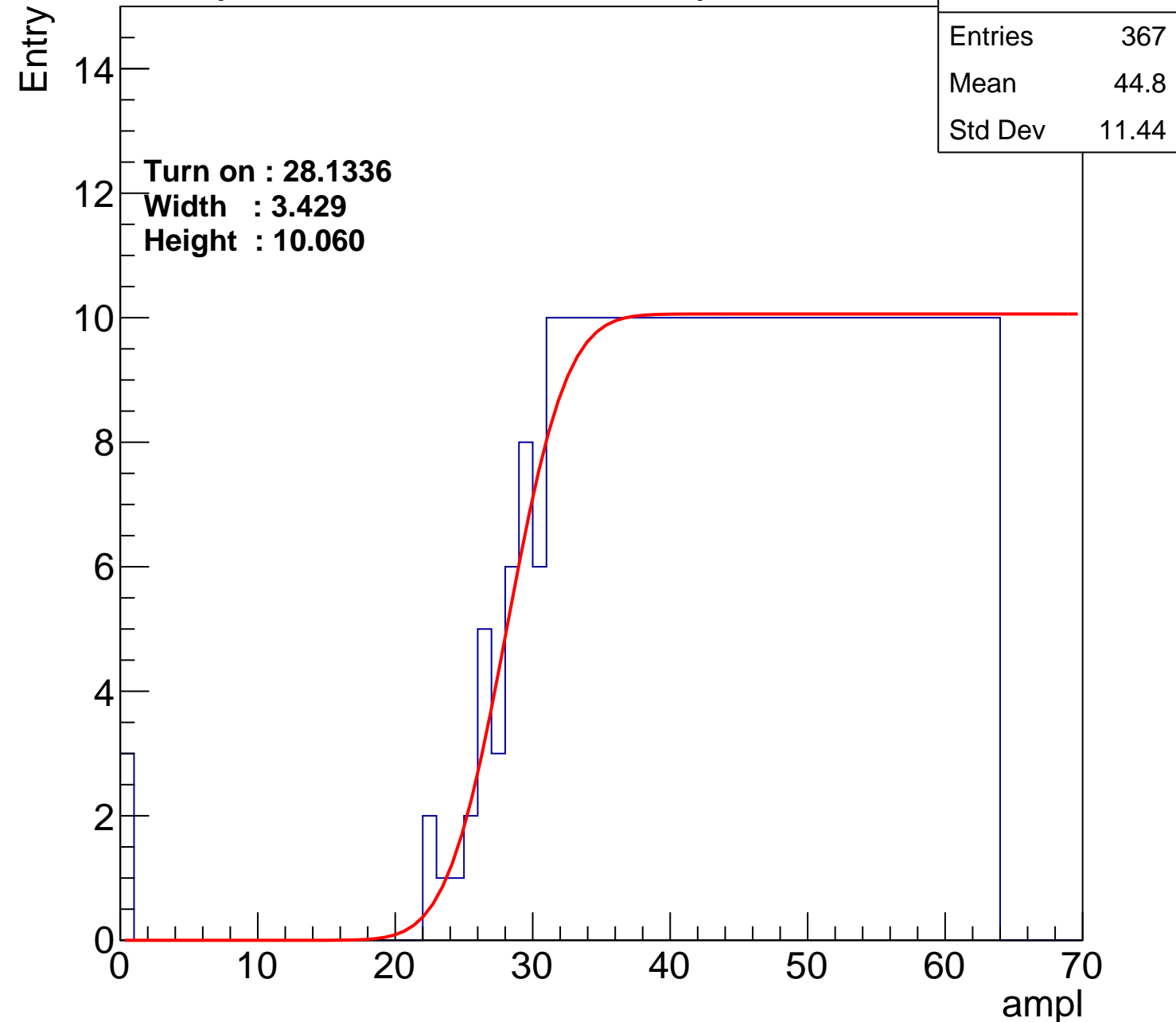
Width : 3.429

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch107

calib_packv5_042523_0143.root, FC#5, port B1

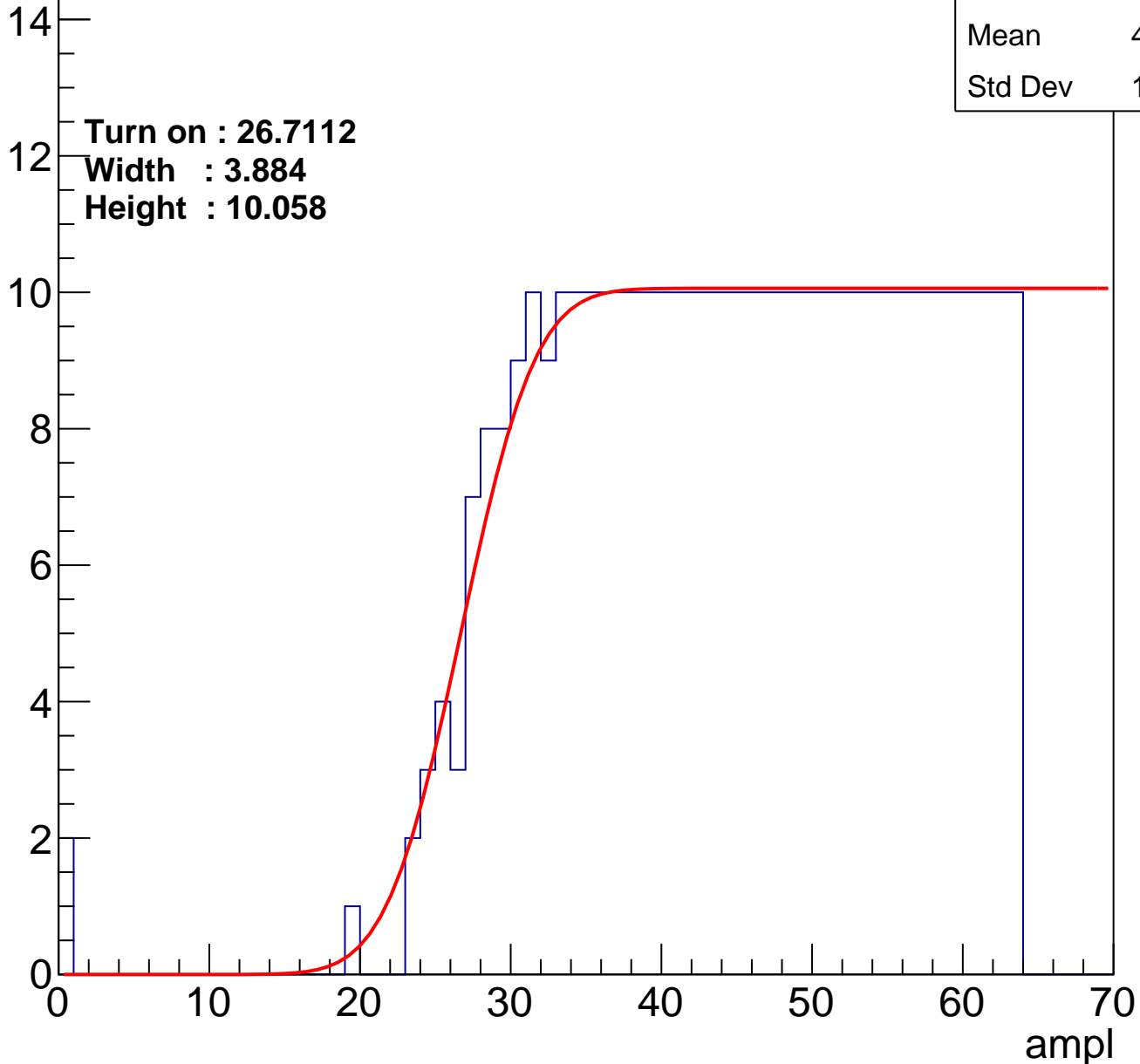
| | |
|---------|-------|
| Entries | 376 |
| Mean | 44.44 |
| Std Dev | 11.45 |

Turn on : 26.7112

Width : 3.884

Height : 10.058

Entry



B0L000S, U23-ch108

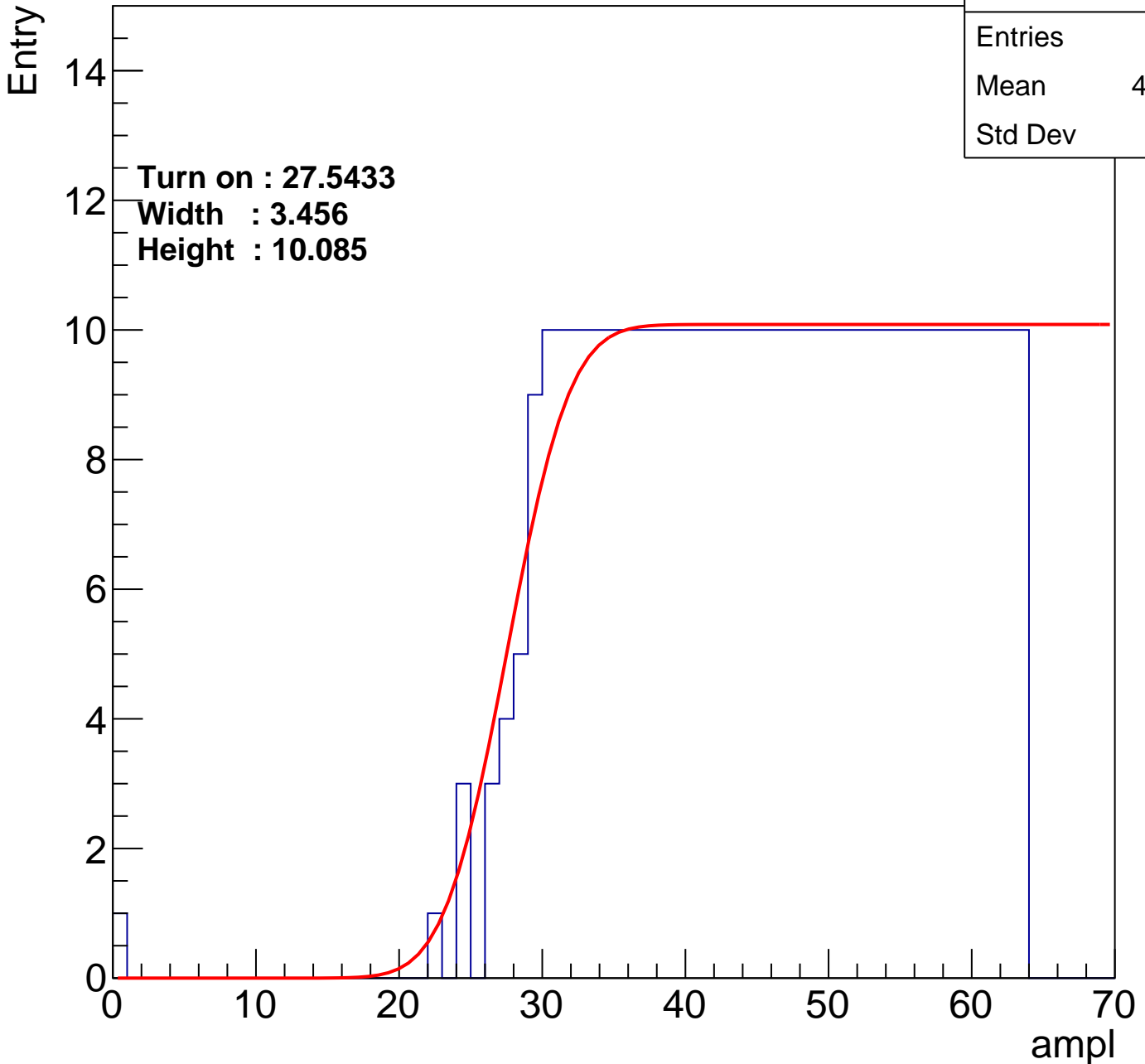
calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 366 |
| Mean | 45.06 |
| Std Dev | 10.9 |

Turn on : 27.5433

Width : 3.456

Height : 10.085



B0L000S, U23-ch109

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 358 |
| Mean | 45.31 |
| Std Dev | 11.02 |

Turn on : 28.8806

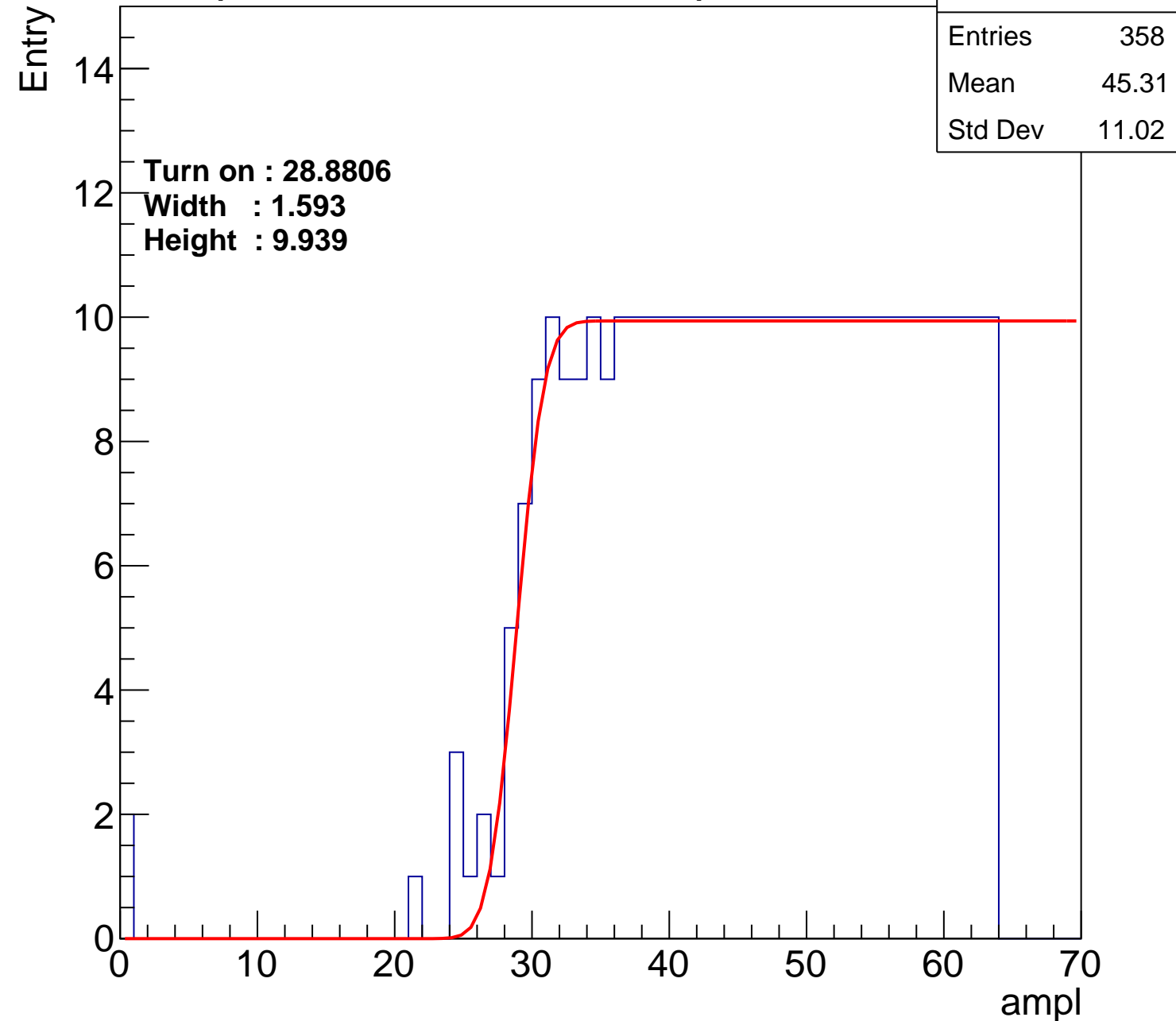
Width : 1.593

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch110

calib_packv5_042523_0143.root, FC#5, port B1

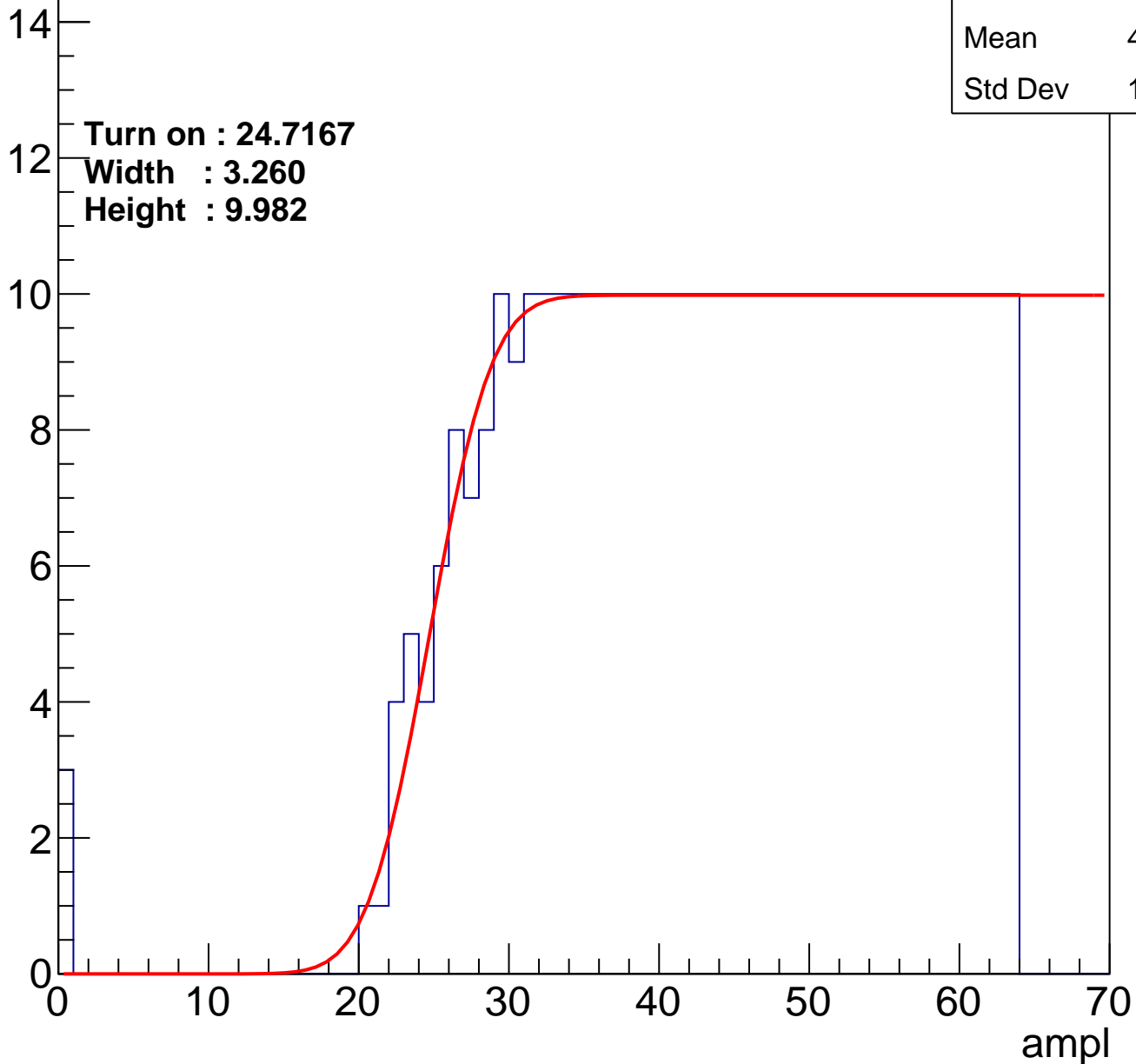
| | |
|---------|-------|
| Entries | 396 |
| Mean | 43.39 |
| Std Dev | 12.13 |

Turn on : 24.7167

Width : 3.260

Height : 9.982

Entry



B0L000S, U23-ch111

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.67 |
| Std Dev | 11.11 |

Turn on : 27.2239

Width : 2.235

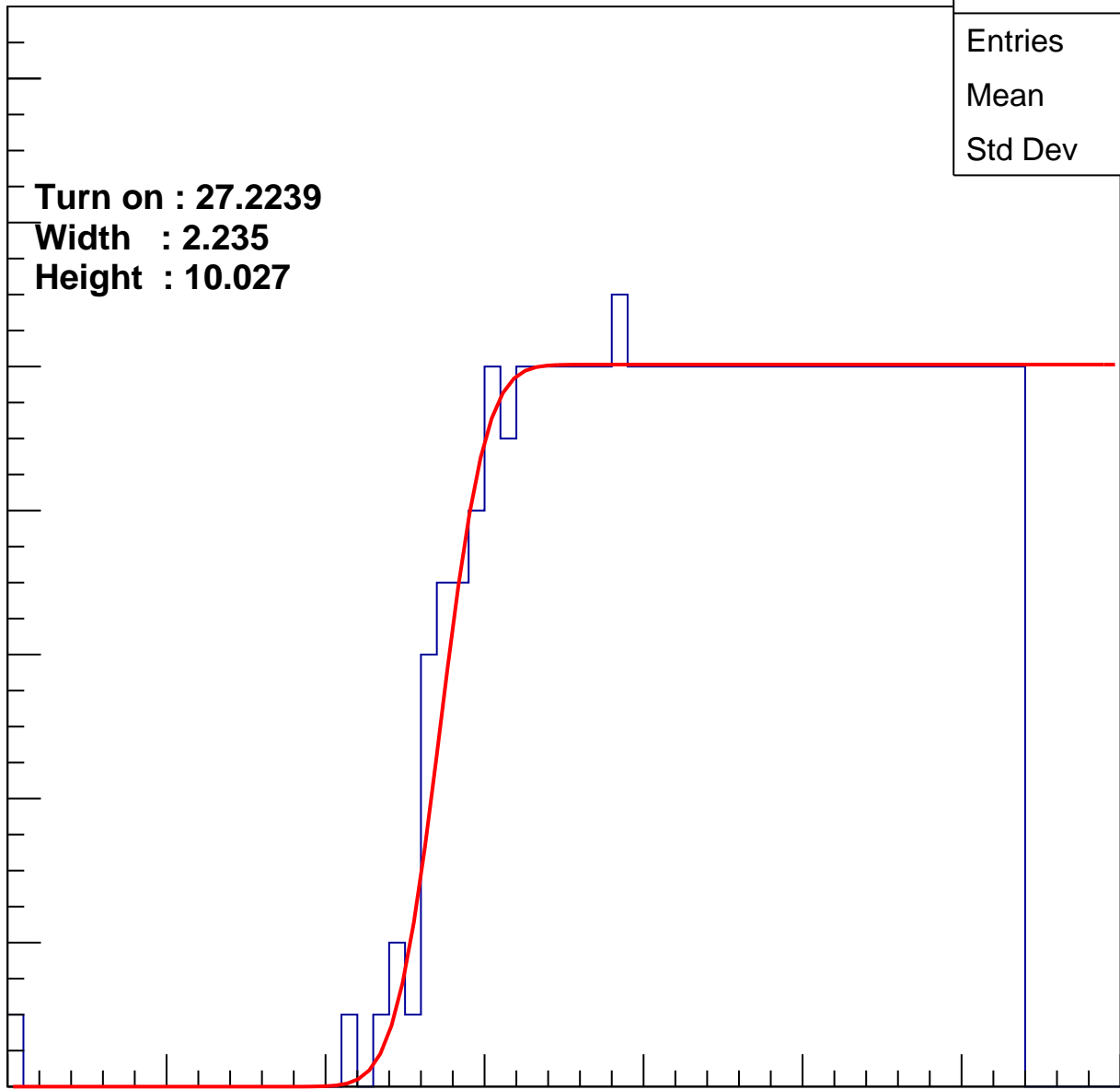
Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U23-ch112

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 387 |
| Mean | 43.91 |
| Std Dev | 11.72 |

Turn on : 25.8181

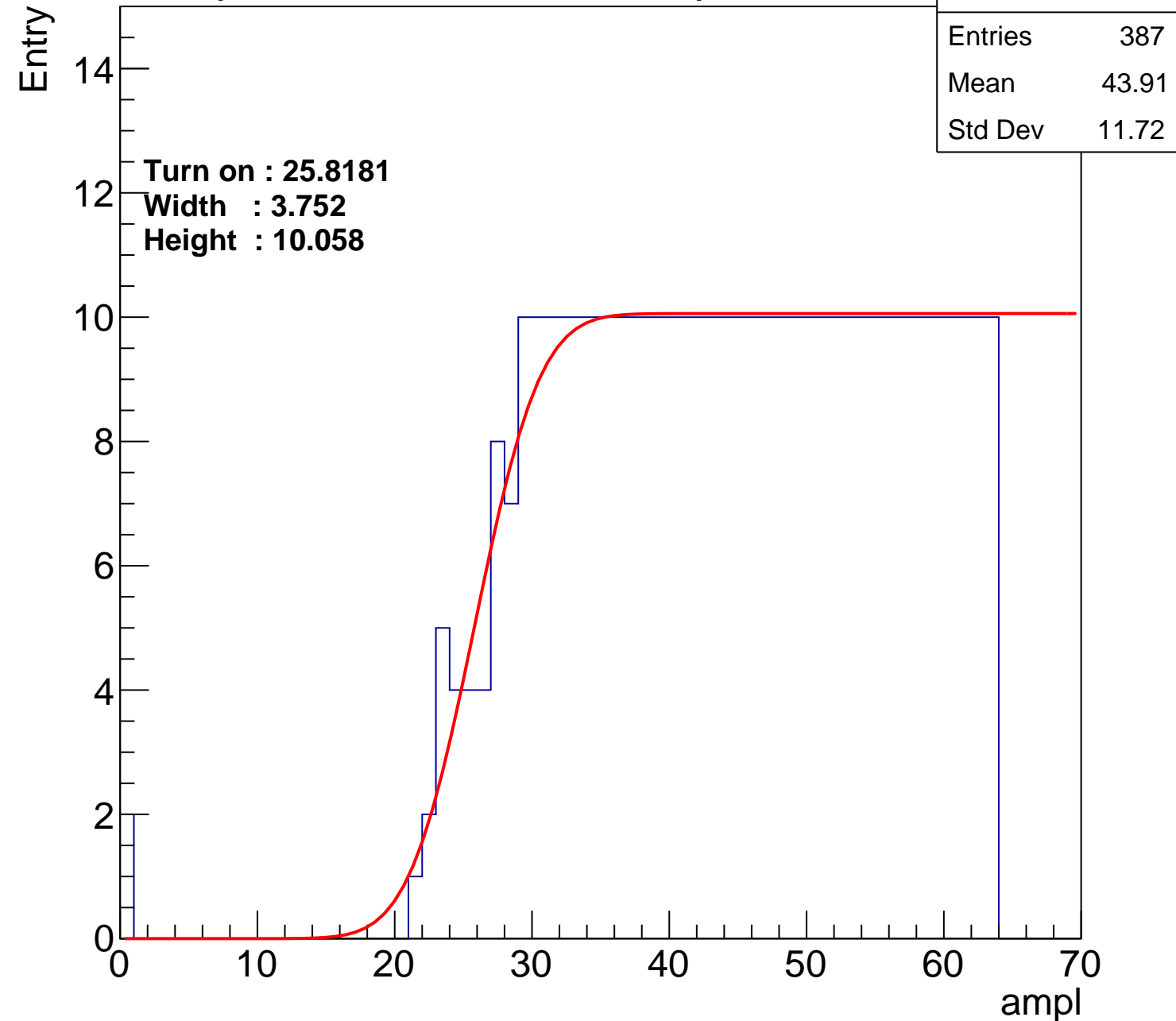
Width : 3.752

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch113

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 361 |
| Mean | 45.18 |
| Std Dev | 11.05 |

Turn on : 28.4702

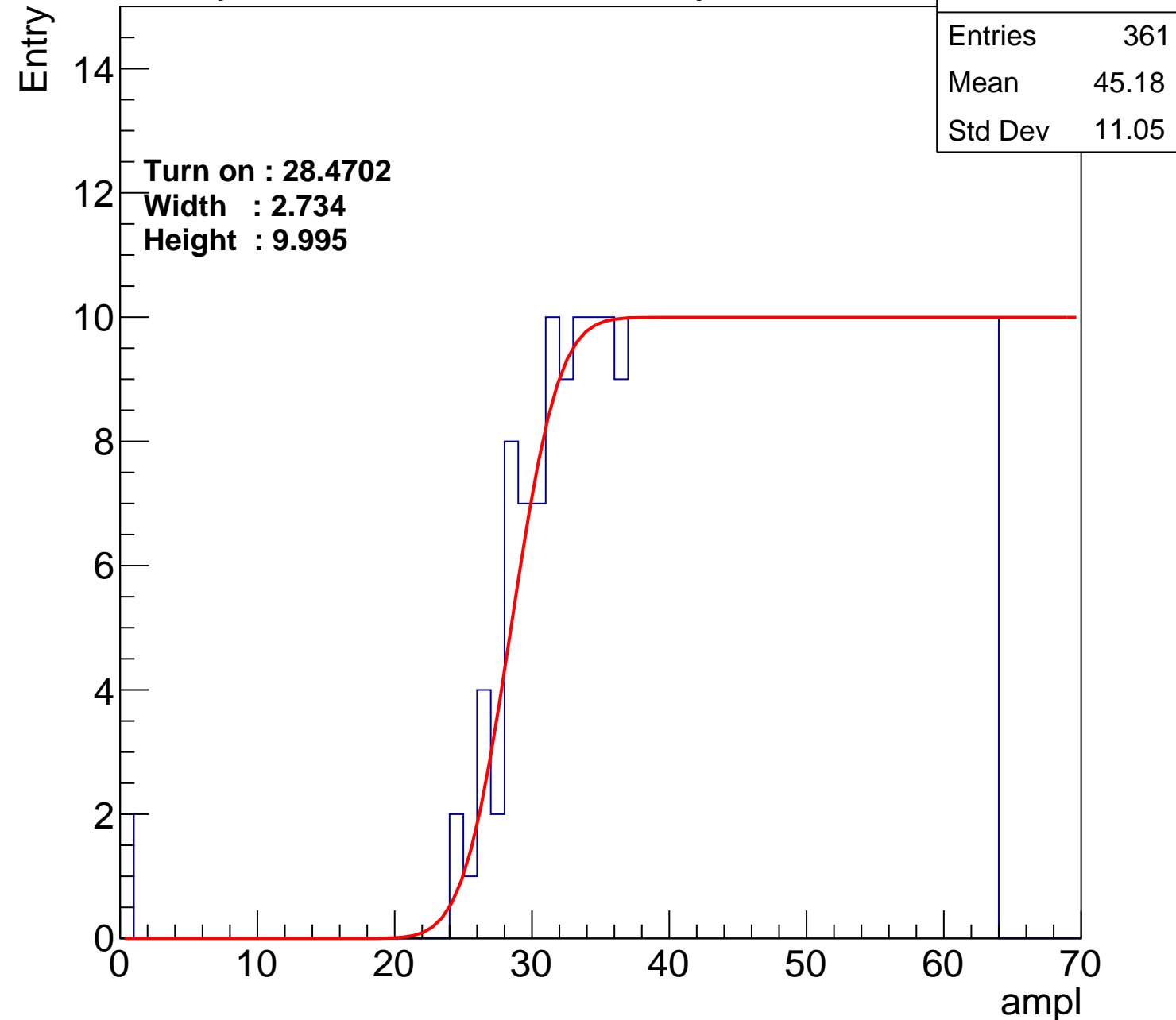
Width : 2.734

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch114

calib_packv5_042523_0143.root, FC#5, port B1

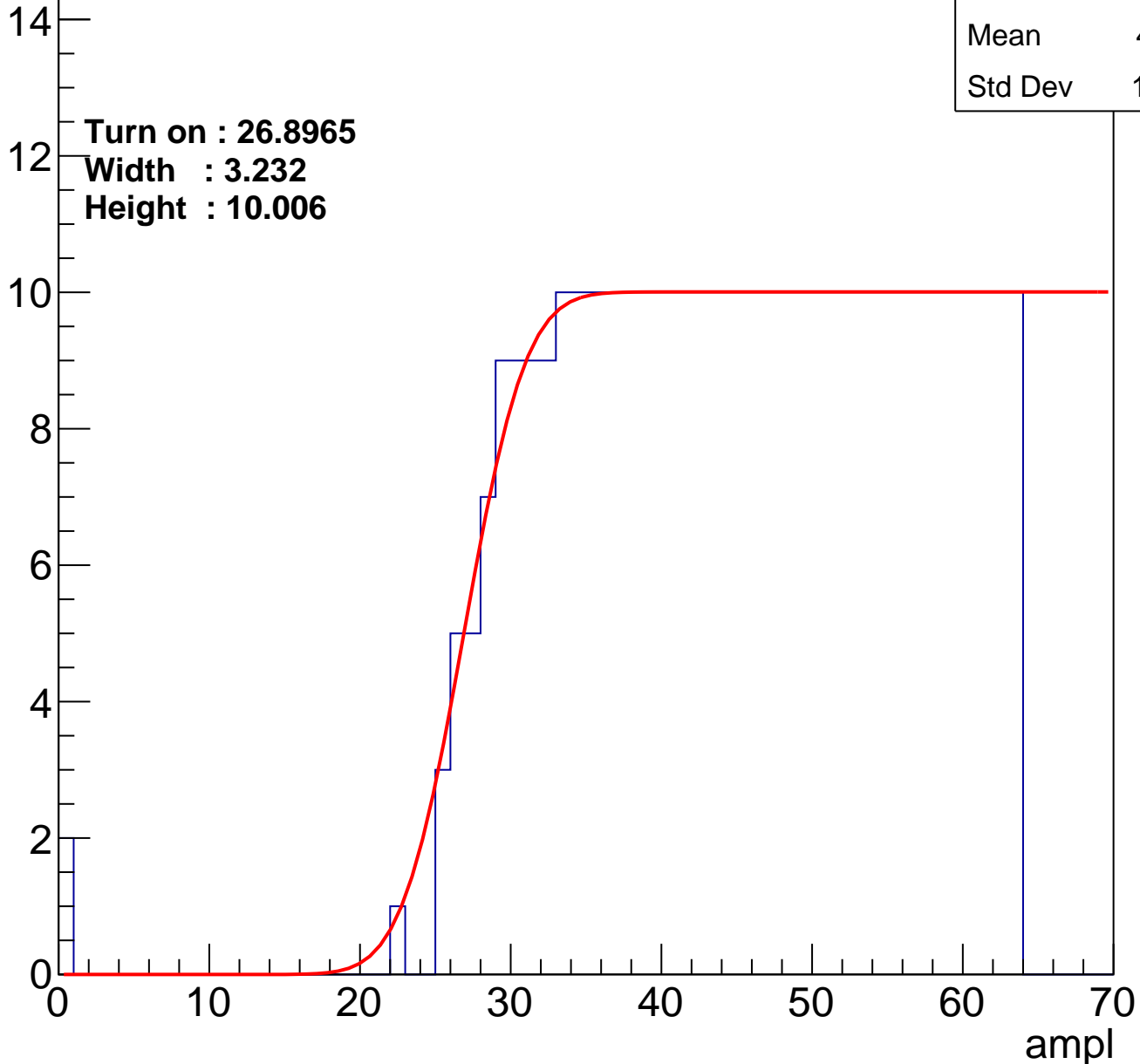
| | |
|---------|-------|
| Entries | 369 |
| Mean | 44.81 |
| Std Dev | 11.22 |

Turn on : 26.8965

Width : 3.232

Height : 10.006

Entry



B0L000S, U23-ch115

calib_packv5_042523_0143.root, FC#5, port B1

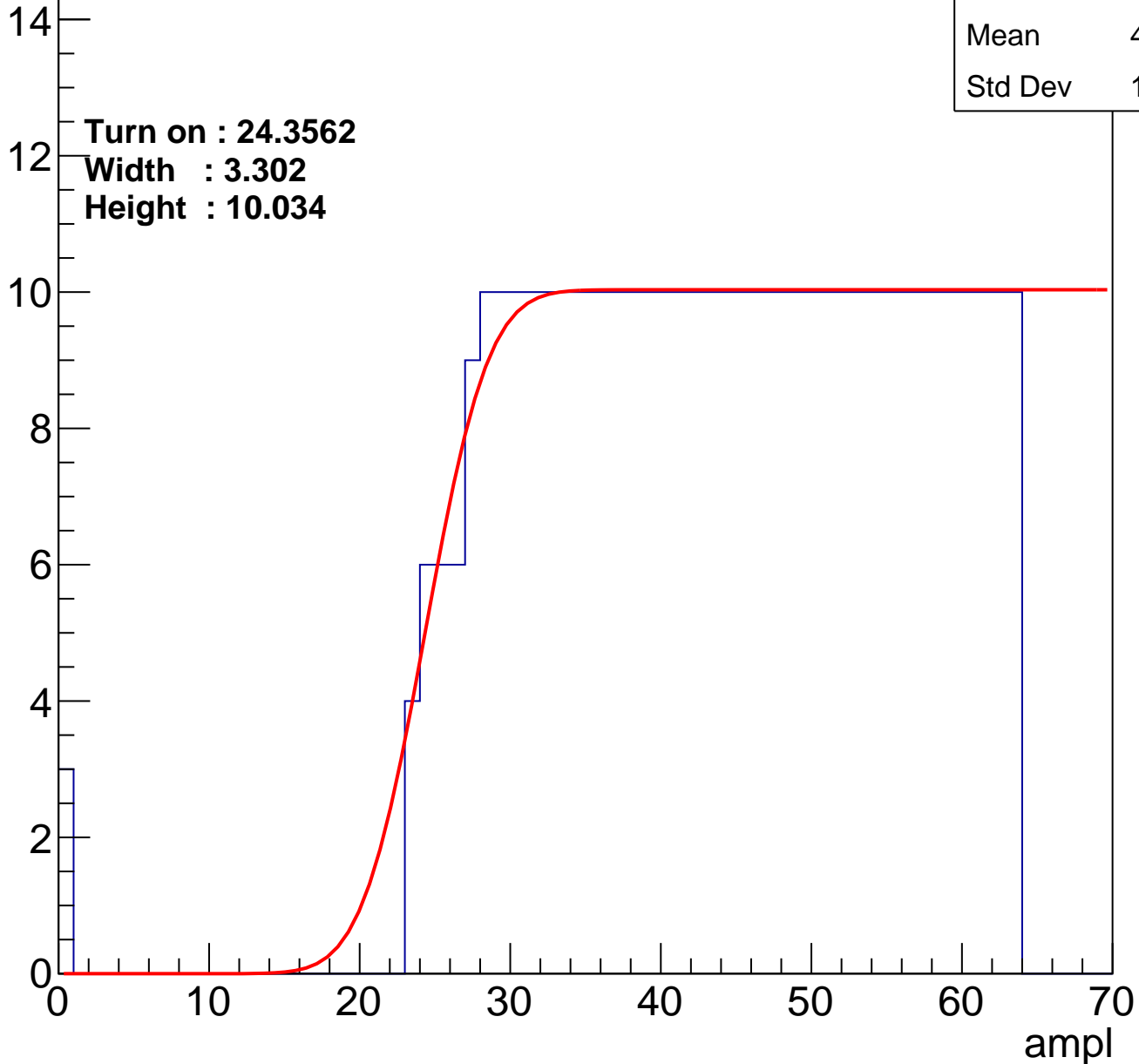
| | |
|---------|-------|
| Entries | 394 |
| Mean | 43.57 |
| Std Dev | 11.95 |

Turn on : 24.3562

Width : 3.302

Height : 10.034

Entry



B0L000S, U23-ch116

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 378 |
| Mean | 44.33 |
| Std Dev | 11.52 |

Turn on : 26.7825

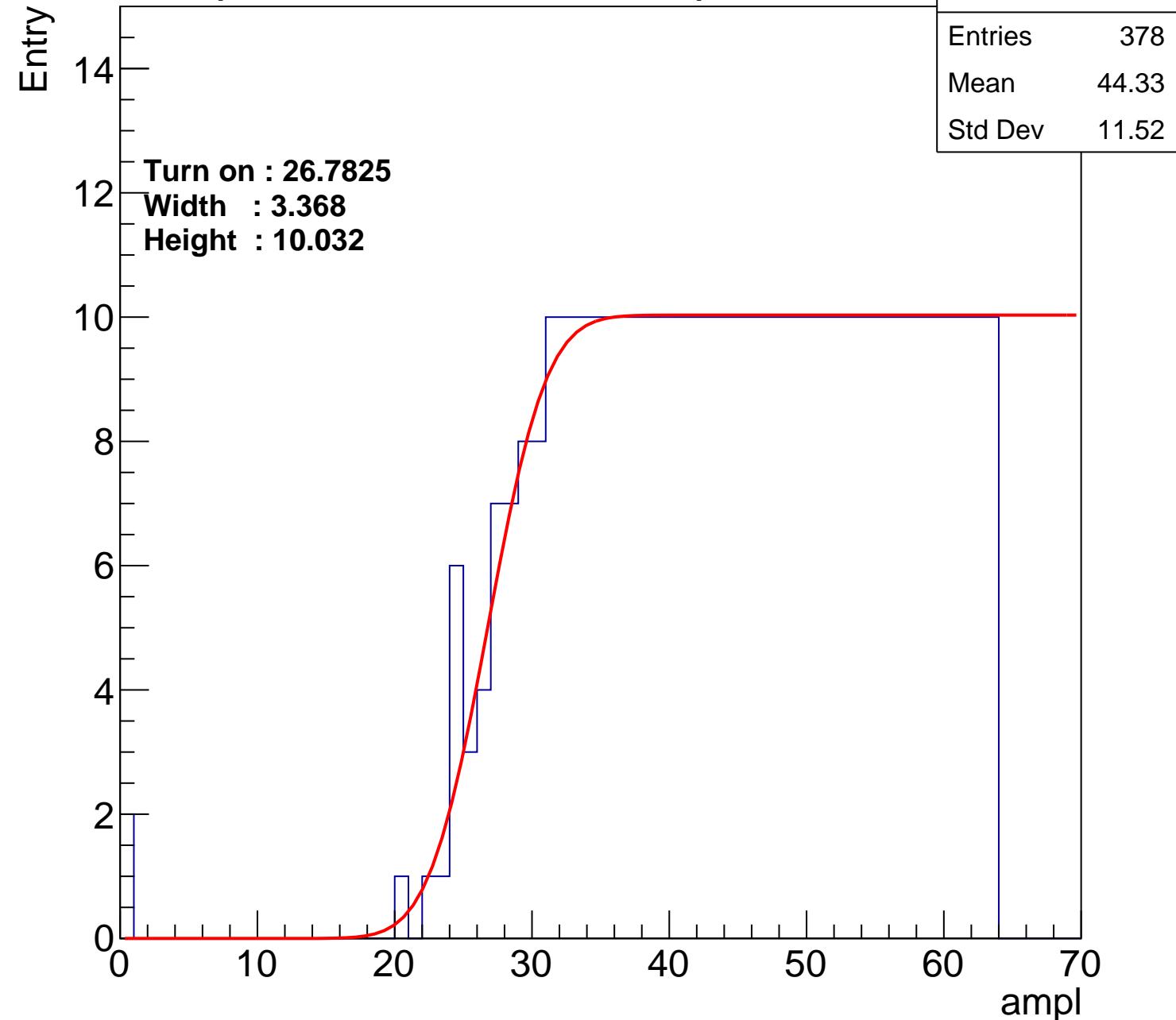
Width : 3.368

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch117

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-----|
| Entries | 353 |
|---------|-----|

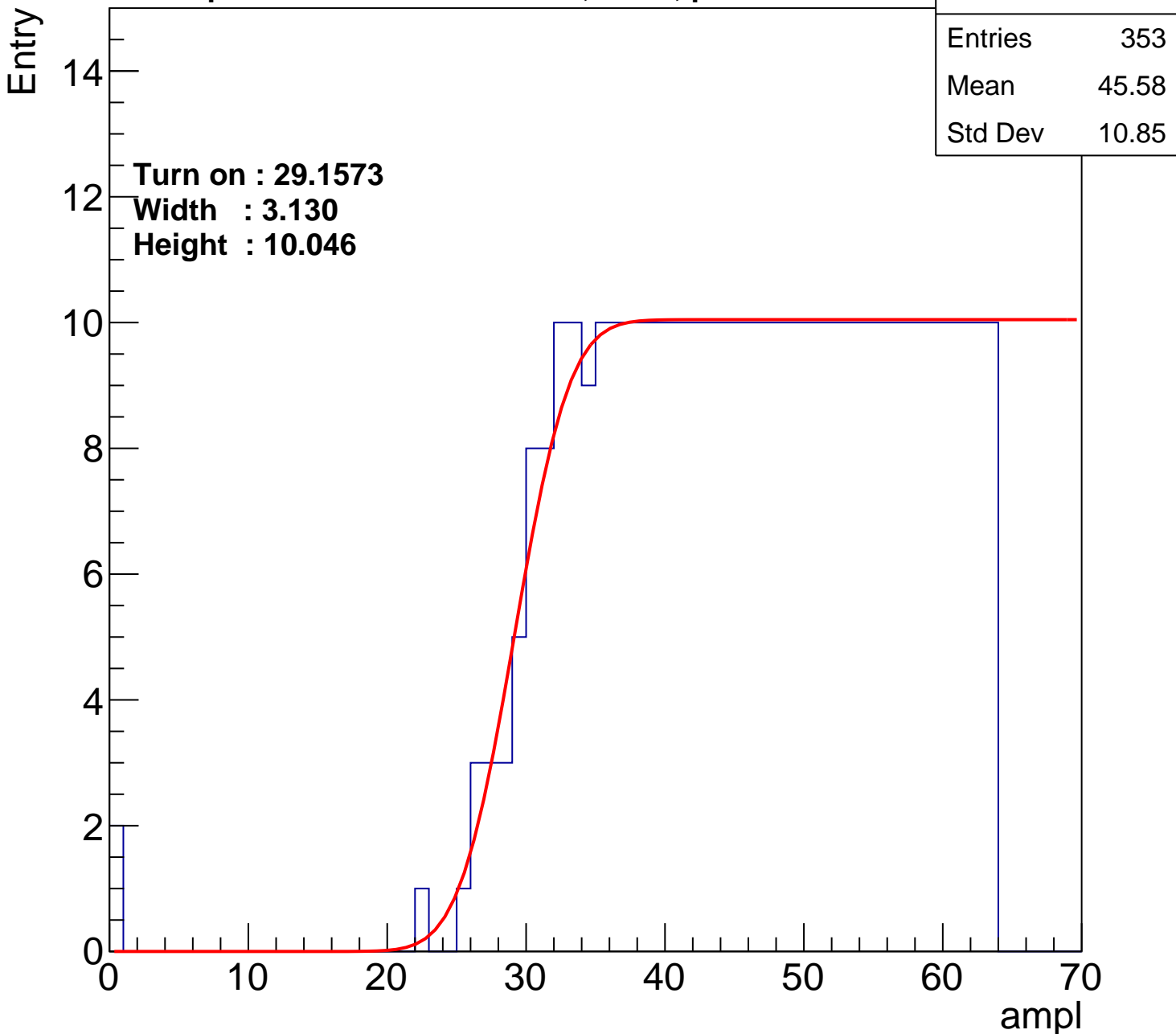
| | |
|------|-------|
| Mean | 45.58 |
|------|-------|

| | |
|---------|-------|
| Std Dev | 10.85 |
|---------|-------|

Turn on : 29.1573

Width : 3.130

Height : 10.046



B0L000S, U23-ch118

calib_packv5_042523_0143.root, FC#5, port B1

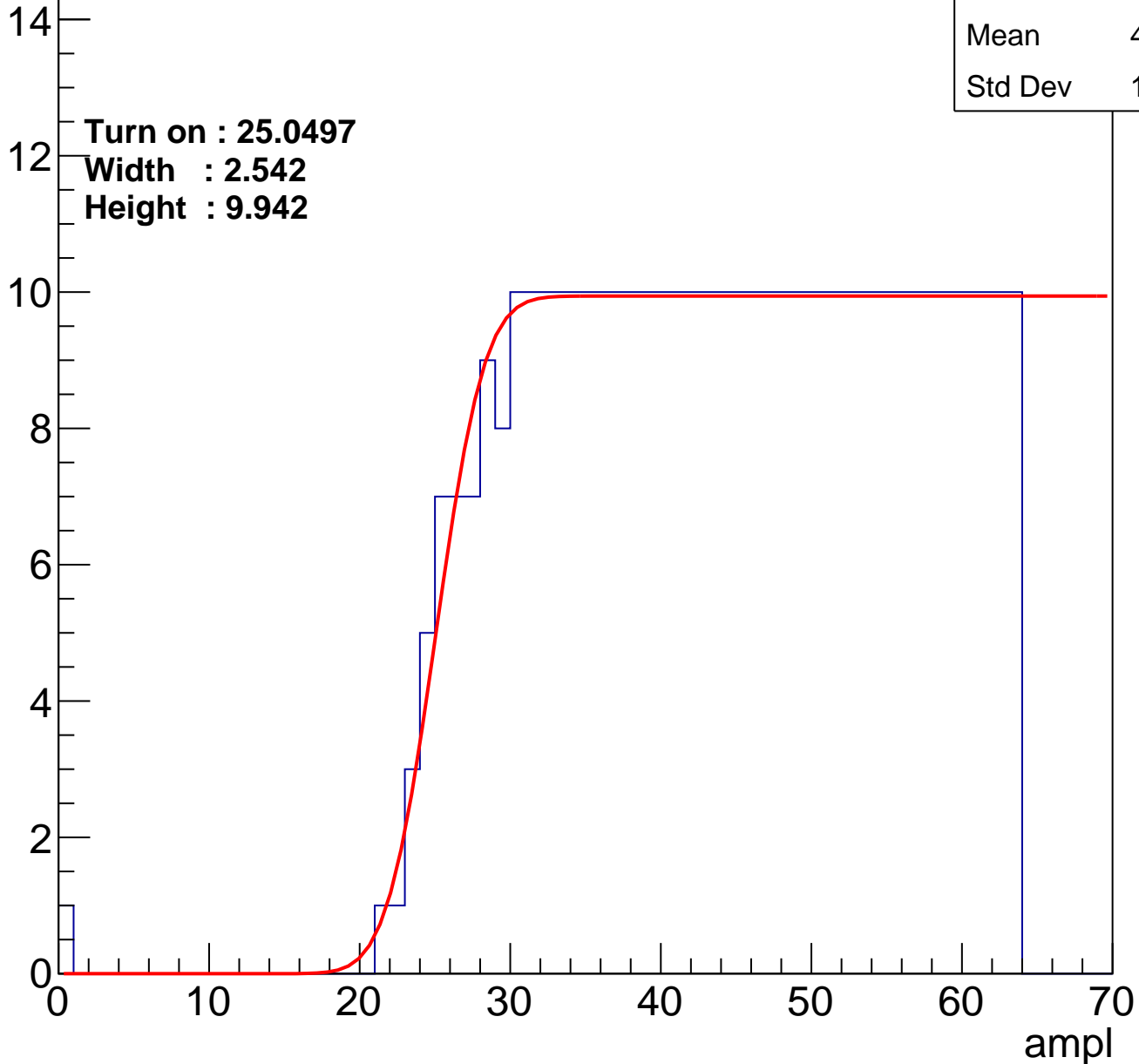
| | |
|---------|-------|
| Entries | 389 |
| Mean | 43.89 |
| Std Dev | 11.57 |

Turn on : 25.0497

Width : 2.542

Height : 9.942

Entry



B0L000S, U23-ch119

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 380 |
| Mean | 44.35 |
| Std Dev | 11.3 |

Turn on : 26.5753

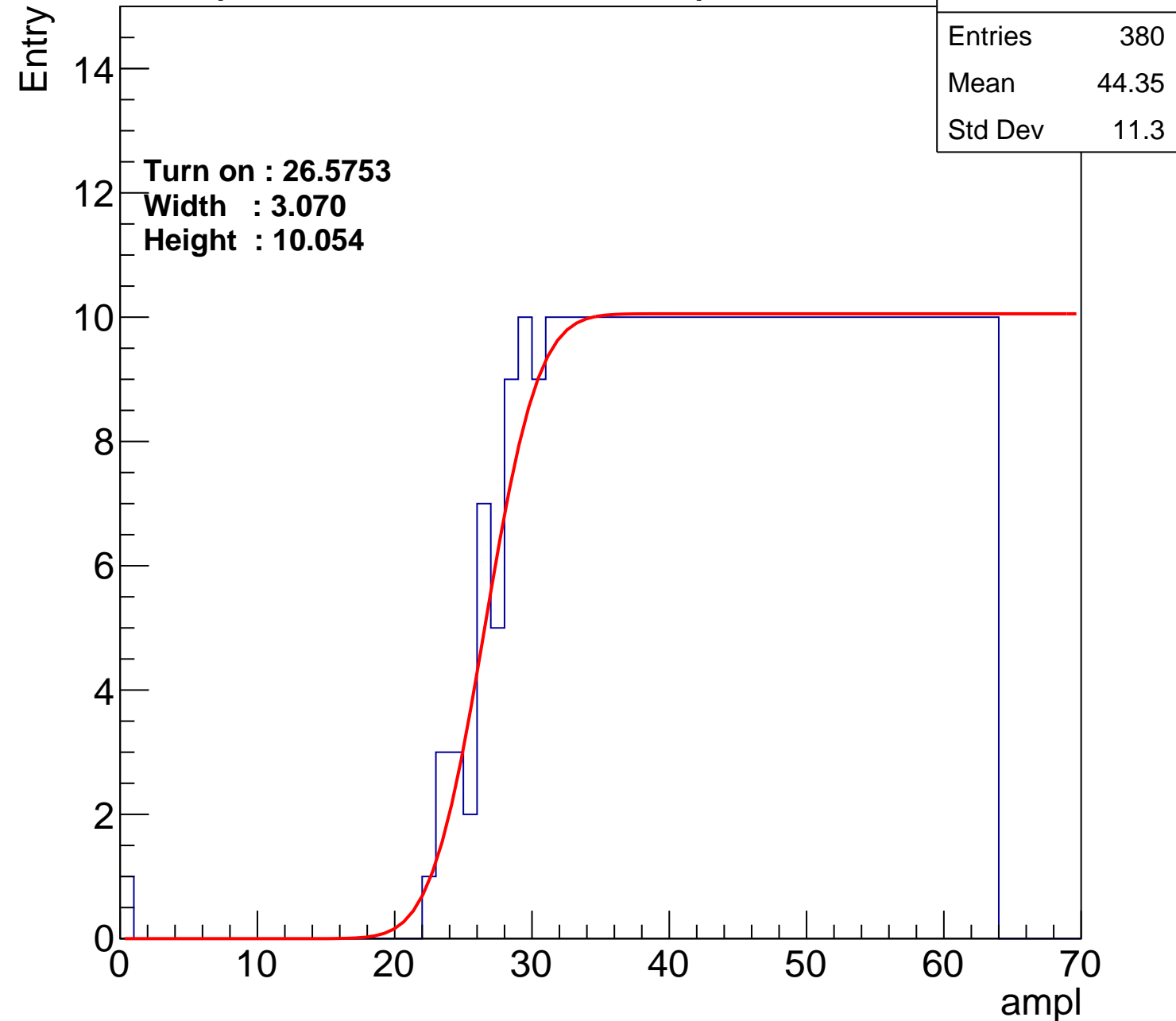
Width : 3.070

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch120

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 377 |
| Mean | 44.43 |
| Std Dev | 11.33 |

Turn on : 26.7248

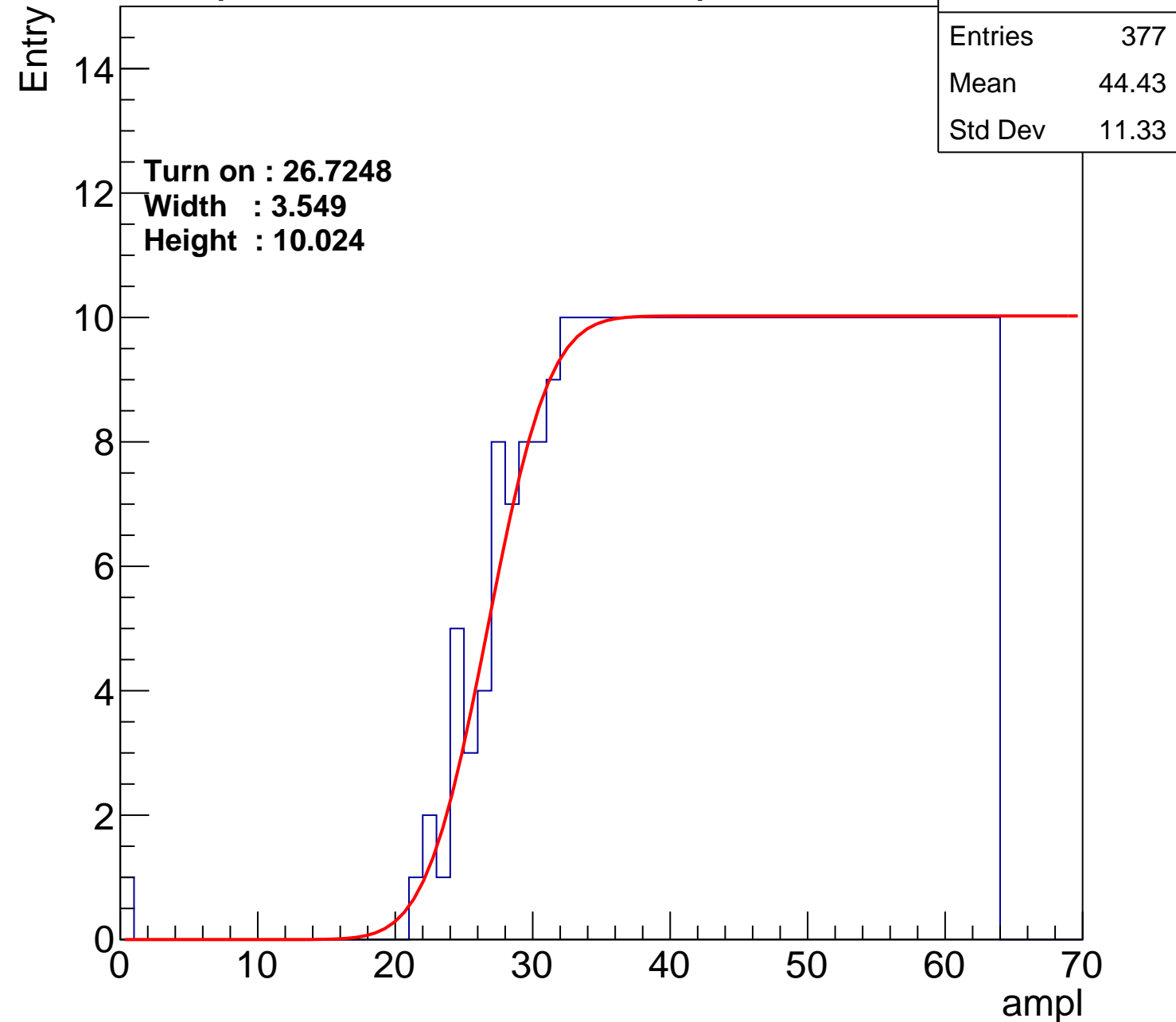
Width : 3.549

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch121

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.58 |
| Std Dev | 11.33 |

Turn on : 26.8749

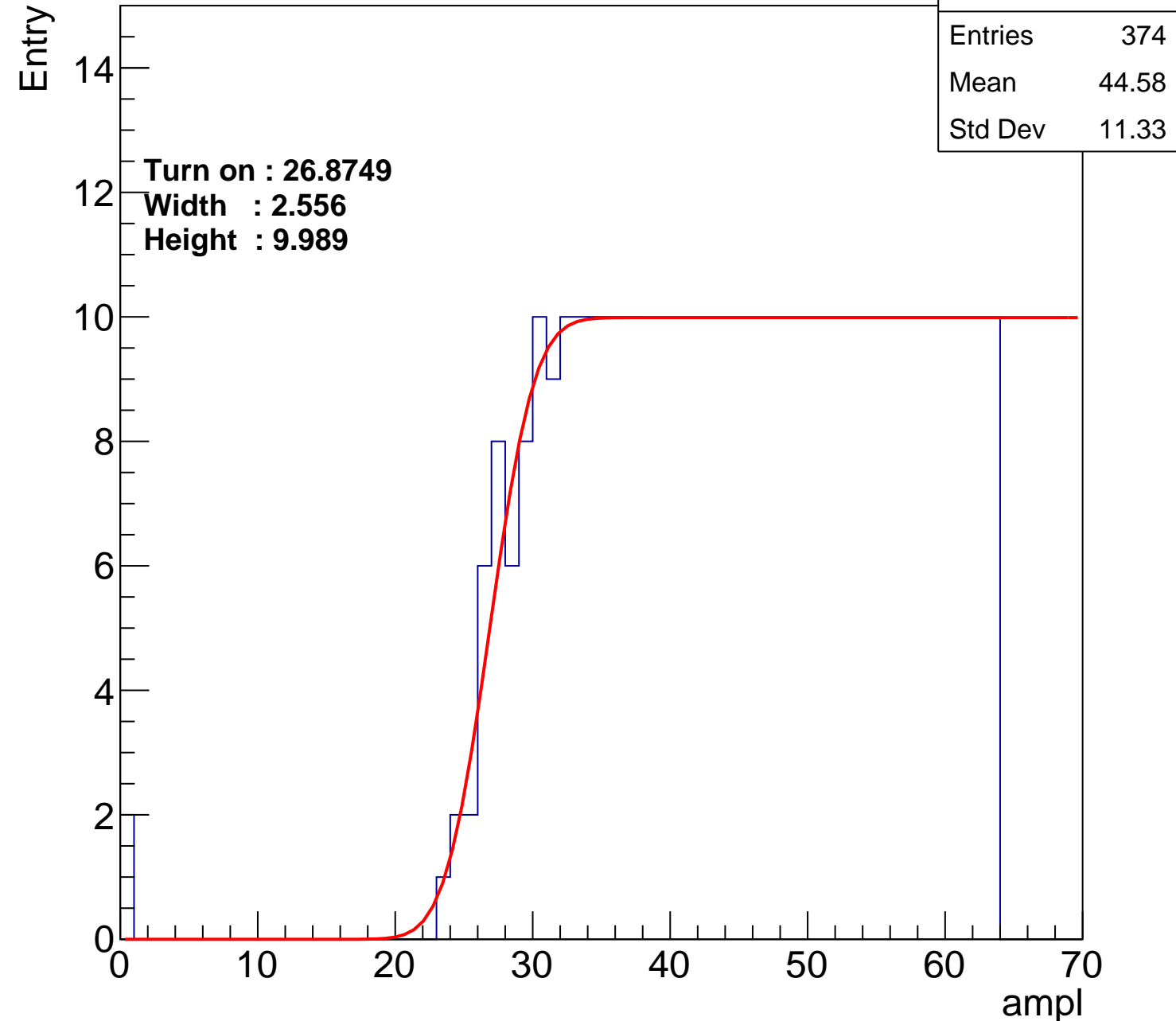
Width : 2.556

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch122

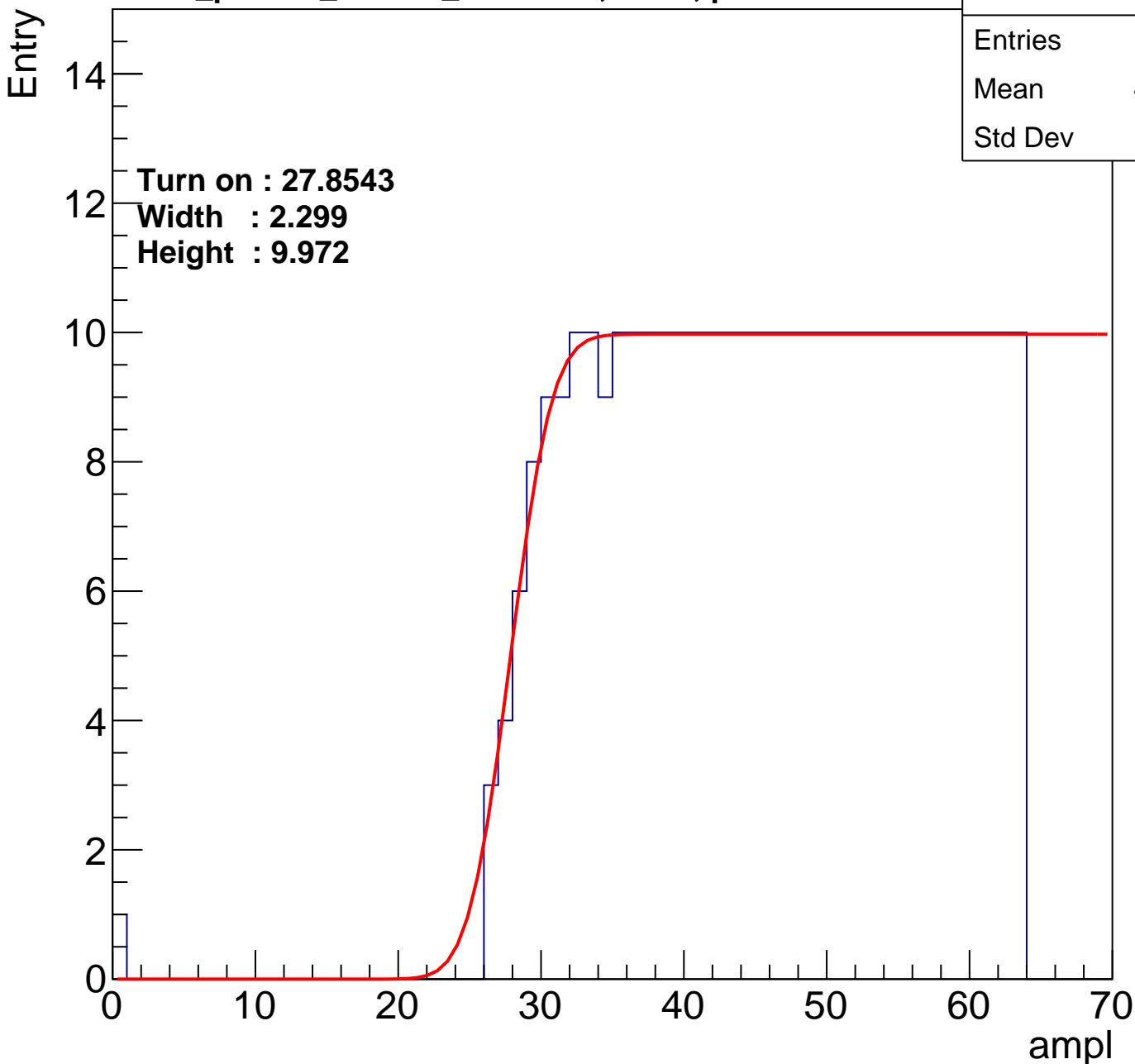
calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 359 |
| Mean | 45.41 |
| Std Dev | 10.7 |

Turn on : 27.8543

Width : 2.299

Height : 9.972



B0L000S, U23-ch123

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 367 |
| Mean | 44.84 |
| Std Dev | 11.38 |

Turn on : 28.1215

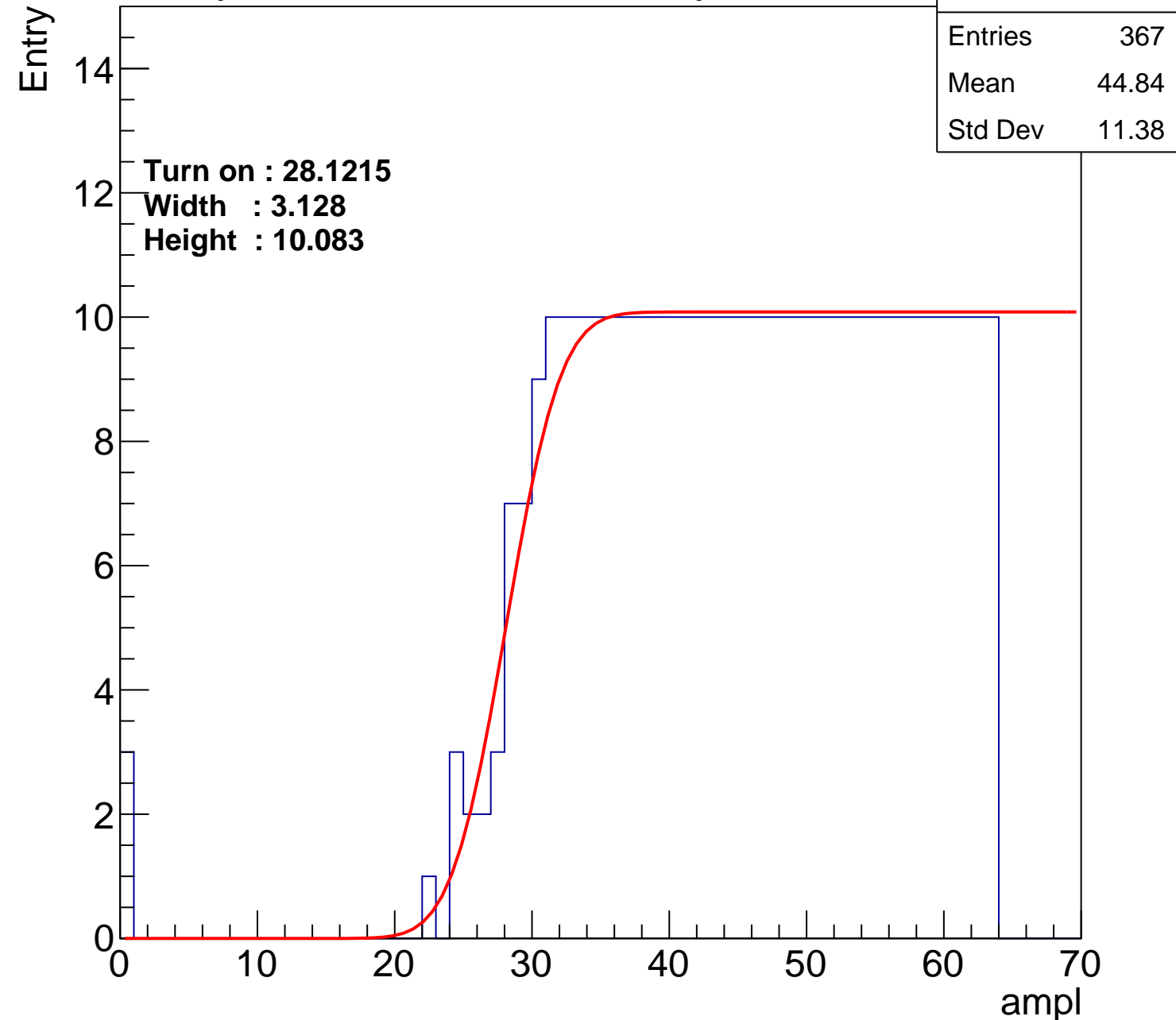
Width : 3.128

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch124

calib_packv5_042523_0143.root, FC#5, port B1

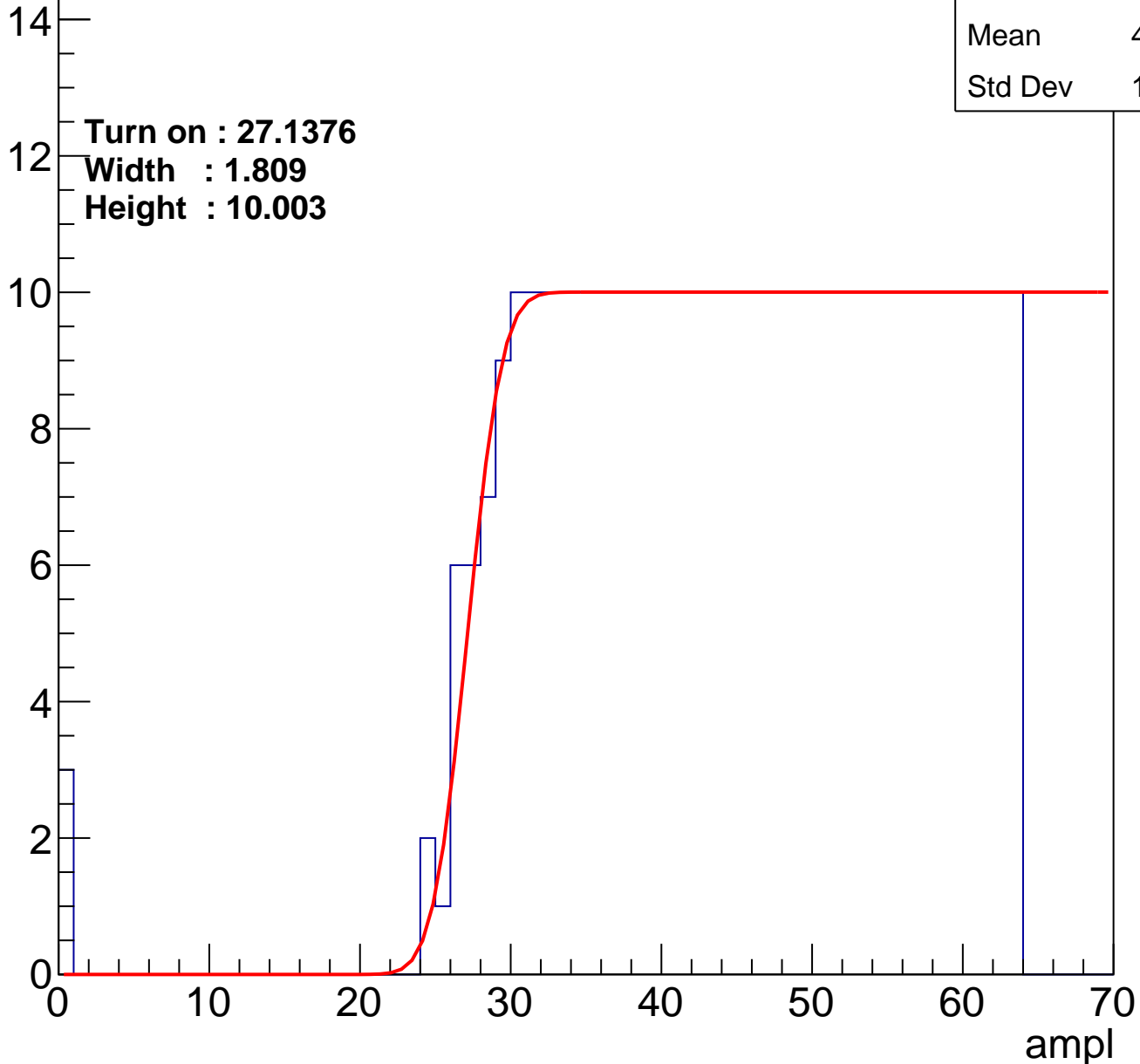
| | |
|---------|-------|
| Entries | 374 |
| Mean | 44.54 |
| Std Dev | 11.47 |

Turn on : 27.1376

Width : 1.809

Height : 10.003

Entry



B0L000S, U23-ch125

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 372 |
| Mean | 44.54 |
| Std Dev | 11.66 |

Turn on : 27.9922

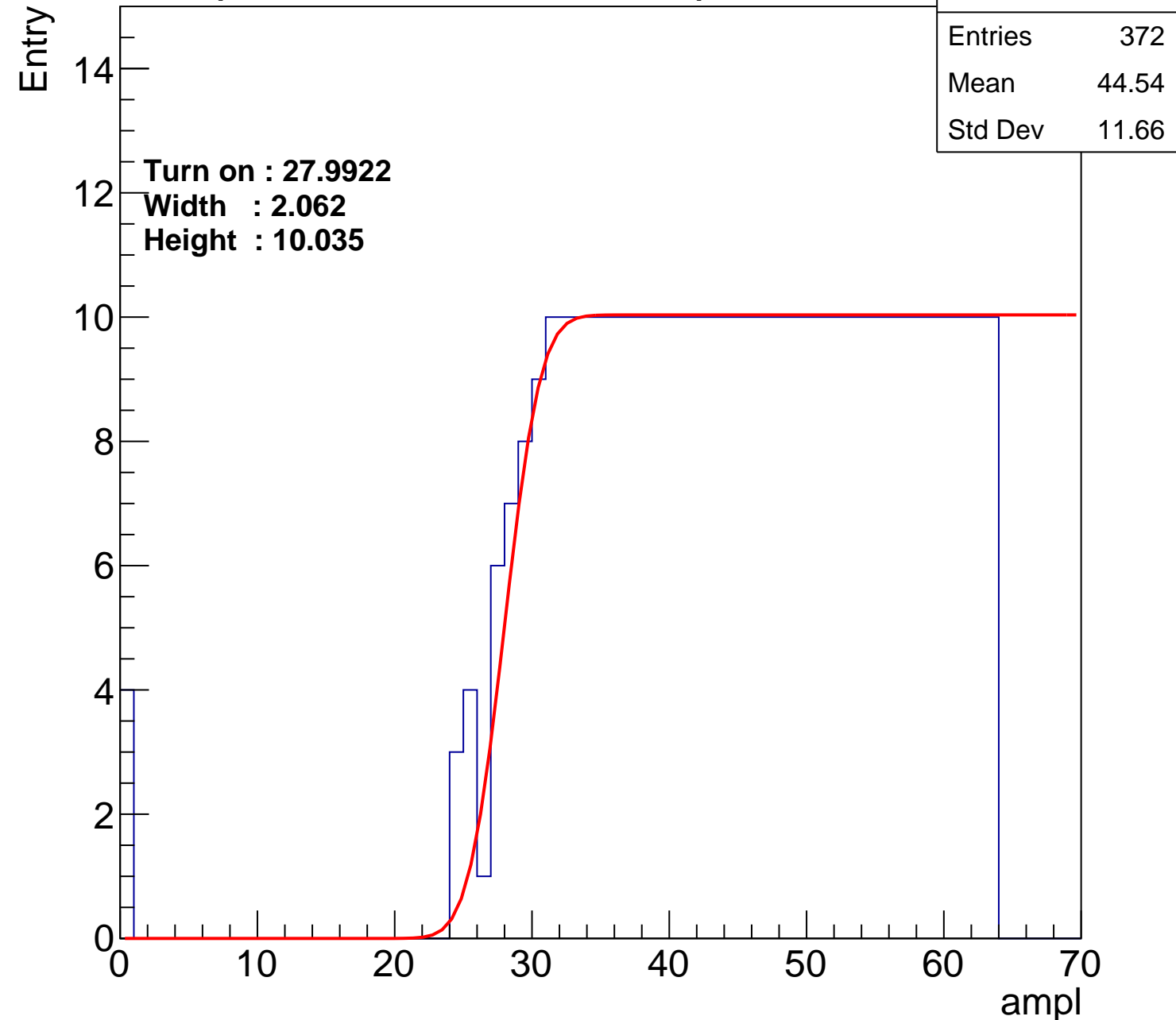
Width : 2.062

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch126

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 384 |
| Mean | 44.09 |
| Std Dev | 11.58 |

Turn on : 25.7237

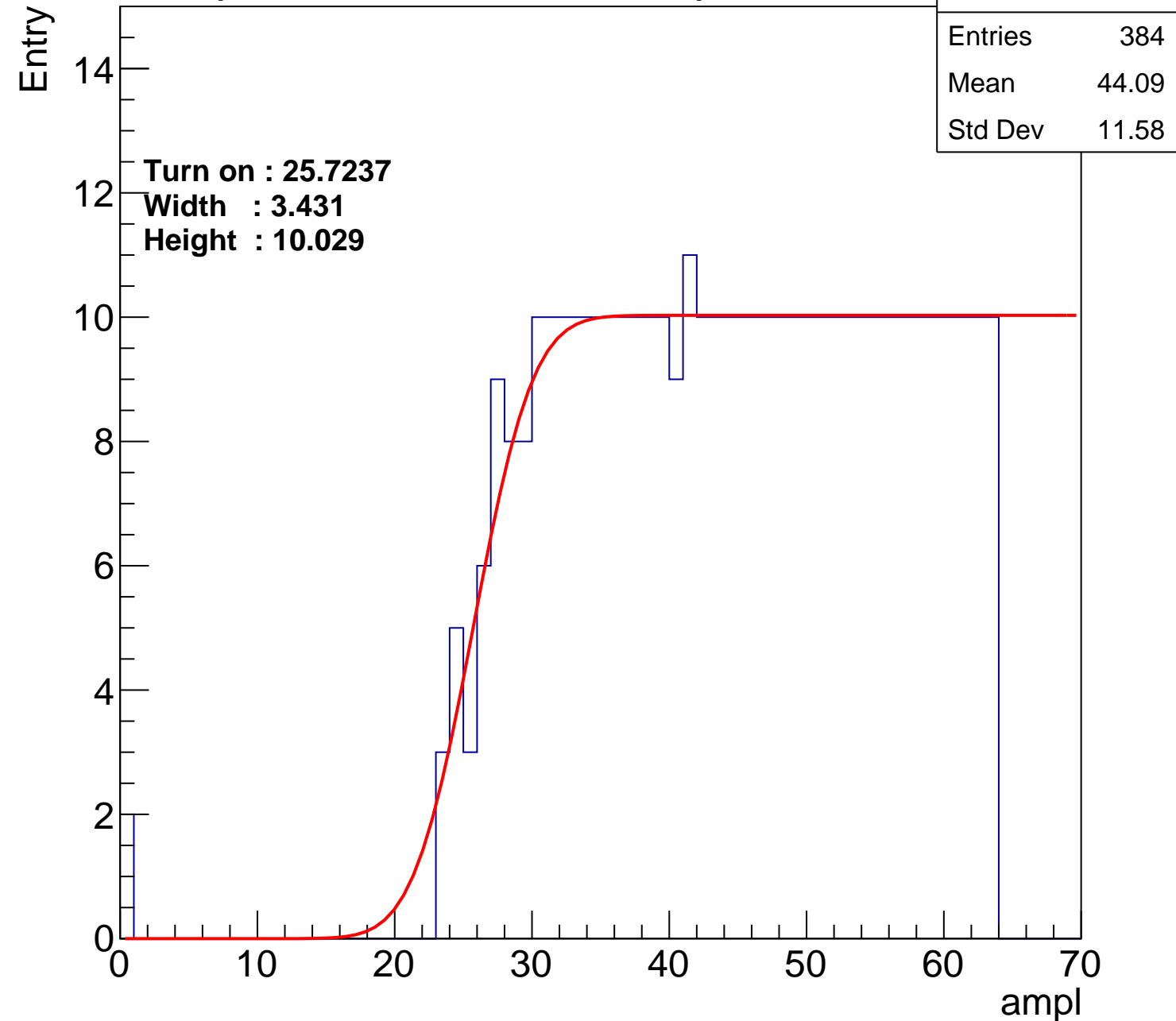
Width : 3.431

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch127

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.78 |
| Std Dev | 11.08 |

Turn on : 27.2210

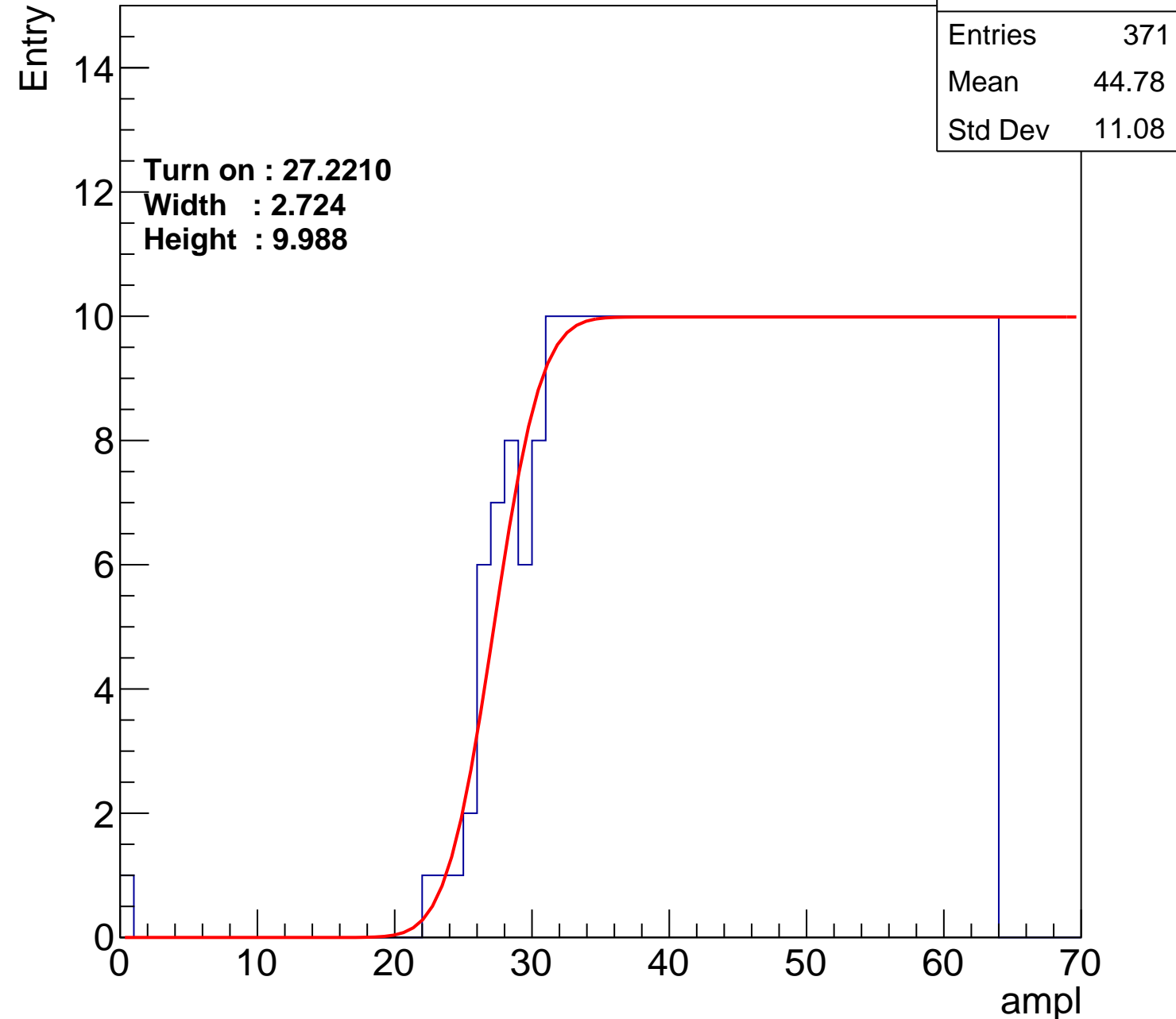
Width : 2.724

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U23-ch127

calib_packv5_042523_0143.root, FC#5, port B1

| | |
|---------|-------|
| Entries | 371 |
| Mean | 44.78 |
| Std Dev | 11.08 |

Turn on : 27.2210

Width : 2.724

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl

