

B0L101S, U15-ch0, adc0

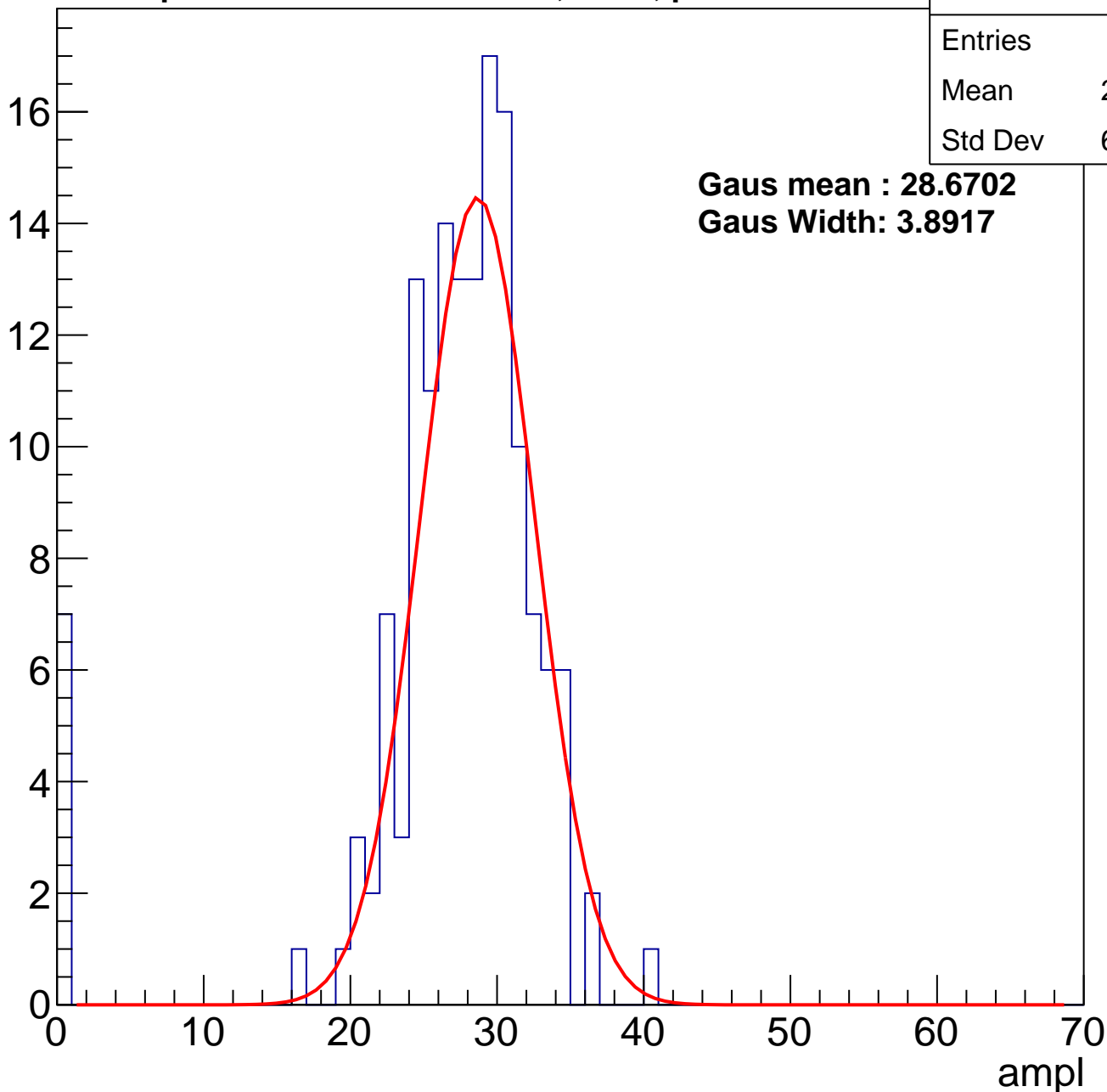
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	26.45
Std Dev	6.888

Gaus mean : 28.6702

Gaus Width: 3.8917

Entry



B0L101S, U15-ch0, adc1

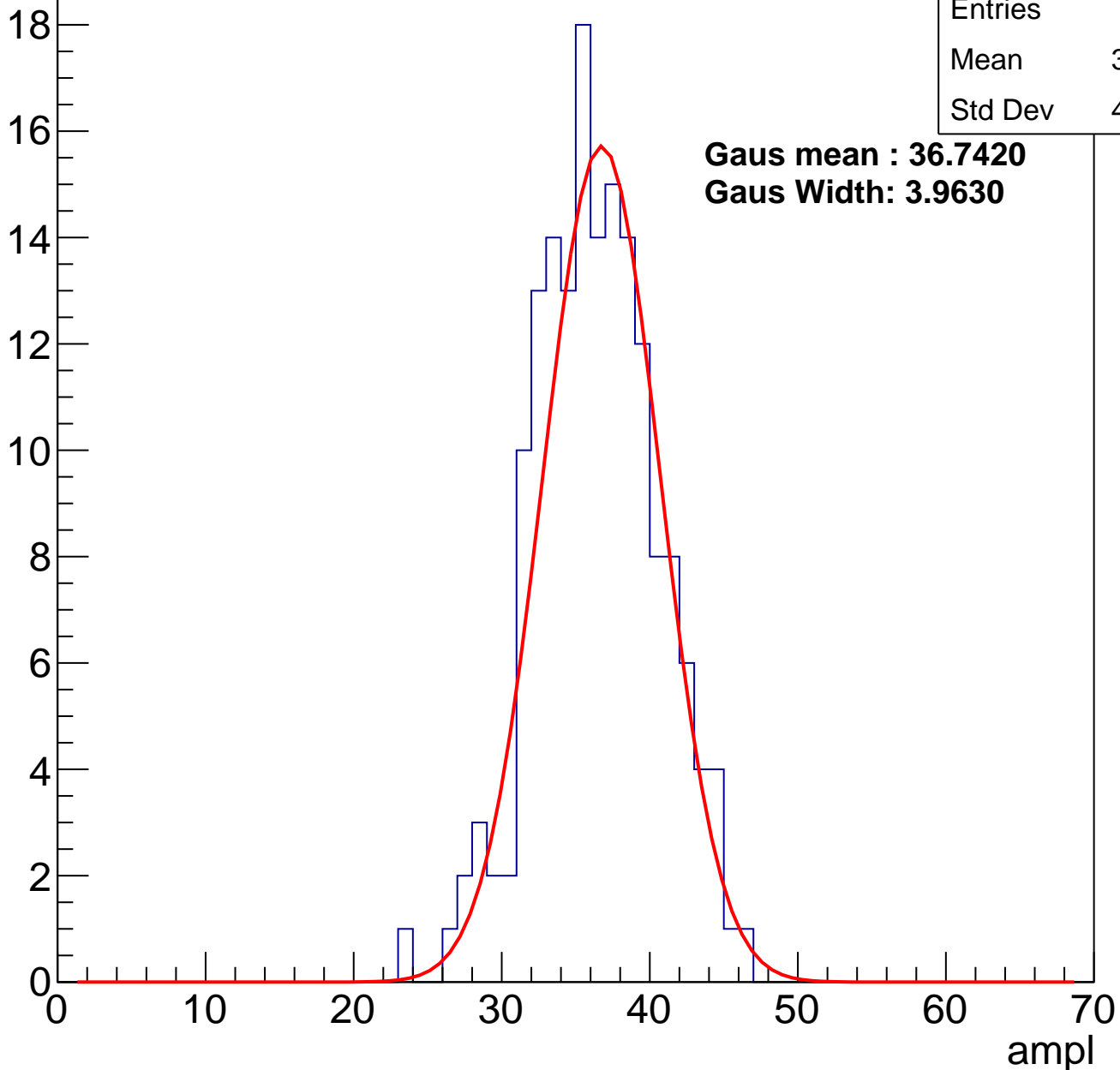
calib_packv5_042523_0143.root, FC#1, port C1

Entries	166
Mean	35.92
Std Dev	4.108

Gaus mean : 36.7420

Gaus Width: 3.9630

Entry



B0L101S, U15-ch0, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	117
Mean	42.65
Std Dev	3.767

Gaus mean : 43.2646

Gaus Width: 4.1805

0

10

20

30

40

50

60

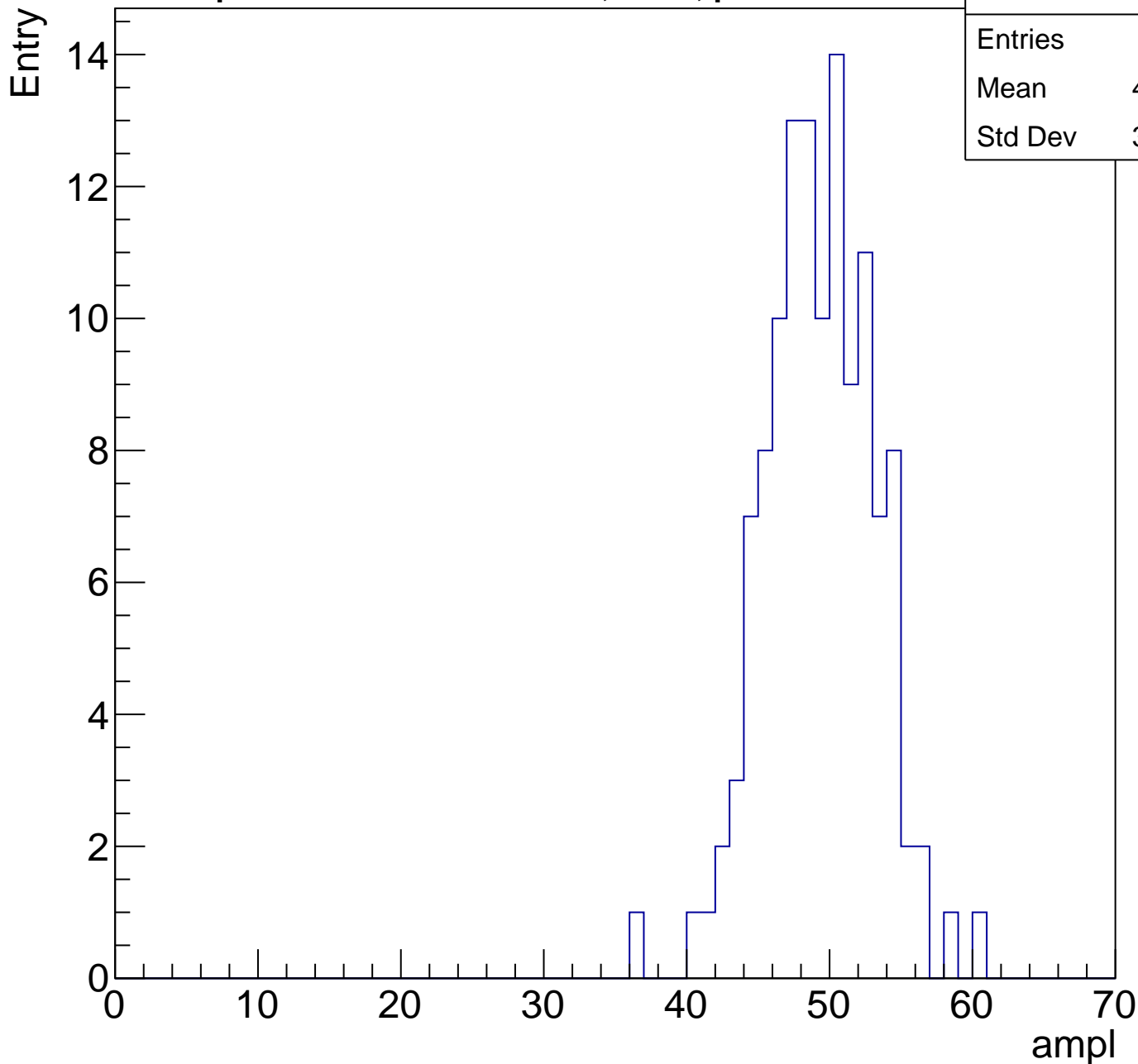
70

ampl

B0L101S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	48.85
Std Dev	3.819



B0L101S, U15-ch0, adc4

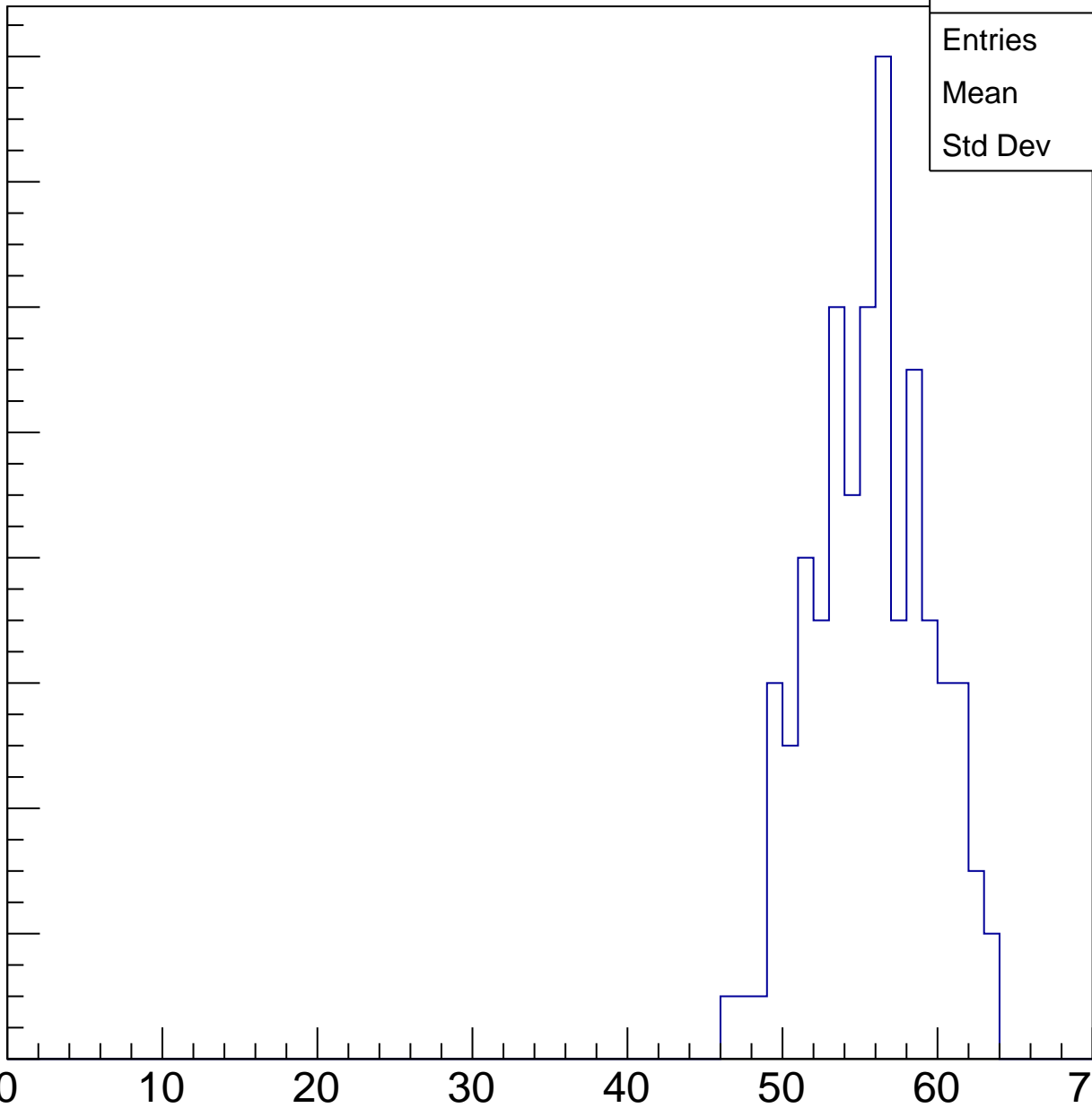
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	55.19
Std Dev	3.736

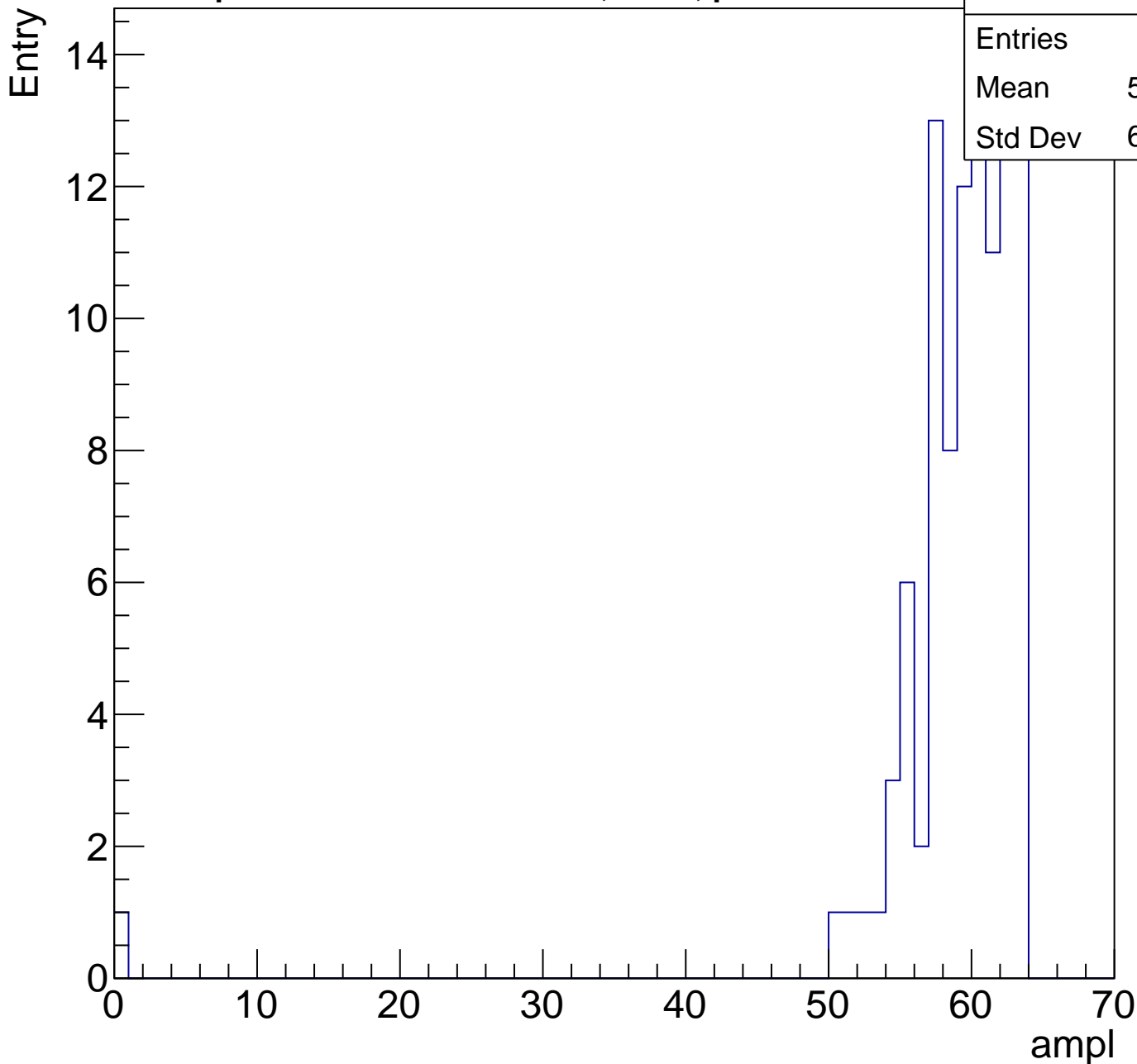
ampl



B0L101S, U15-ch0, adc5

calib_packv5_042523_0143.root, FC#1, port C1

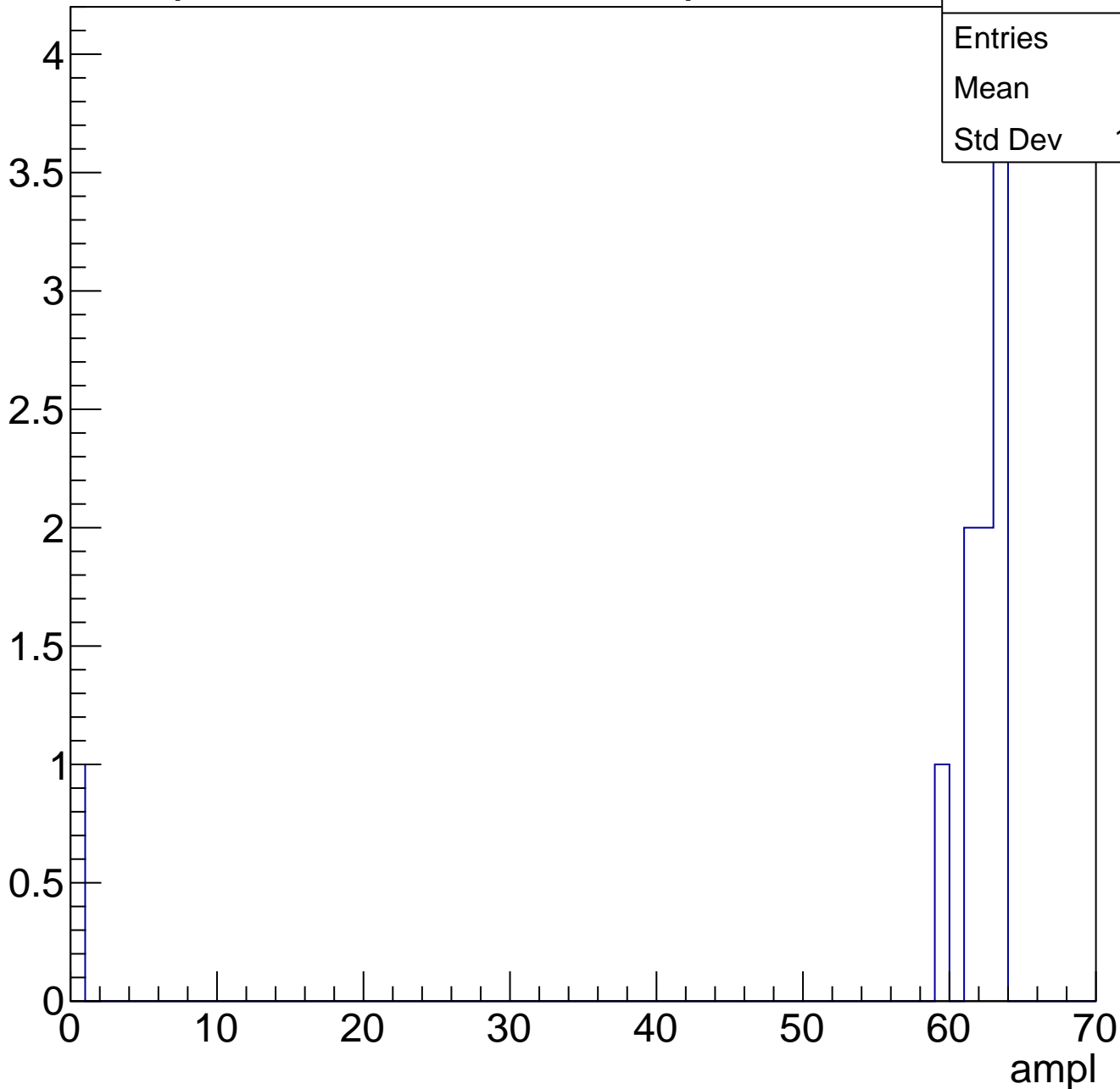
Entries	101
Mean	58.65
Std Dev	6.566



B0L101S, U15-ch0, adc6

calib_packv5_042523_0143.root, FC#1, port C1

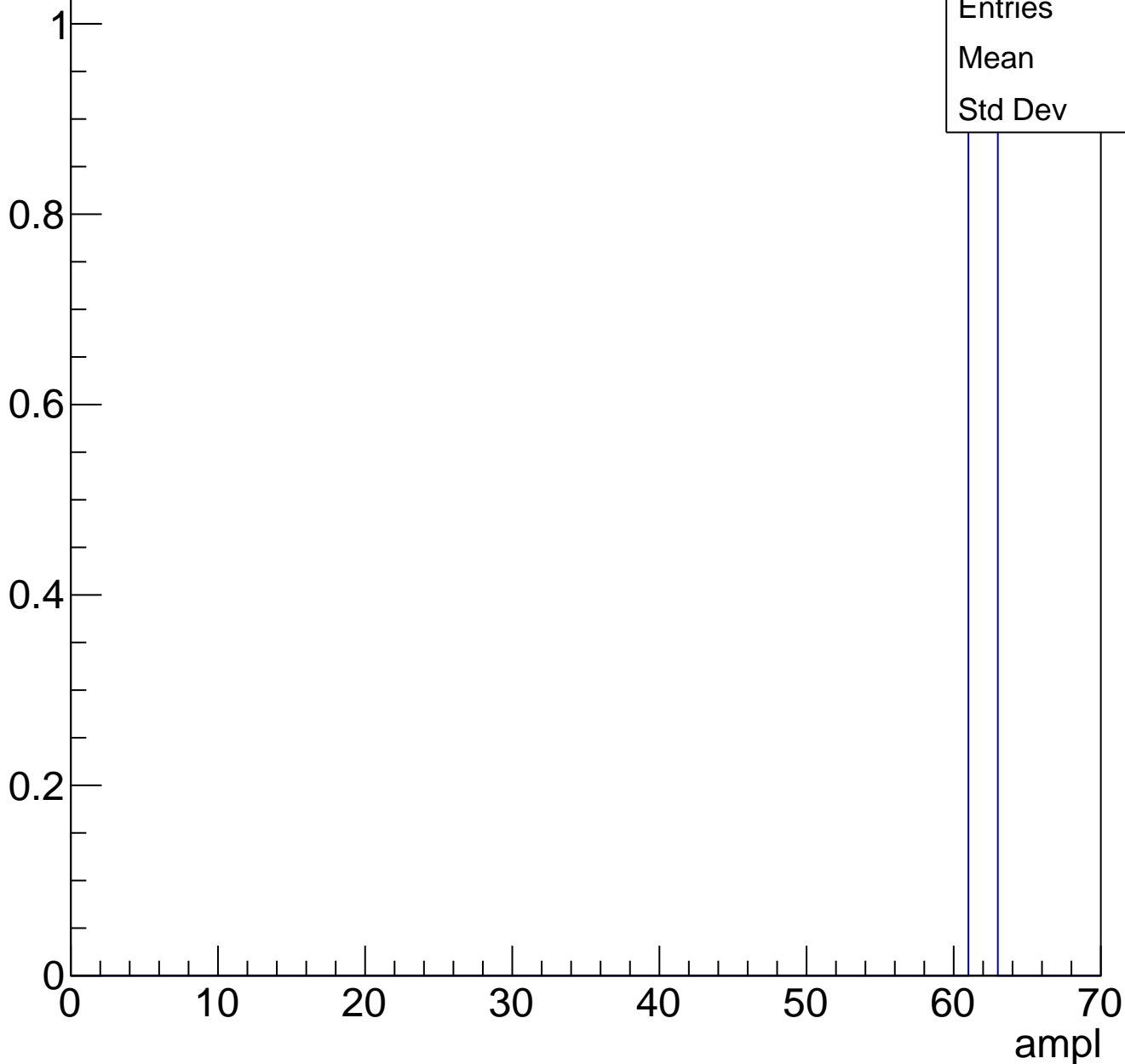
Entry



B0L101S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch1, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	27.37
Std Dev	5.35

Gaus mean : 28.5089

Gaus Width: 3.8302

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

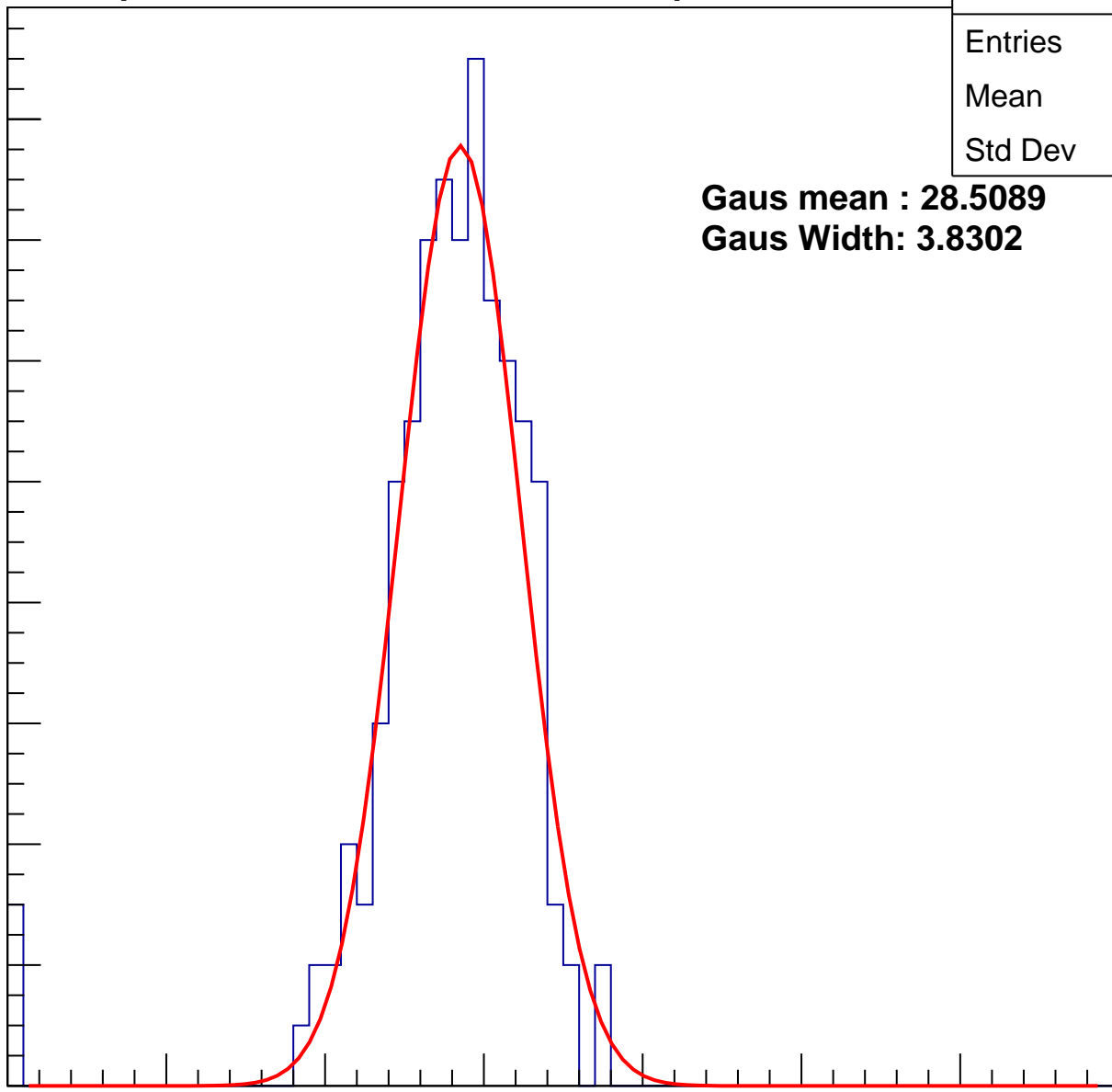
40

50

60

70

ampl



B0L101S, U15-ch1, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	35.17
Std Dev	3.405

Gaus mean : 35.8593

Gaus Width: 3.6896

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

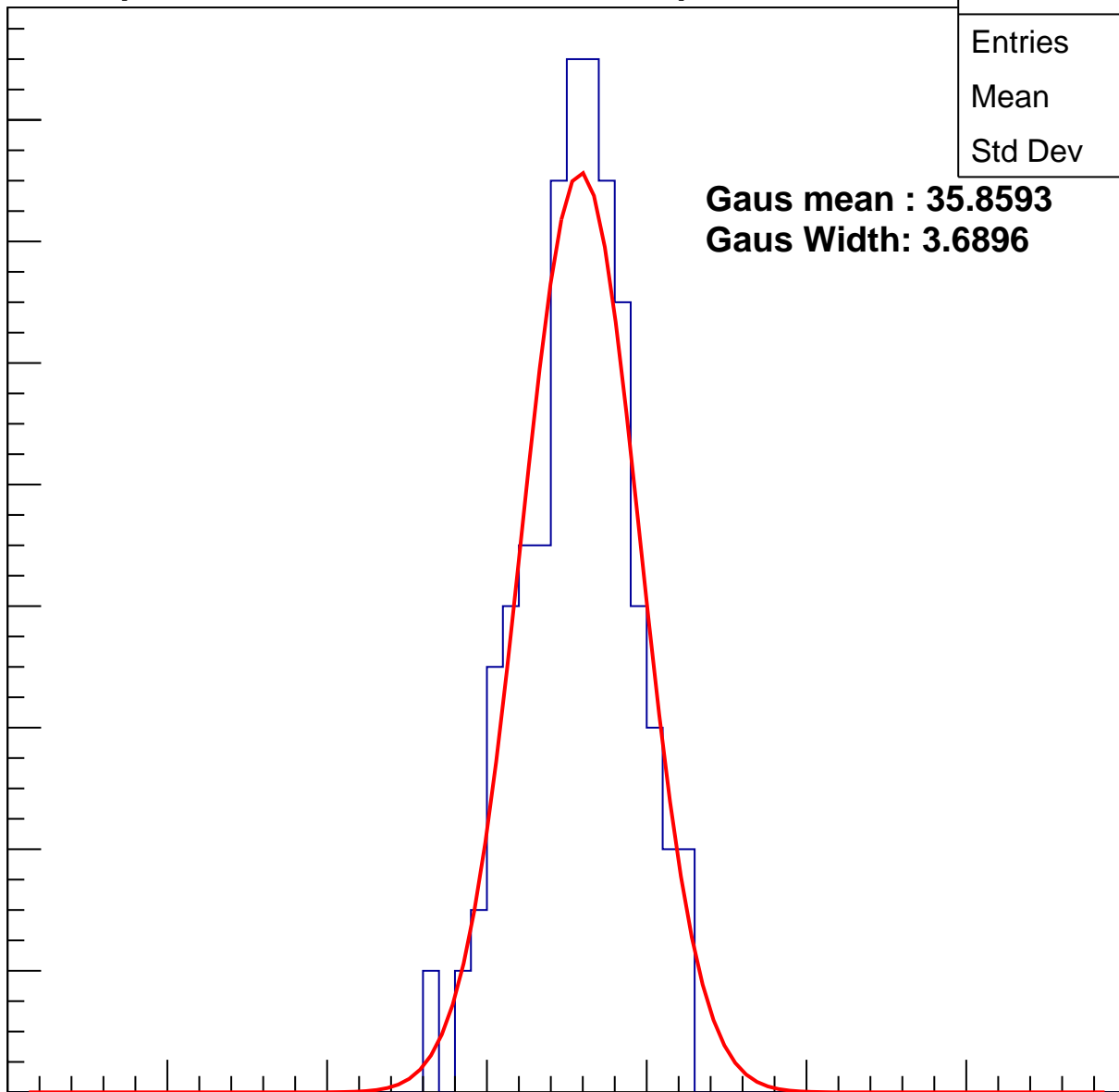
40

50

60

70

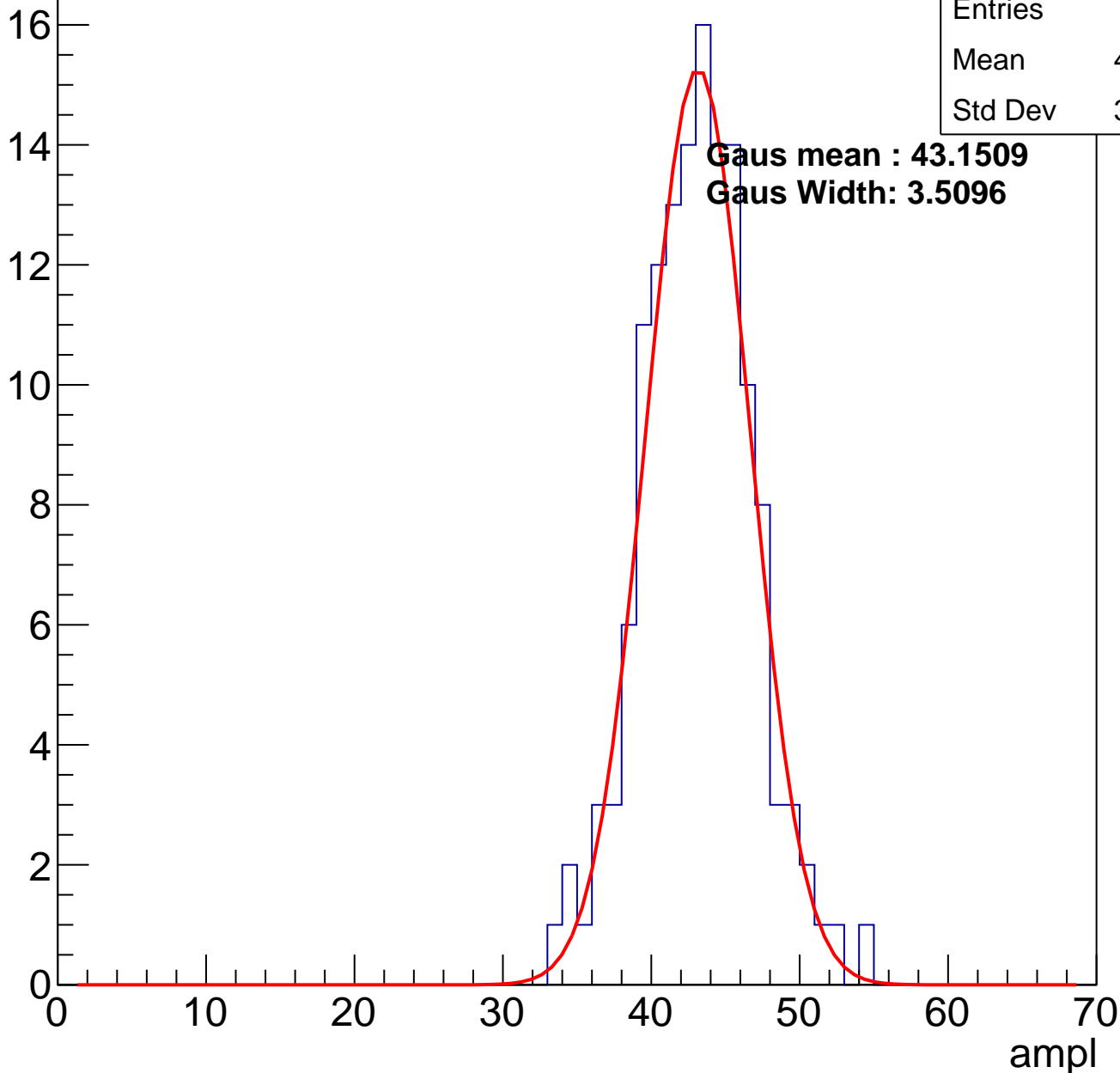
ampl



B0L101S, U15-ch1, adc2

calib_packv5_042523_0143.root, FC#1, port C1

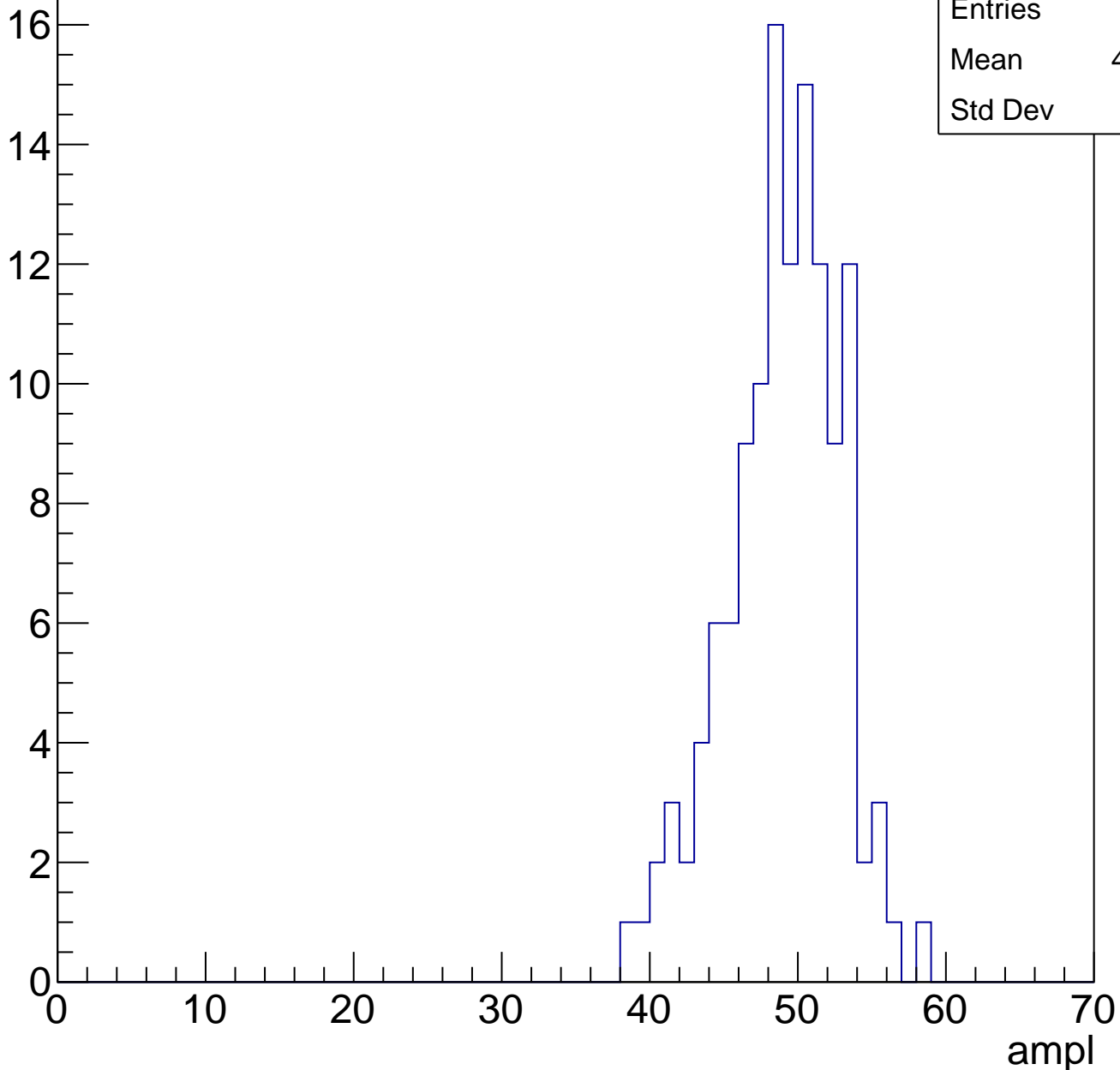
Entry



B0L101S, U15-ch1, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	127
Mean	48.53
Std Dev	3.76

B0L101S, U15-ch1, adc4

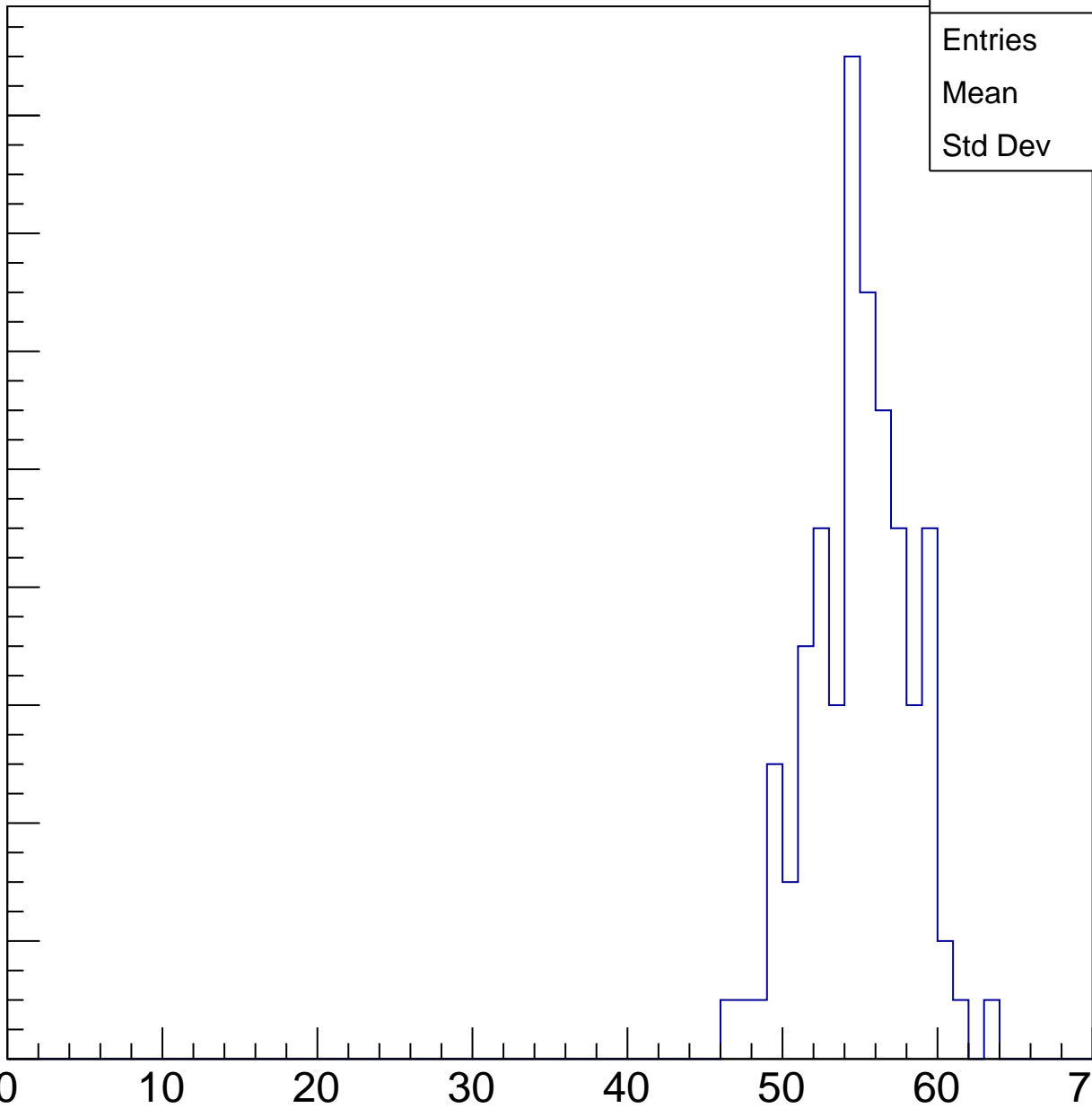
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	54.55
Std Dev	3.22

ampl



B0L101S, U15-ch1, adc5

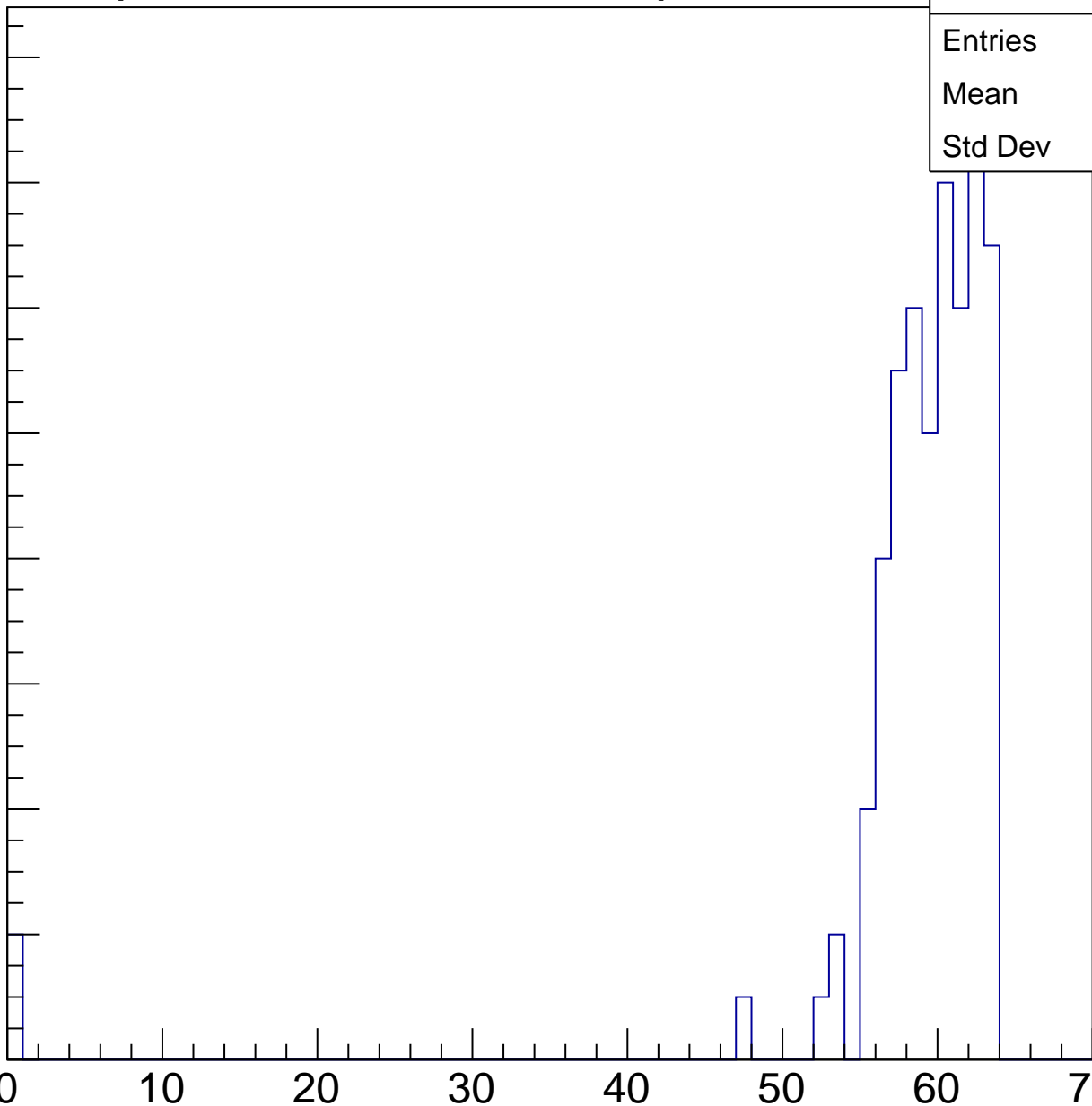
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	106
Mean	58.2
Std Dev	8.559

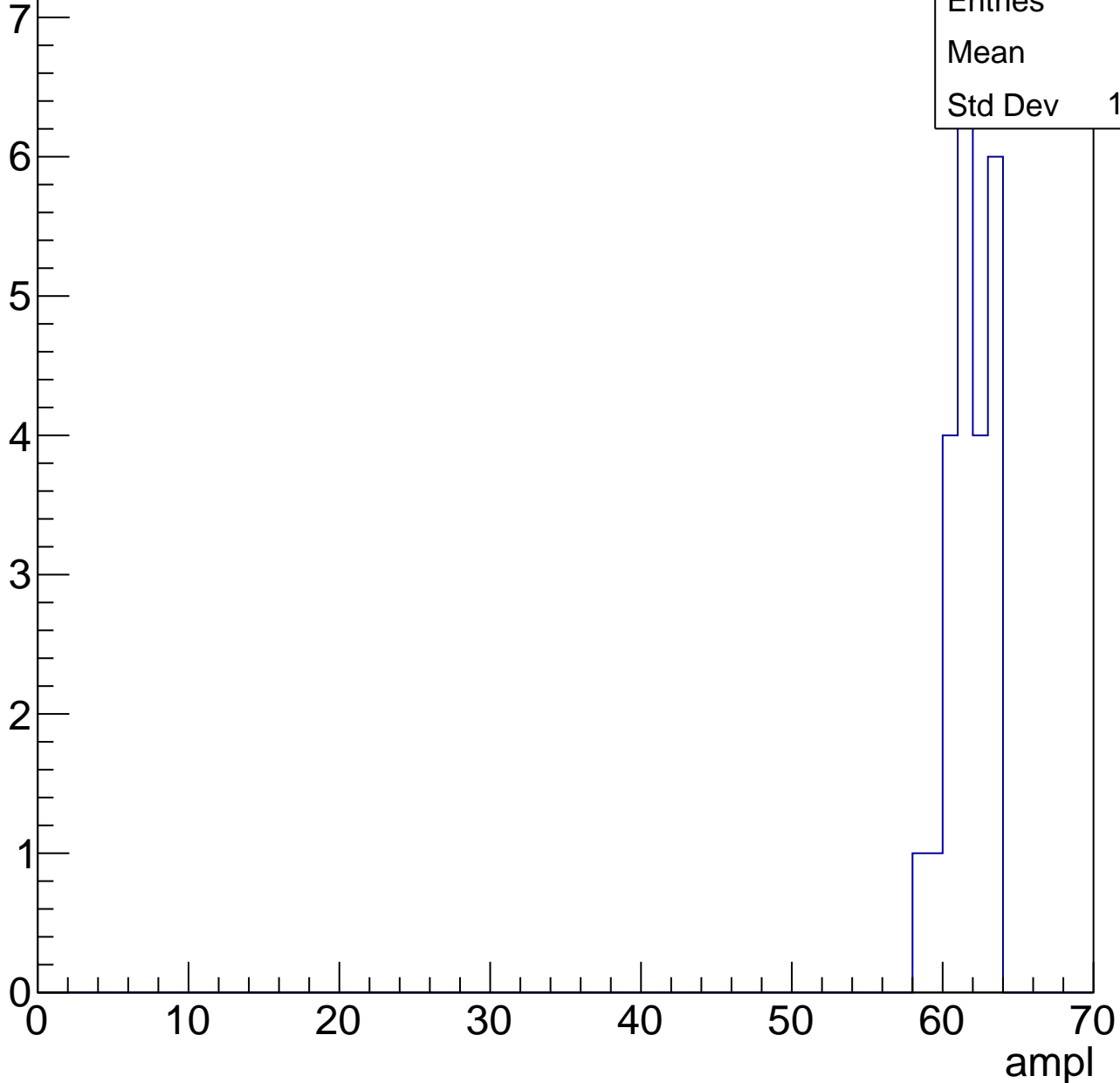
ampl



B0L101S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch2, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

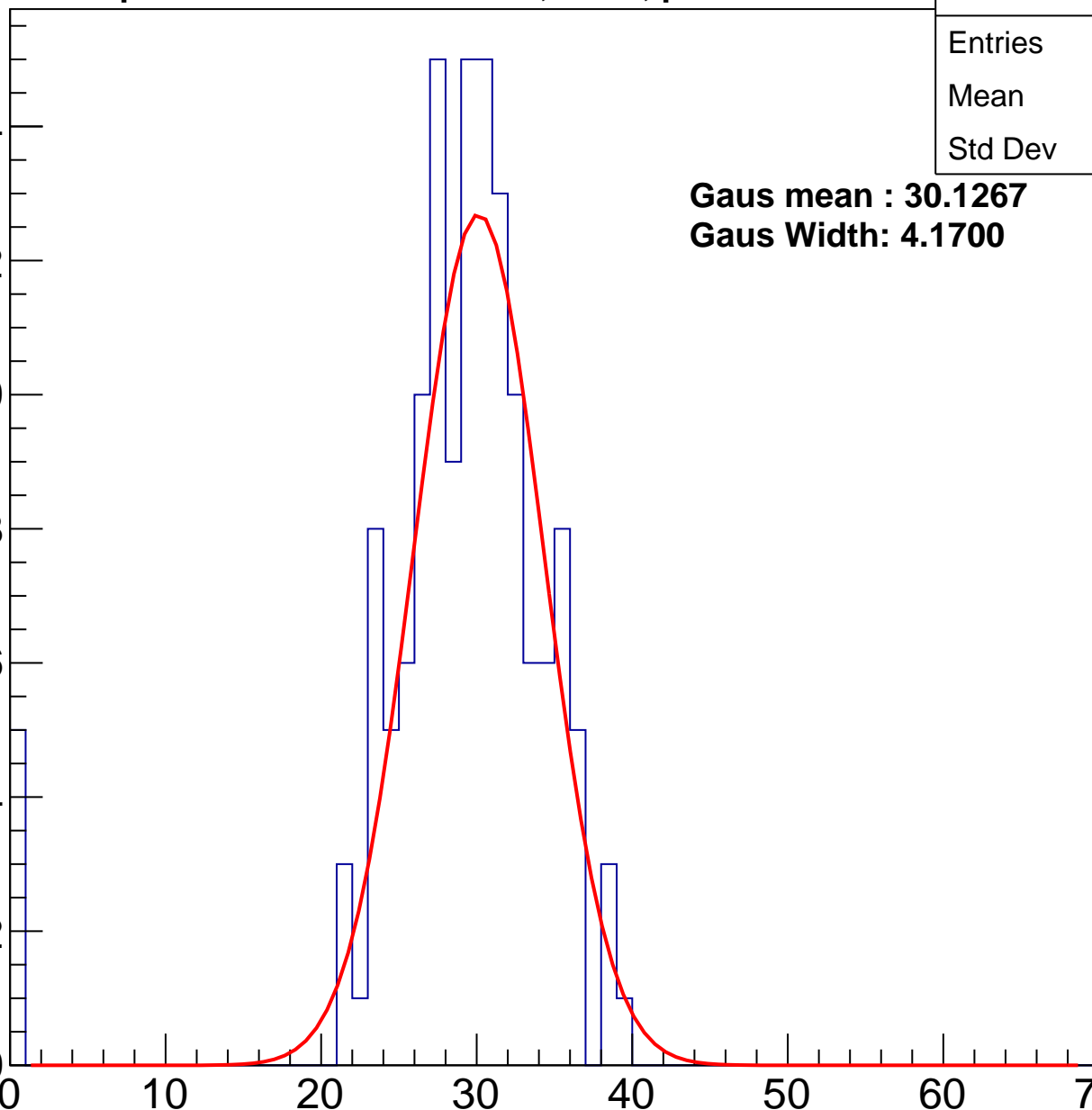
70

ampl

Entries	144
Mean	28.33
Std Dev	6.629

Gaus mean : 30.1267

Gaus Width: 4.1700



B0L101S, U15-ch2, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	35.9
Std Dev	3.222

Gaus mean : 36.0676

Gaus Width: 3.6104

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

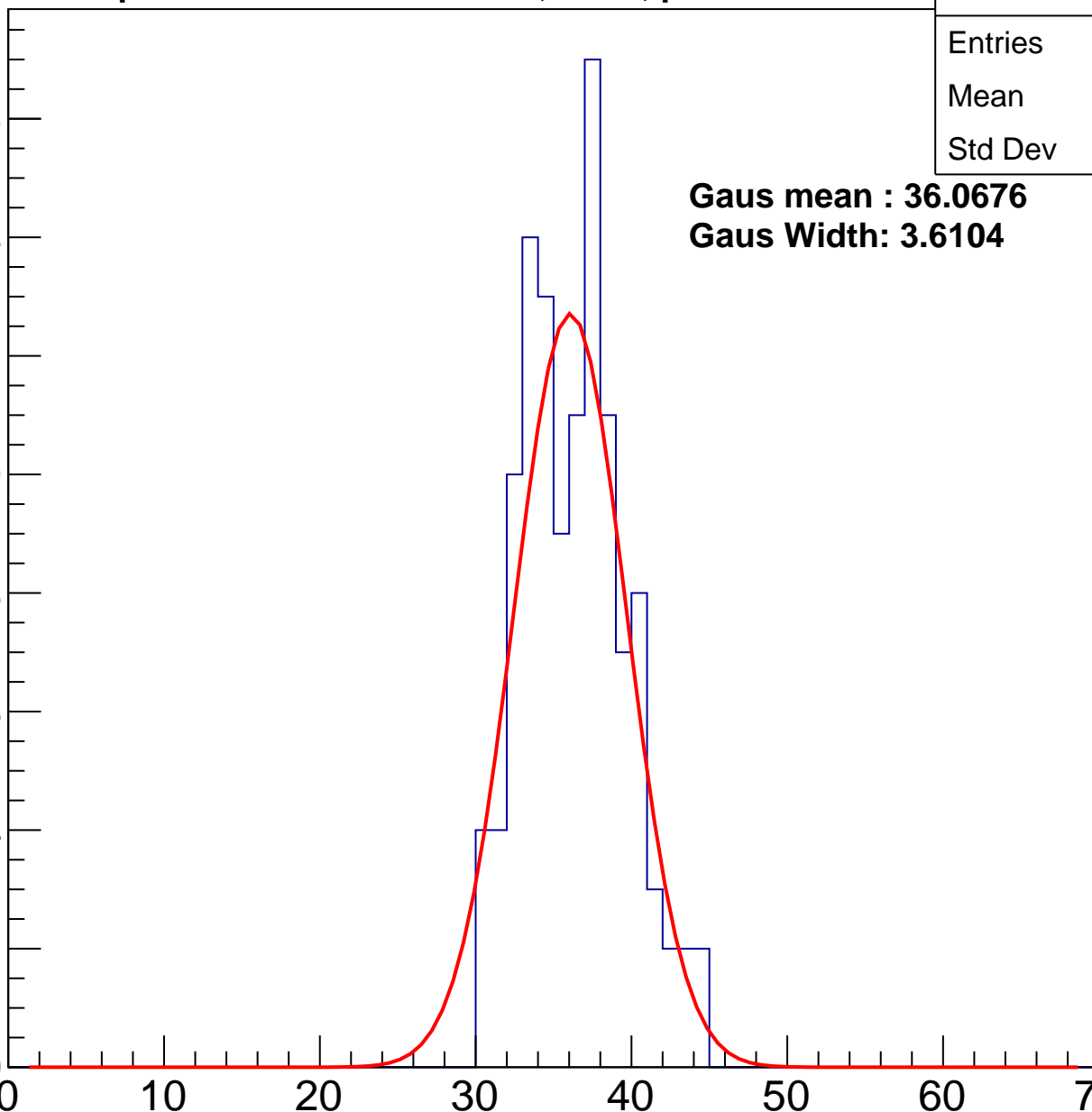
40

50

60

70

ampl



B0L101S, U15-ch2, adc2

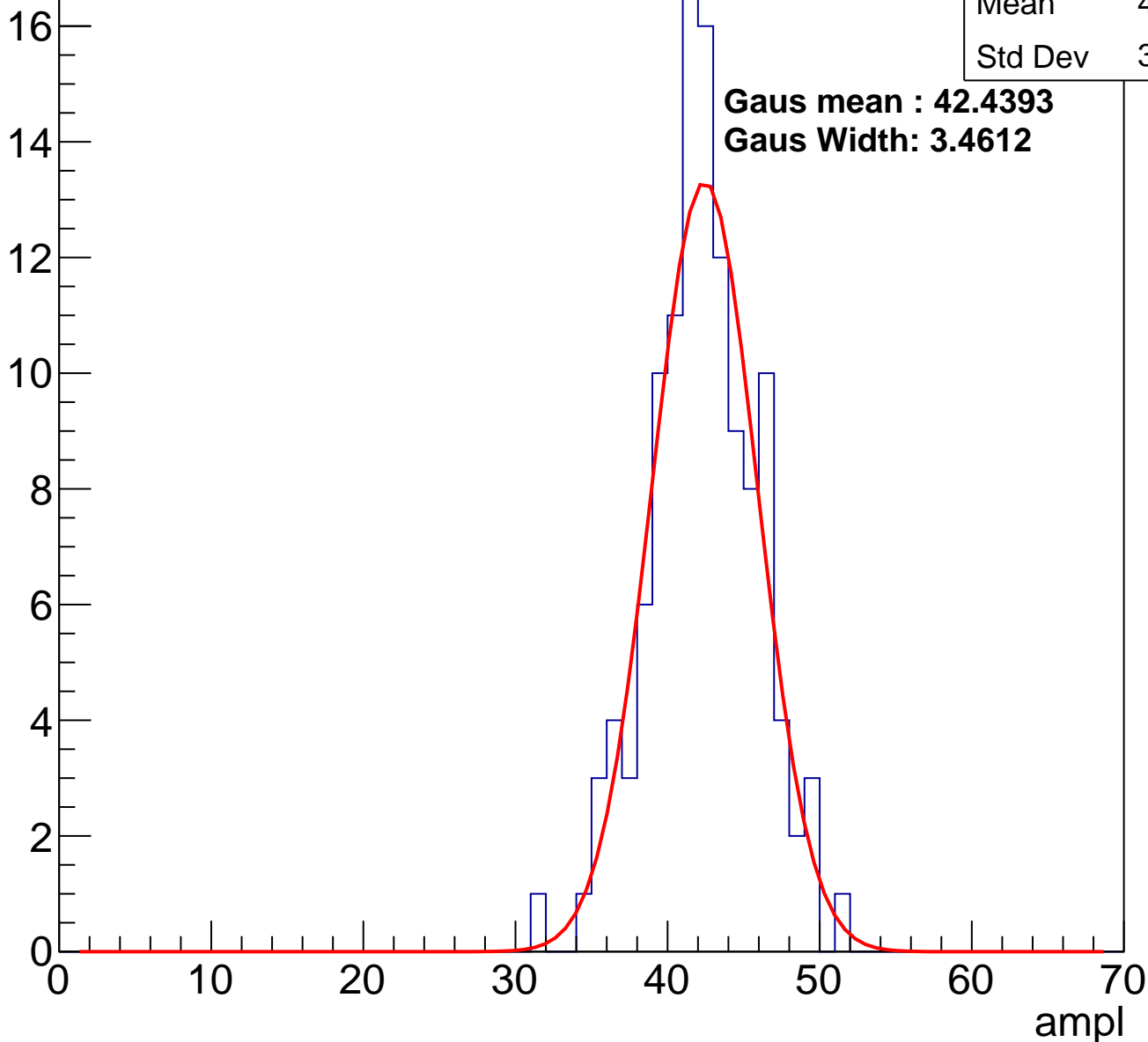
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	121
Mean	41.87
Std Dev	3.492

Gaus mean : 42.4393

Gaus Width: 3.4612



B0L101S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	49.05
Std Dev	3.842

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

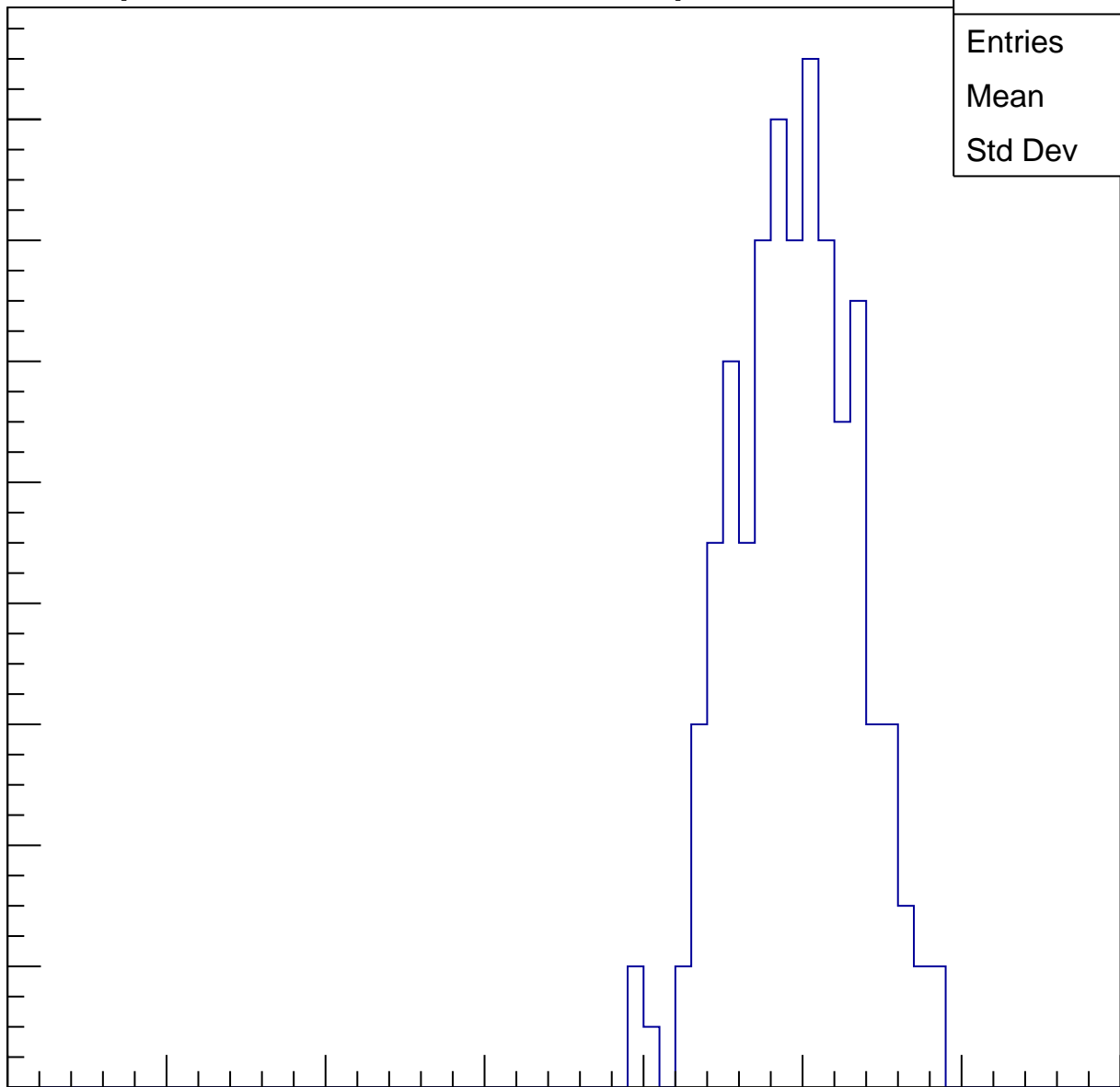
40

50

60

70

ampl



B0L101S, U15-ch2, adc4

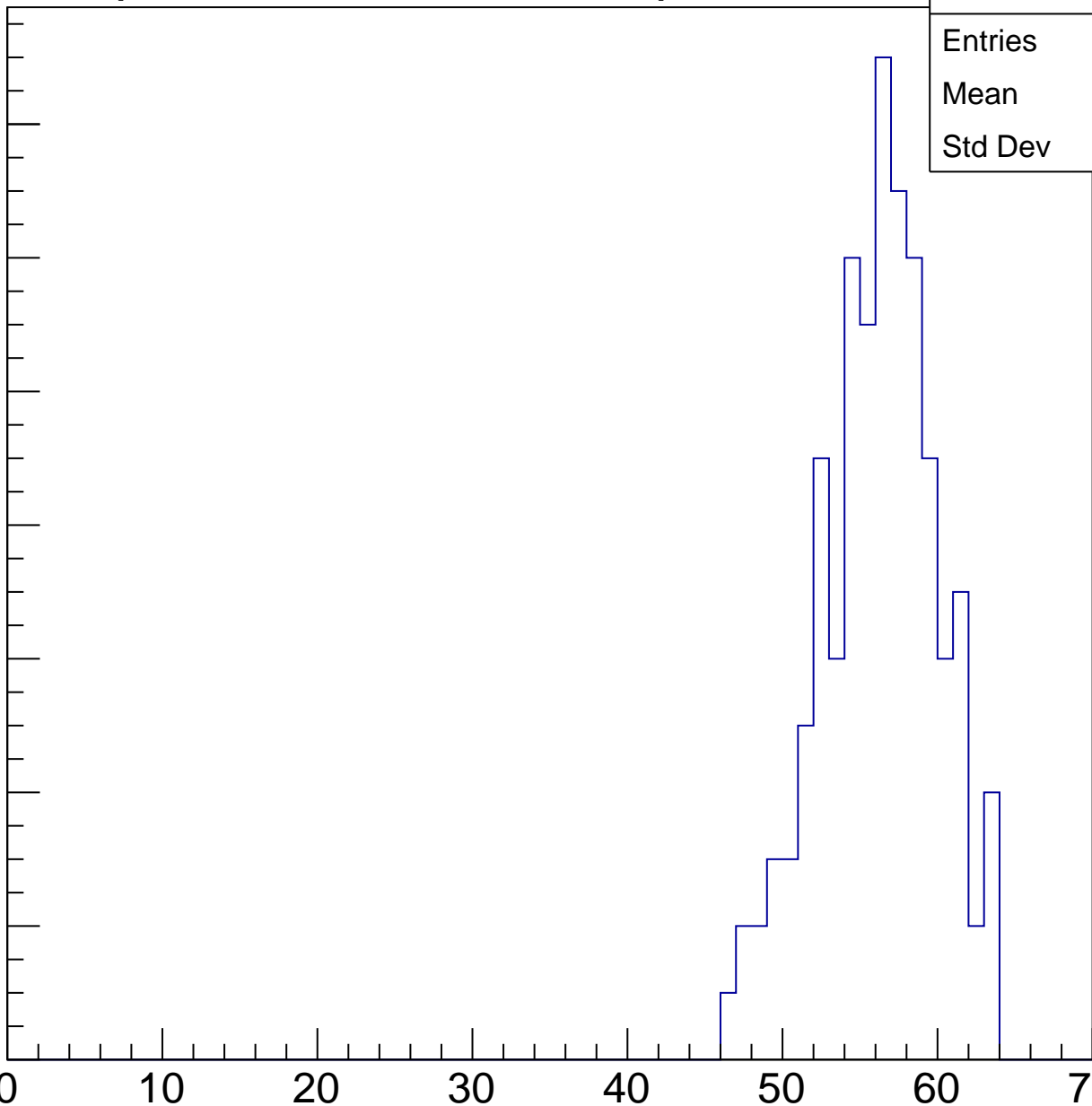
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	122
Mean	55.72
Std Dev	3.731

ampl

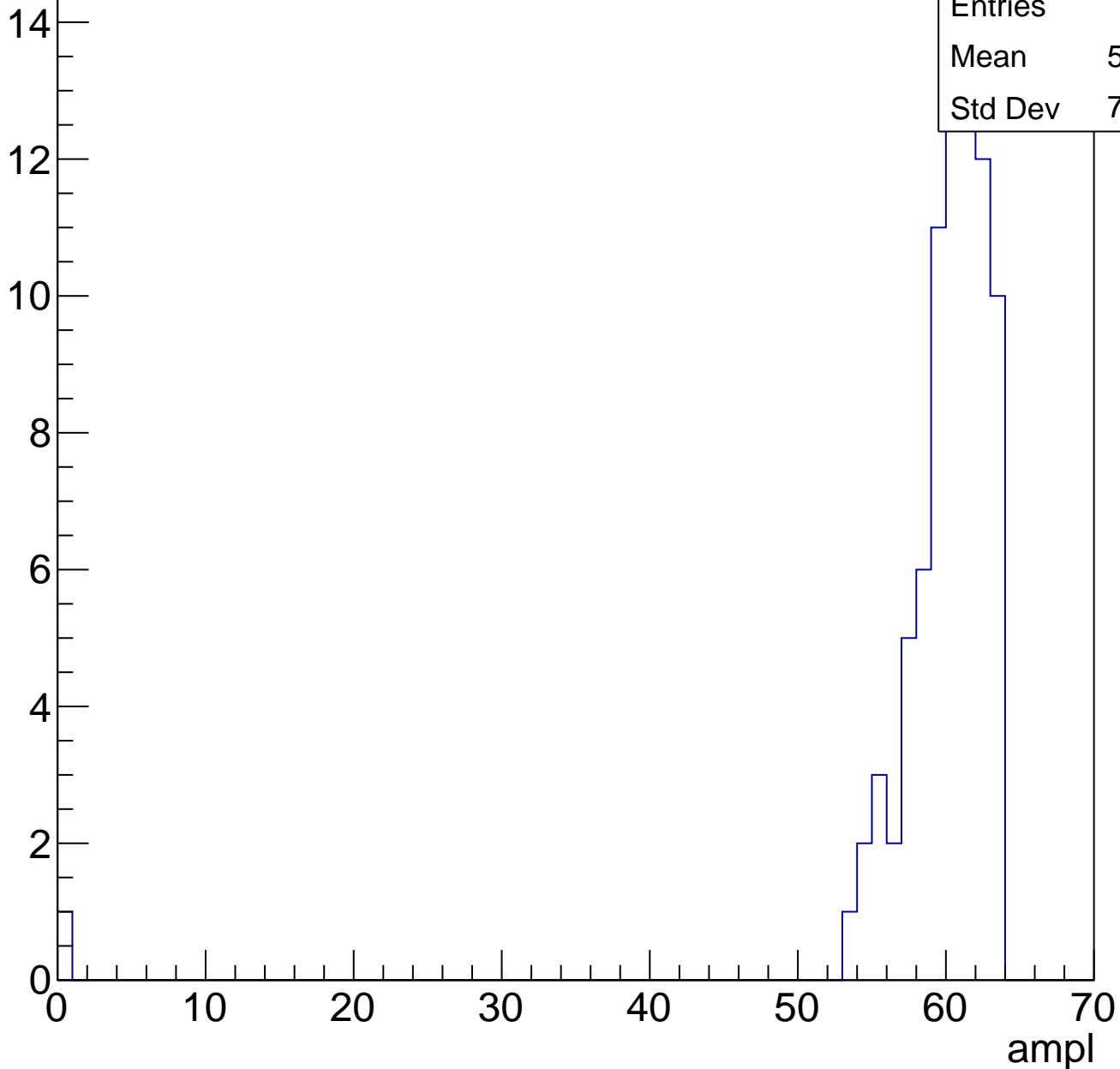


B0L101S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	80
Mean	59.09
Std Dev	7.064

Entry

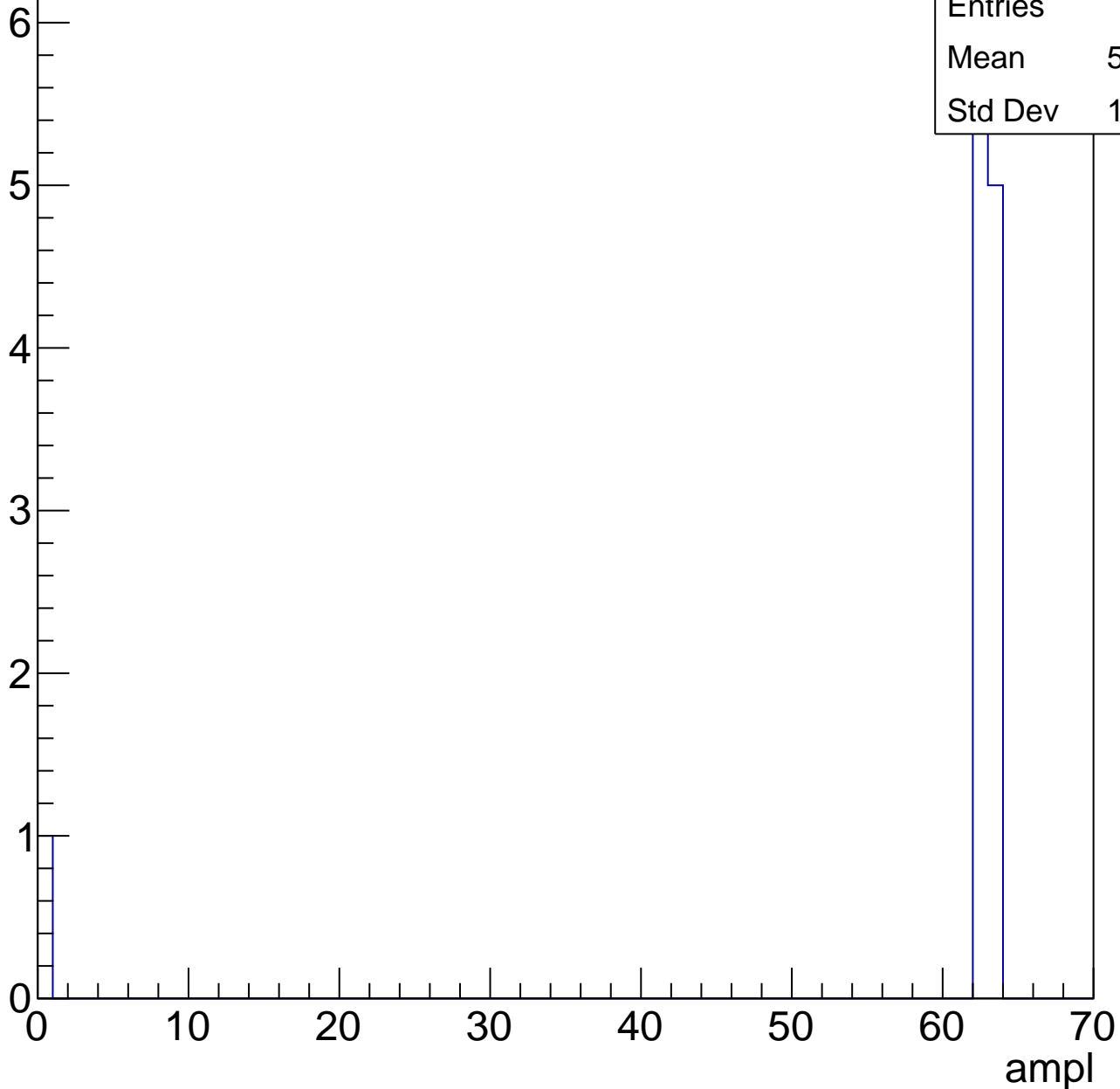


B0L101S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	57.25
Std Dev	17.27



B0L101S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U15-ch3, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

114

Mean

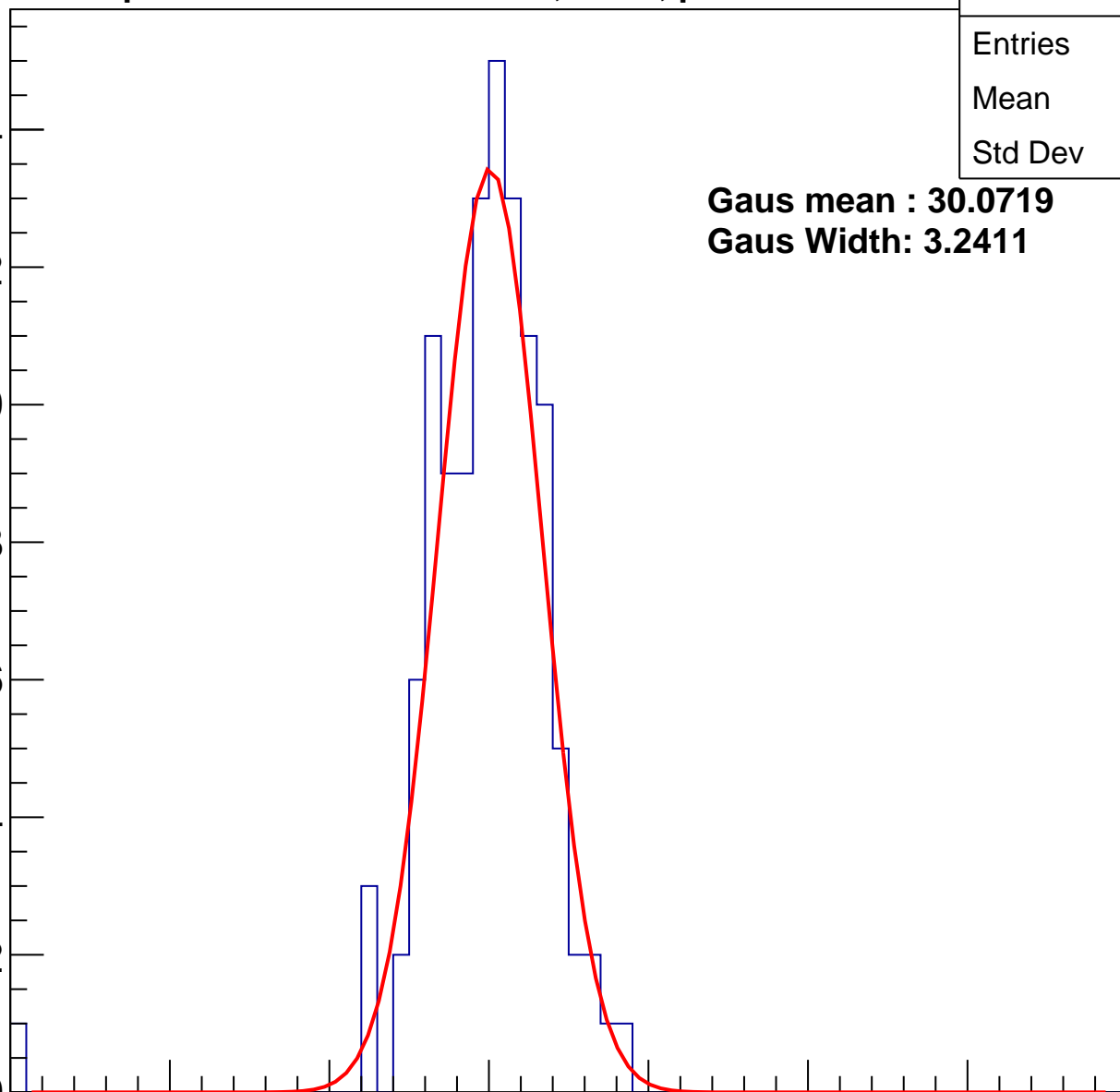
29.33

Std Dev

4.221

Gaus mean : 30.0719

Gaus Width: 3.2411



B0L101S, U15-ch3, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	35.82
Std Dev	3.533

Gaus mean : 36.3840

Gaus Width: 3.4700

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

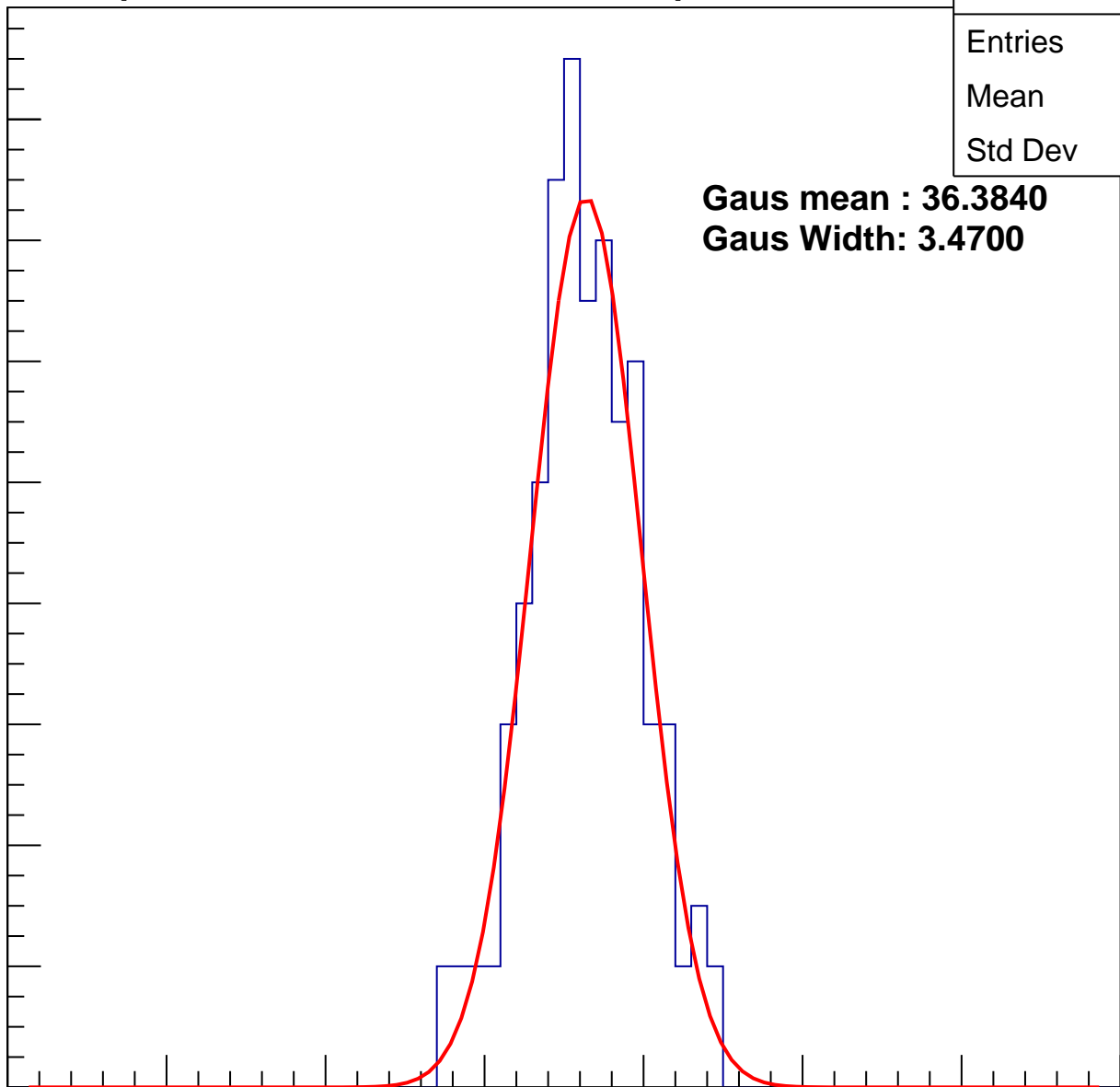
40

50

60

70

ampl



B0L101S, U15-ch3, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

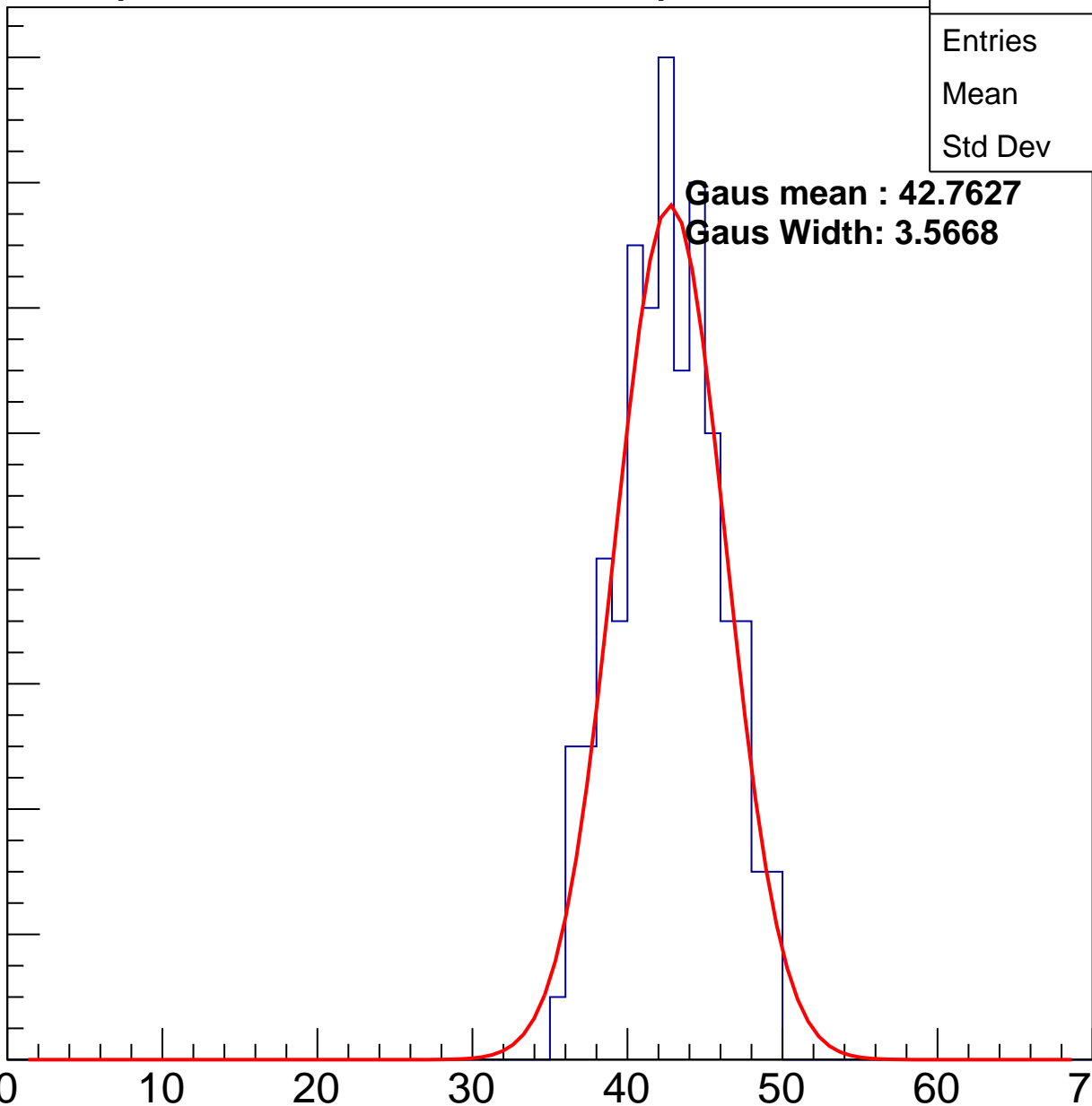
16
14
12
10
8
6
4
2
0

Entries	122
Mean	42.15
Std Dev	3.271

Gaus mean : 42.7627

Gaus Width: 3.5668

ampl



B0L101S, U15-ch3, adc3

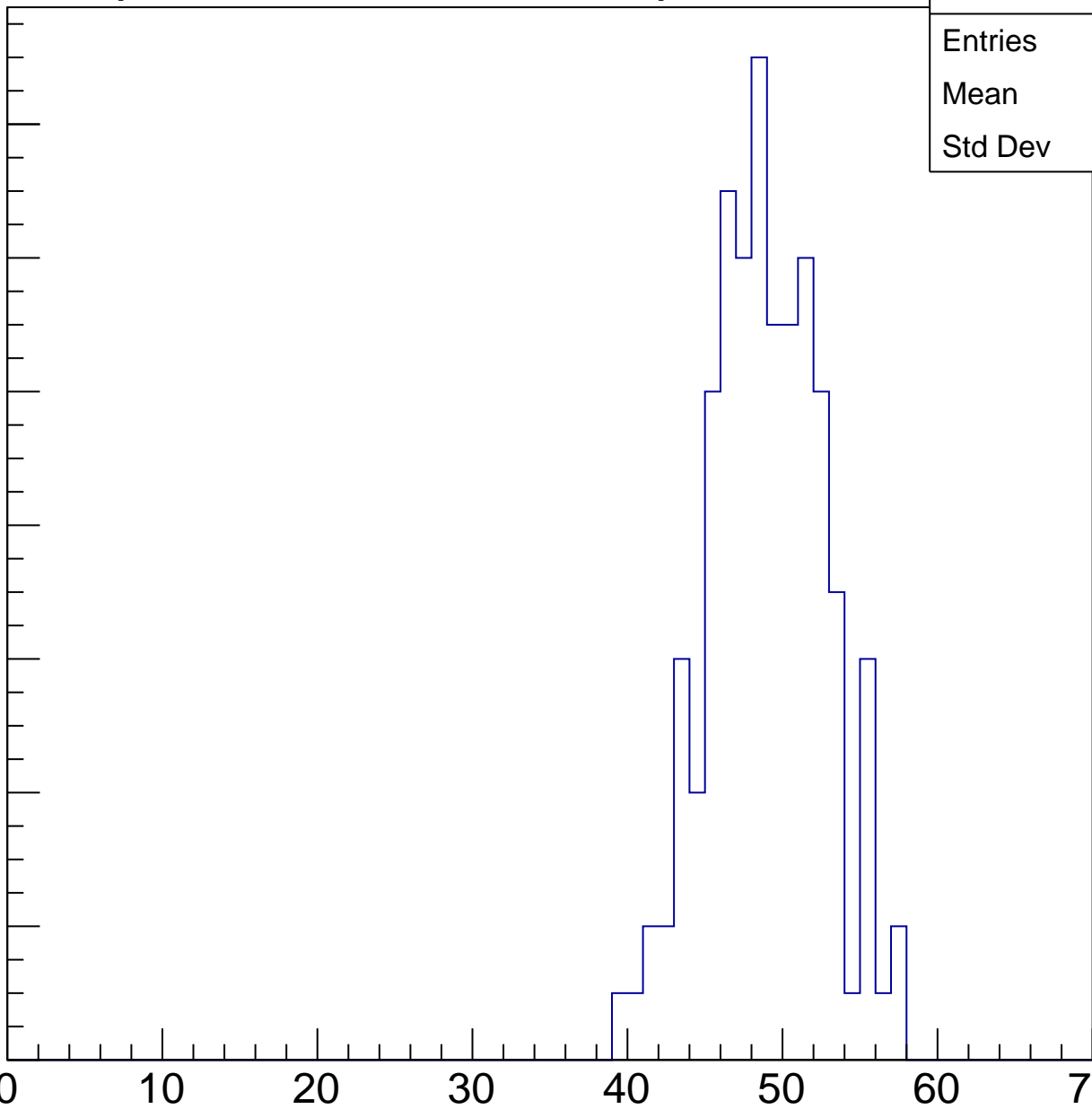
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	48.48
Std Dev	3.667

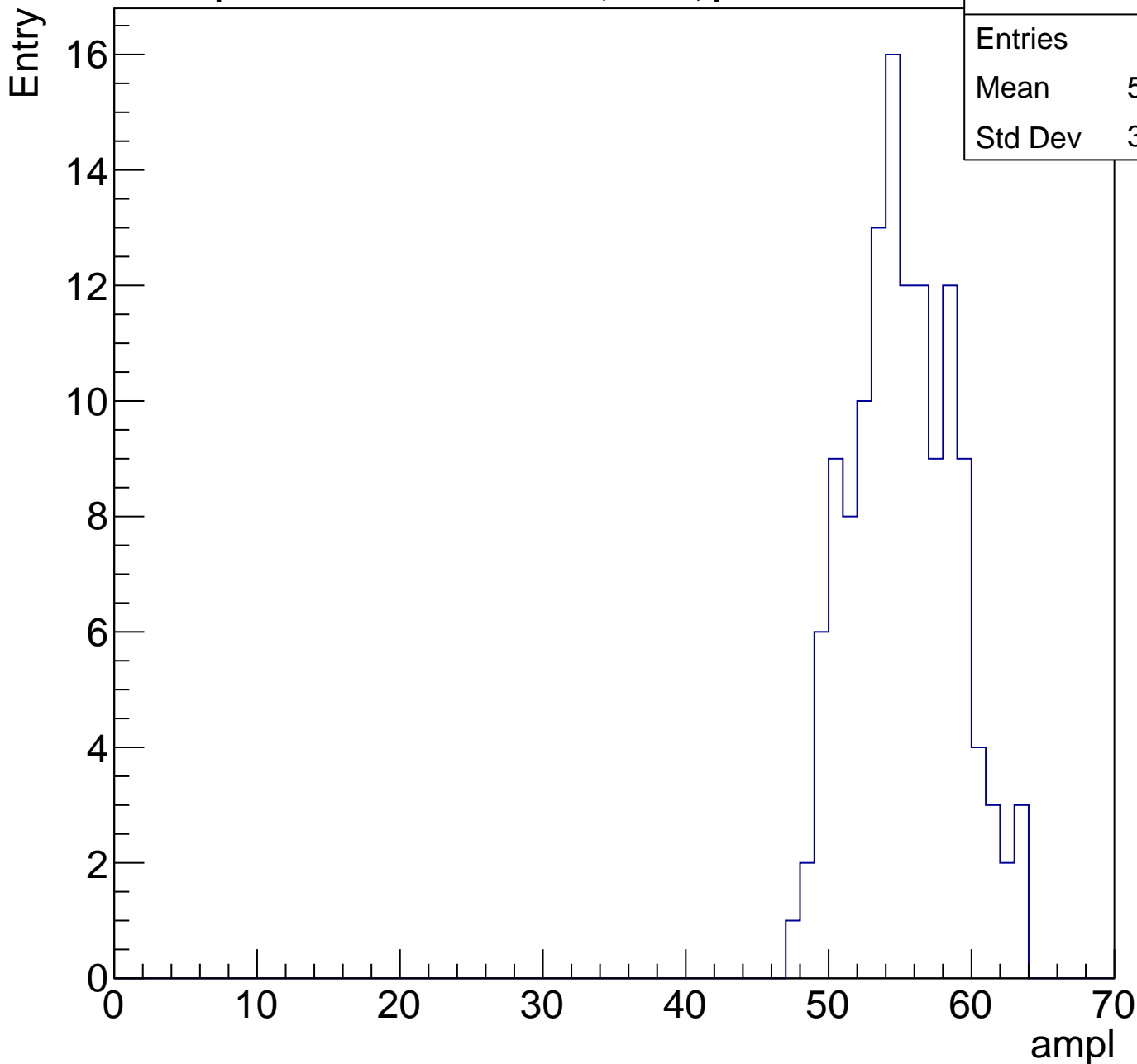
ampl



B0L101S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	54.78
Std Dev	3.582



B0L101S, U15-ch3, adc5

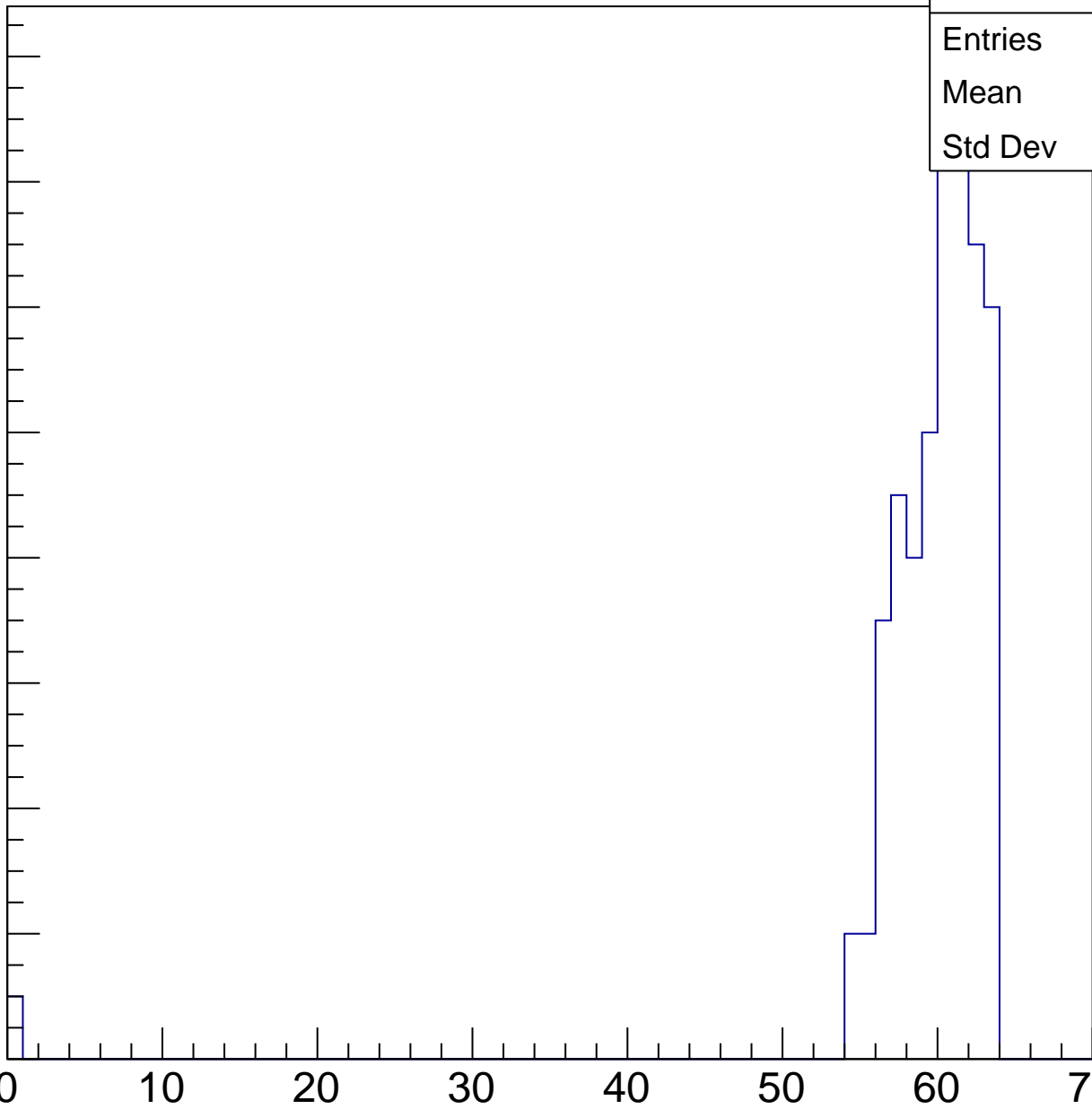
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.11
Std Dev	6.501

ampl

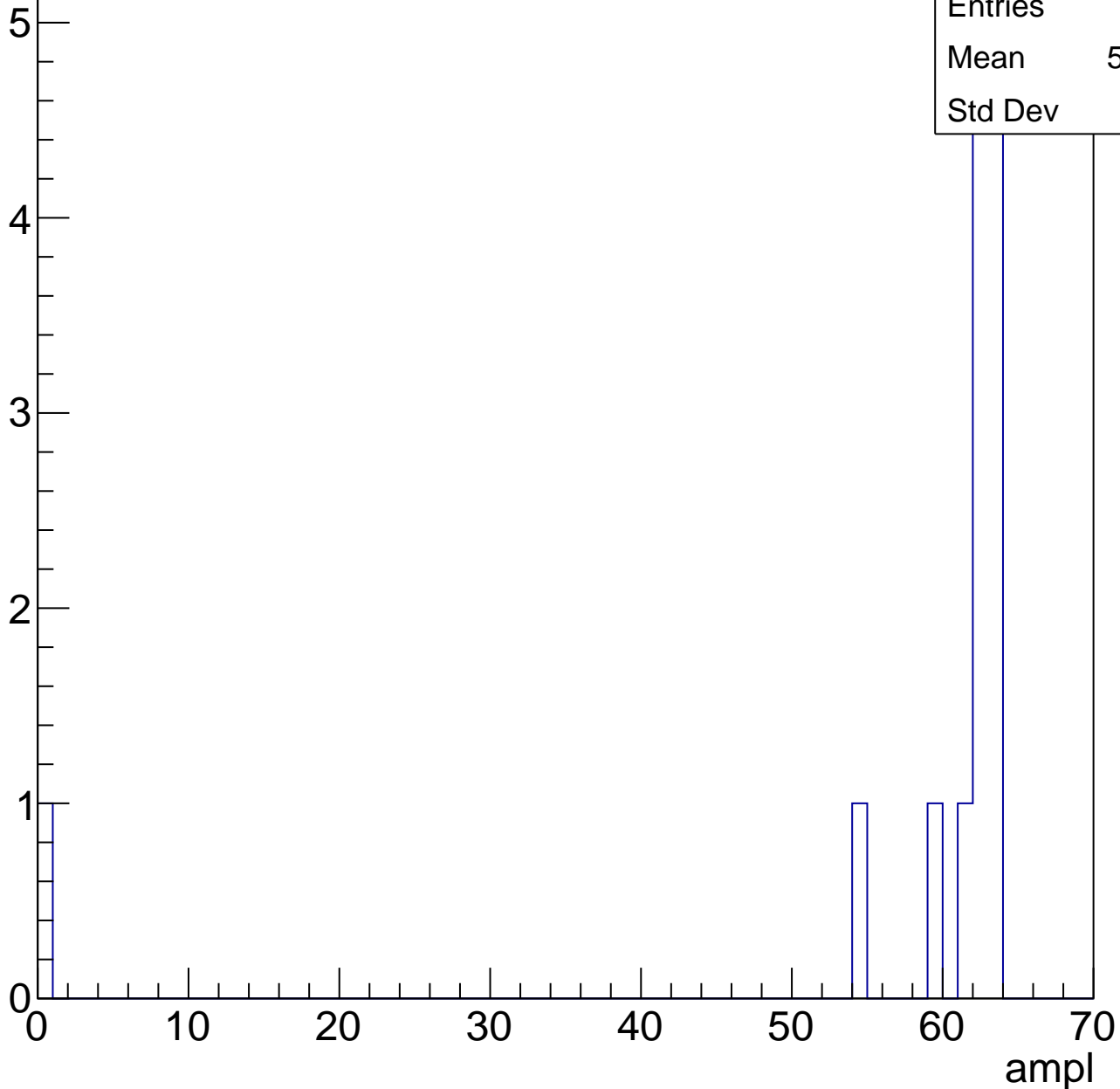


B0L101S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.07
Std Dev	16



B0L101S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	21
Std Dev	0

B0L101S, U15-ch4, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	29.1
Std Dev	3.229

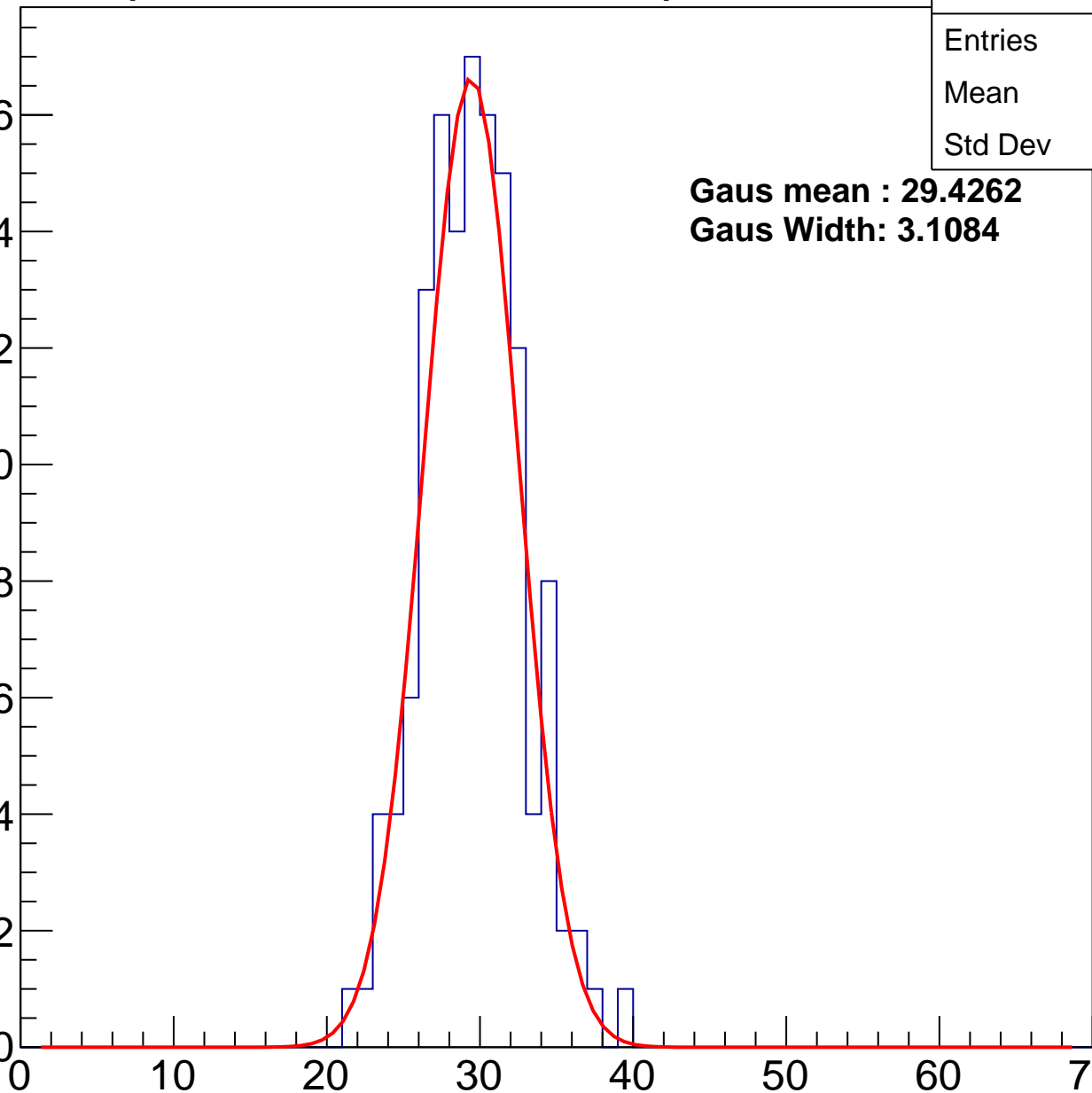
Gaus mean : 29.4262

Gaus Width: 3.1084

Entry

16
14
12
10
8
6
4
2
0

ampl



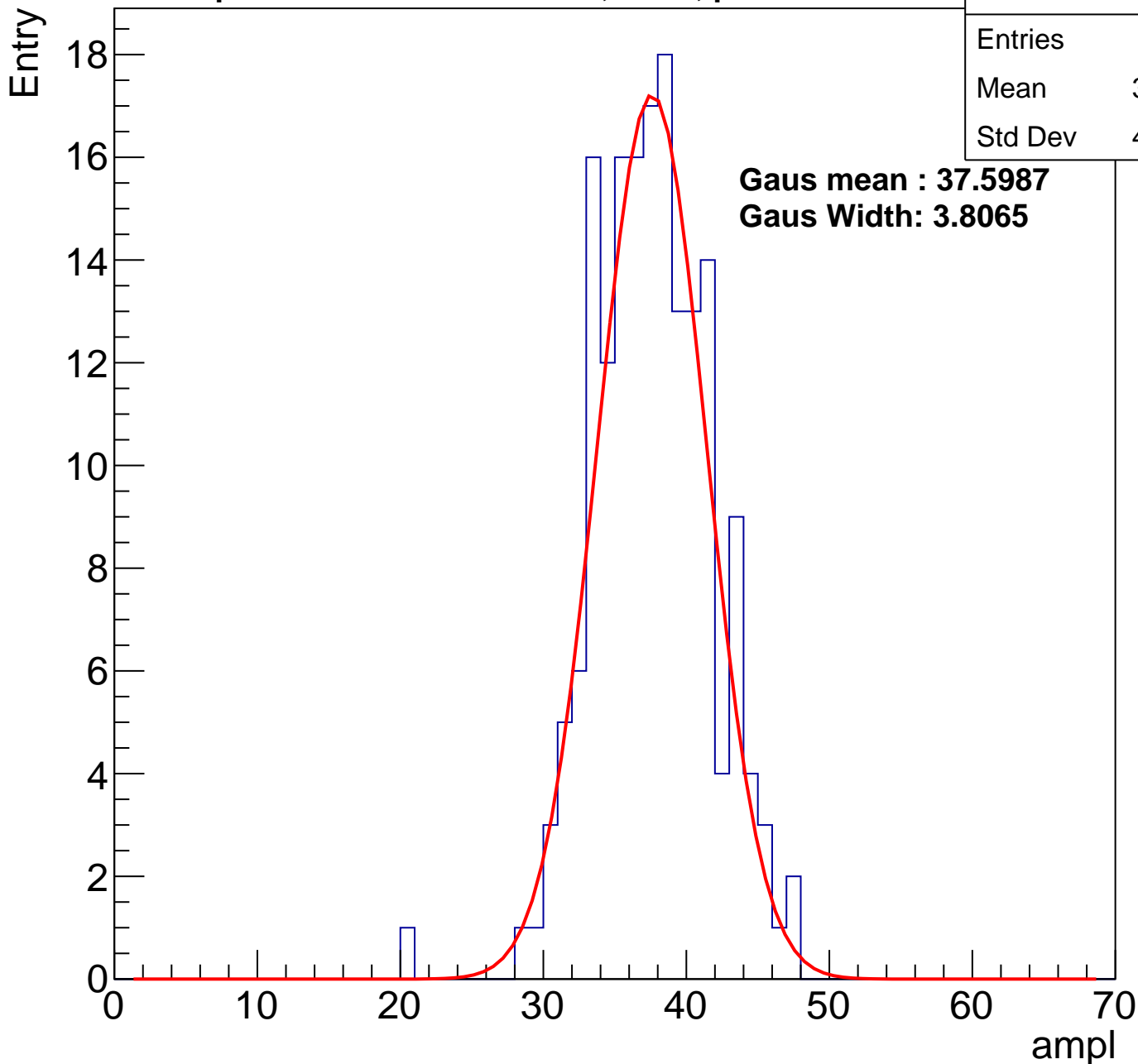
B0L101S, U15-ch4, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	175
Mean	37.18
Std Dev	4.038

Gaus mean : 37.5987

Gaus Width: 3.8065



B0L101S, U15-ch4, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

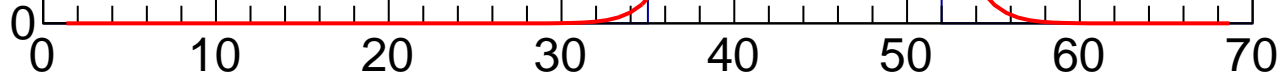
60

ampl

Entries	109
Mean	44.01
Std Dev	3.539

Gaus mean : 44.8414

Gaus Width: 3.5762



B0L101S, U15-ch4, adc3

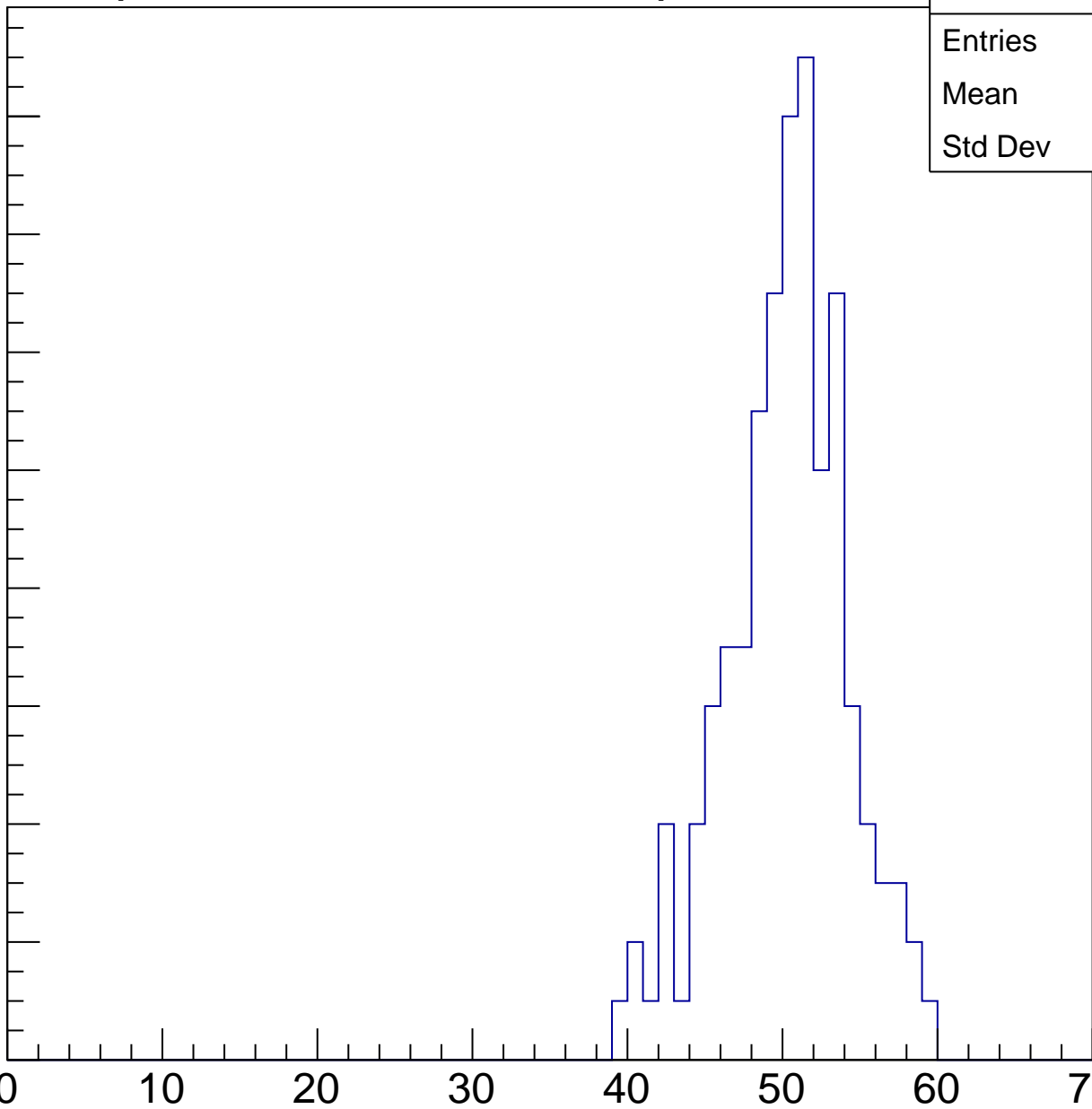
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	49.75
Std Dev	3.949

ampl



B0L101S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#1, port C1

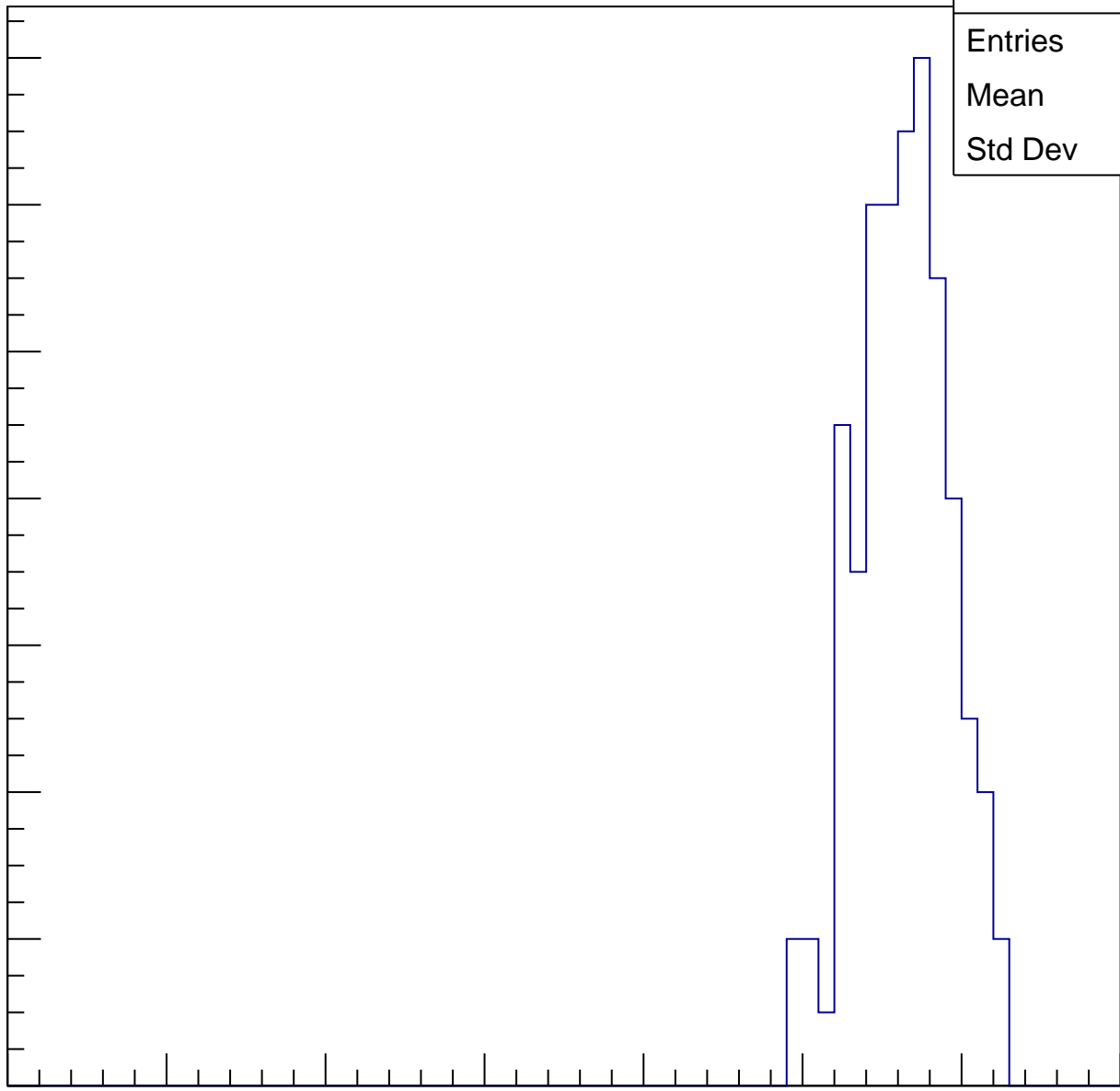
Entries	102
Mean	55.88
Std Dev	2.874

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch4, adc5

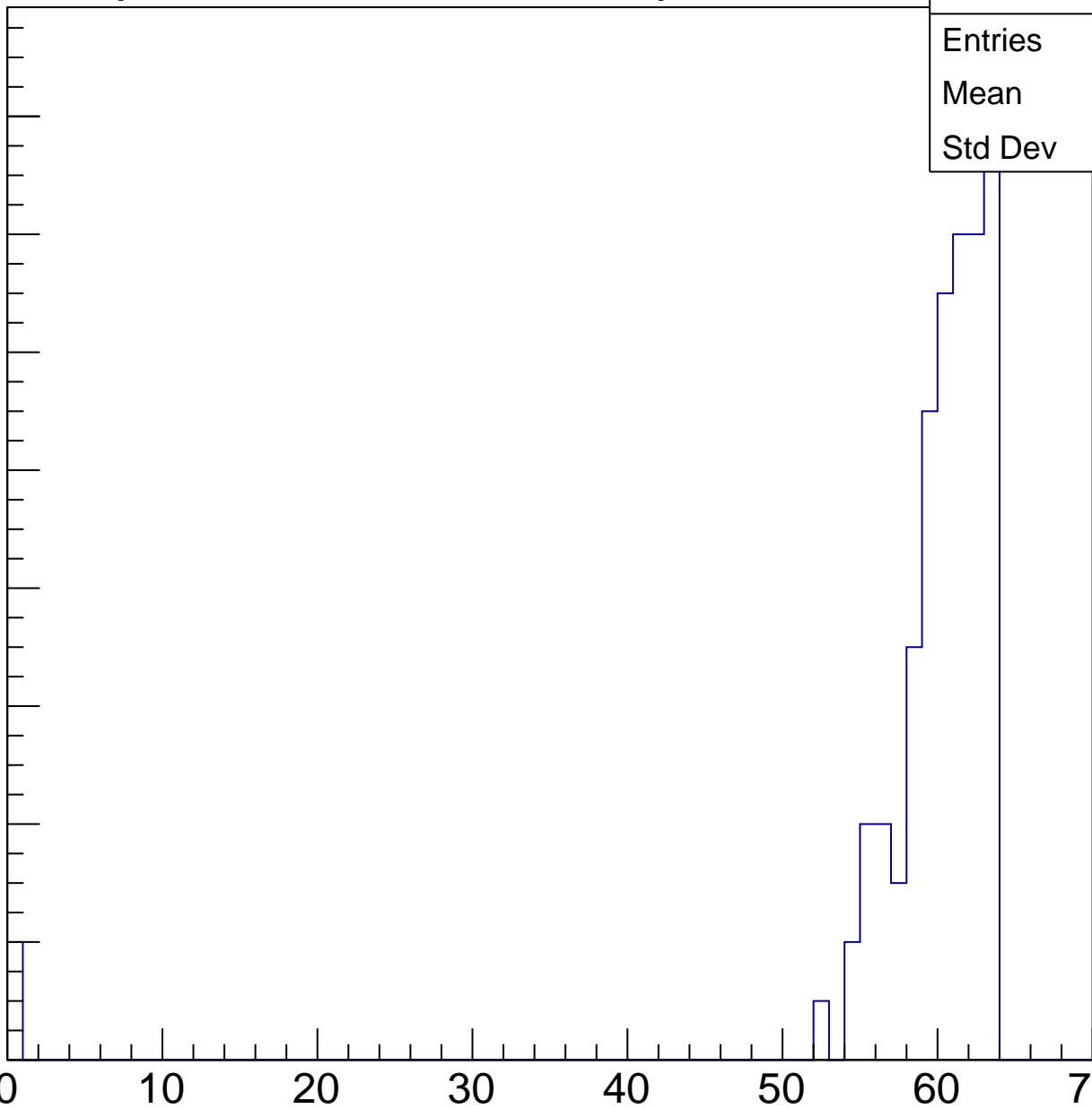
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.73
Std Dev	9.113

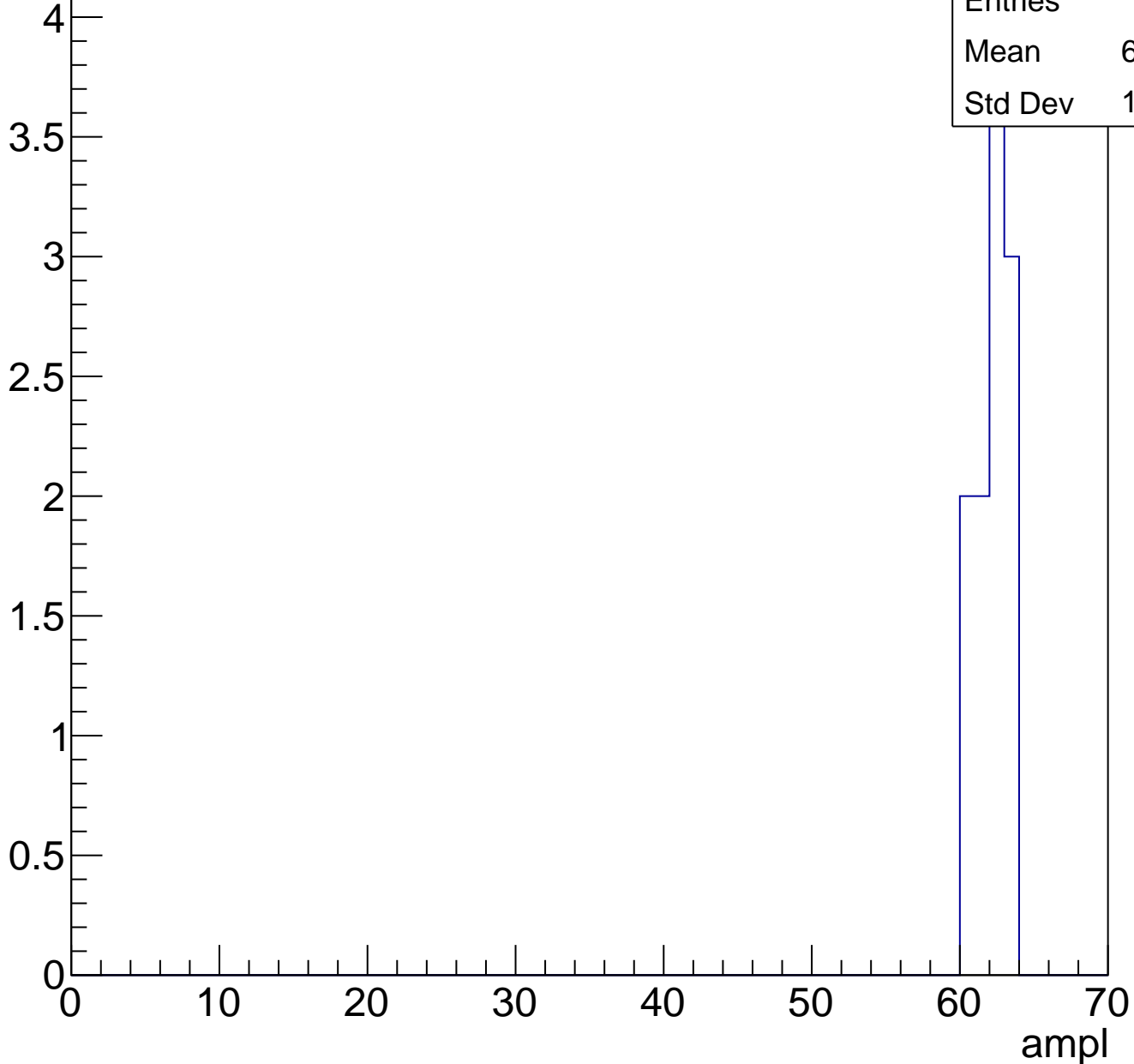
ampl



B0L101S, U15-ch4, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

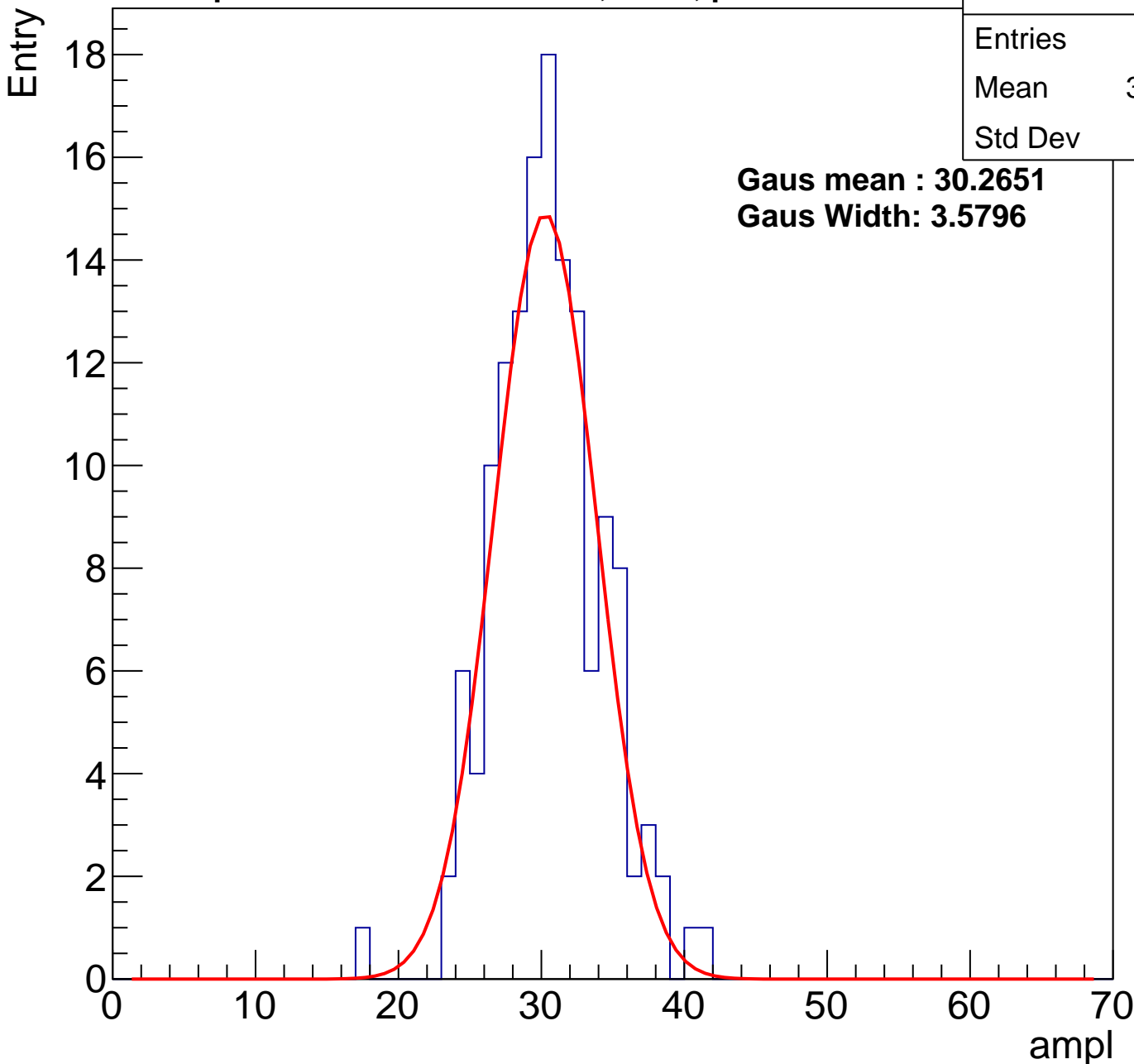
B0L101S, U15-ch5, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	30.02
Std Dev	3.72

Gaus mean : 30.2651

Gaus Width: 3.5796



B0L101S, U15-ch5, adc1

calib_packv5_042523_0143.root, FC#1, port C1

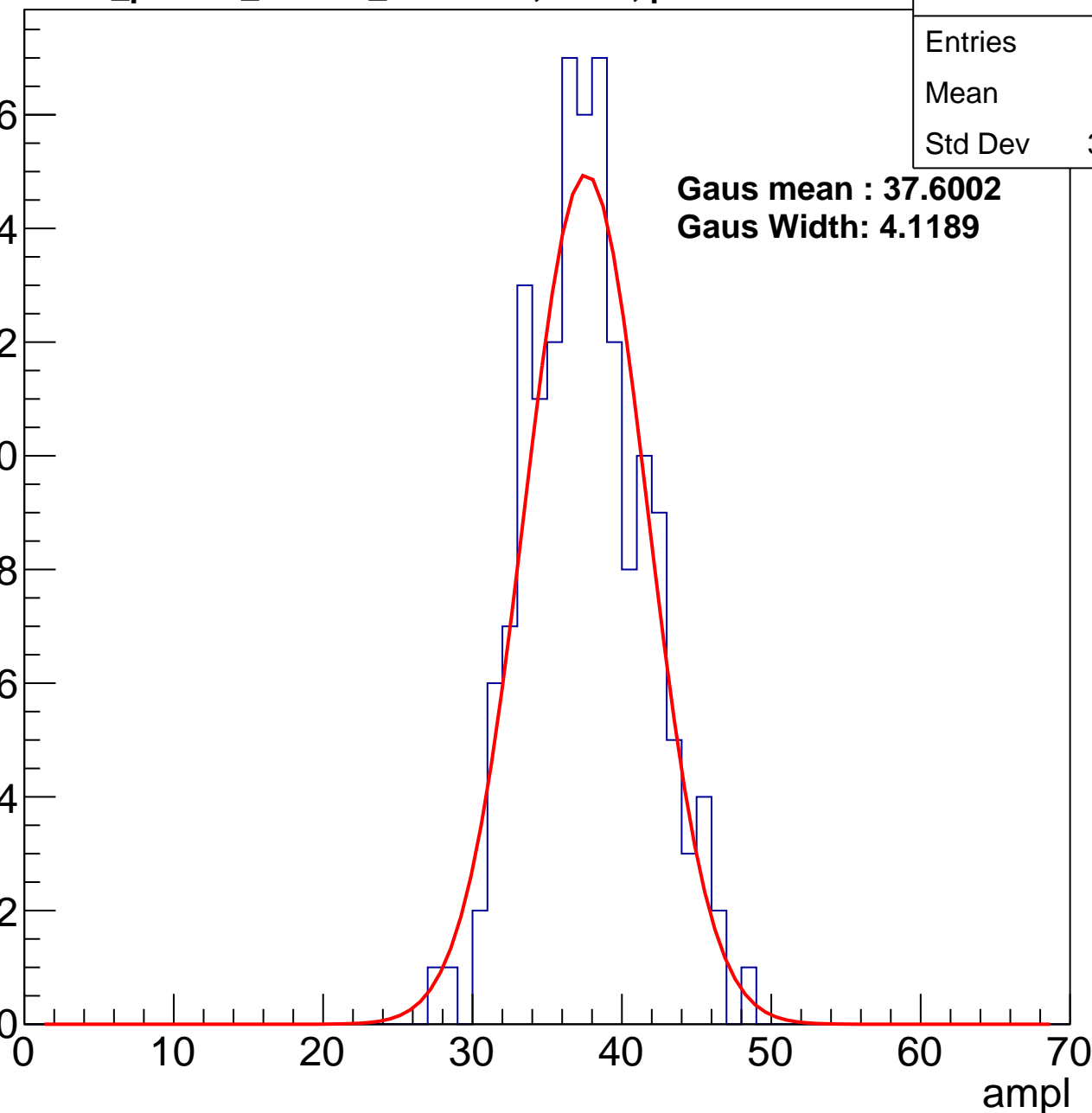
Entry

16
14
12
10
8
6
4
2
0

Entries	157
Mean	37.2
Std Dev	3.929

Gaus mean : 37.6002

Gaus Width: 4.1189



B0L101S, U15-ch5, adc2

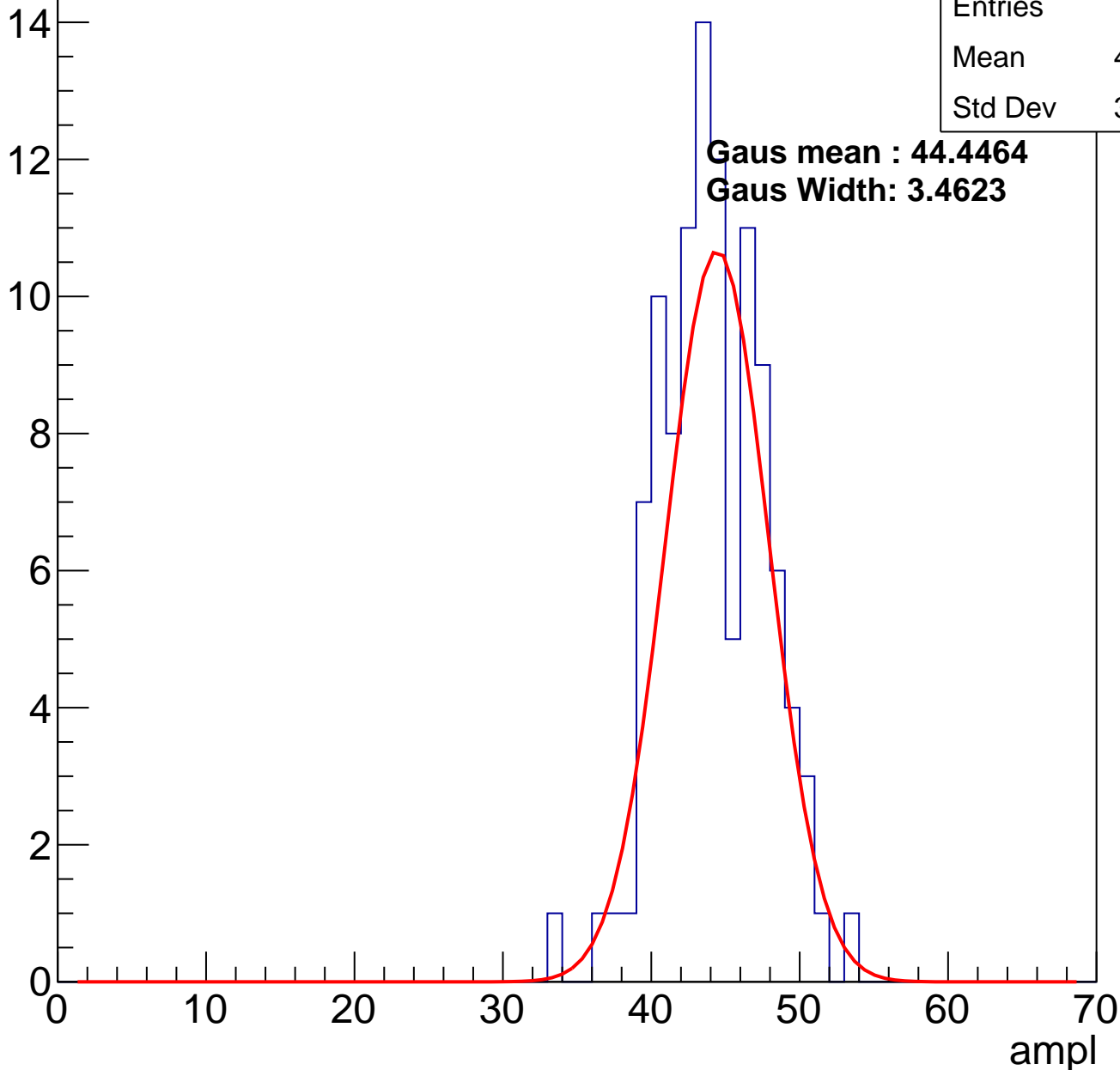
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	43.67
Std Dev	3.485

Gaus mean : 44.4464

Gaus Width: 3.4623

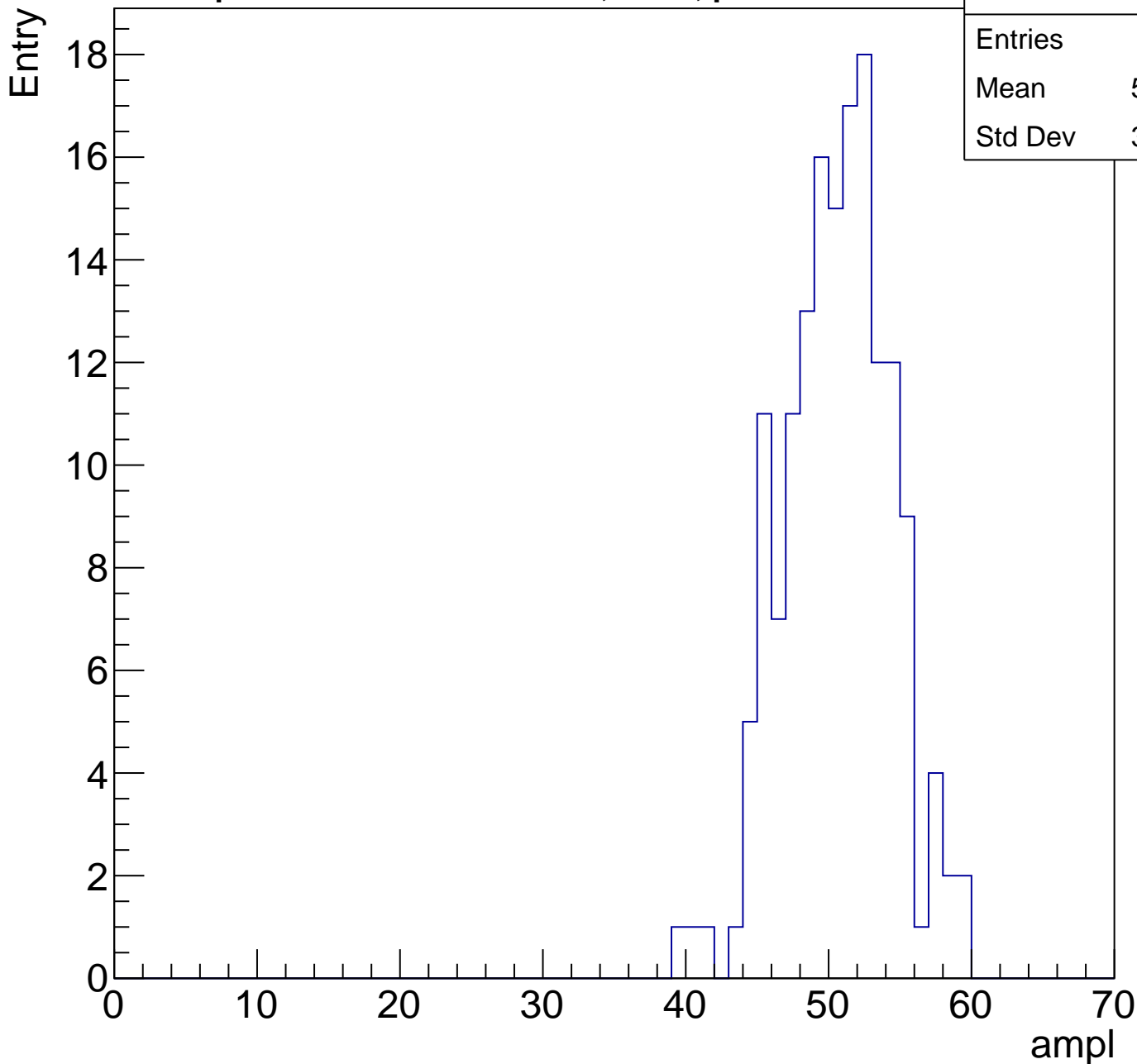
Entry



B0L101S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	50.16
Std Dev	3.734



B0L101S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#1, port C1

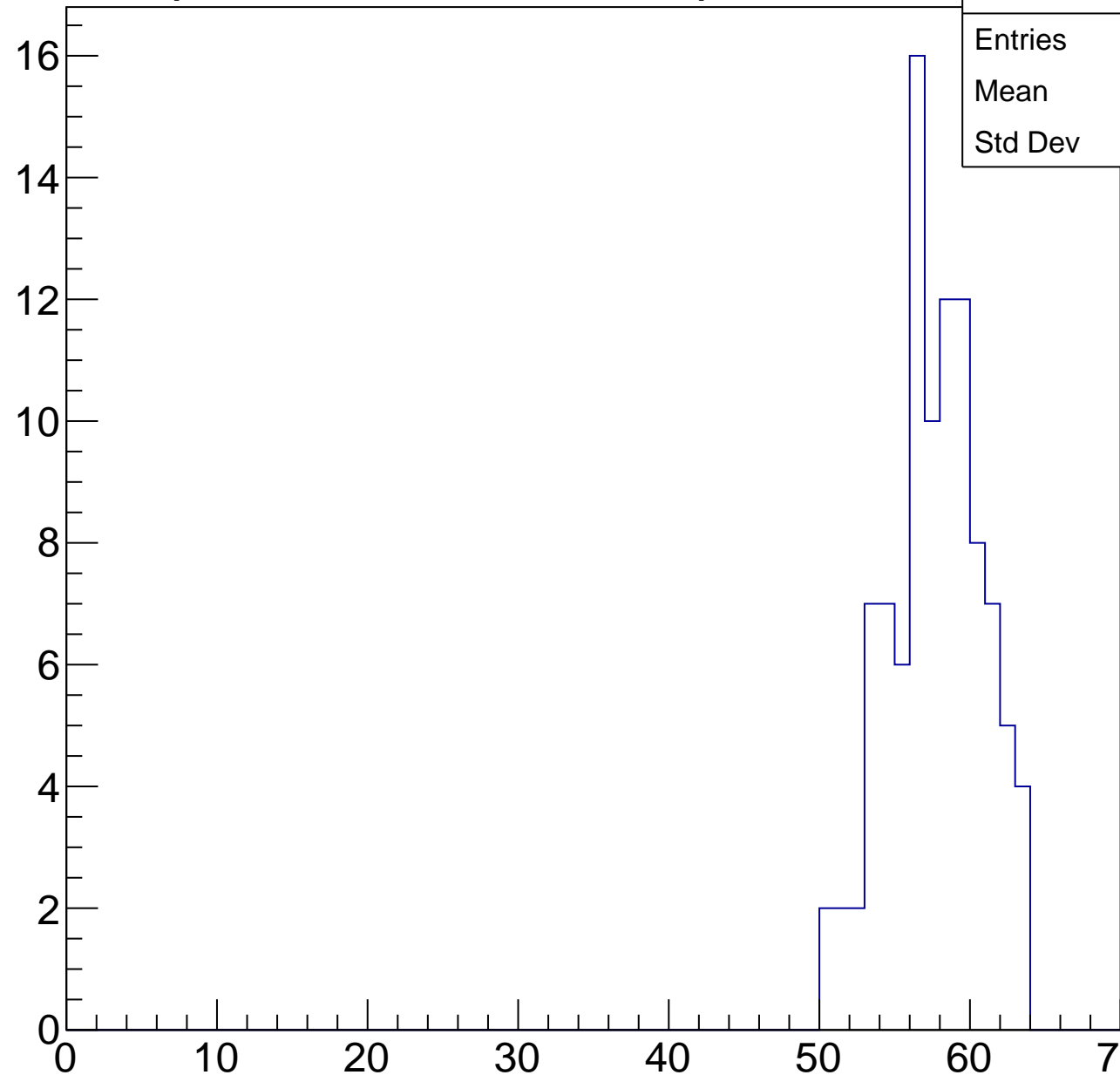
Entries	100
Mean	57.24
Std Dev	3.07

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	76
Mean	58.34
Std Dev	9.904

0

2

4

6

8

10

12

B0L101S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch6, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	27.66
Std Dev	3.419

Gaus mean : 28.1367

Gaus Width: 3.2896

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

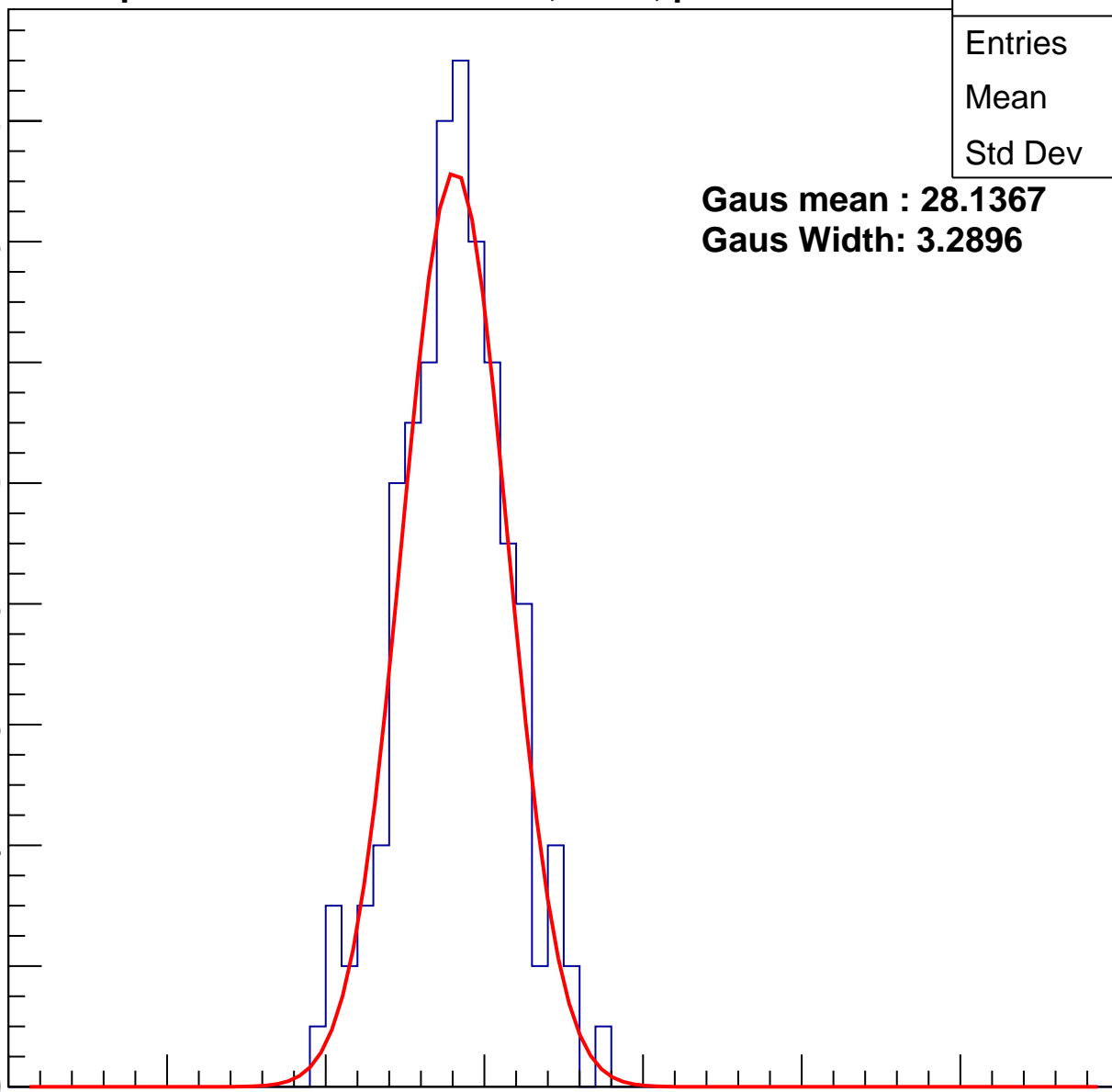
40

50

60

70

ampl



B0L101S, U15-ch6, adc1

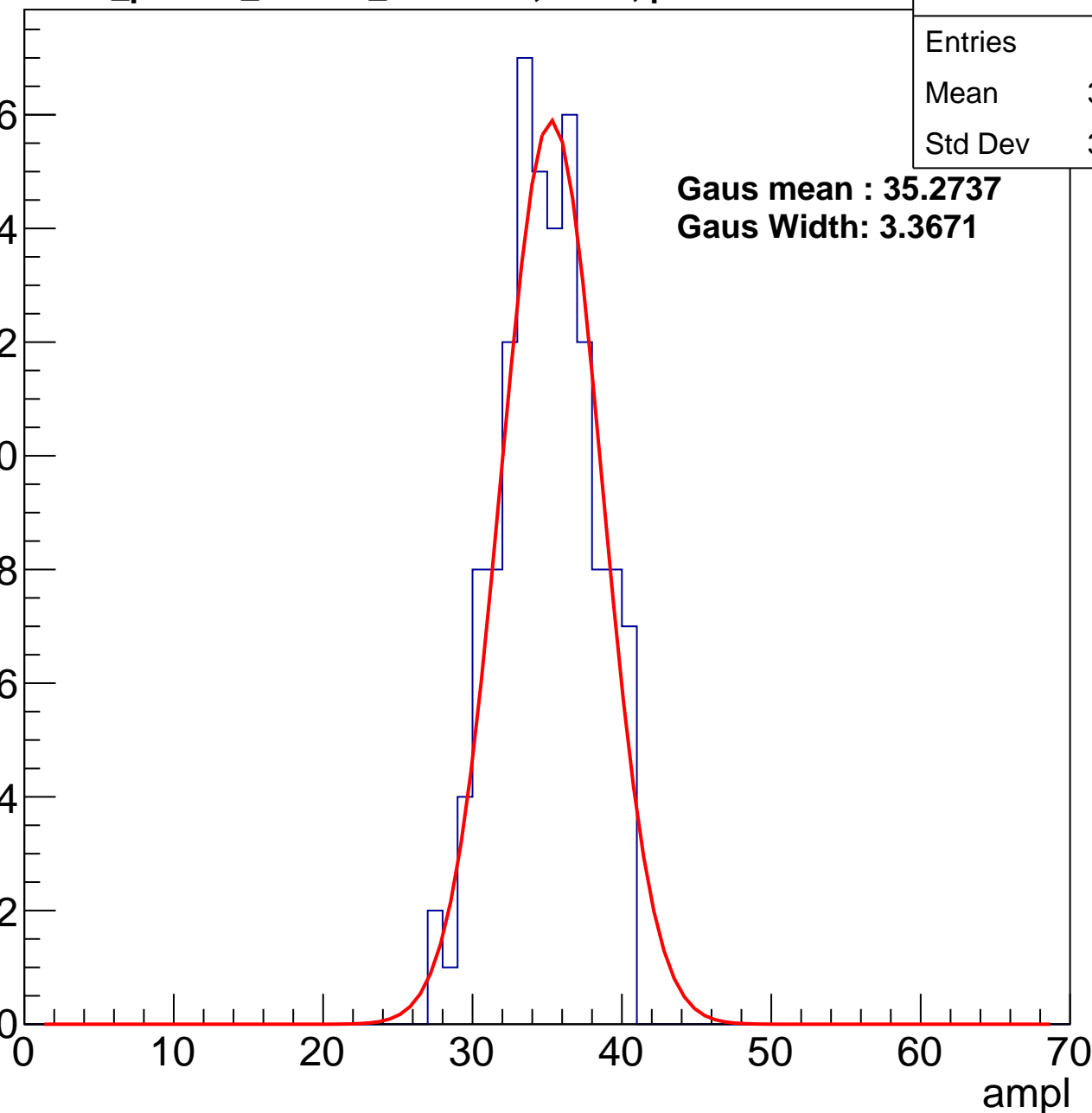
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	34.45
Std Dev	3.063

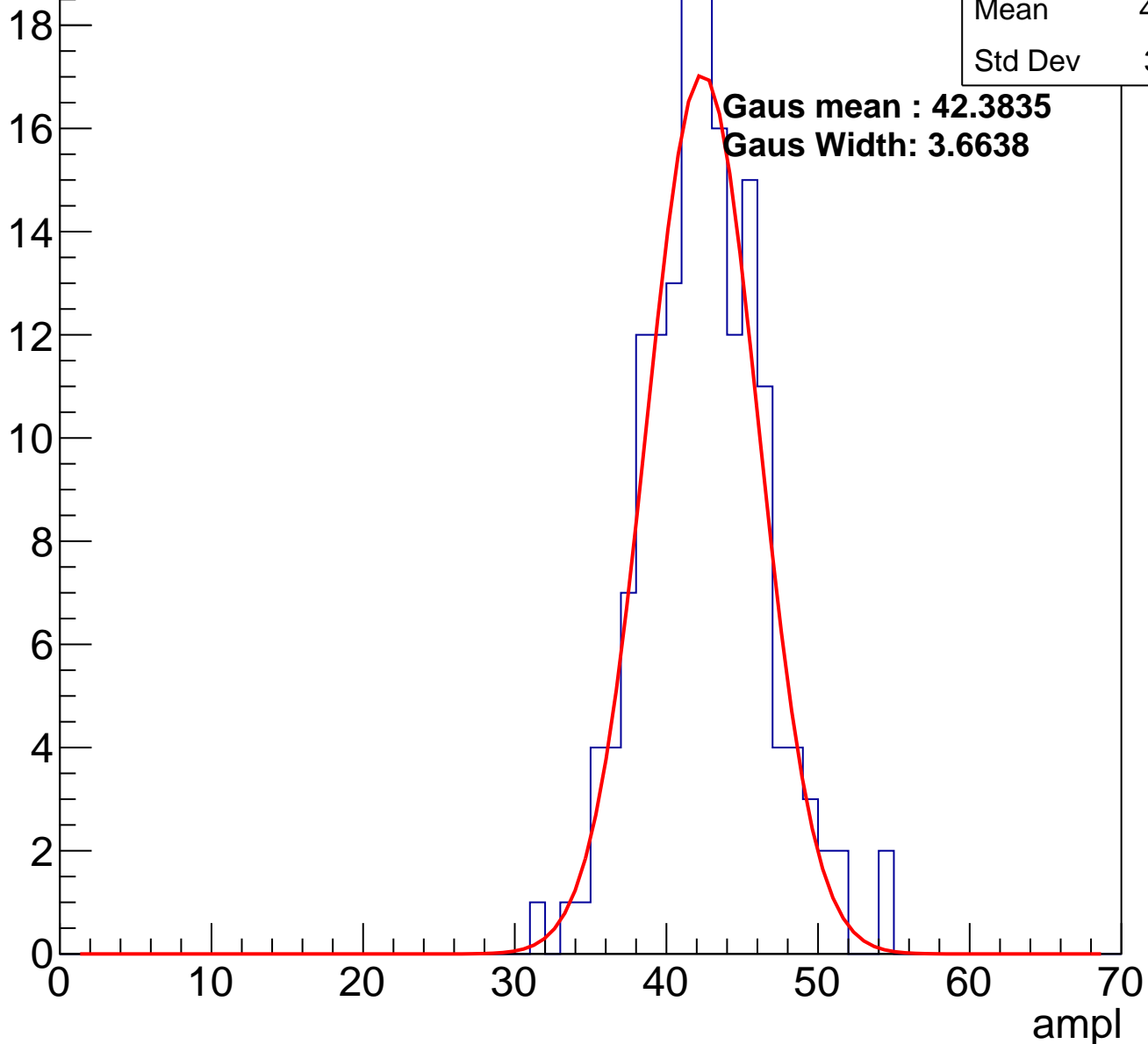
Gaus mean : 35.2737
Gaus Width: 3.3671



B0L101S, U15-ch6, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

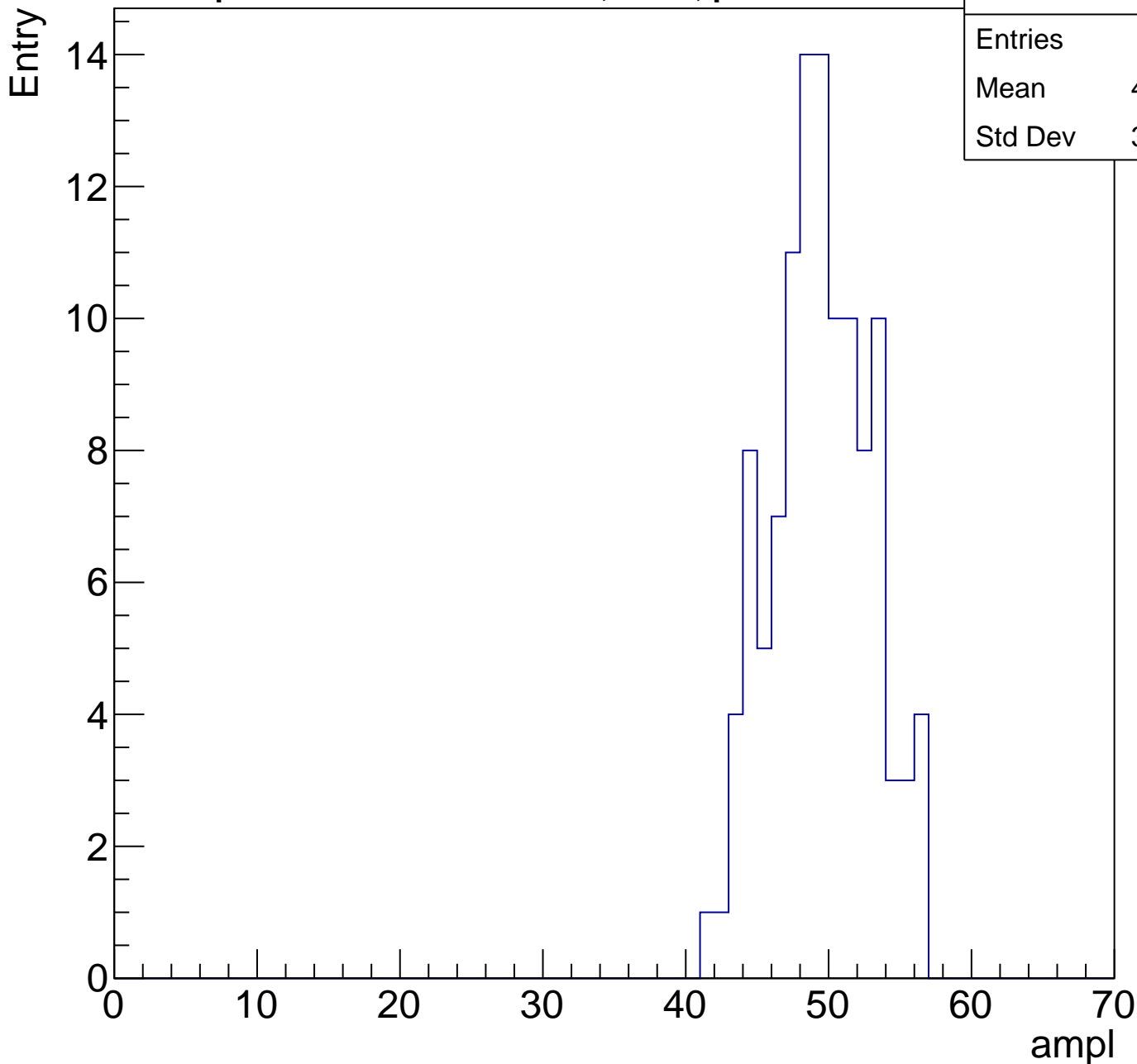


Entries	164
Mean	42.05
Std Dev	3.901

B0L101S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	48.99
Std Dev	3.417



B0L101S, U15-ch6, adc4

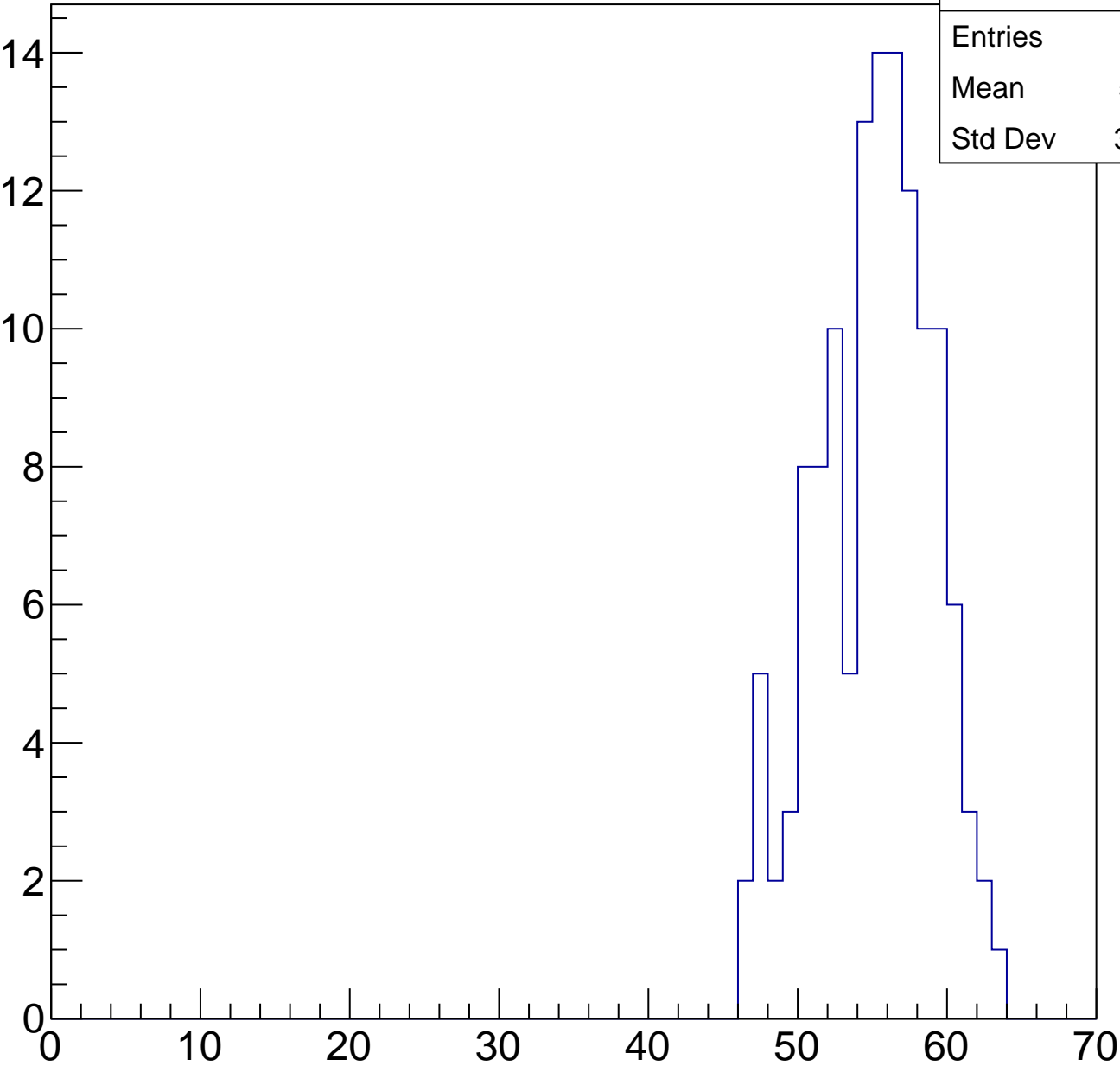
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	128
Mean	54.71
Std Dev	3.812

ampl



B0L101S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

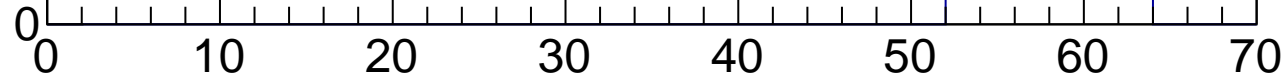
50

60

70

ampl

Entries	77
Mean	59.01
Std Dev	3.106



B0L101S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	35
Mean	57.89
Std Dev	14.3

B0L101S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#1, port C1

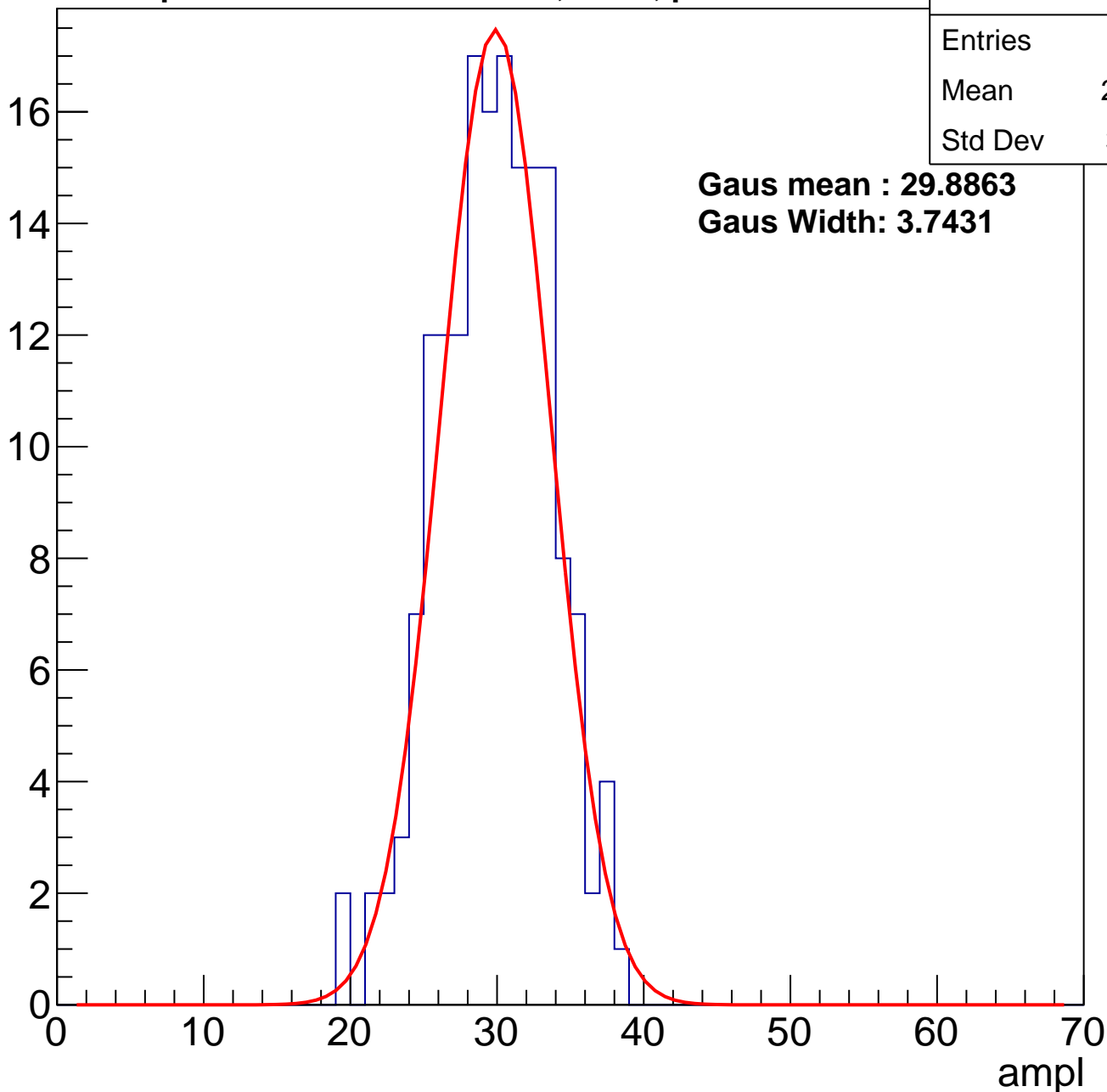
Entry



B0L101S, U15-ch7, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	169
Mean	29.36
Std Dev	3.761

B0L101S, U15-ch7, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	37.29
Std Dev	3.469

Gaus mean : 37.6047

Gaus Width: 3.3564

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

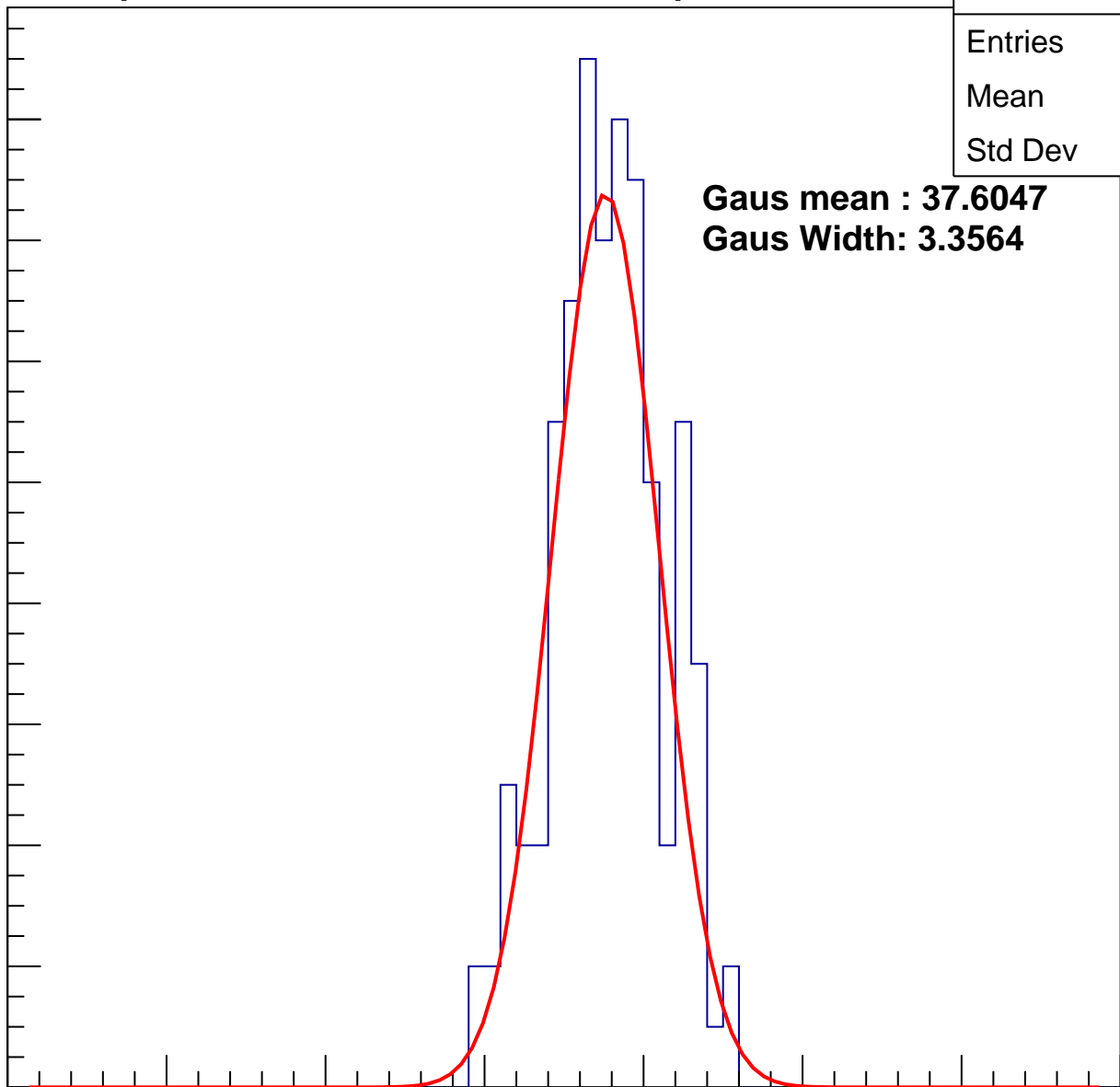
40

50

60

70

ampl



B0L101S, U15-ch7, adc2

calib_packv5_042523_0143.root, FC#1, port C1

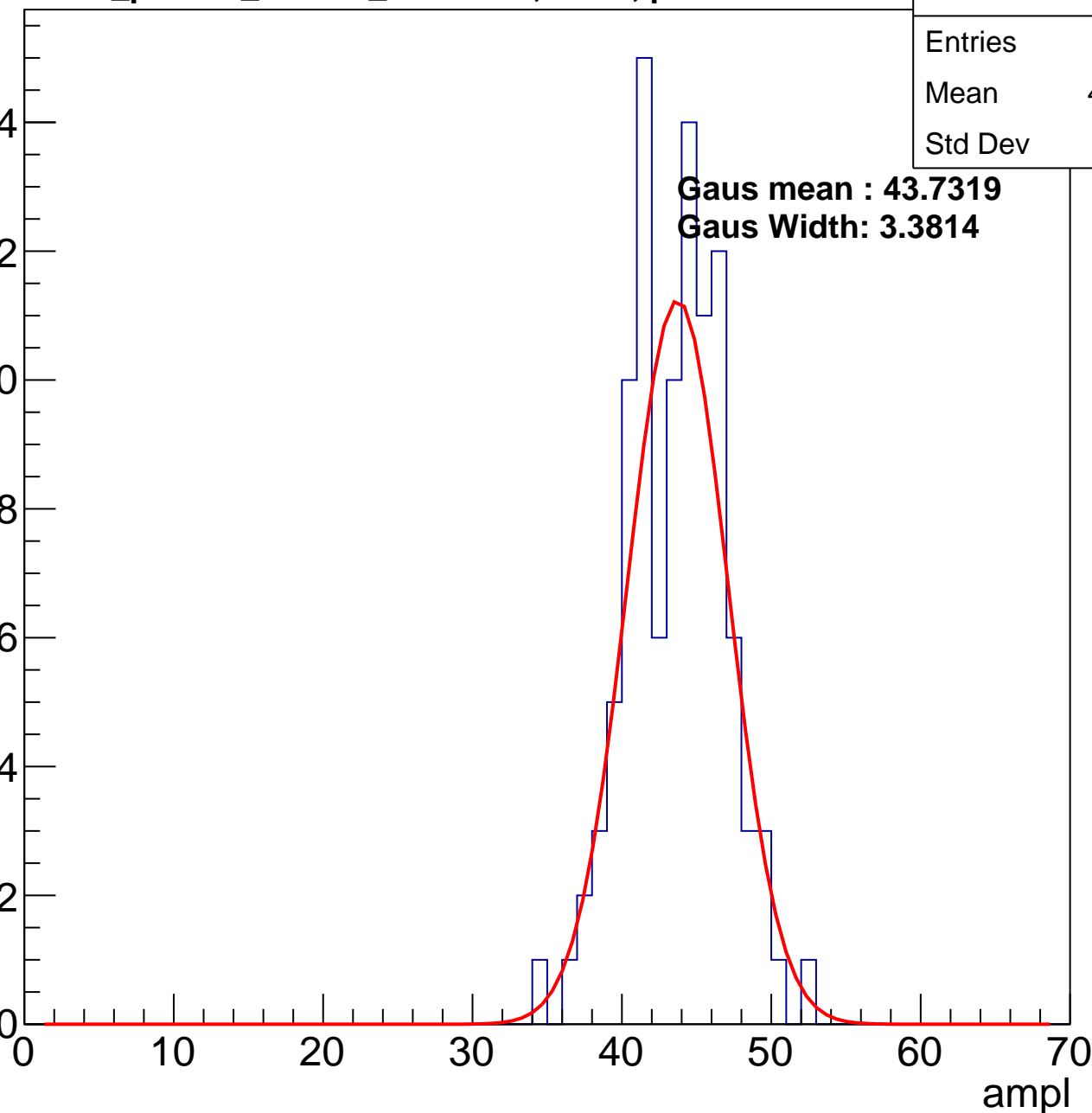
Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	43.15
Std Dev	3.231

Gaus mean : 43.7319

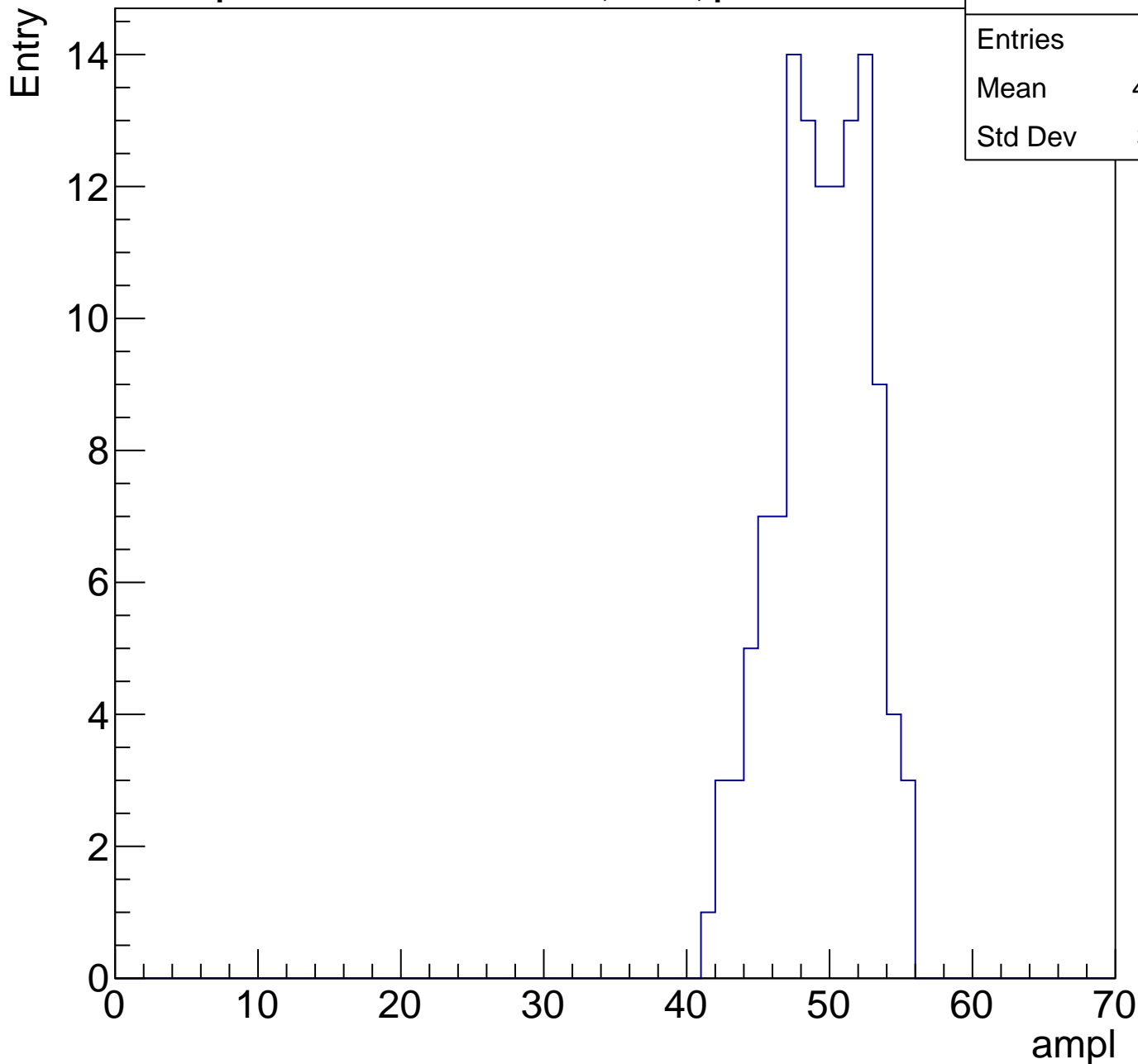
Gaus Width: 3.3814



B0L101S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#1, port C1

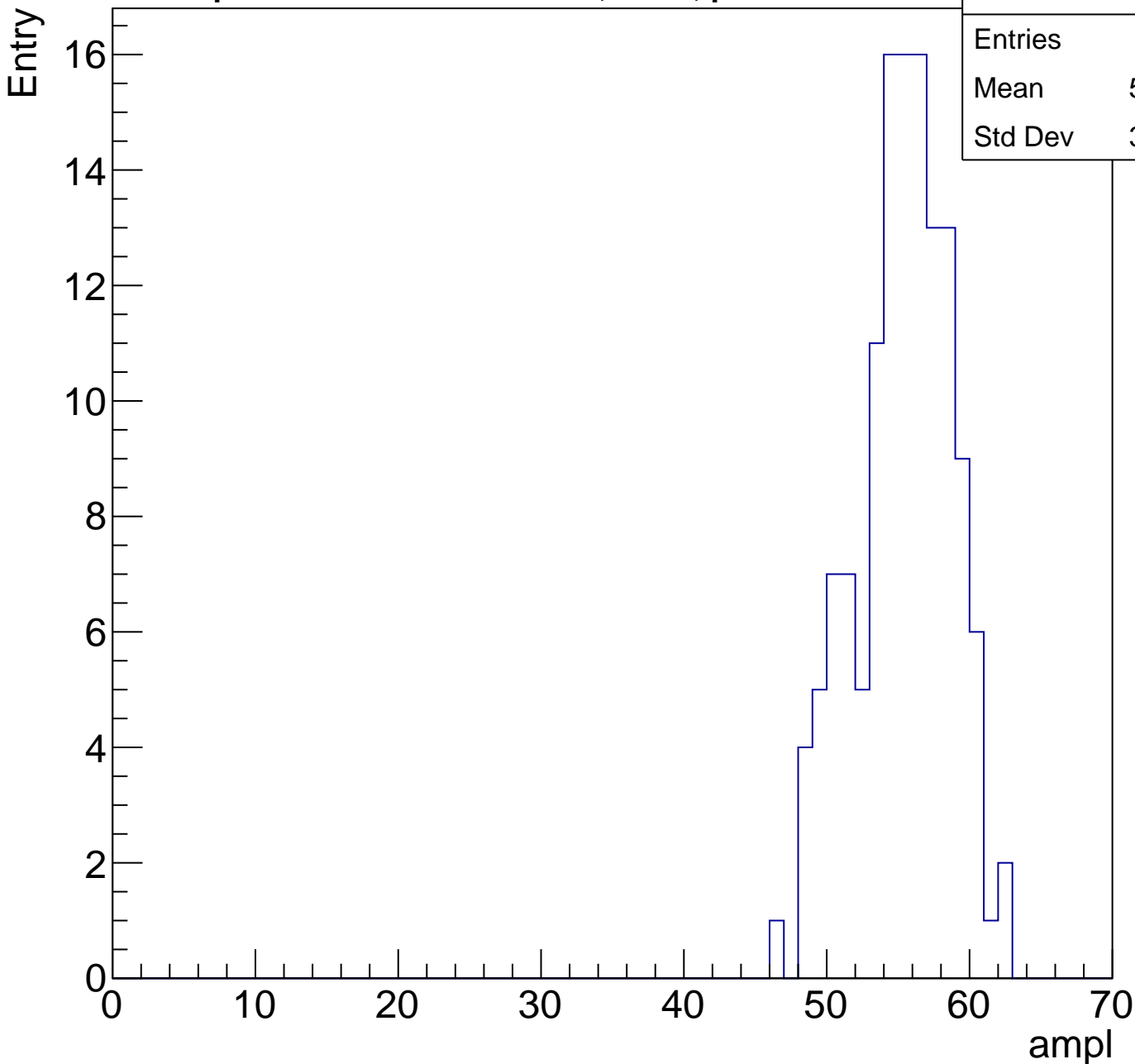
Entries	120
Mean	48.93
Std Dev	3.201



B0L101S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	54.88
Std Dev	3.326



B0L101S, U15-ch7, adc5

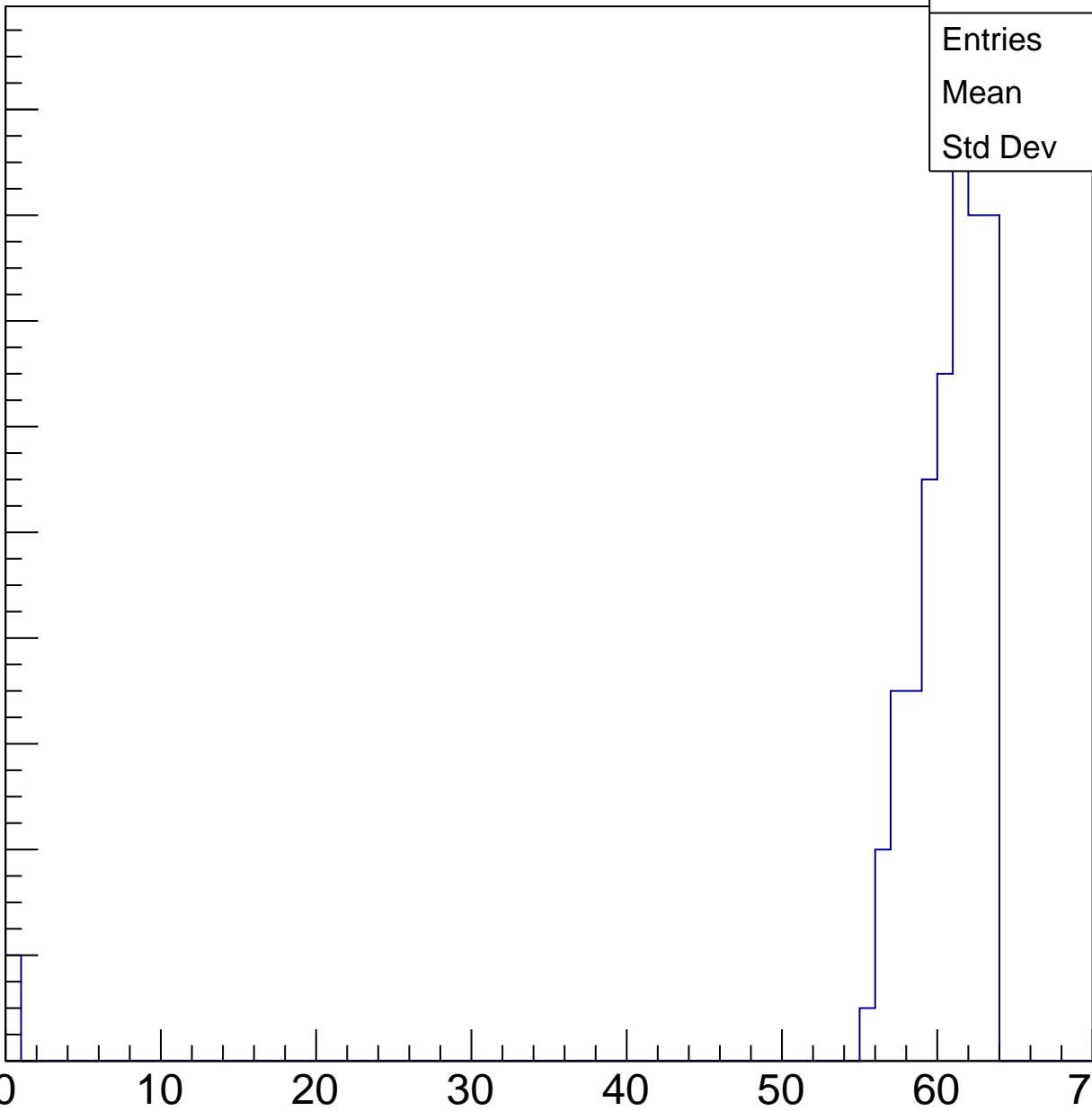
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.08
Std Dev	8.861

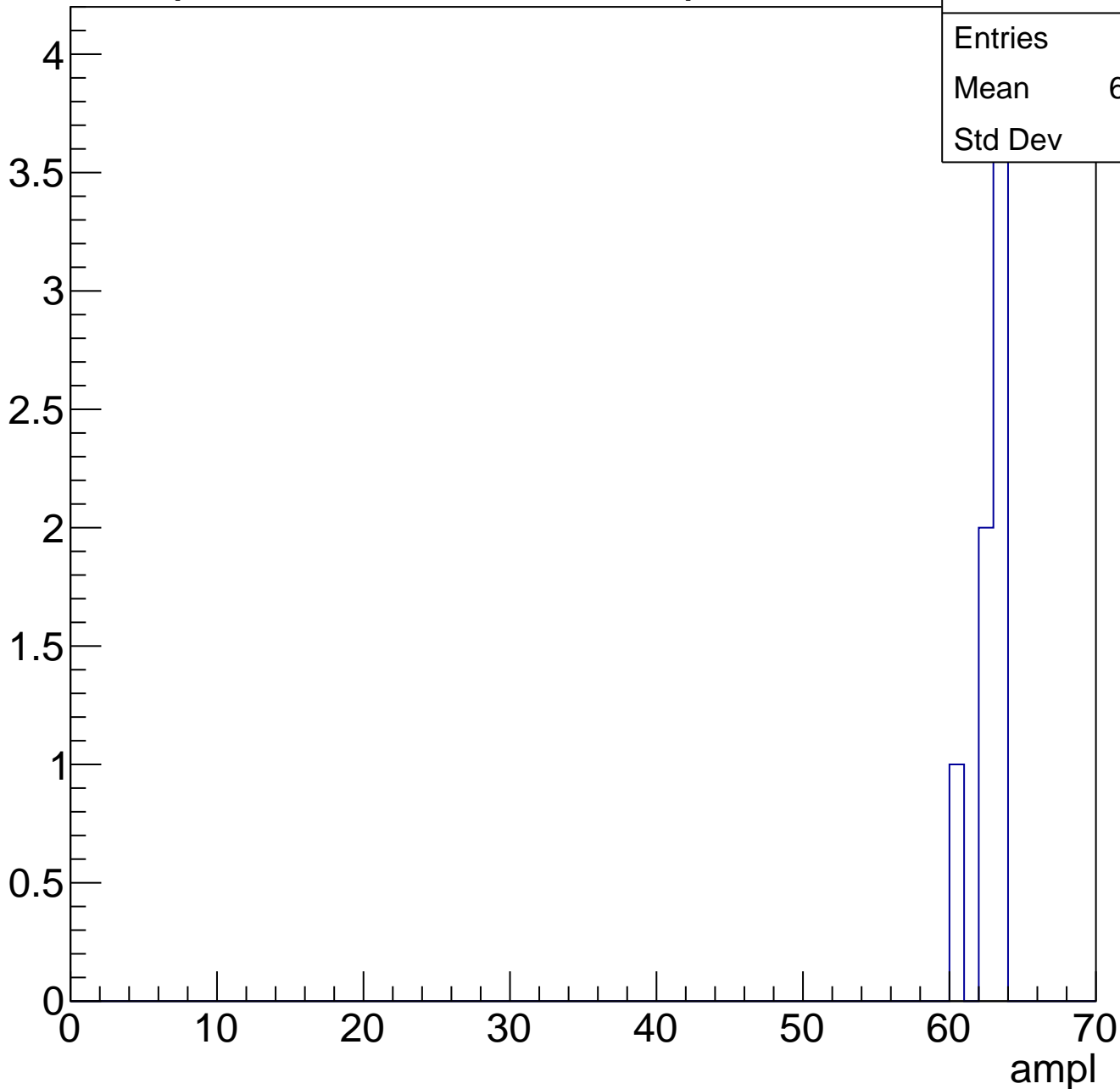
ampl



B0L101S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch8, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

146

Mean

29.84

Std Dev

3.737

Gaus mean : 30.5989

Gaus Width: 3.8800

0

10

20

30

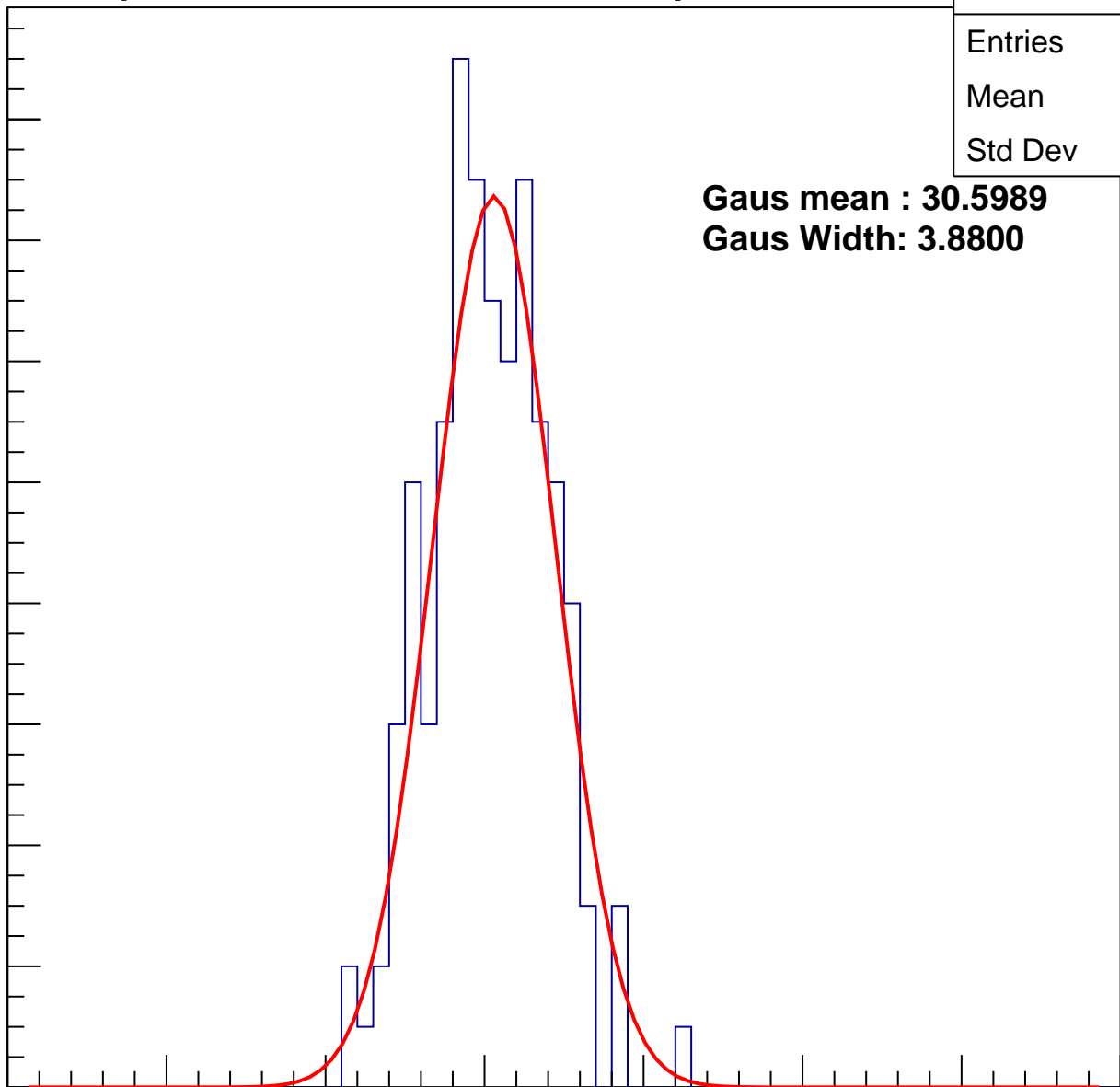
40

50

60

70

ampl



B0L101S, U15-ch8, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

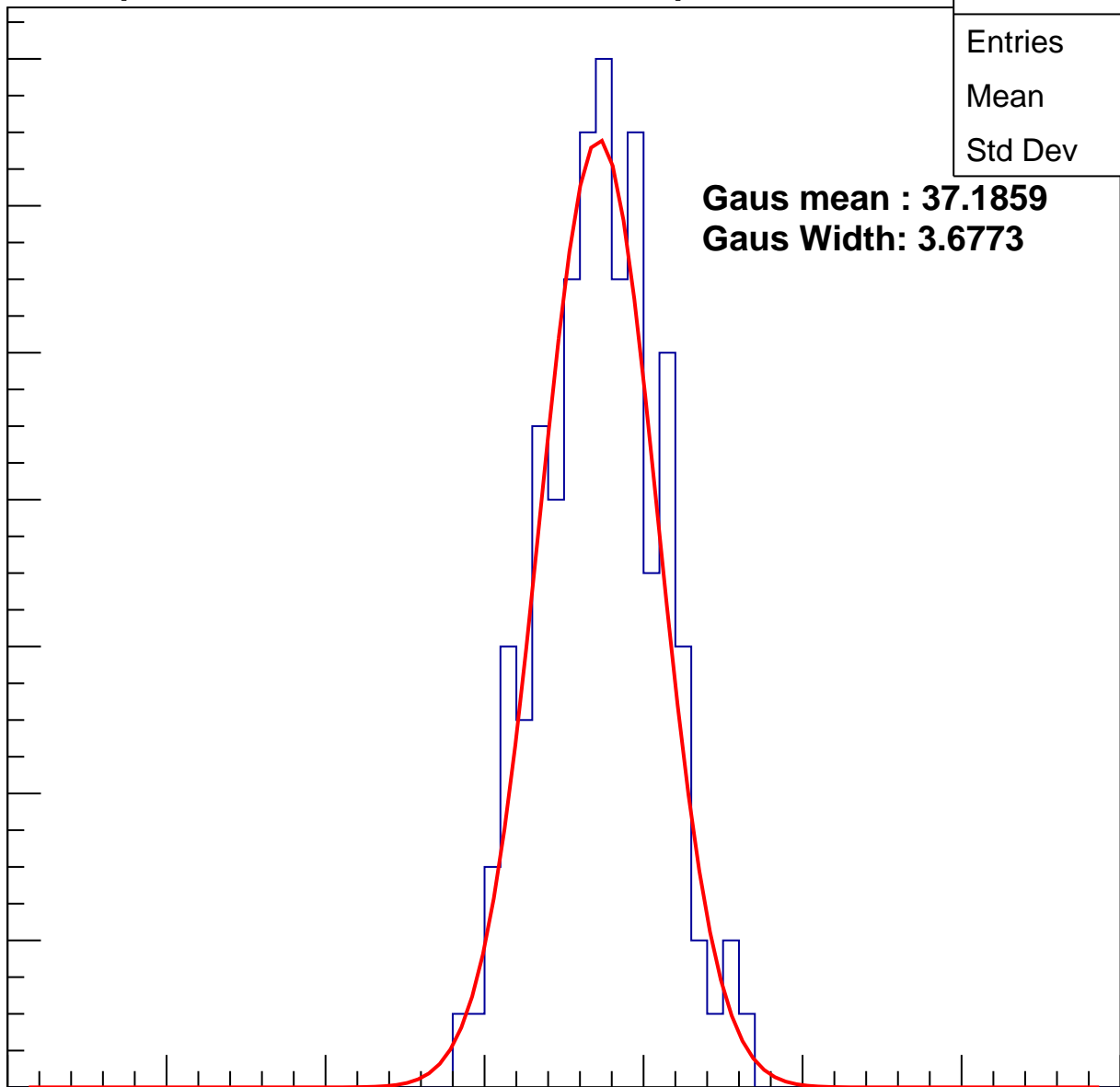
14
12
10
8
6
4
2
0

Entries	124
Mean	36.82
Std Dev	3.668

Gaus mean : 37.1859
Gaus Width: 3.6773

0 10 20 30 40 50 60 70

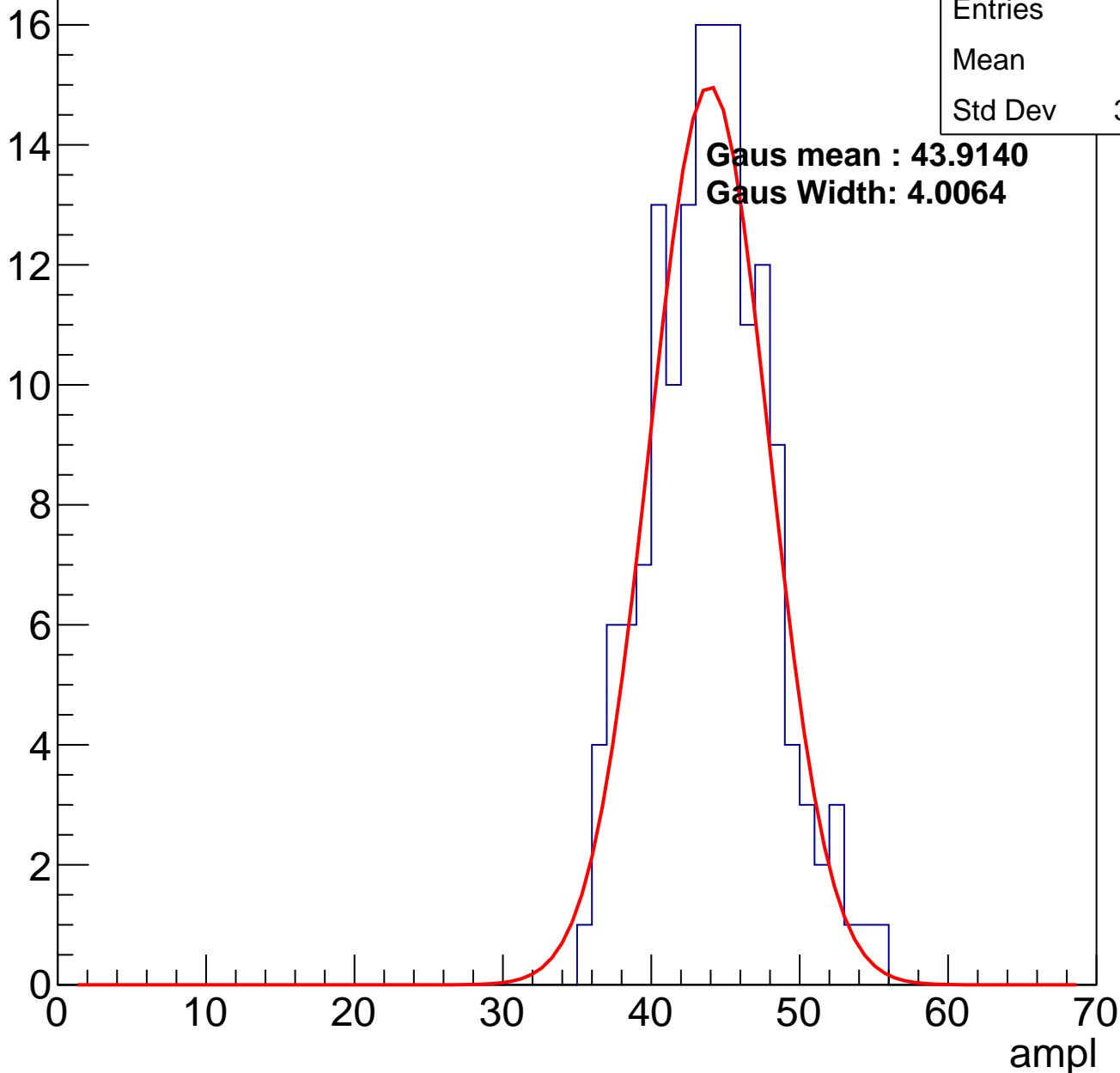
ampl



B0L101S, U15-ch8, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	49.79
Std Dev	2.985

Entry

12

10

8

6

4

2

0

0

10

20

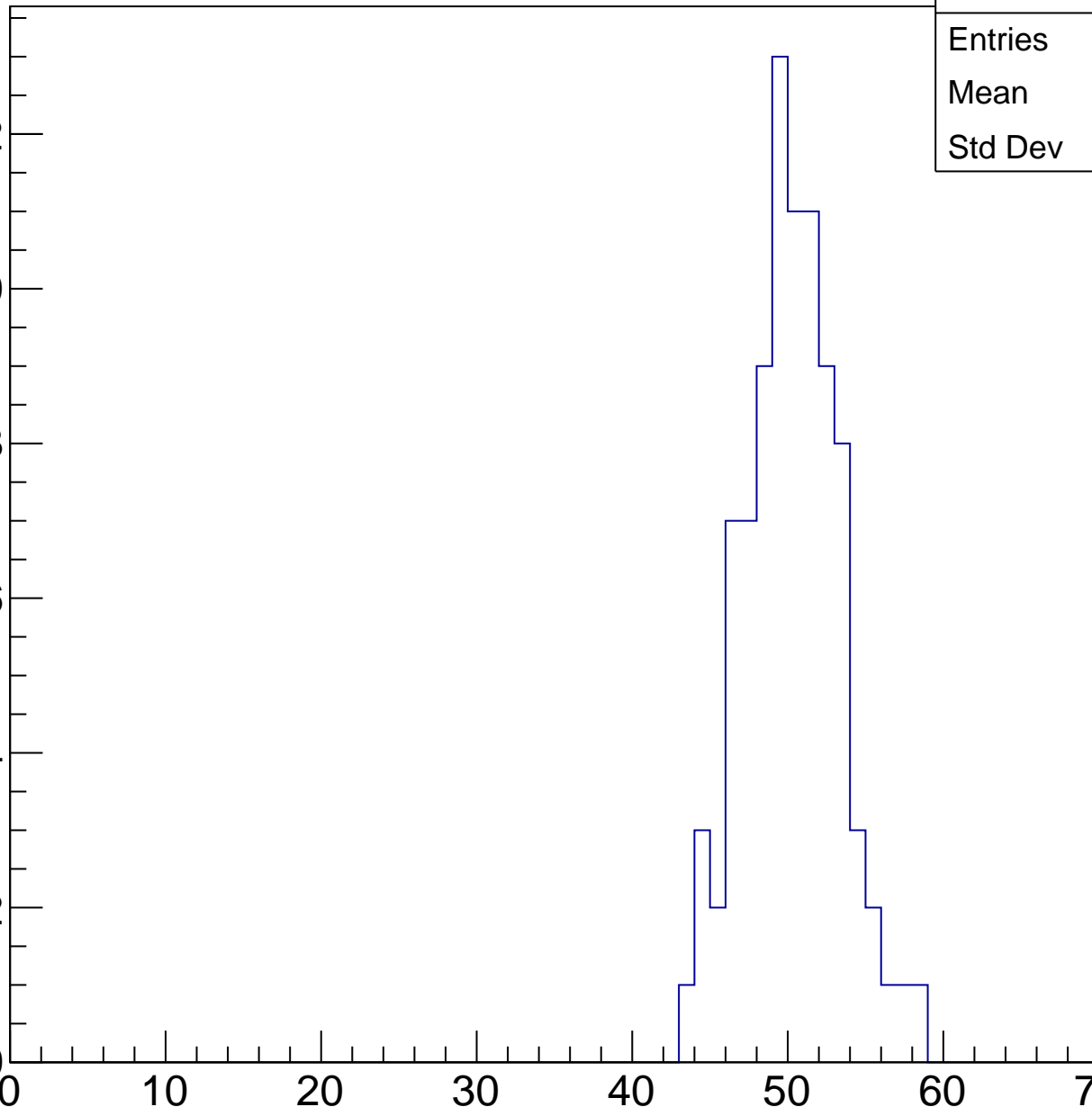
30

40

50

60

ampl



B0L101S, U15-ch8, adc4

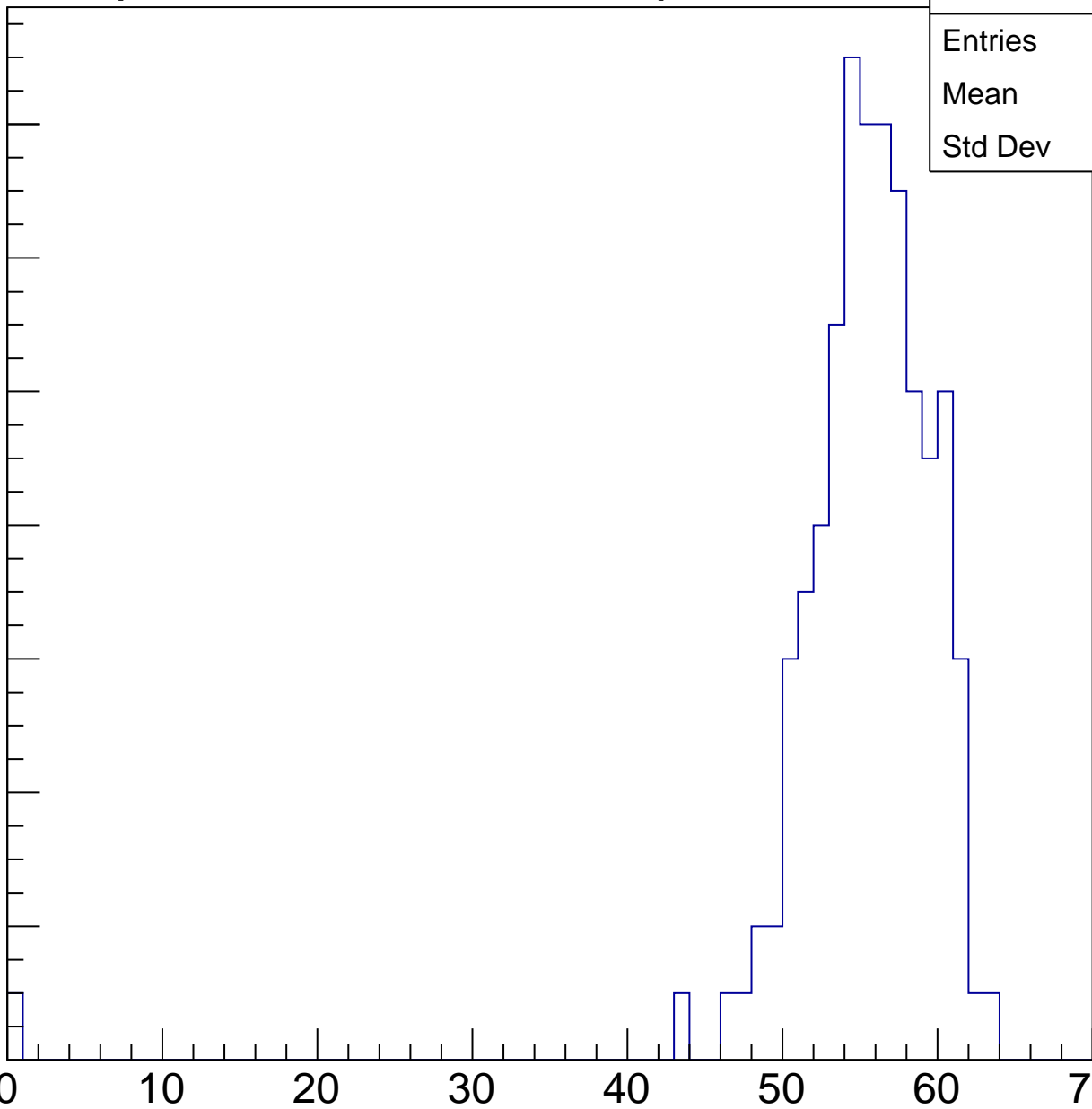
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	133
Mean	54.83
Std Dev	5.978

ampl



B0L101S, U15-ch8, adc5

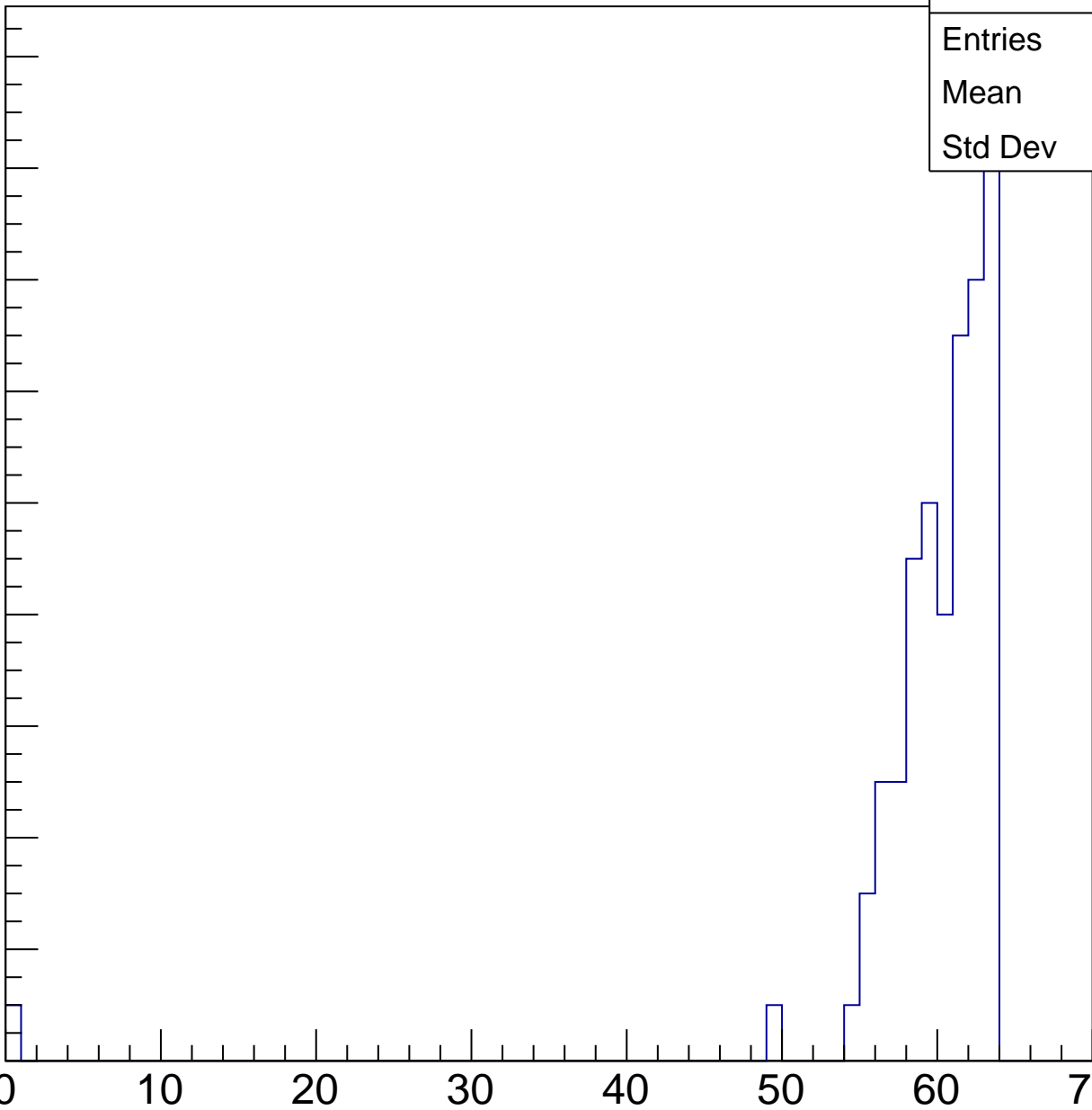
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.32
Std Dev	6.903

ampl

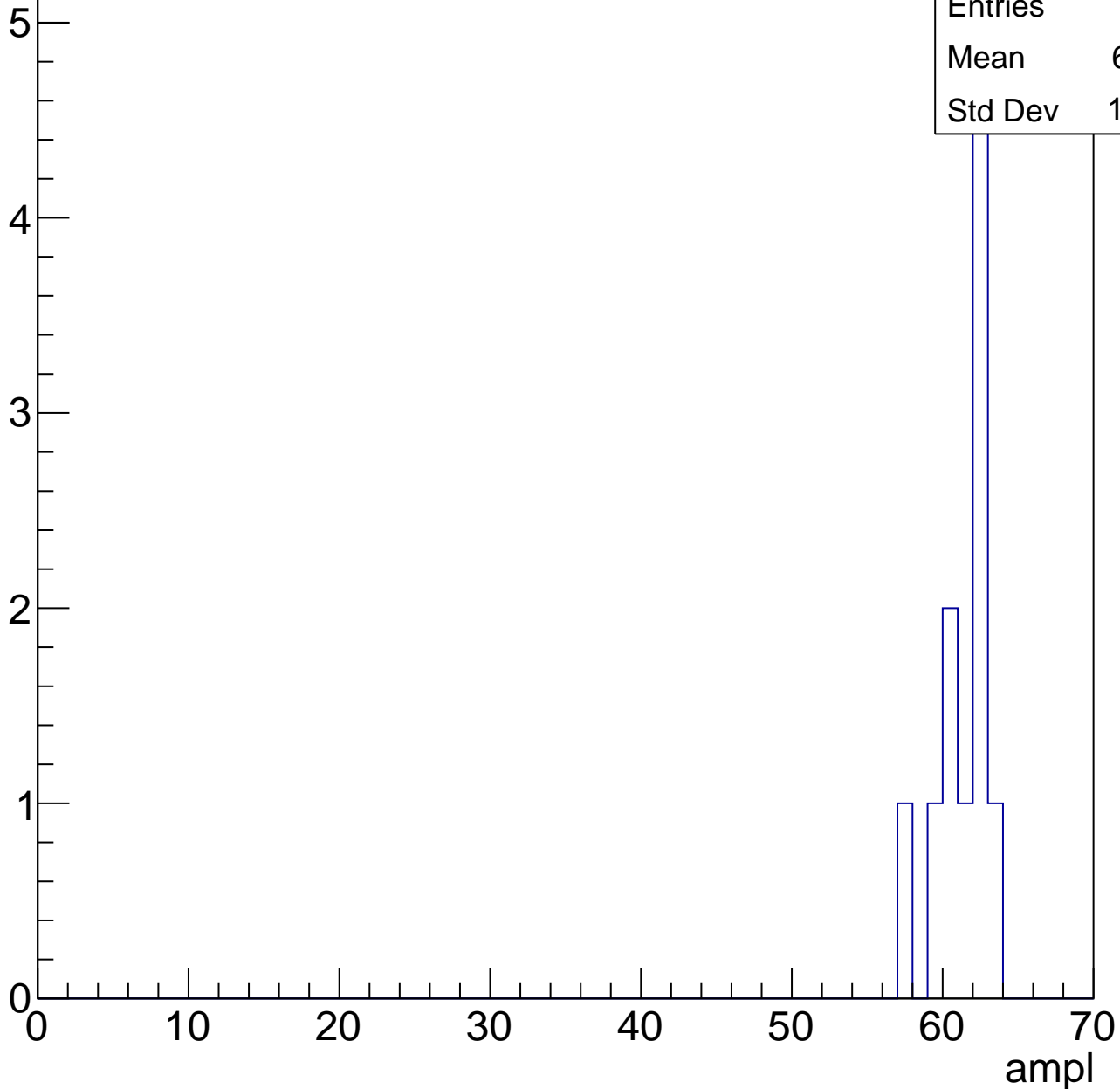


B0L101S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	11
Mean	60.91
Std Dev	1.676



B0L101S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch9, adc0

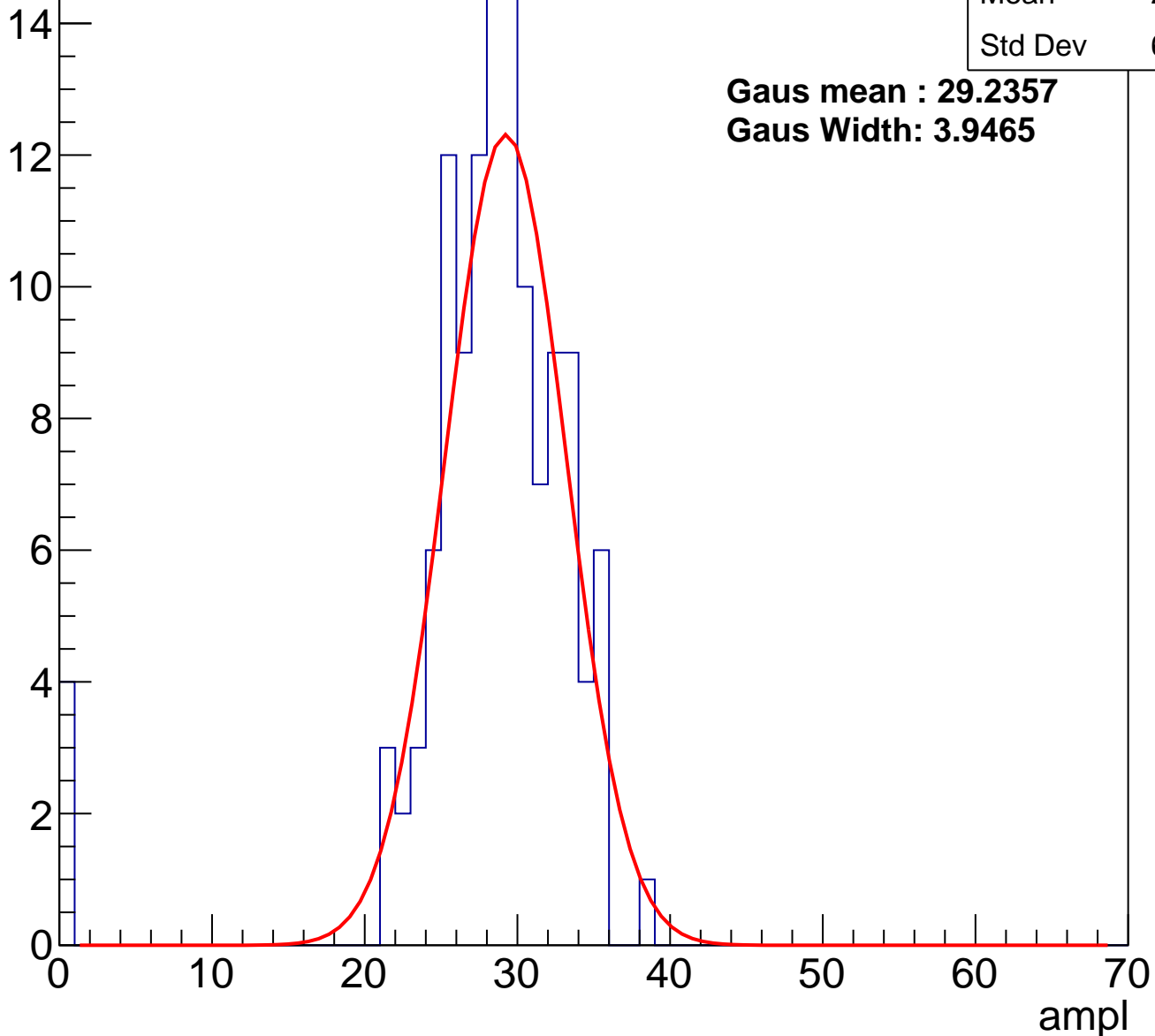
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	27.71
Std Dev	6.081

Gaus mean : 29.2357

Gaus Width: 3.9465

Entry



B0L101S, U15-ch9, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	34.82
Std Dev	3.782

Gaus mean : 35.3114

Gaus Width: 3.7138

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

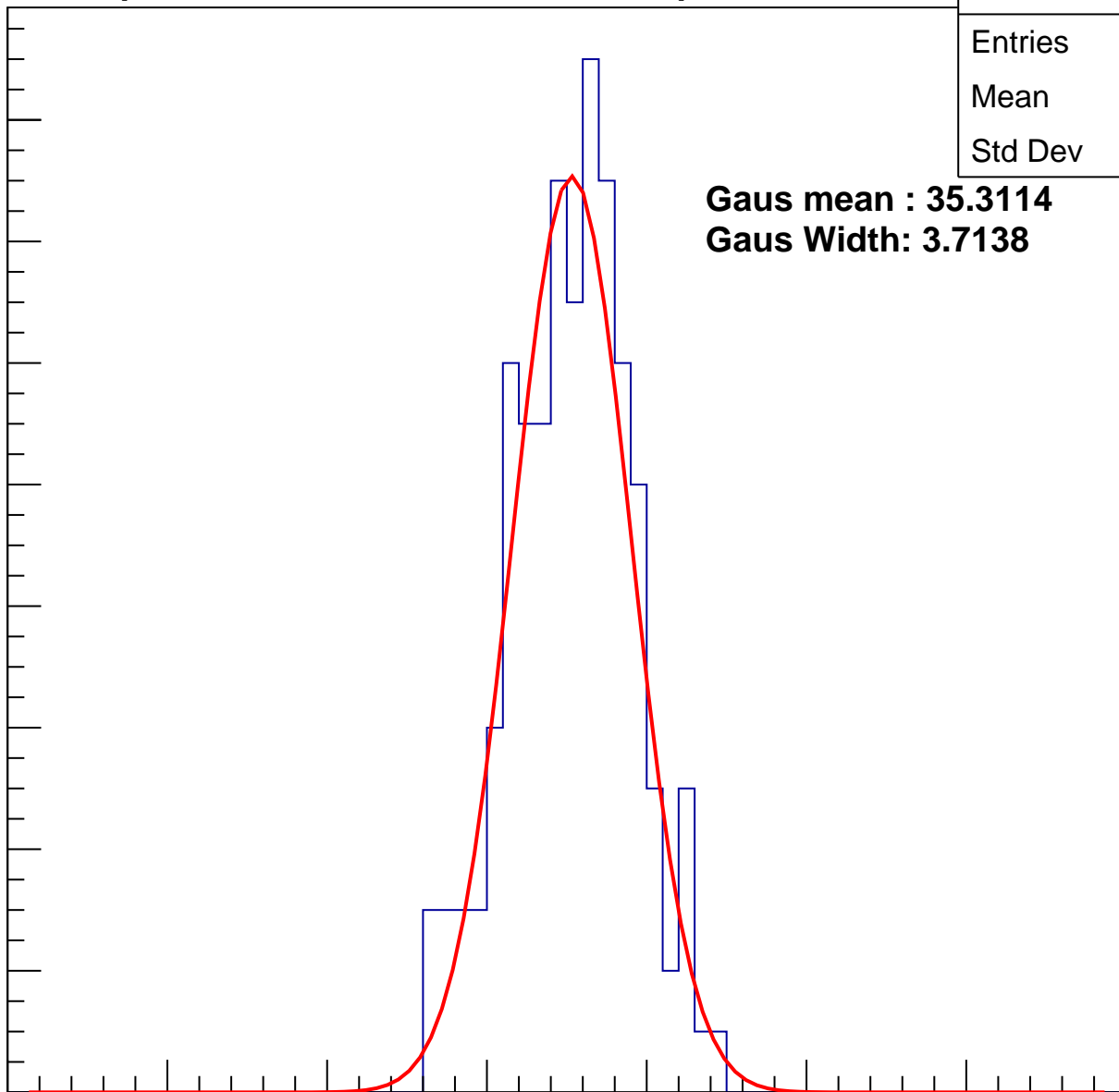
40

50

60

70

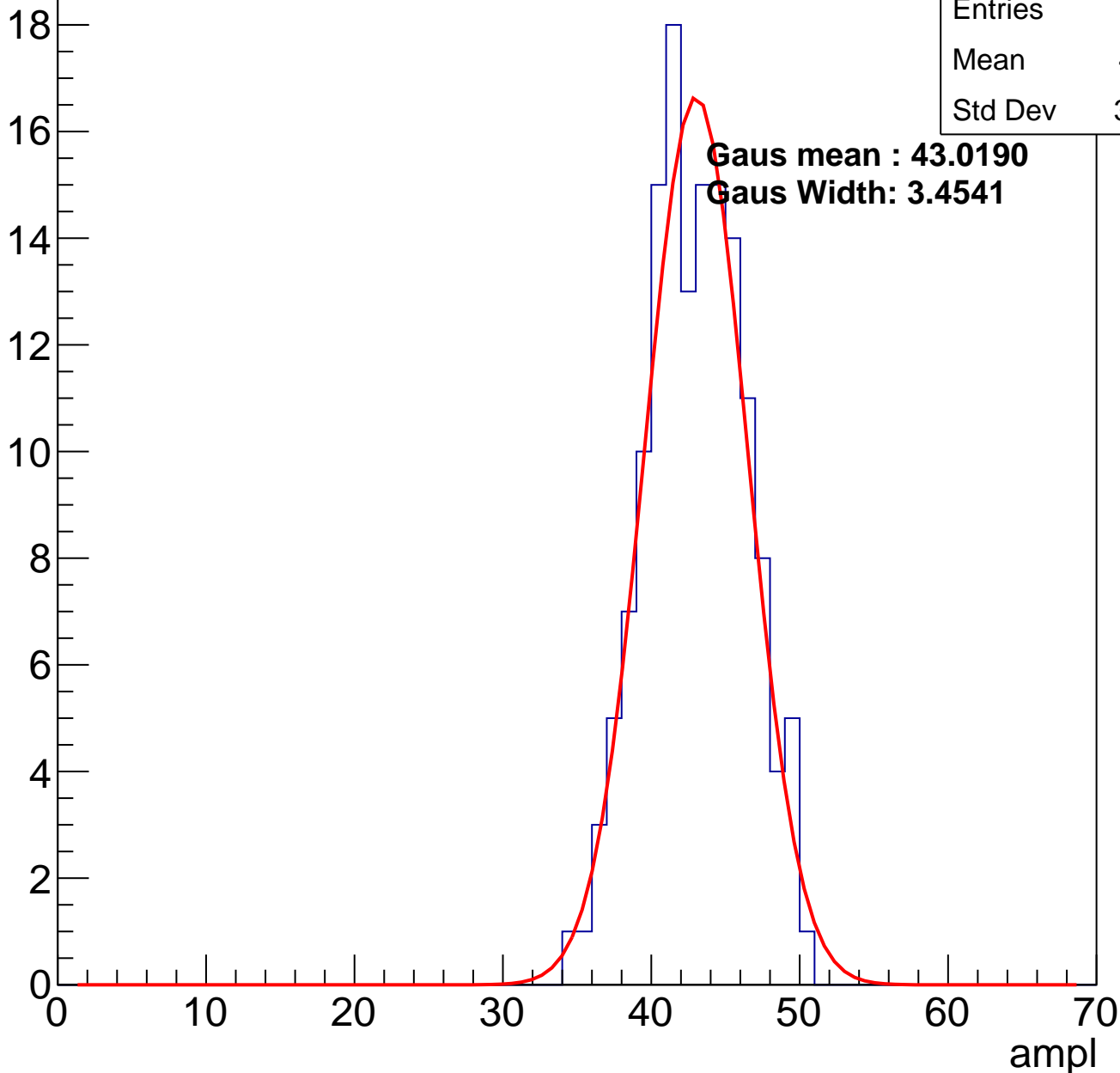
ampl



B0L101S, U15-ch9, adc2

calib_packv5_042523_0143.root, FC#1, port C1

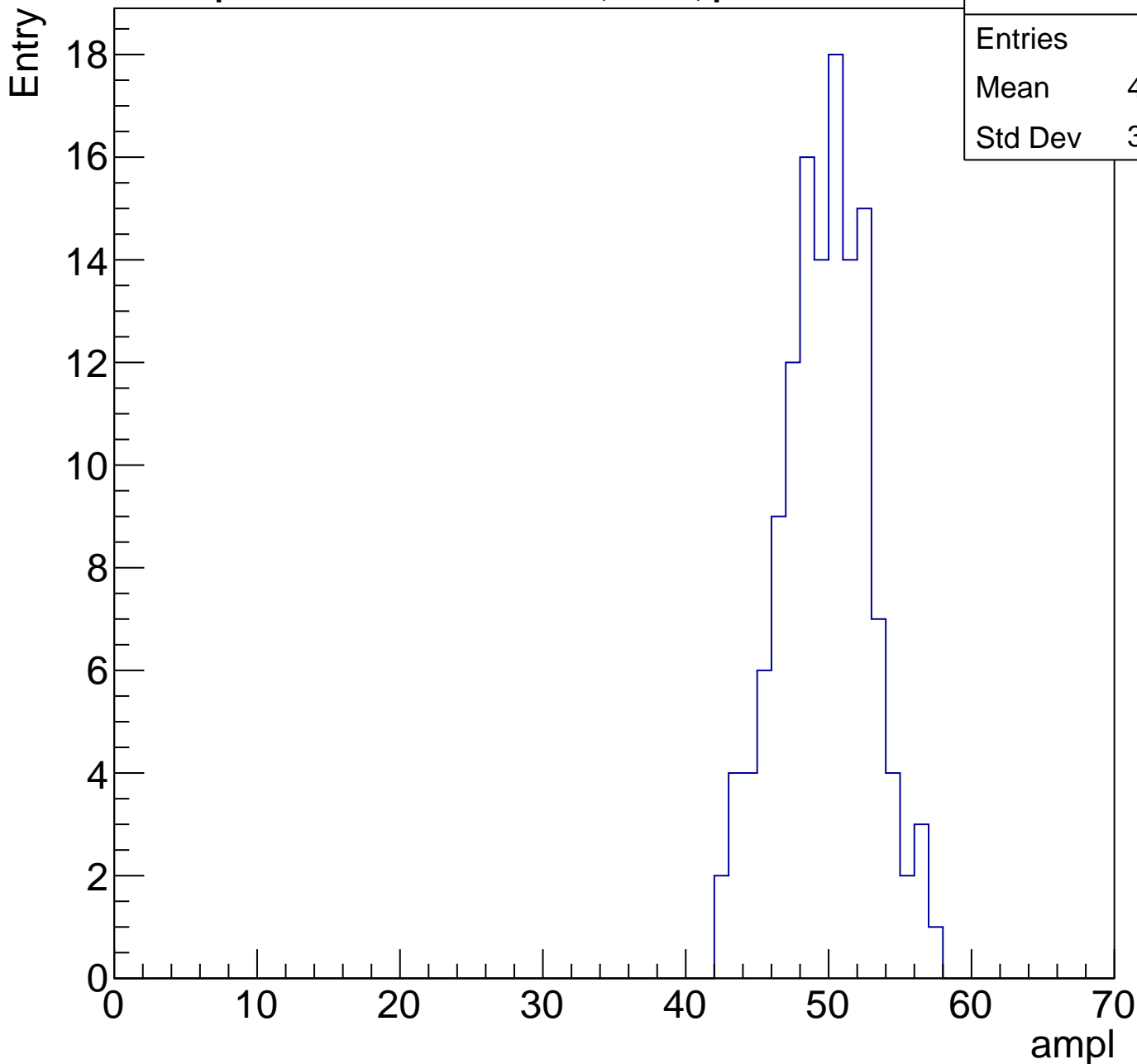
Entry



B0L101S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	49.24
Std Dev	3.125



B0L101S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	55.81
Std Dev	2.843

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

B0L101S, U15-ch9, adc5

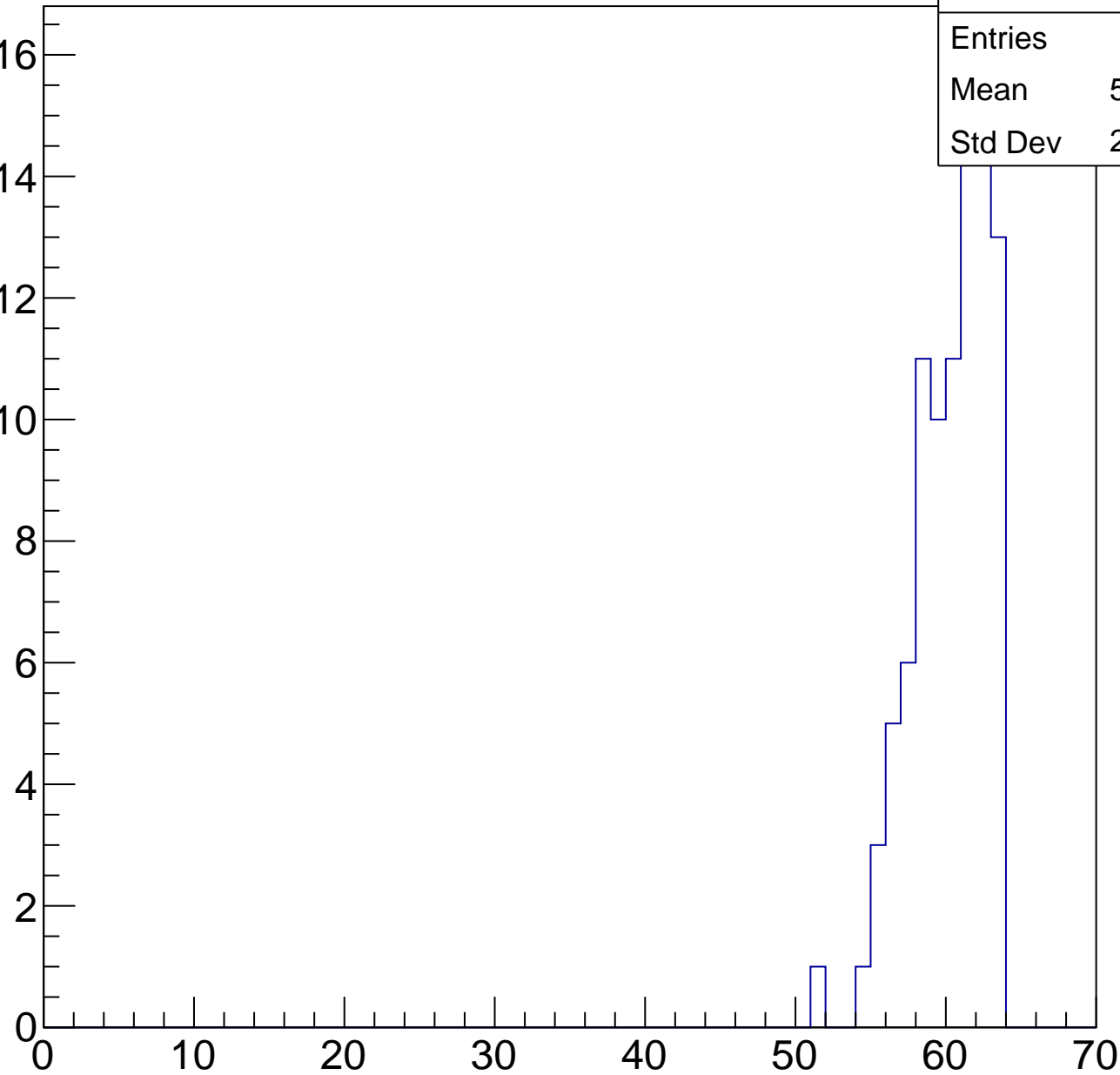
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.85
Std Dev	2.498

ampl

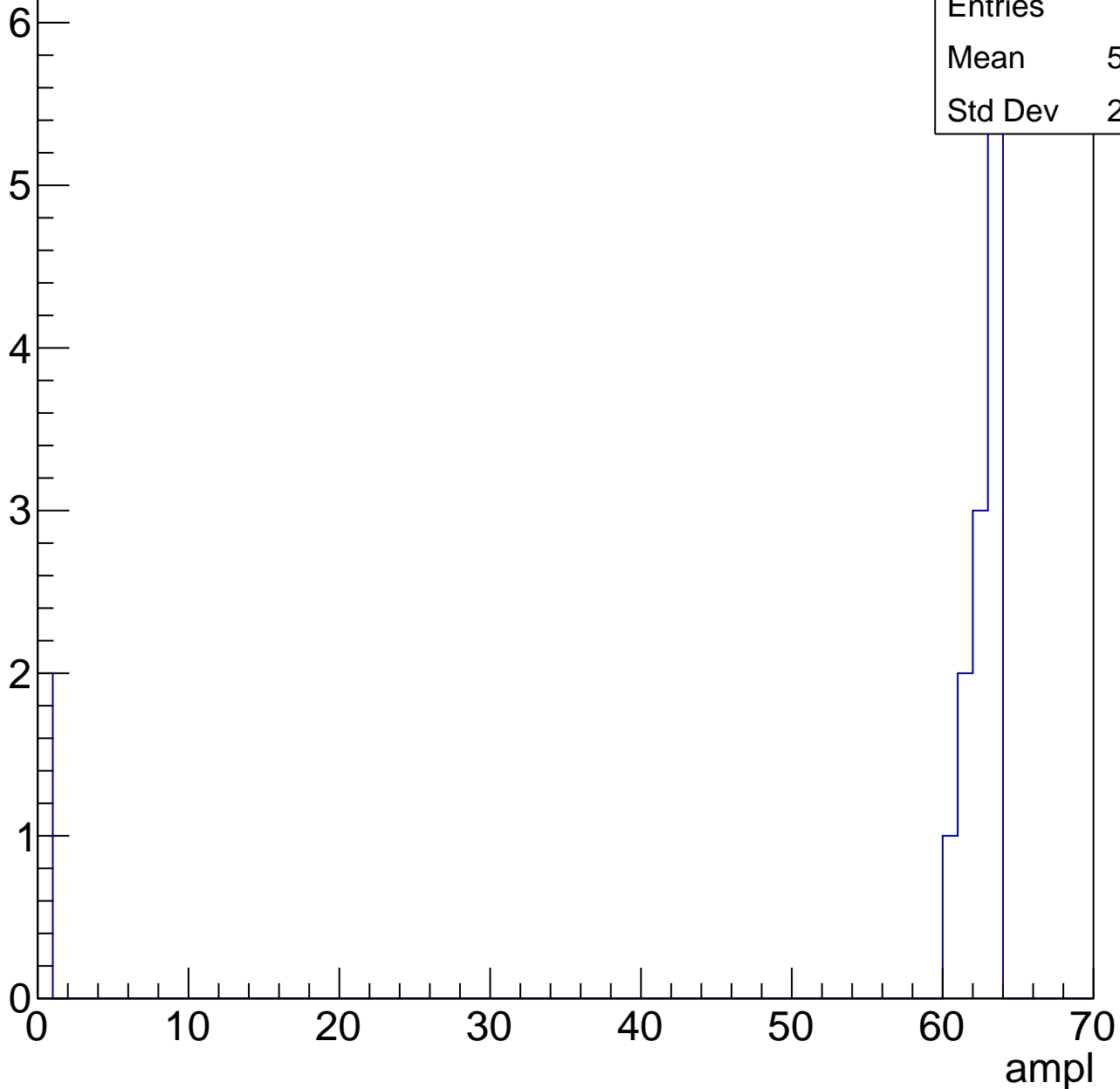


B0L101S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	53.29
Std Dev	21.77



B0L101S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch10, adc0

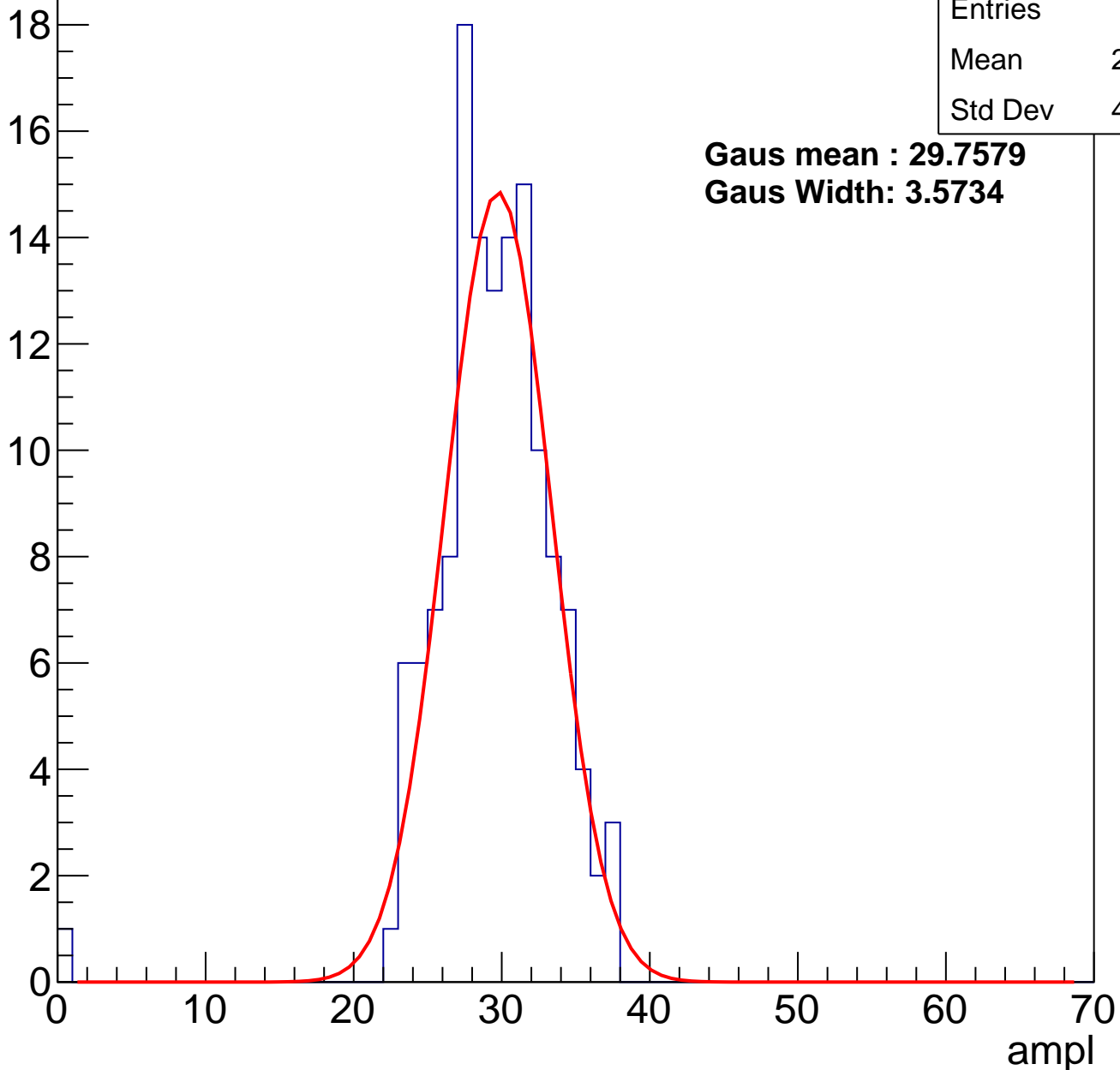
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	28.99
Std Dev	4.209

Gaus mean : 29.7579

Gaus Width: 3.5734

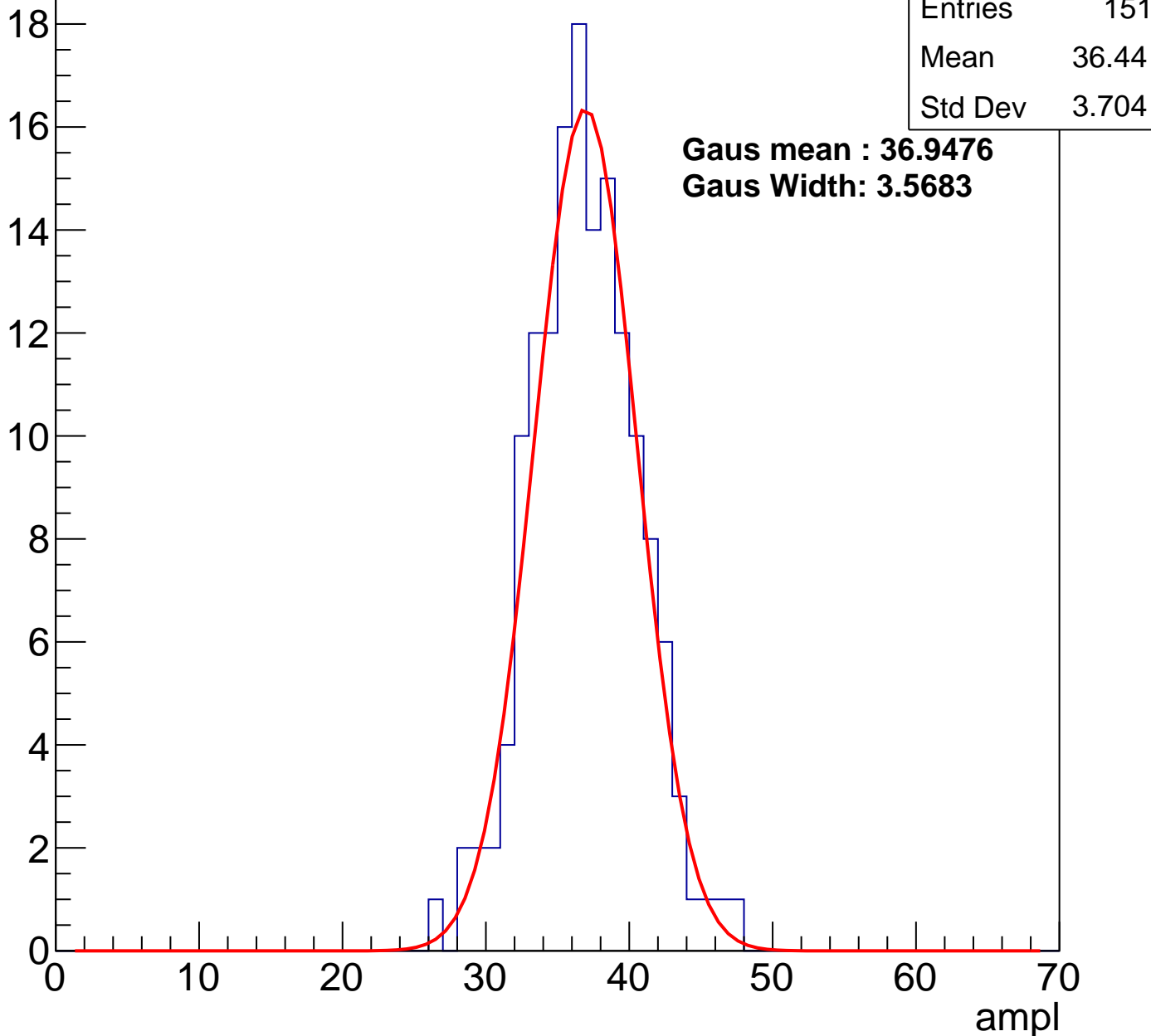
Entry



B0L101S, U15-ch10, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



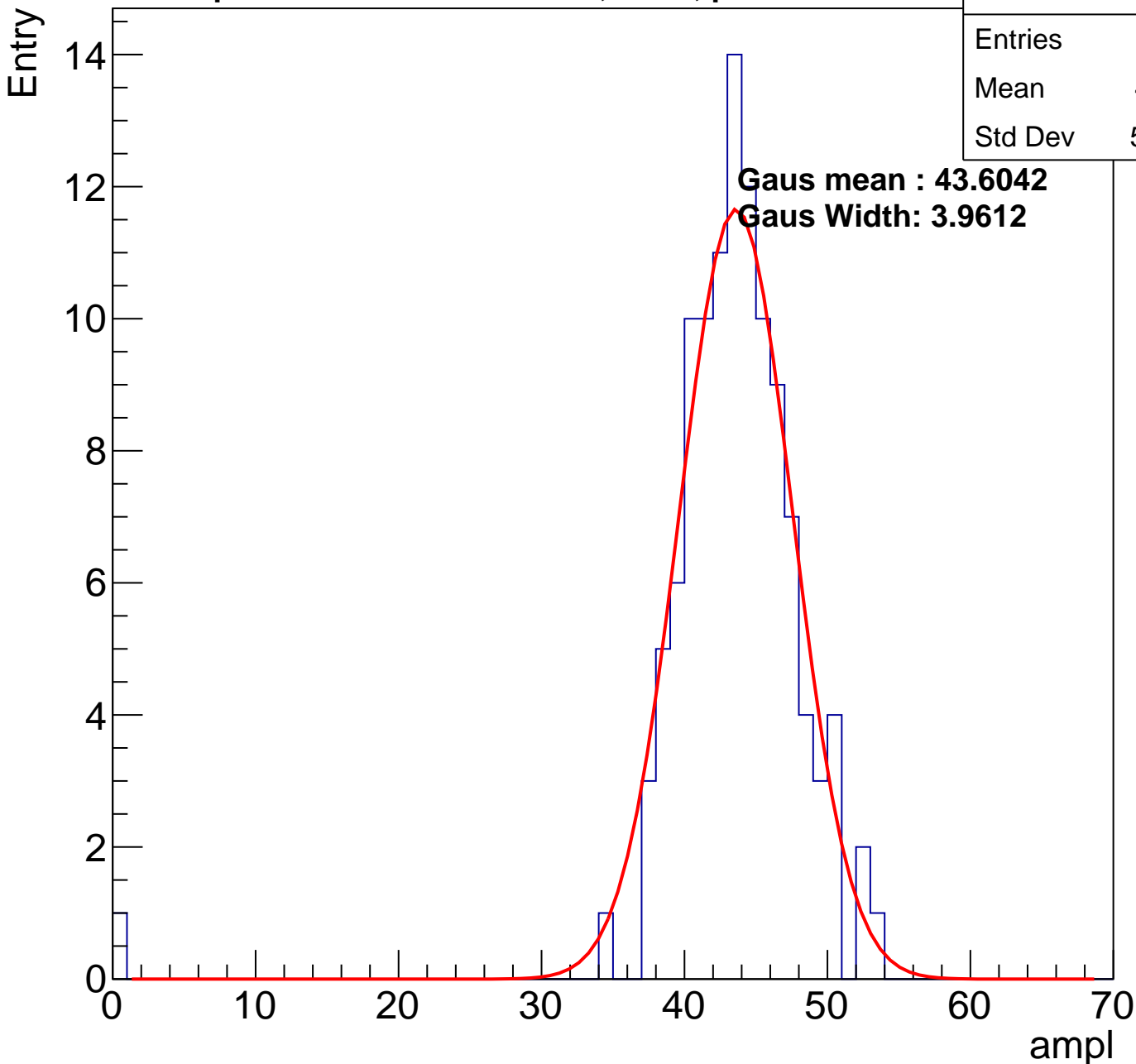
B0L101S, U15-ch10, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	43.01
Std Dev	5.413

Gaus mean : 43.6042

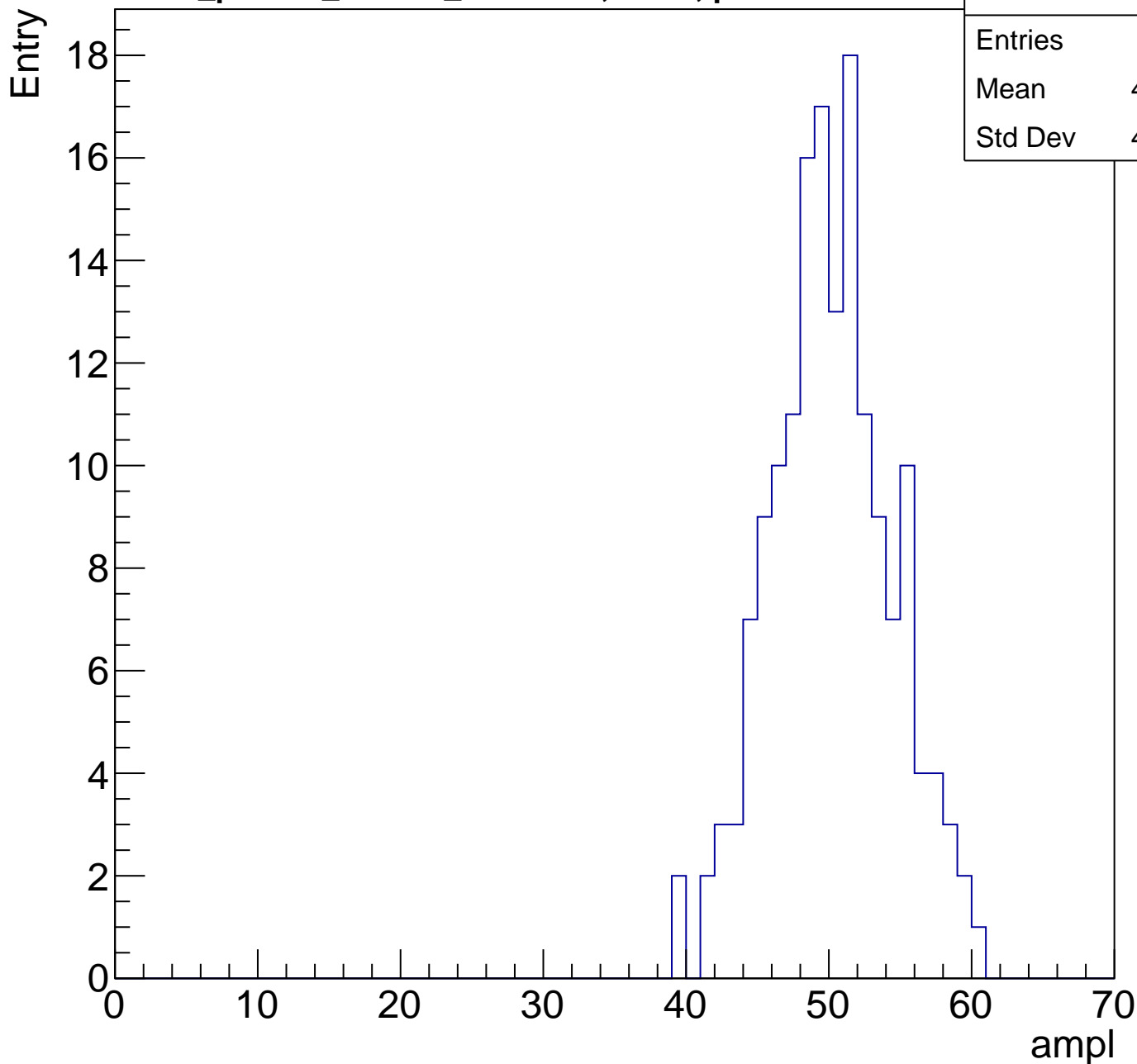
Gaus Width: 3.9612



B0L101S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	49.72
Std Dev	4.192



B0L101S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#1, port C1

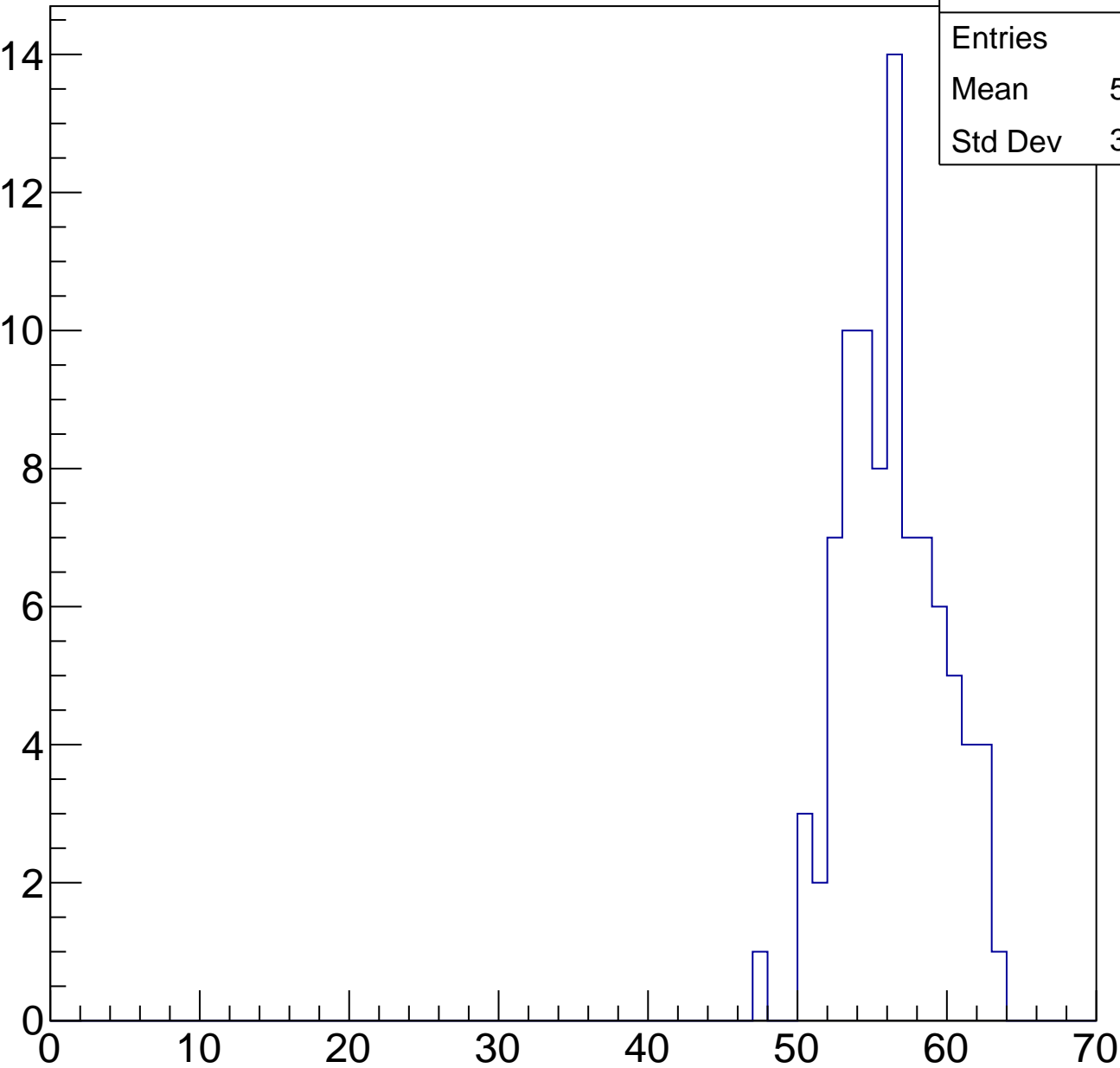
Entries	89
Mean	55.85
Std Dev	3.266

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0 10 20 30 40 50 60 70

ampl

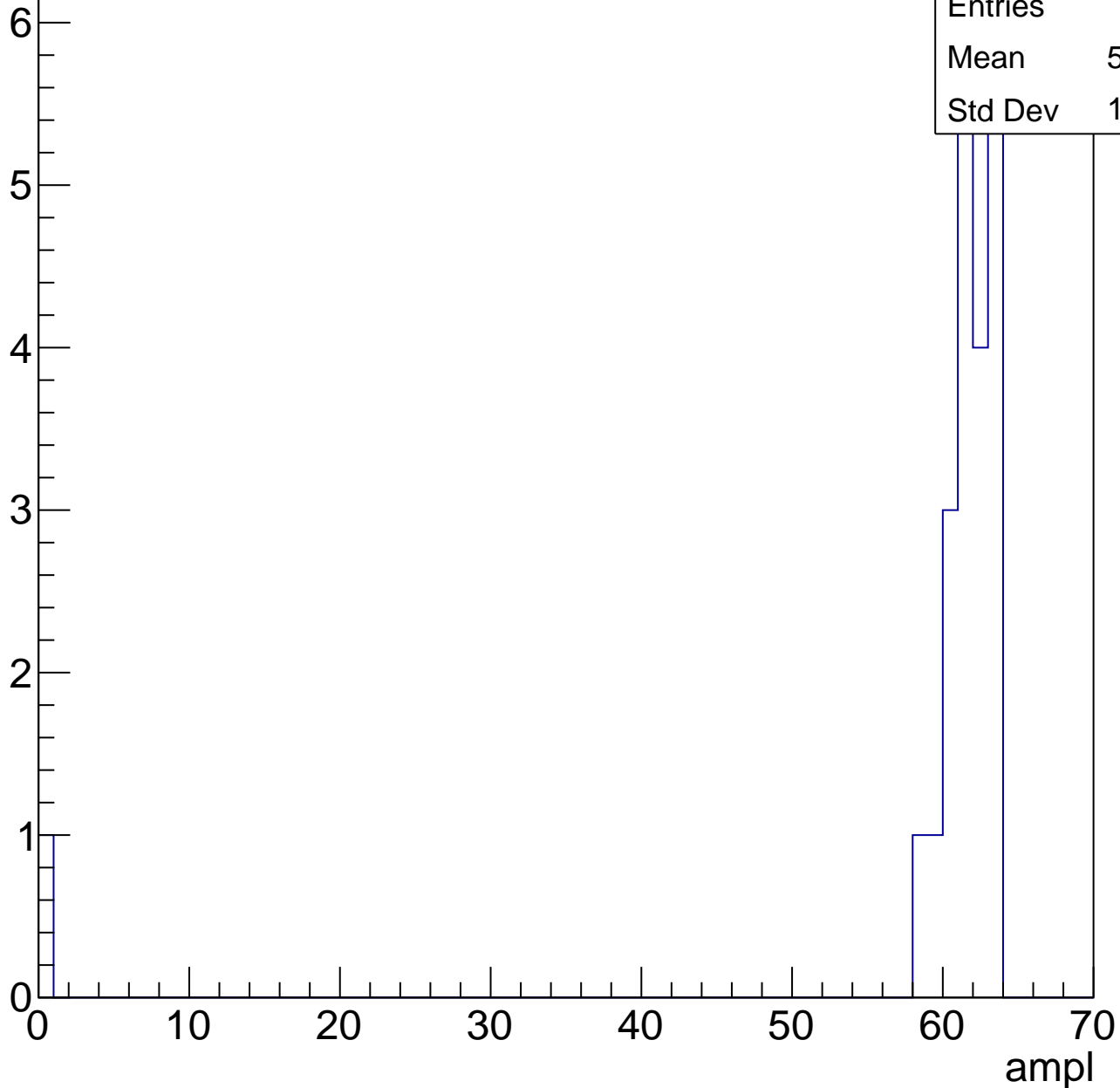
Entries	83
Mean	59.05
Std Dev	6.958

0 10 20 30 40 50 60 70

B0L101S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

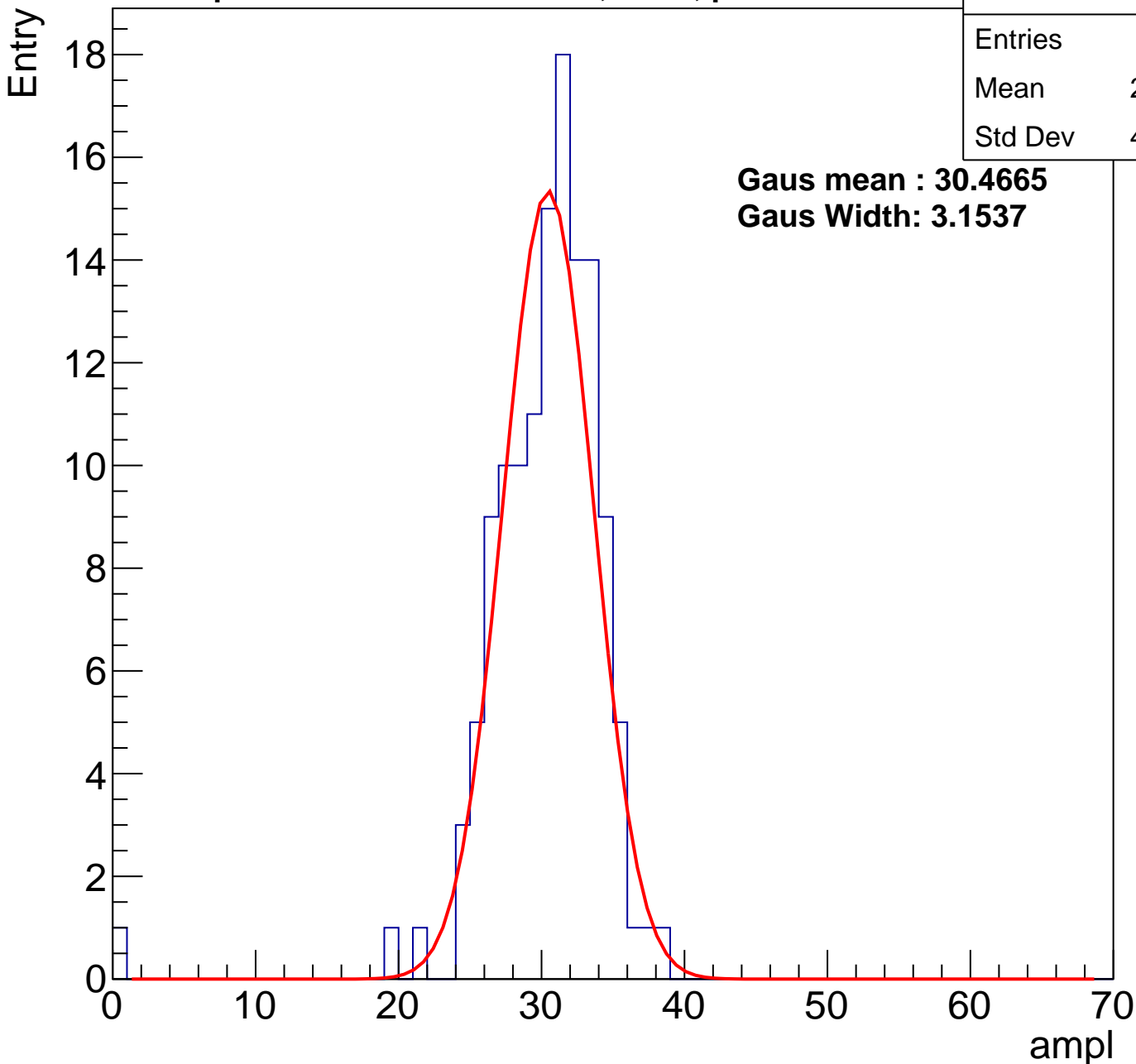
B0L101S, U15-ch11, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	29.84
Std Dev	4.162

Gaus mean : 30.4665

Gaus Width: 3.1537



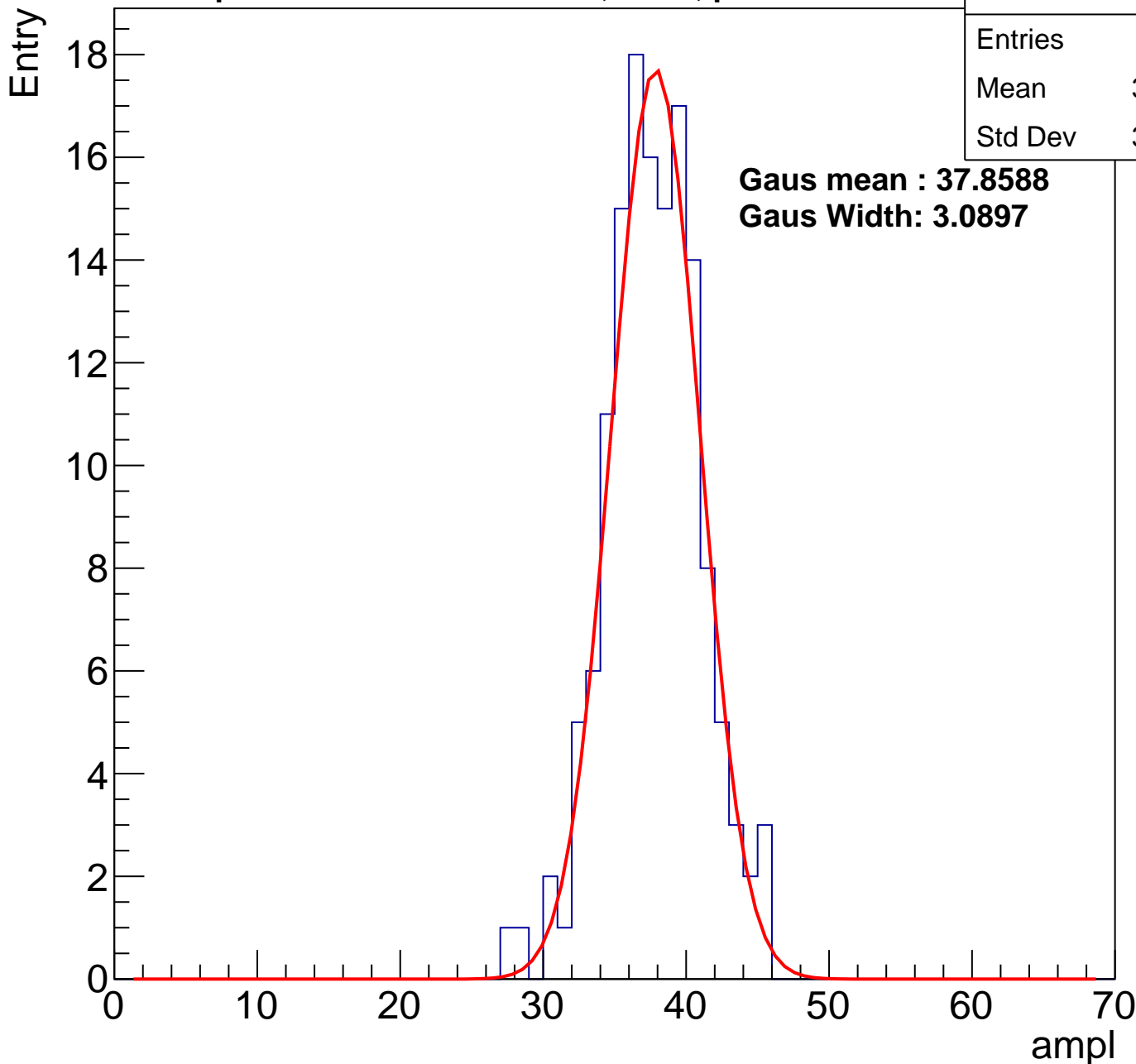
B0L101S, U15-ch11, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	37.24
Std Dev	3.308

Gaus mean : 37.8588

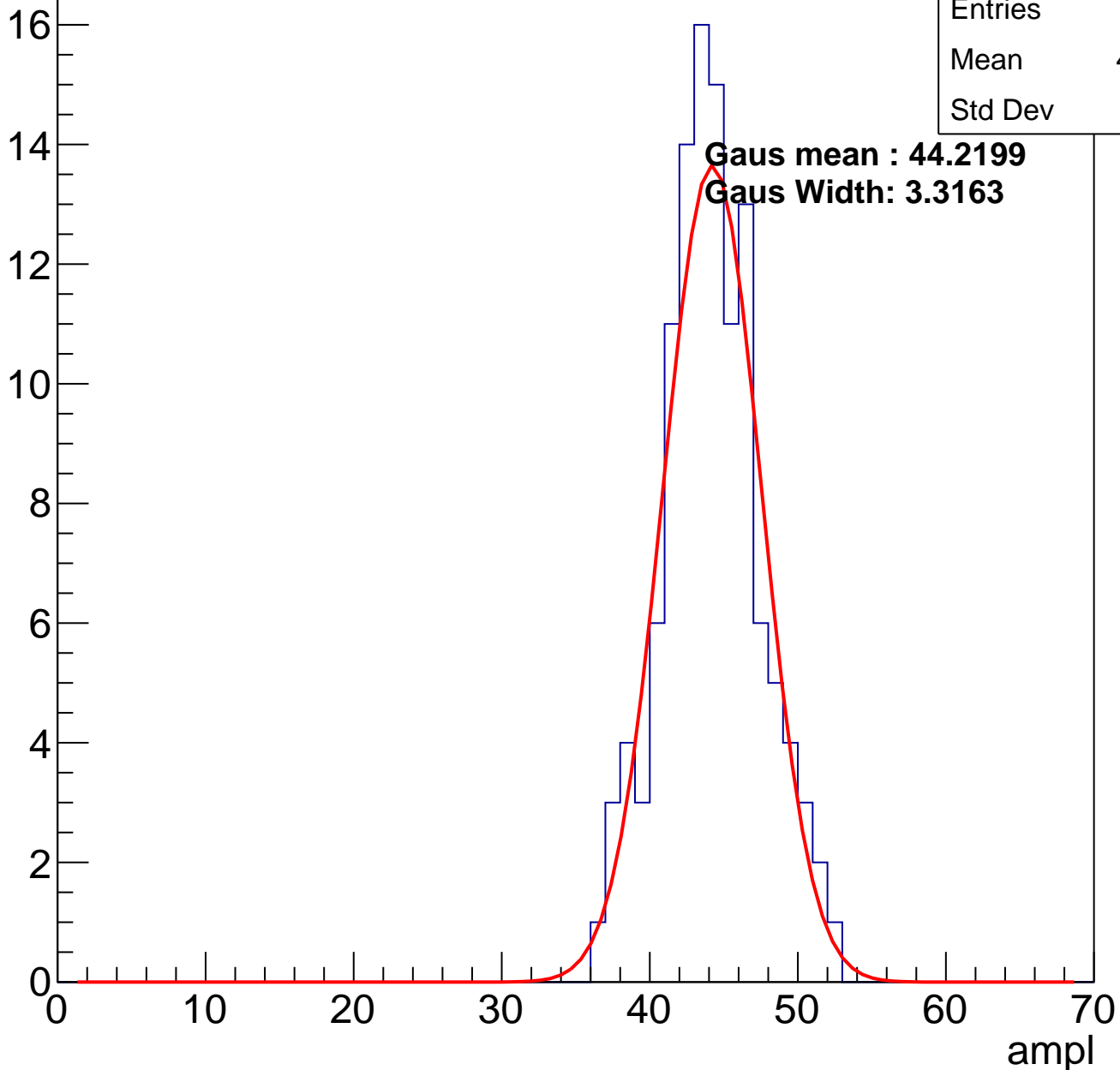
Gaus Width: 3.0897



B0L101S, U15-ch11, adc2

calib_packv5_042523_0143.root, FC#1, port C1

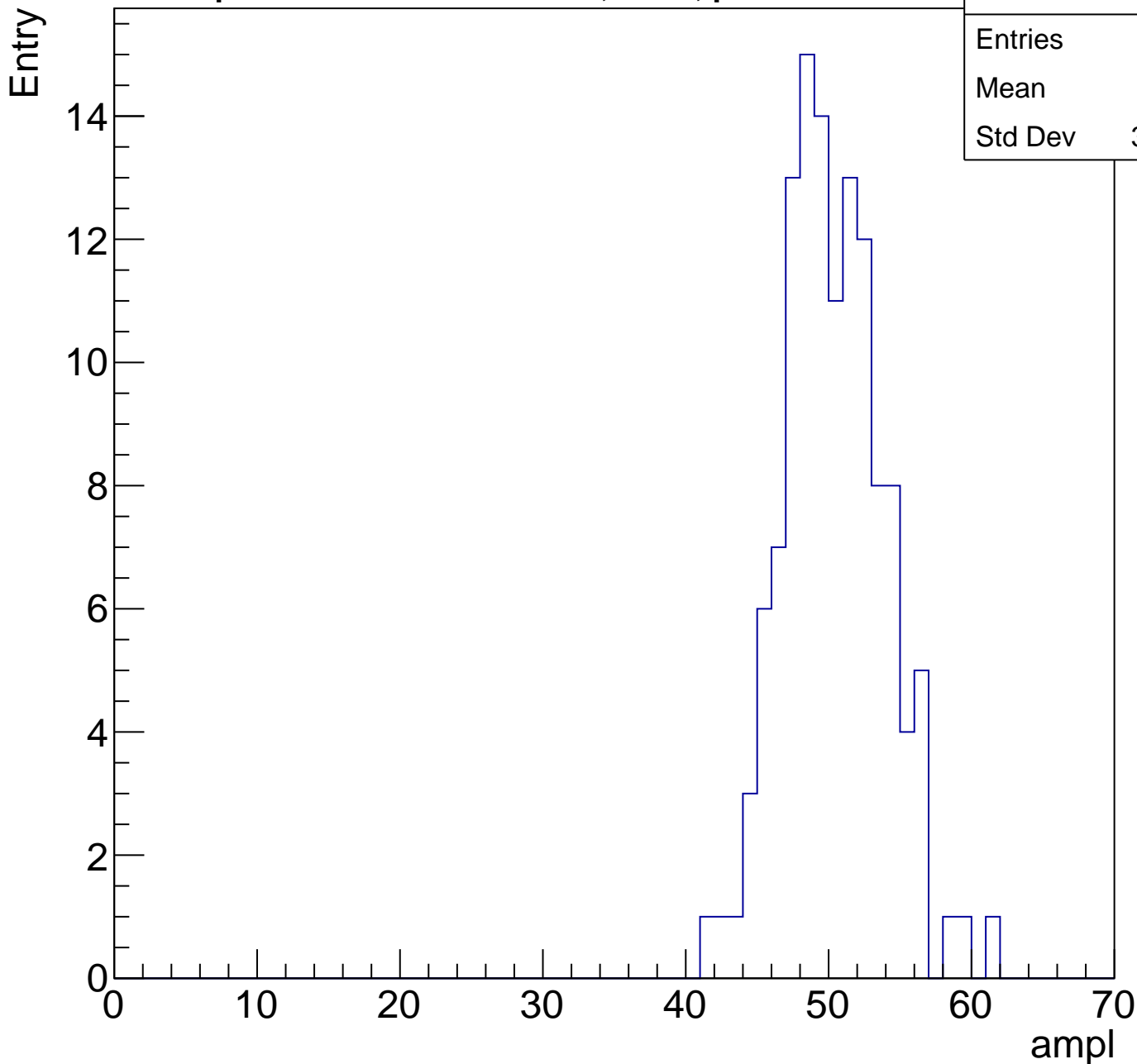
Entry



B0L101S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#1, port C1

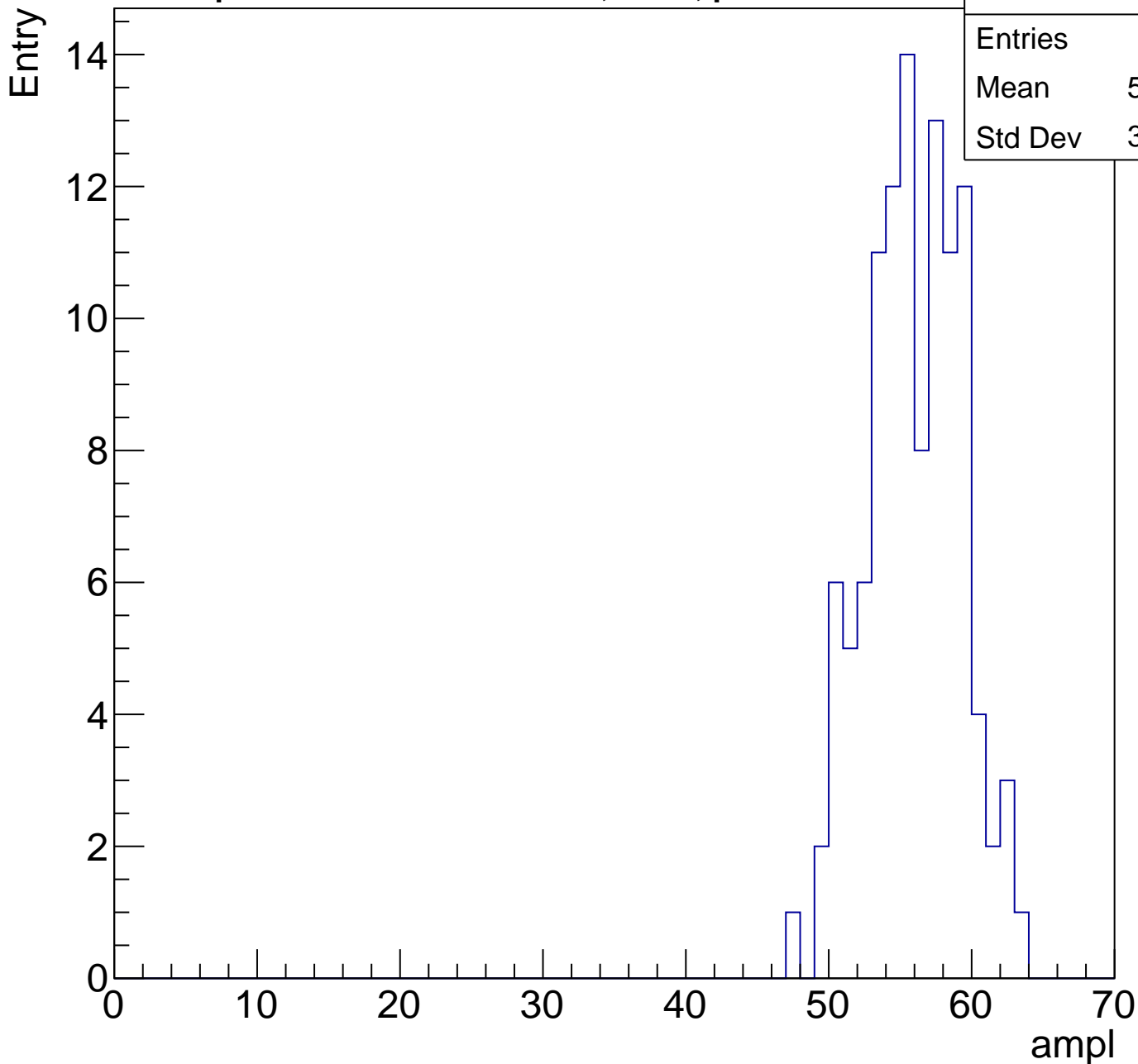
Entries	125
Mean	49.9
Std Dev	3.534



B0L101S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	55.49
Std Dev	3.266



B0L101S, U15-ch11, adc5

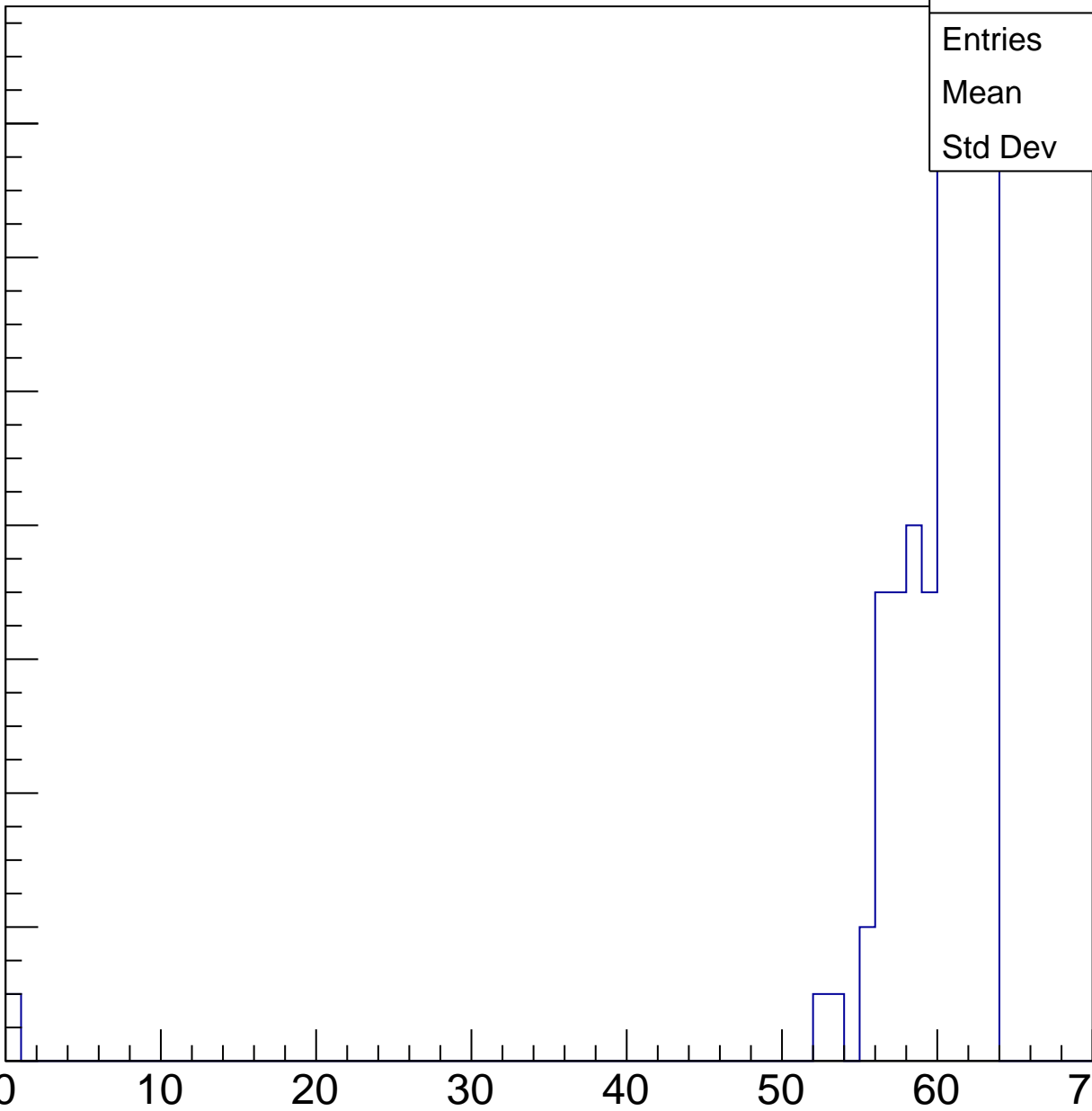
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	59.24
Std Dev	6.696

ampl

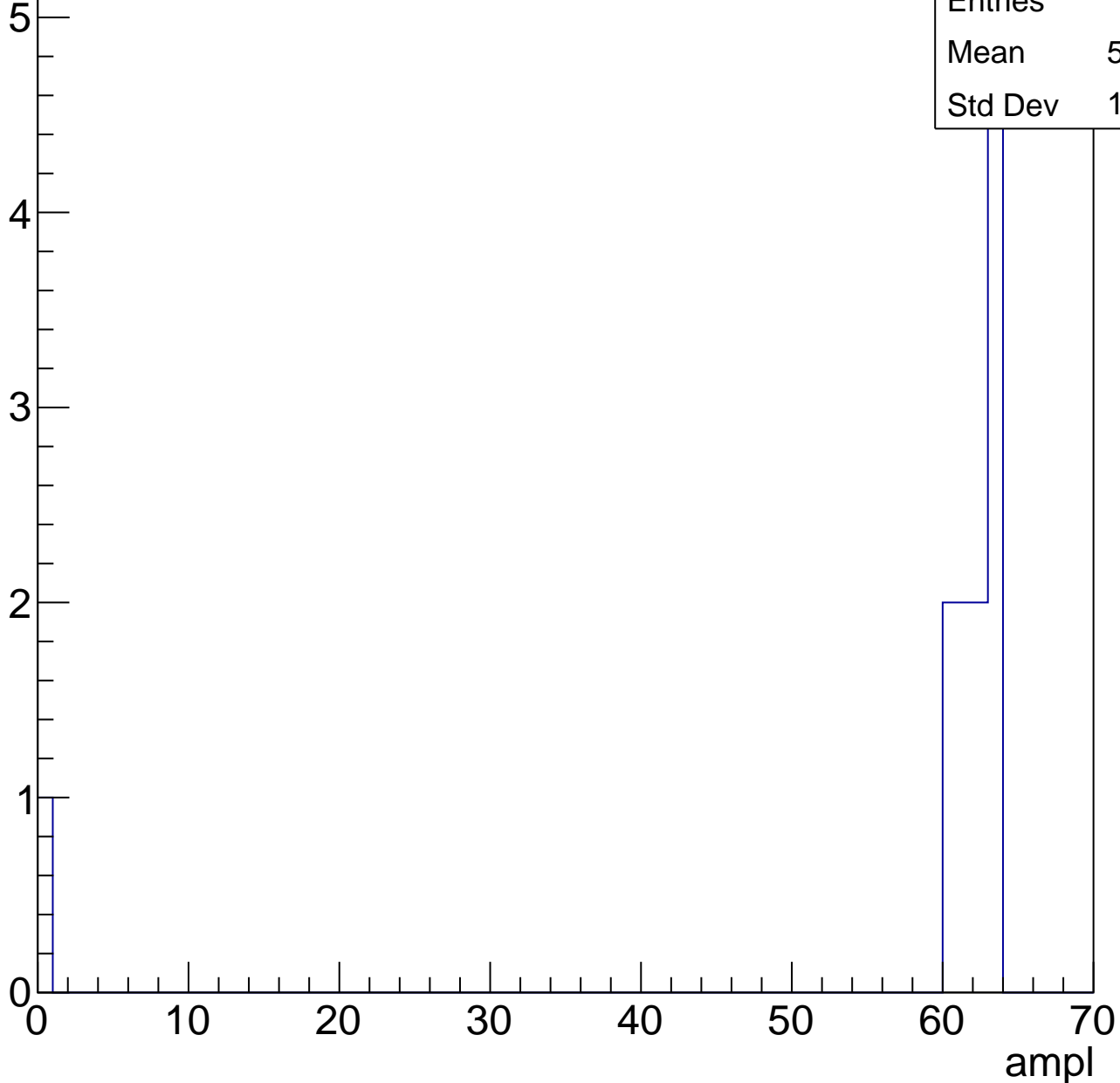


B0L101S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.75
Std Dev	17.15



B0L101S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L101S, U15-ch12, adc0

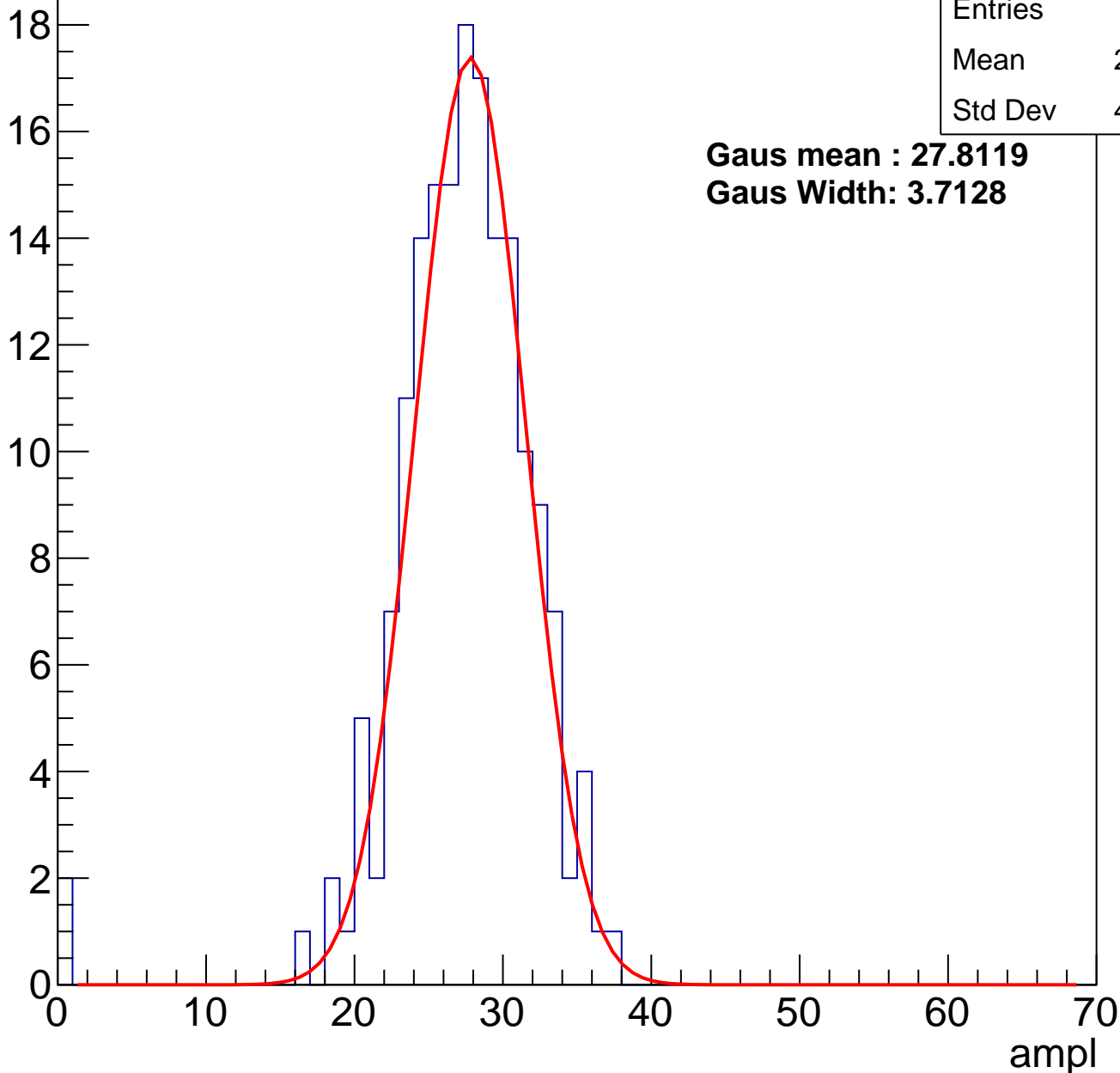
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	26.85
Std Dev	4.847

Gaus mean : 27.8119

Gaus Width: 3.7128

Entry



B0L101S, U15-ch12, adc1

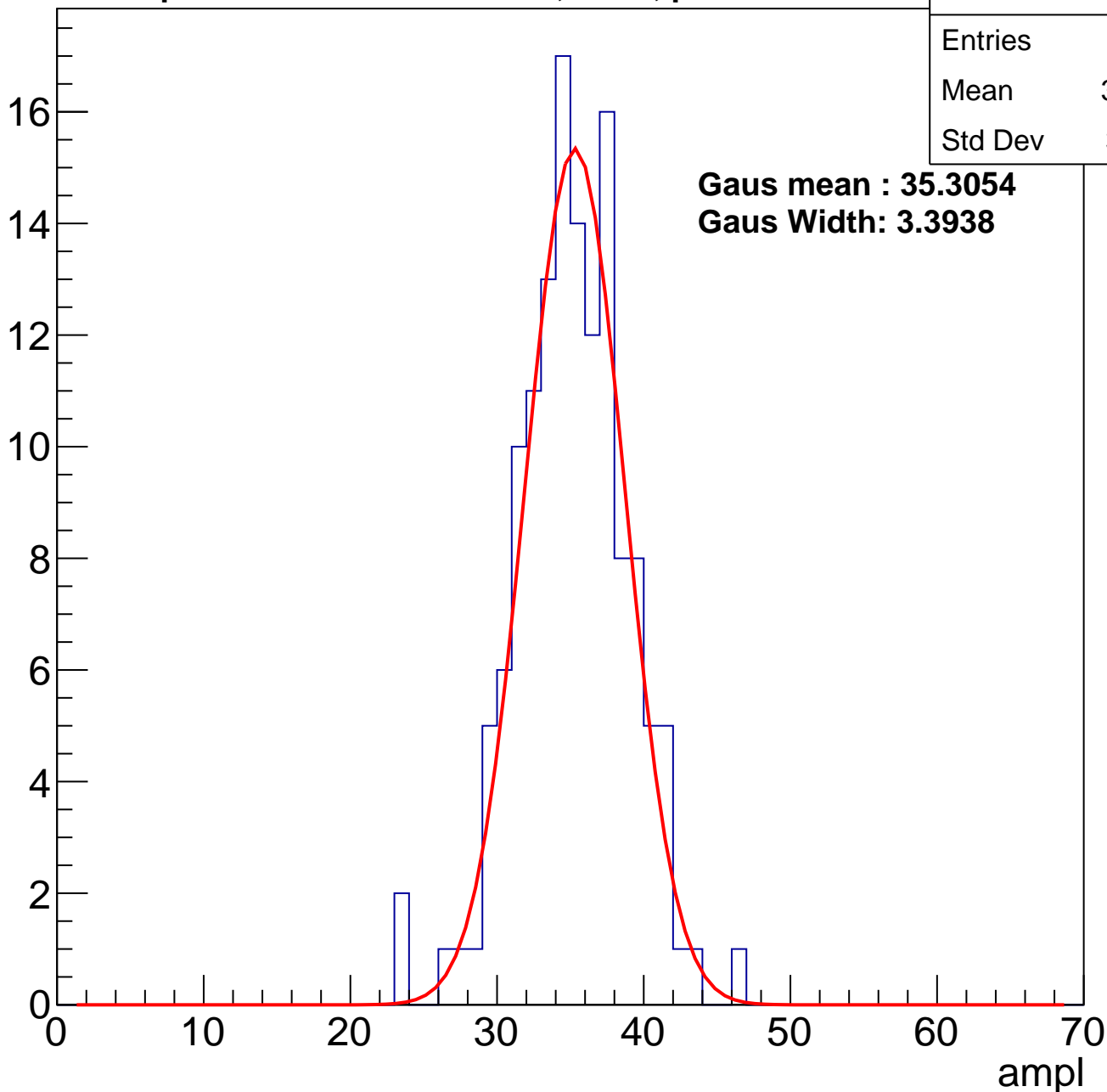
calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	34.69
Std Dev	3.751

Gaus mean : 35.3054

Gaus Width: 3.3938

Entry



B0L101S, U15-ch12, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

136

Mean

42.04

Std Dev

3.678

Gaus mean : 42.6138

Gaus Width: 3.6763

0

10

20

30

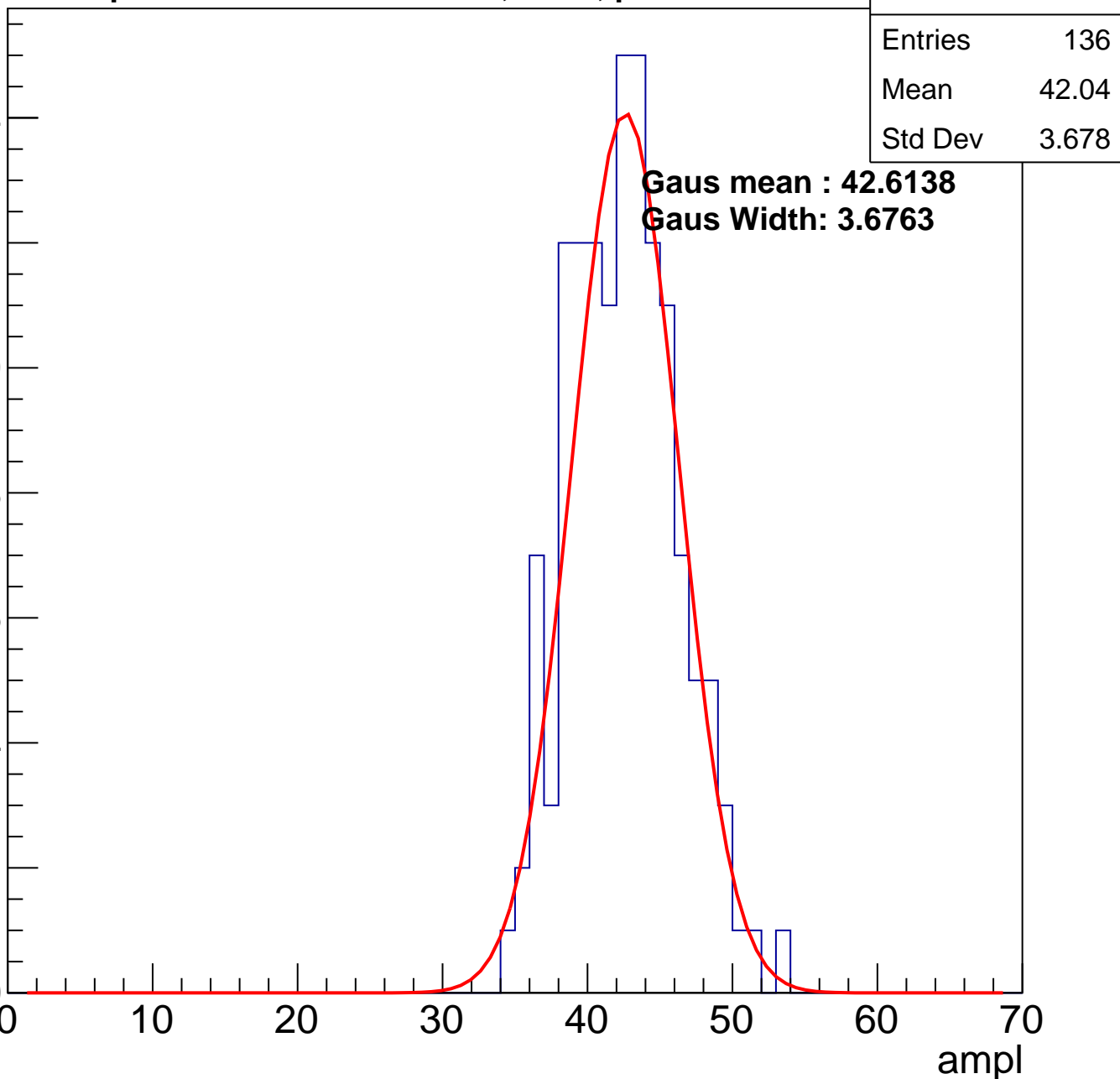
40

50

60

70

ampl



B0L101S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	47.26
Std Dev	3.436

Entry

12

10

8

6

4

2

0

0

10

20

30

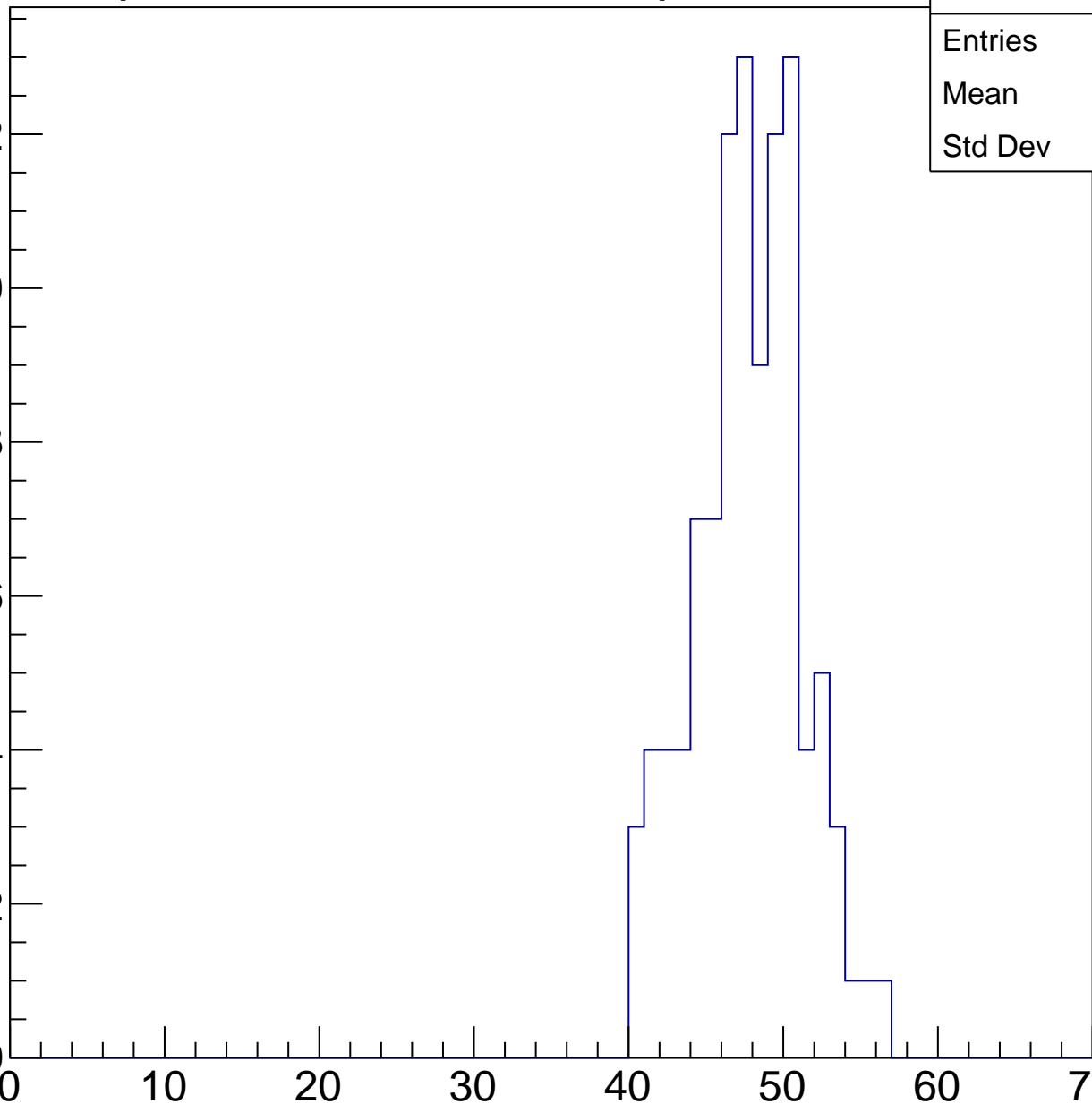
40

50

60

70

ampl



B0L101S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	52.82
Std Dev	3.157

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

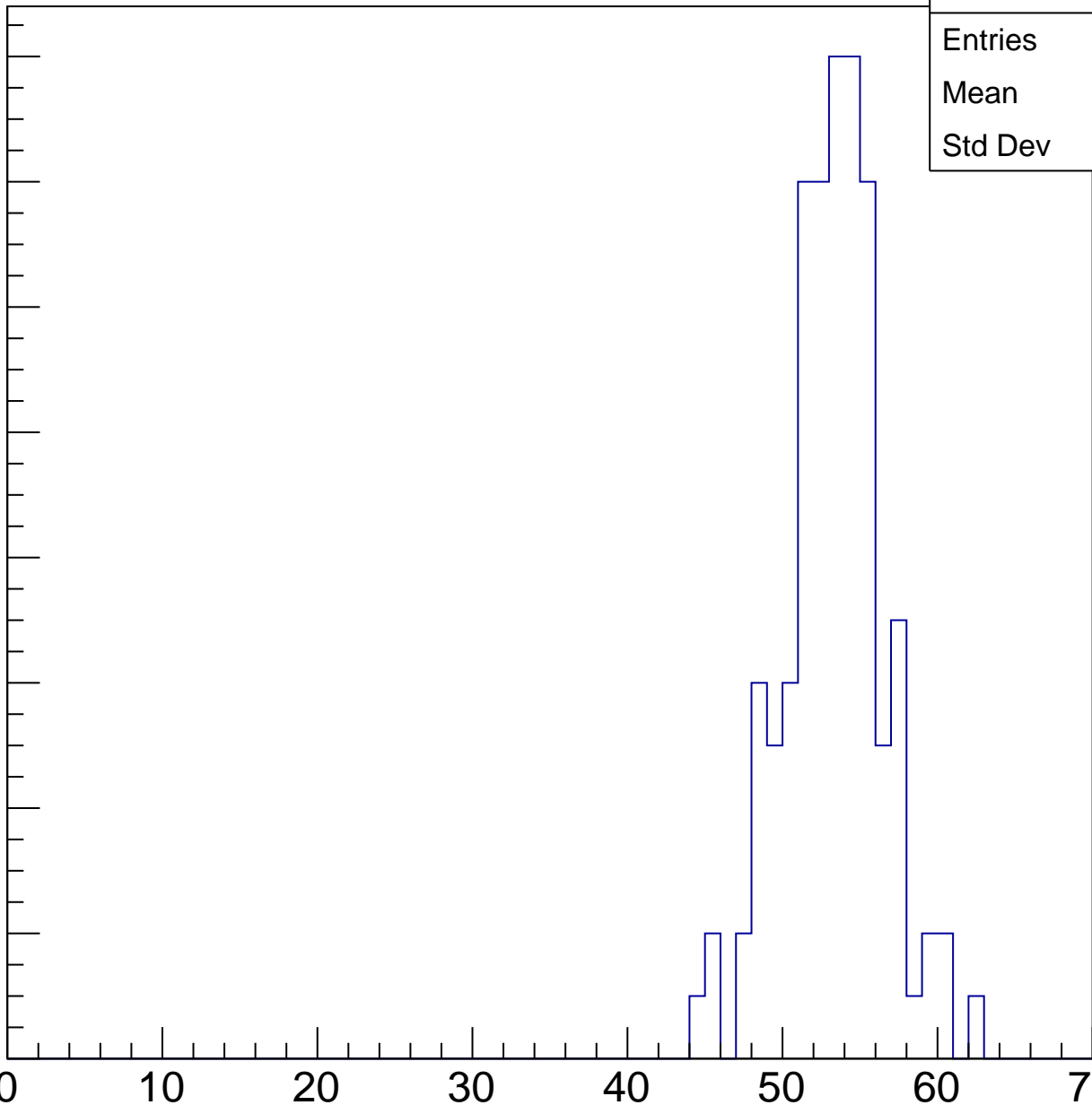
40

50

60

70

ampl



B0L101S, U15-ch12, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries	129
Mean	58.67
Std Dev	5.825

ampl

0

10

20

30

40

50

60

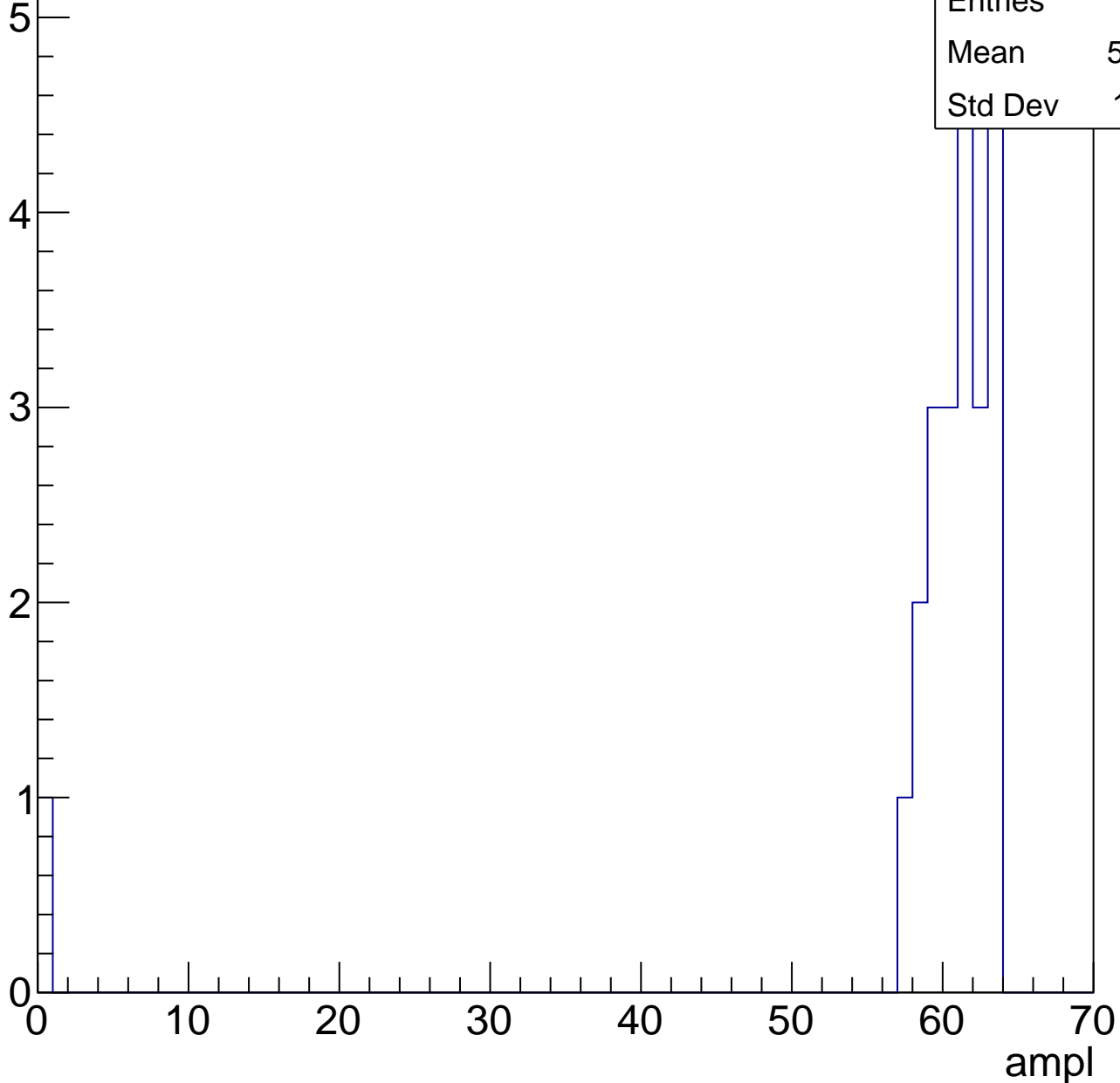
70

B0L101S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	23
Mean	58.09
Std Dev	12.51



B0L101S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#1, port C1

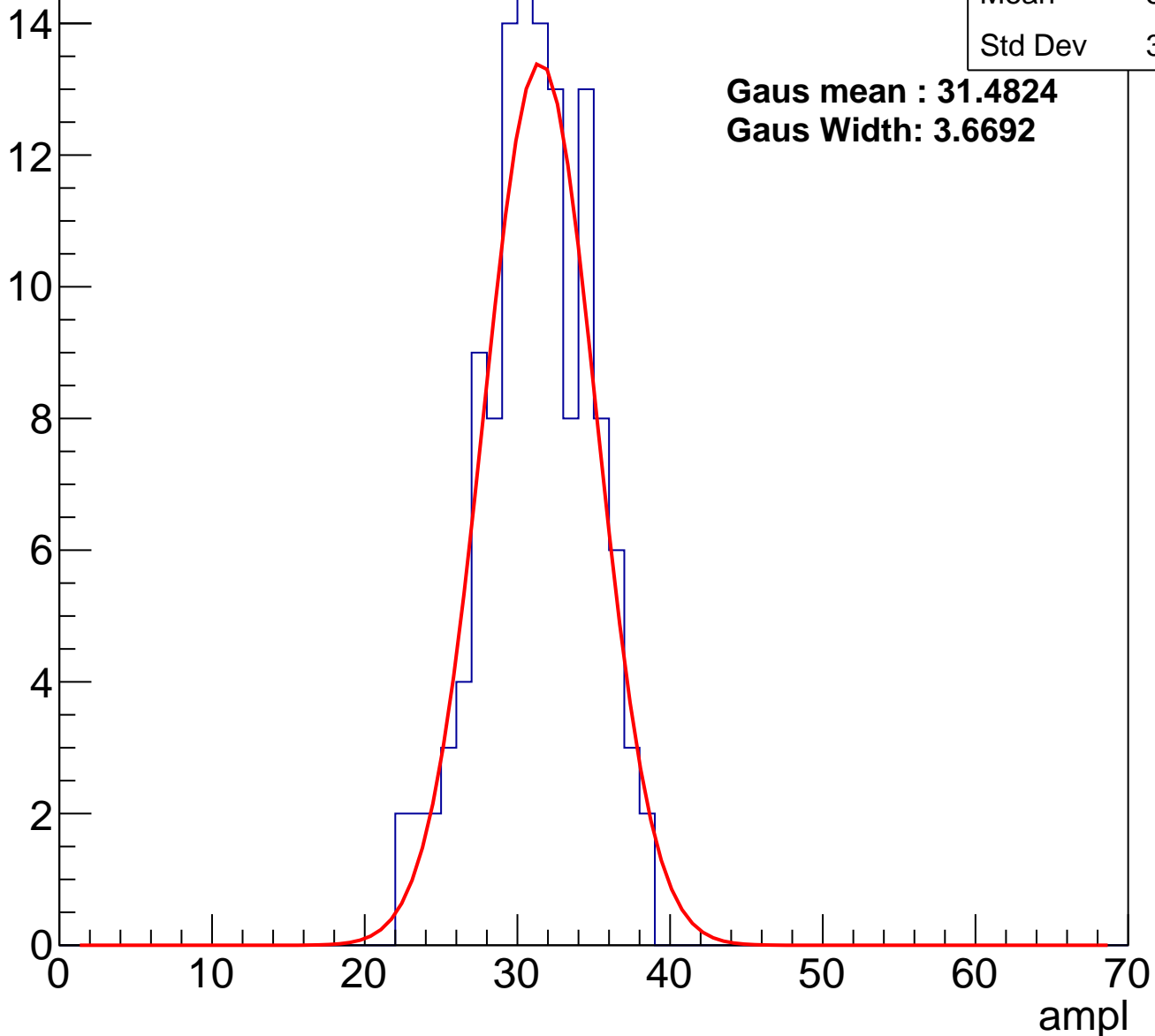
Entry



B0L101S, U15-ch13, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



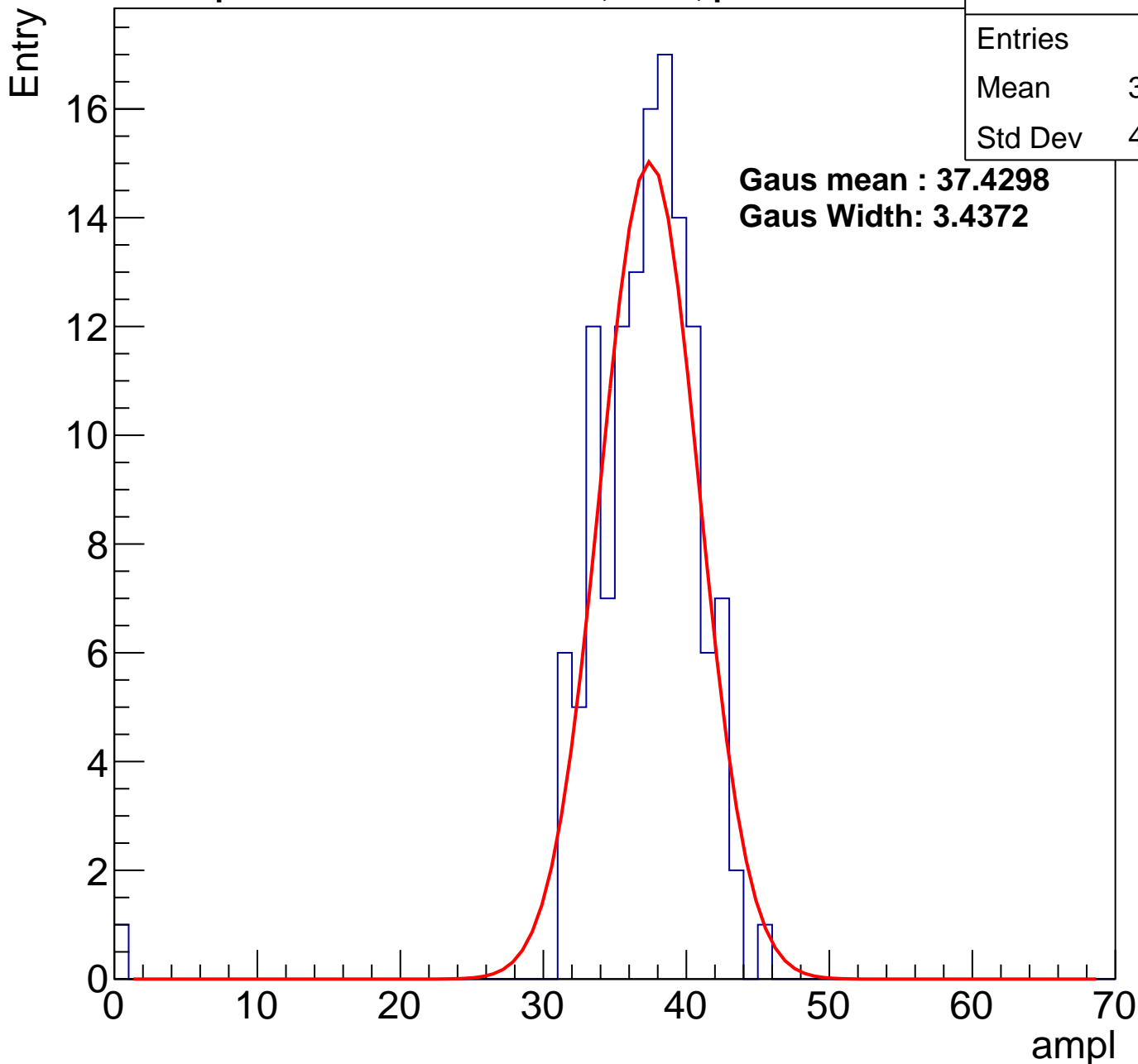
B0L101S, U15-ch13, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.66
Std Dev	4.454

Gaus mean : 37.4298

Gaus Width: 3.4372



B0L101S, U15-ch13, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	44.74
Std Dev	3.35

Gaus mean : 45.0746

Gaus Width: 3.1503

Entry

20

18

16

14

12

10

8

6

4

2

0

0

10

20

30

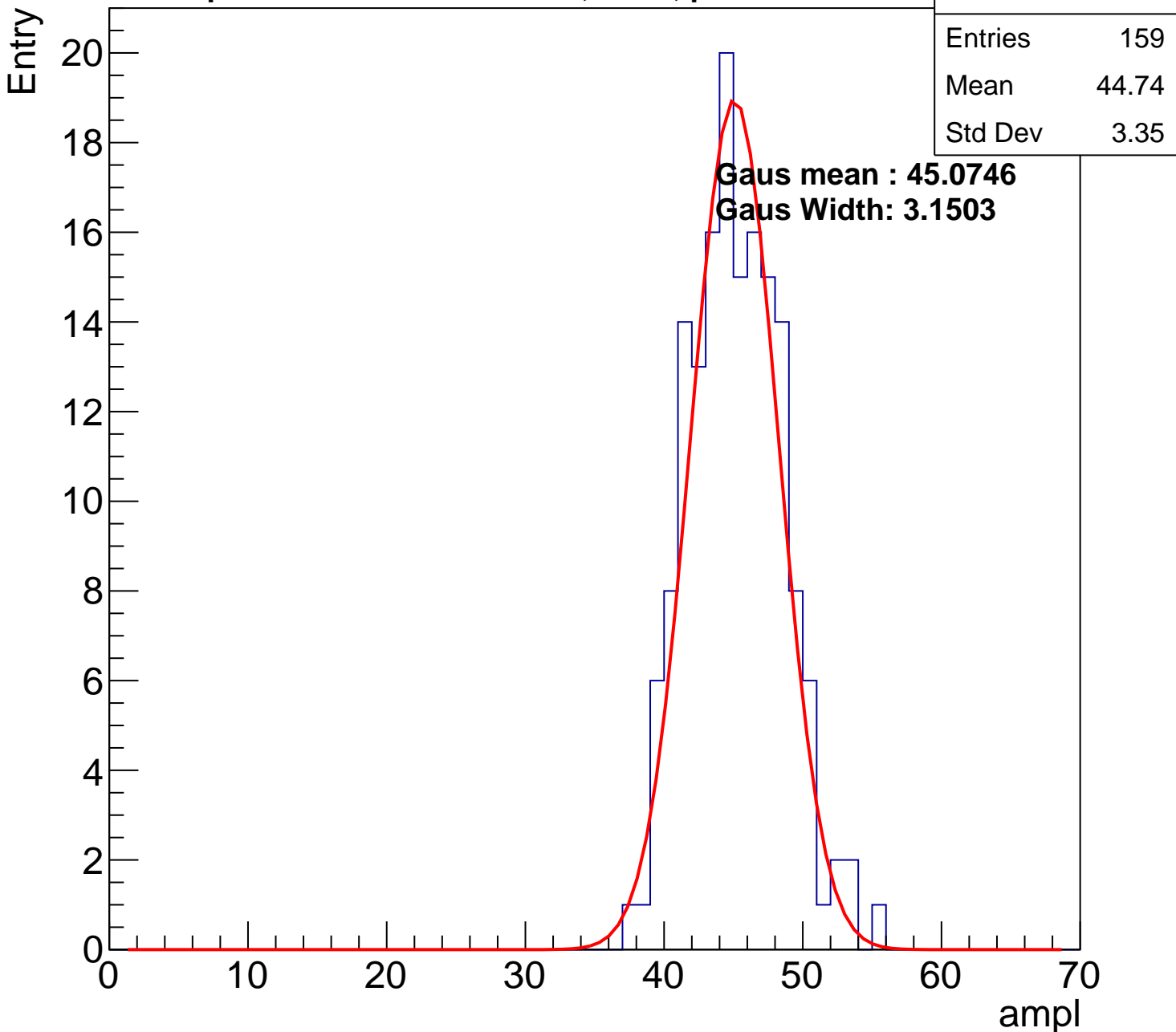
40

50

60

70

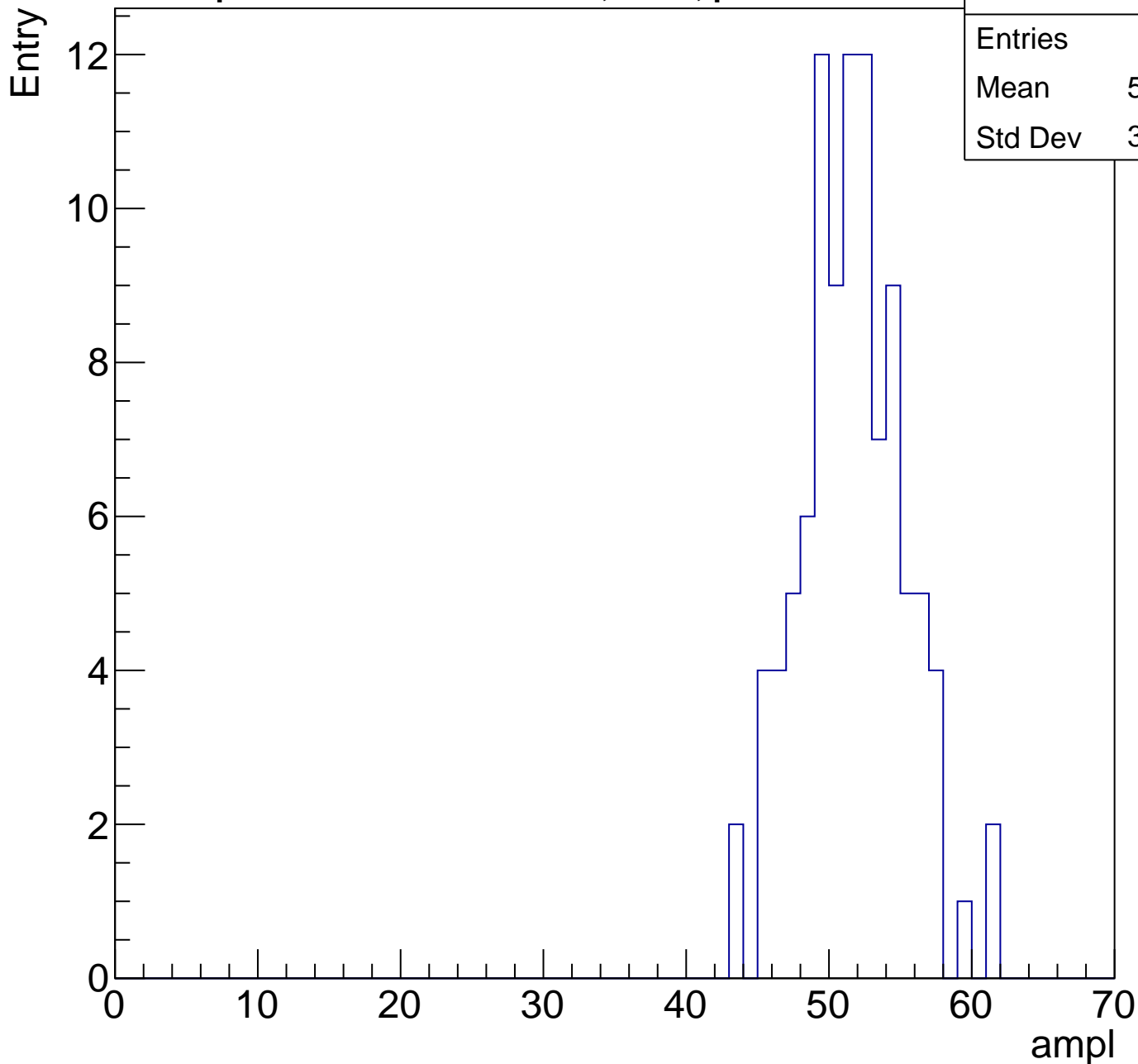
ampl



B0L101S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	51.19
Std Dev	3.614



B0L101S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	56.46
Std Dev	3.303

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

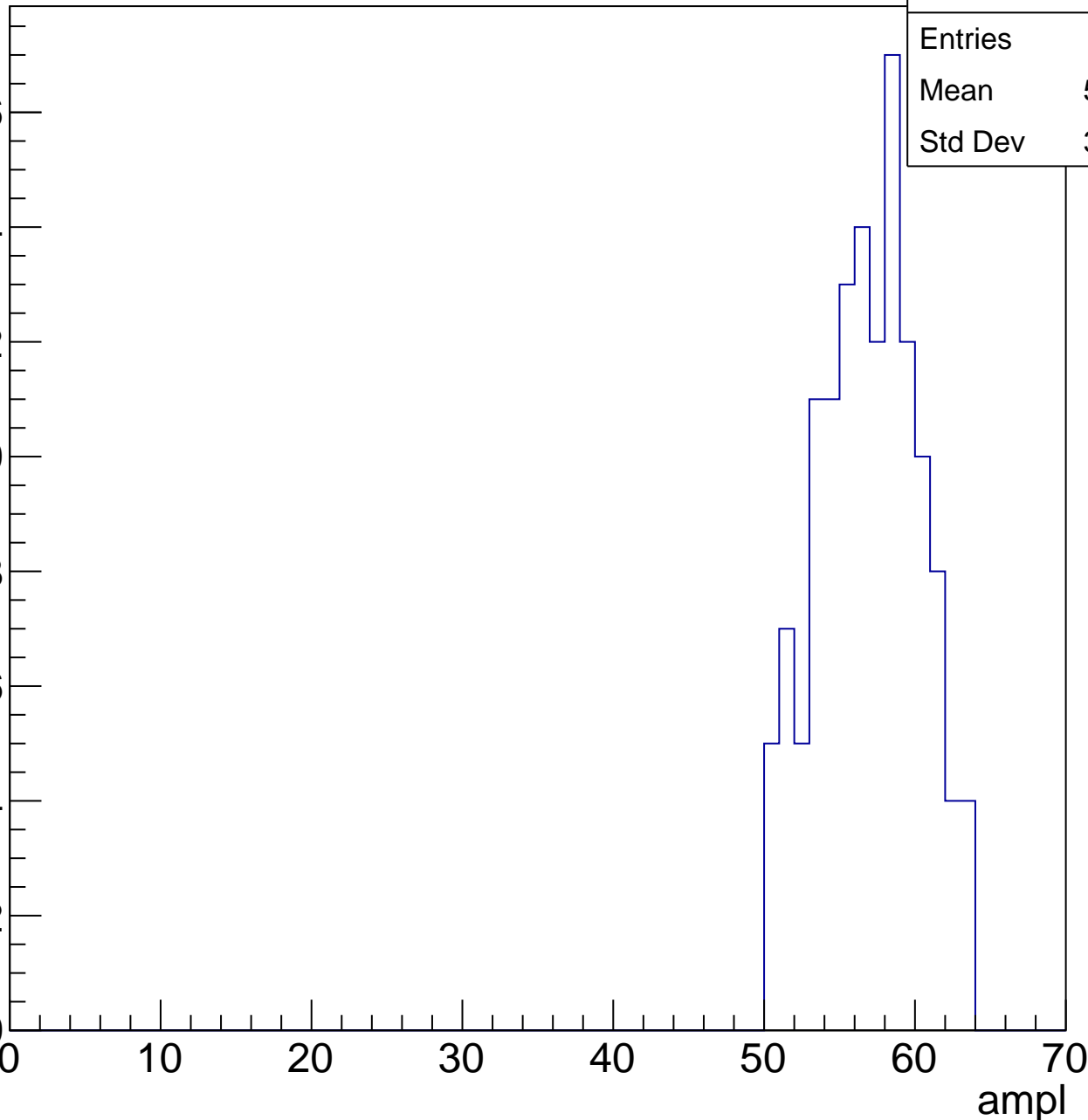
40

50

60

70

ampl



B0L101S, U15-ch13, adc5

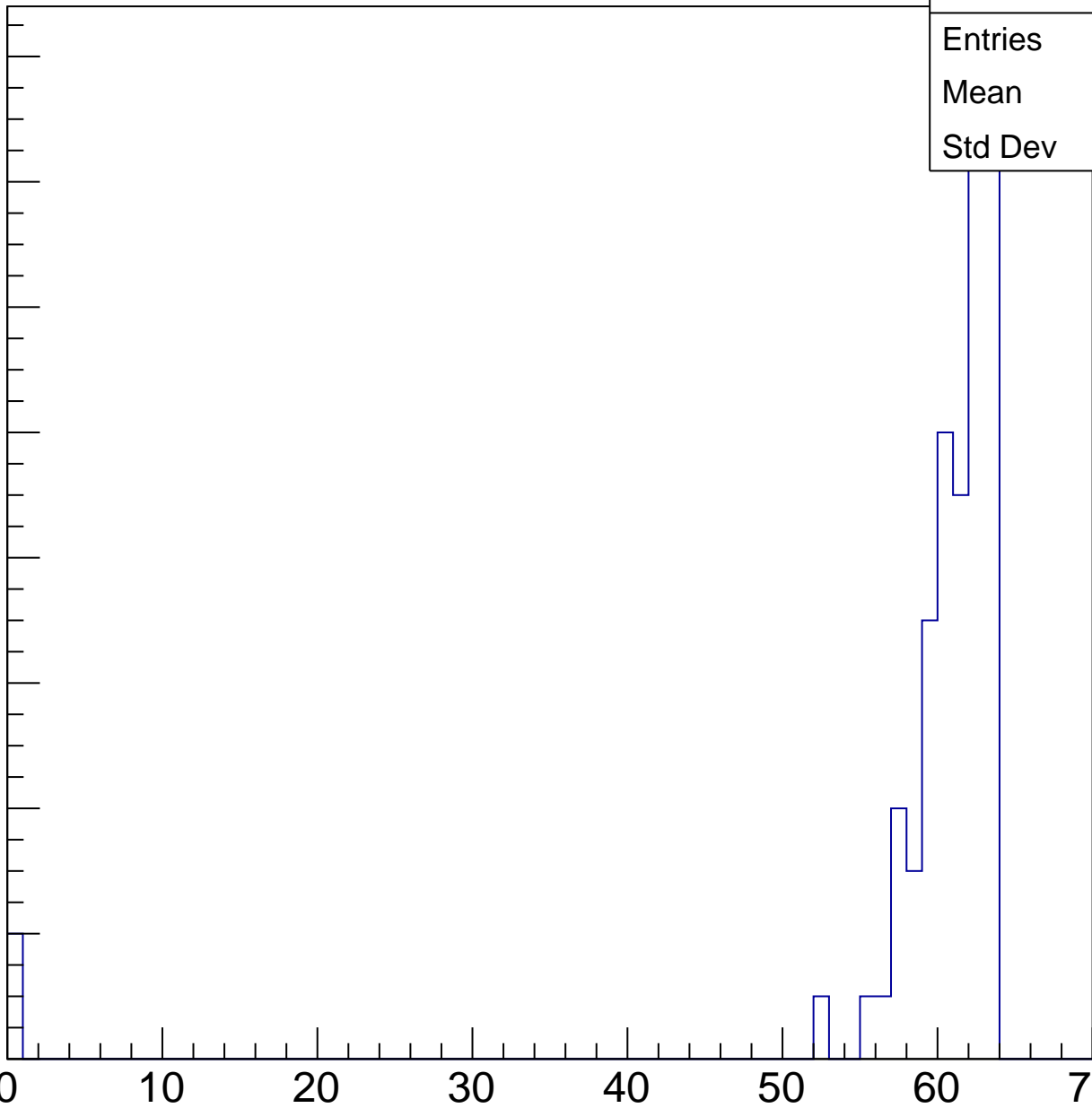
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	69
Mean	58.91
Std Dev	10.42

ampl



B0L101S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch14, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	26.97
Std Dev	5.453

Gaus mean : 28.0452

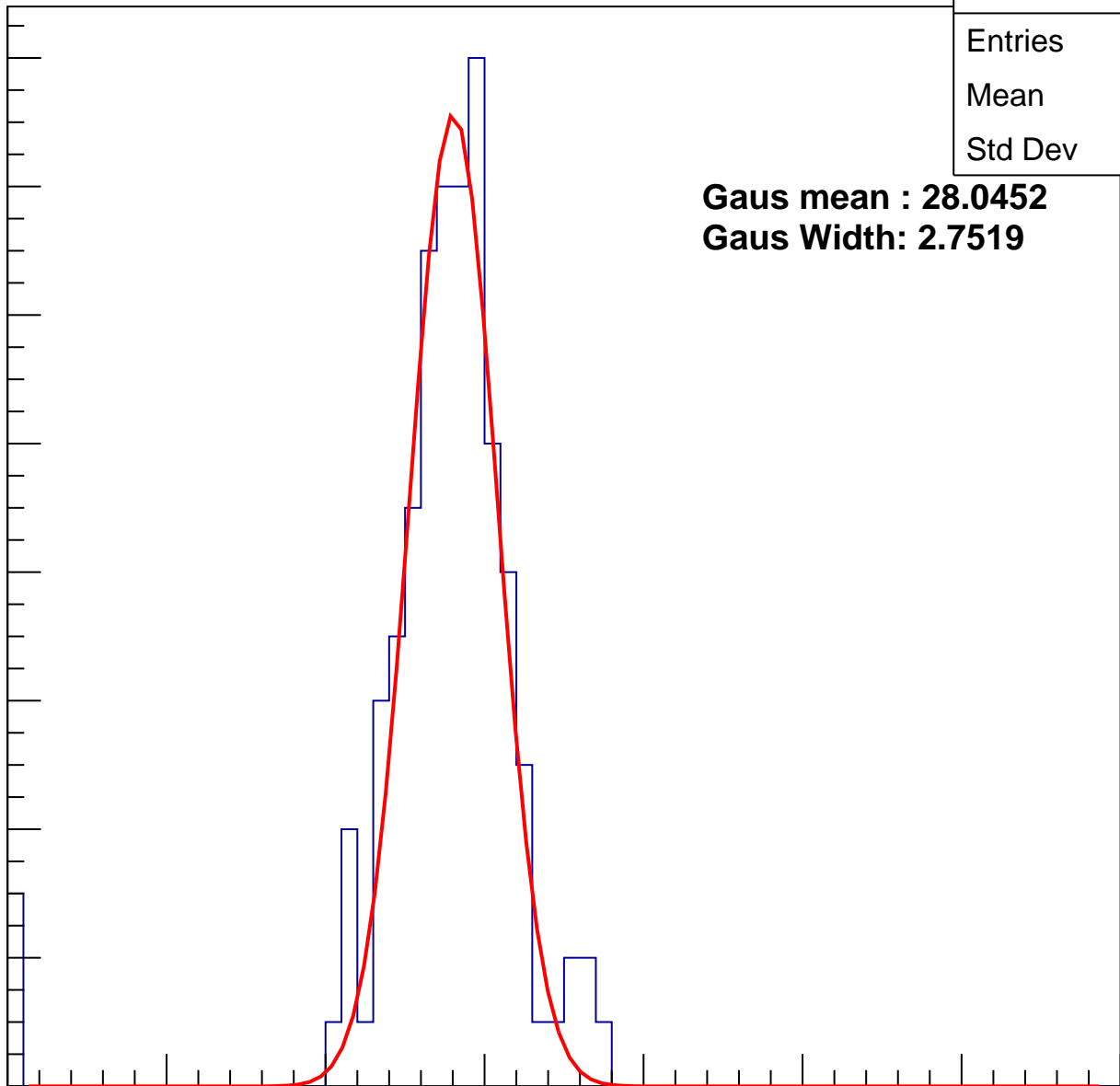
Gaus Width: 2.7519

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch14, adc1

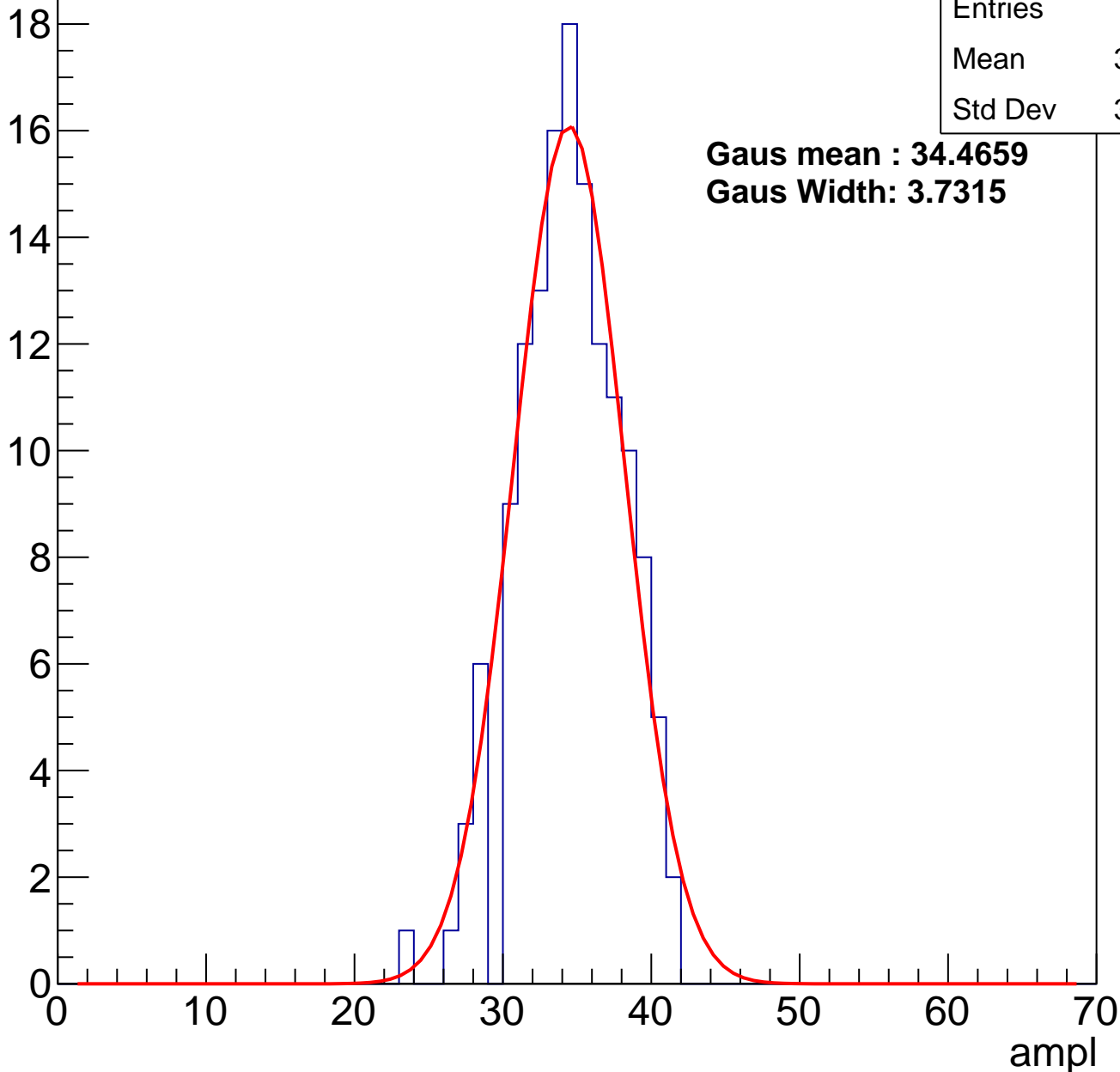
calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	34.04
Std Dev	3.427

Gaus mean : 34.4659

Gaus Width: 3.7315

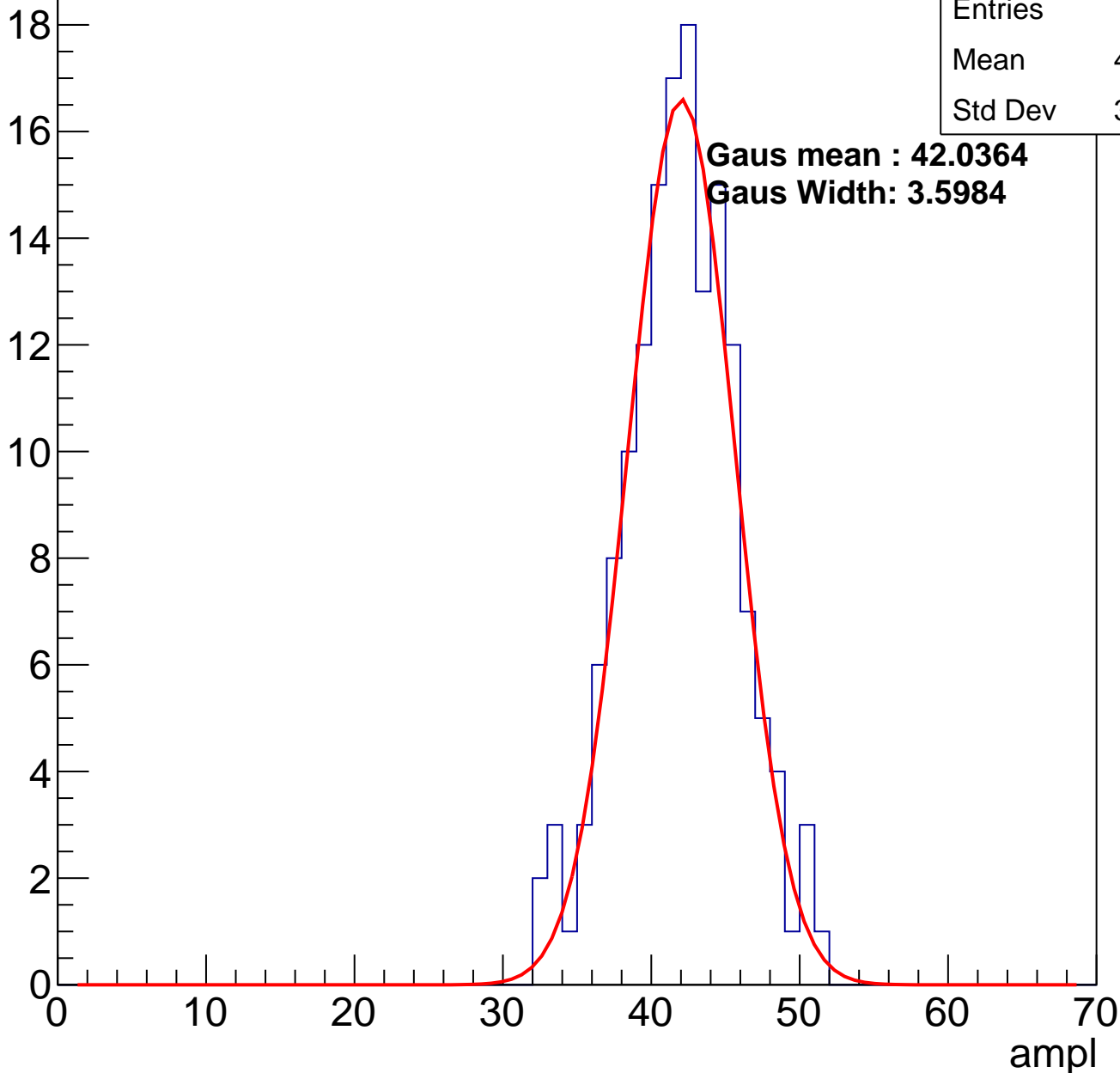
Entry



B0L101S, U15-ch14, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch14, adc3

calib_packv5_042523_0143.root, FC#1, port C1

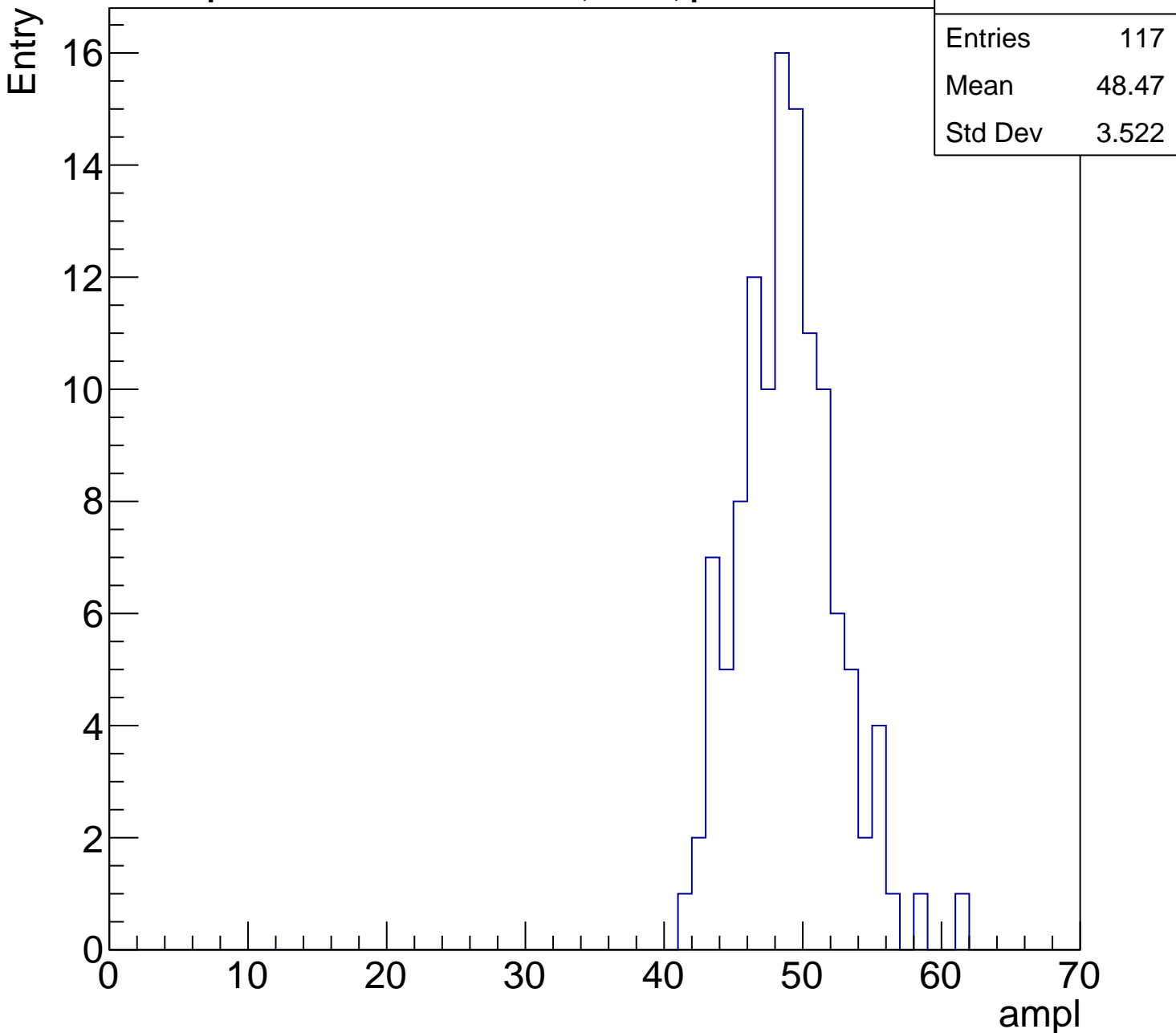
Entries	117
Mean	48.47
Std Dev	3.522

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch14, adc4

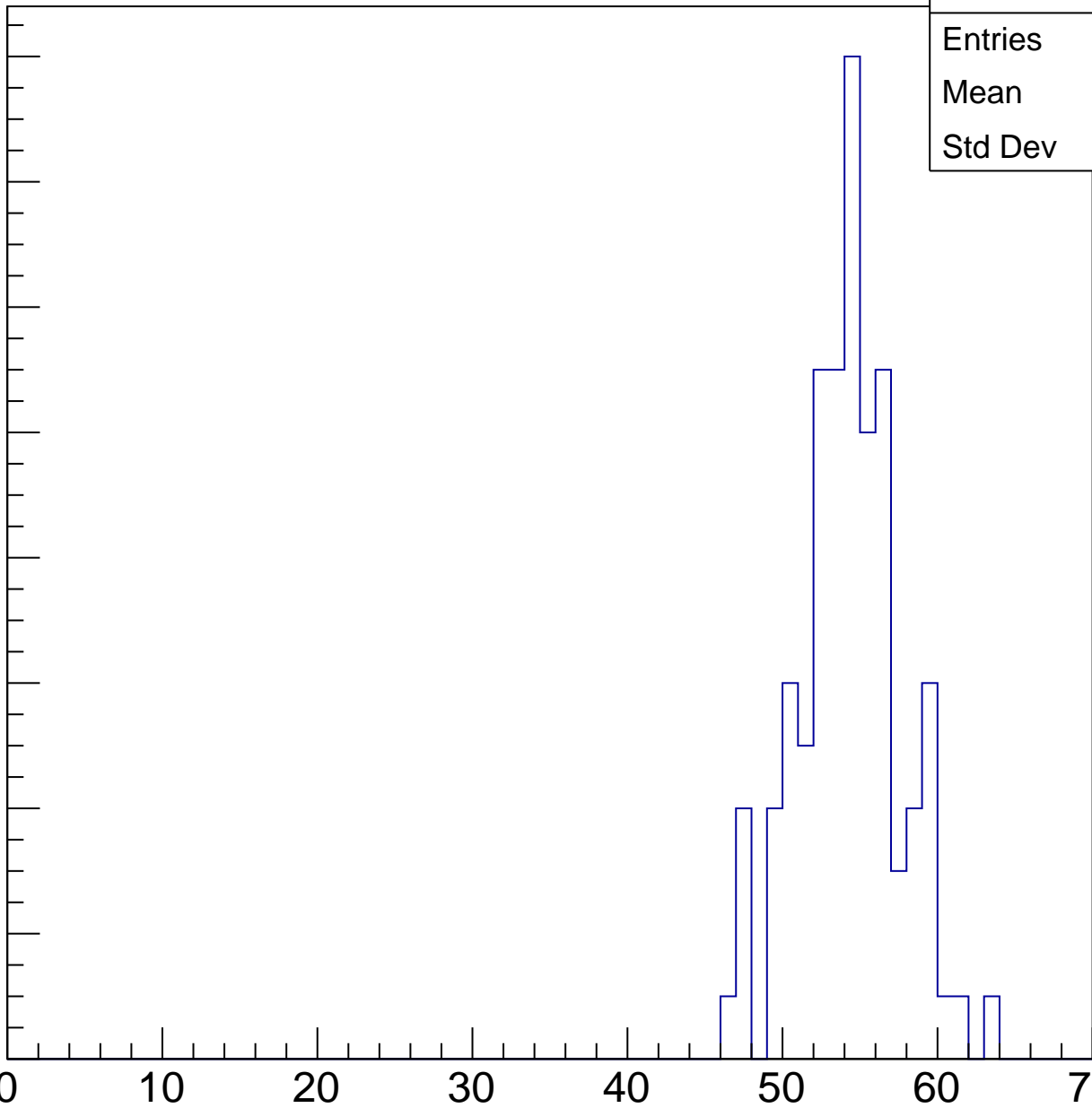
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	95
Mean	53.8
Std Dev	3.266

ampl



B0L101S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#1, port C1

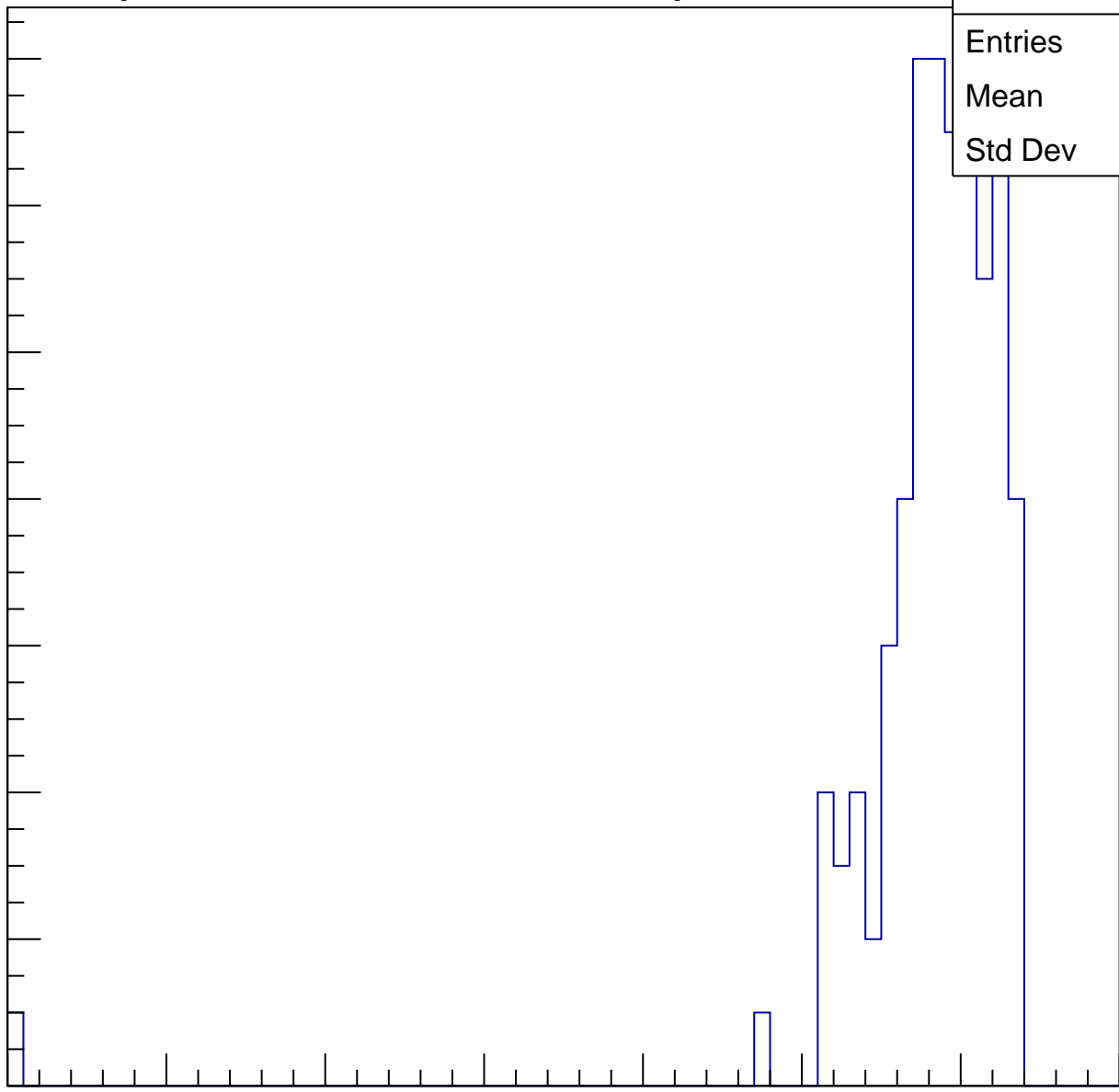
Entries	116
Mean	57.78
Std Dev	6.291

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

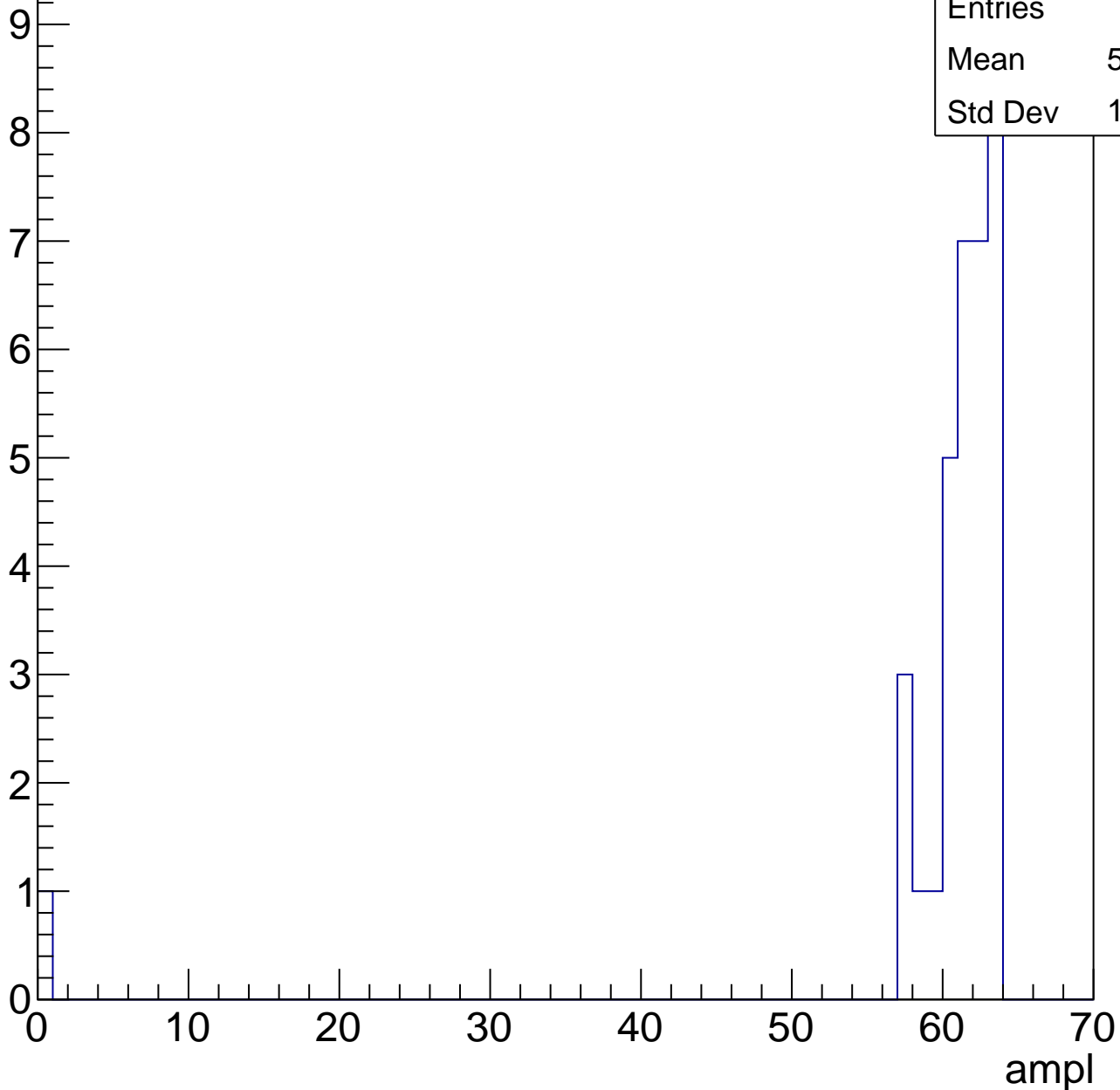


B0L101S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	34
Mean	59.29
Std Dev	10.48



B0L101S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch15, adc0

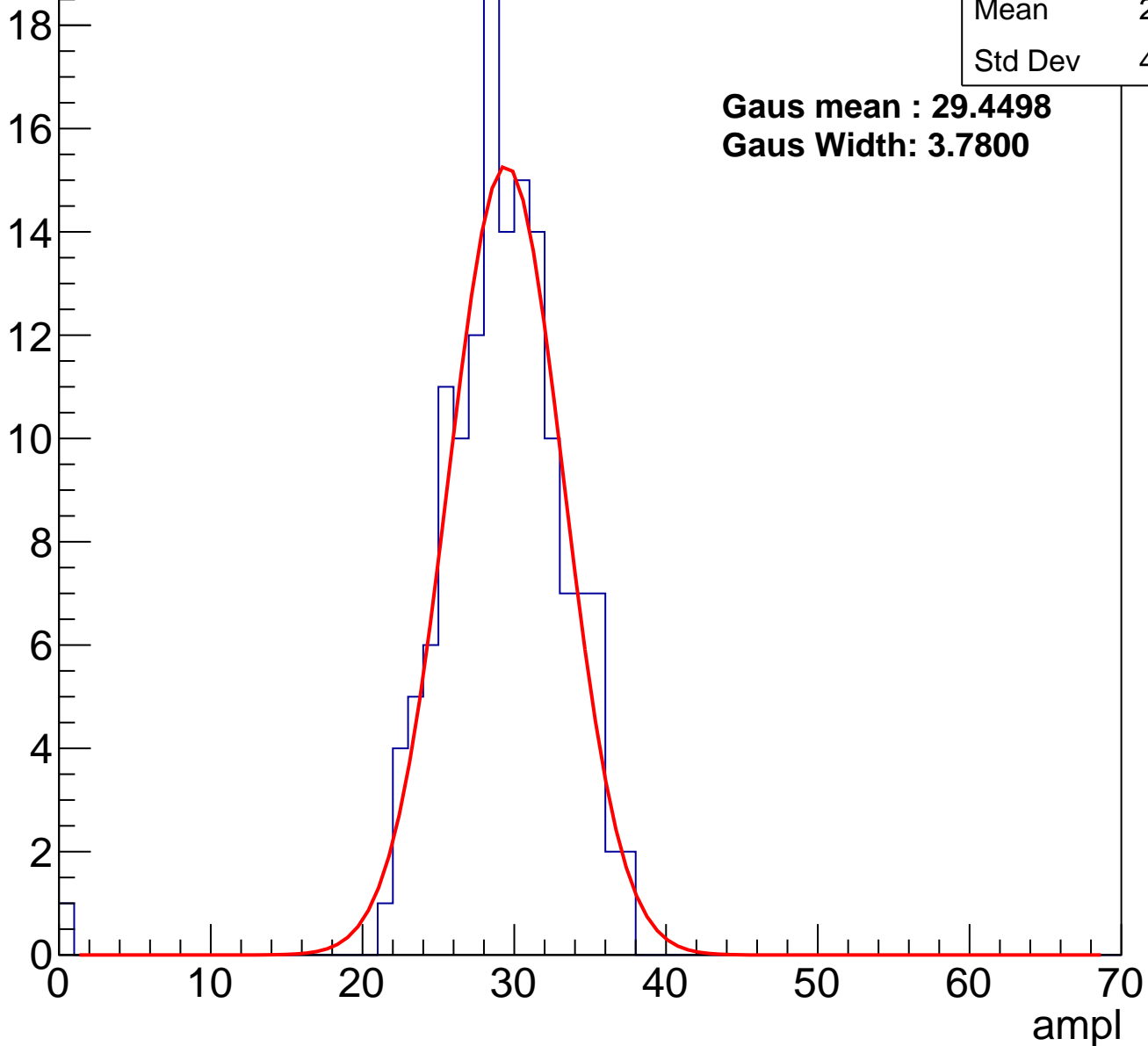
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	28.77
Std Dev	4.272

Gaus mean : 29.4498

Gaus Width: 3.7800

Entry



B0L101S, U15-ch15, adc1

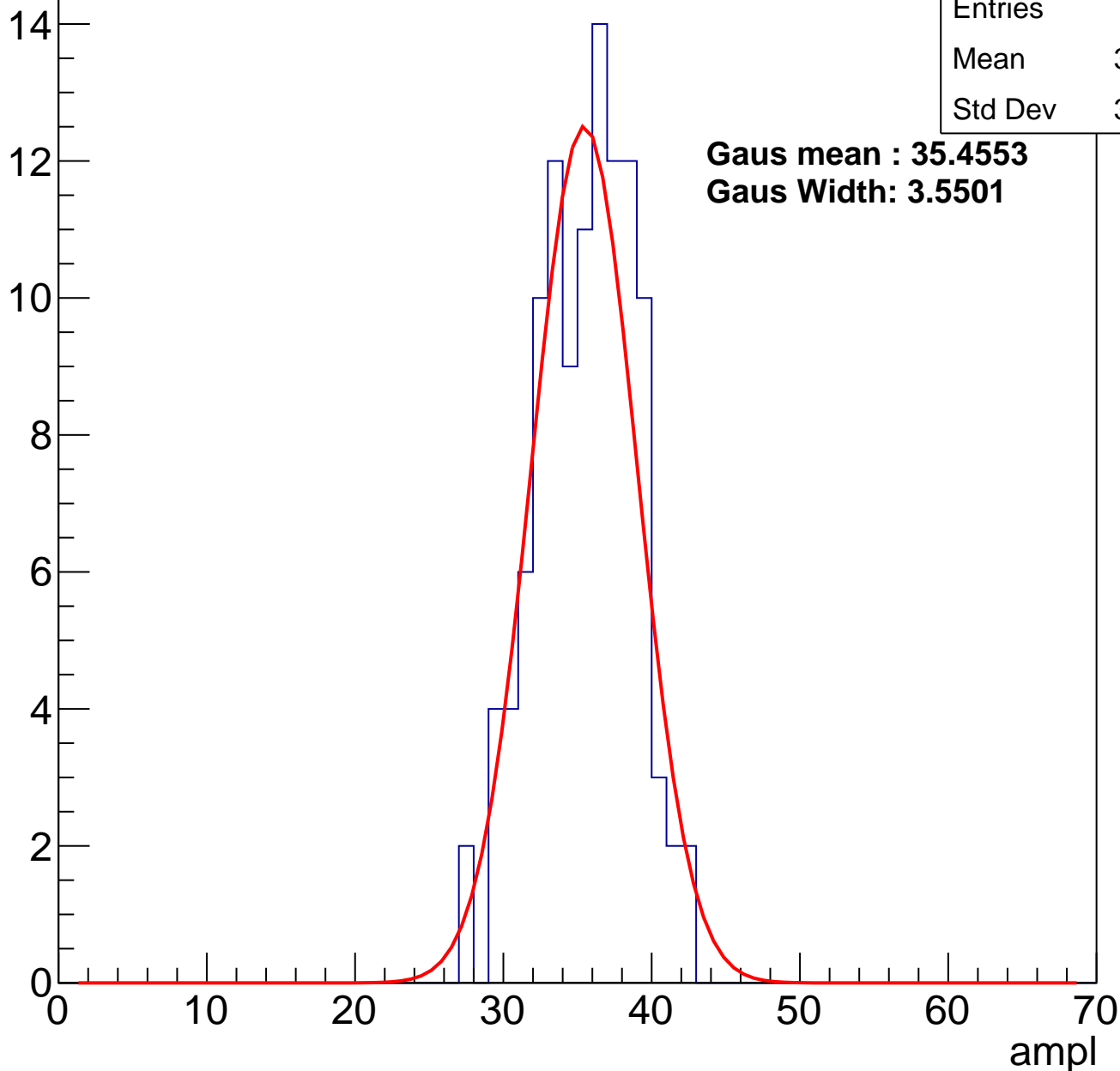
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.07
Std Dev	3.244

Gaus mean : 35.4553

Gaus Width: 3.5501

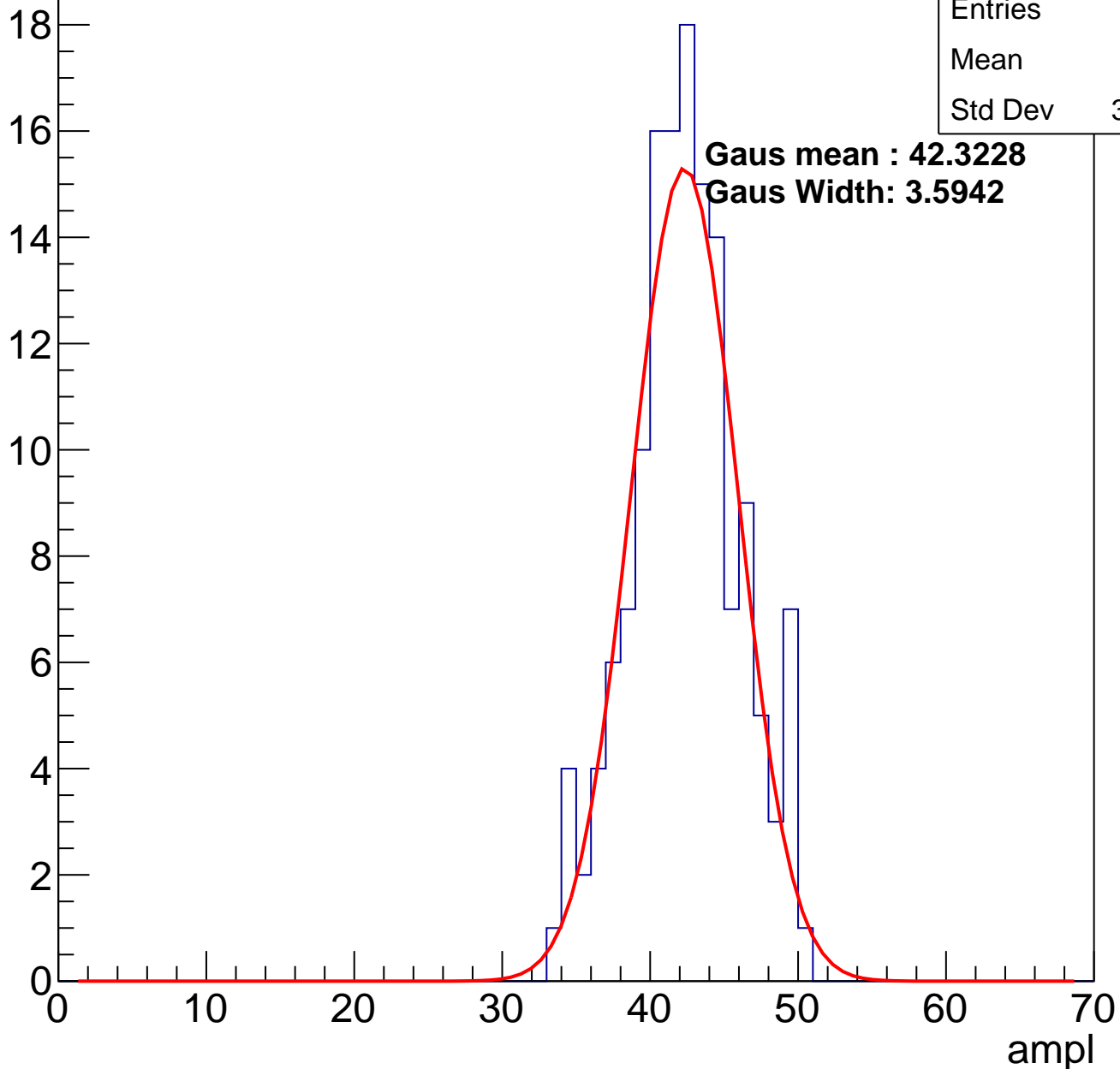
Entry



B0L101S, U15-ch15, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch15, adc3

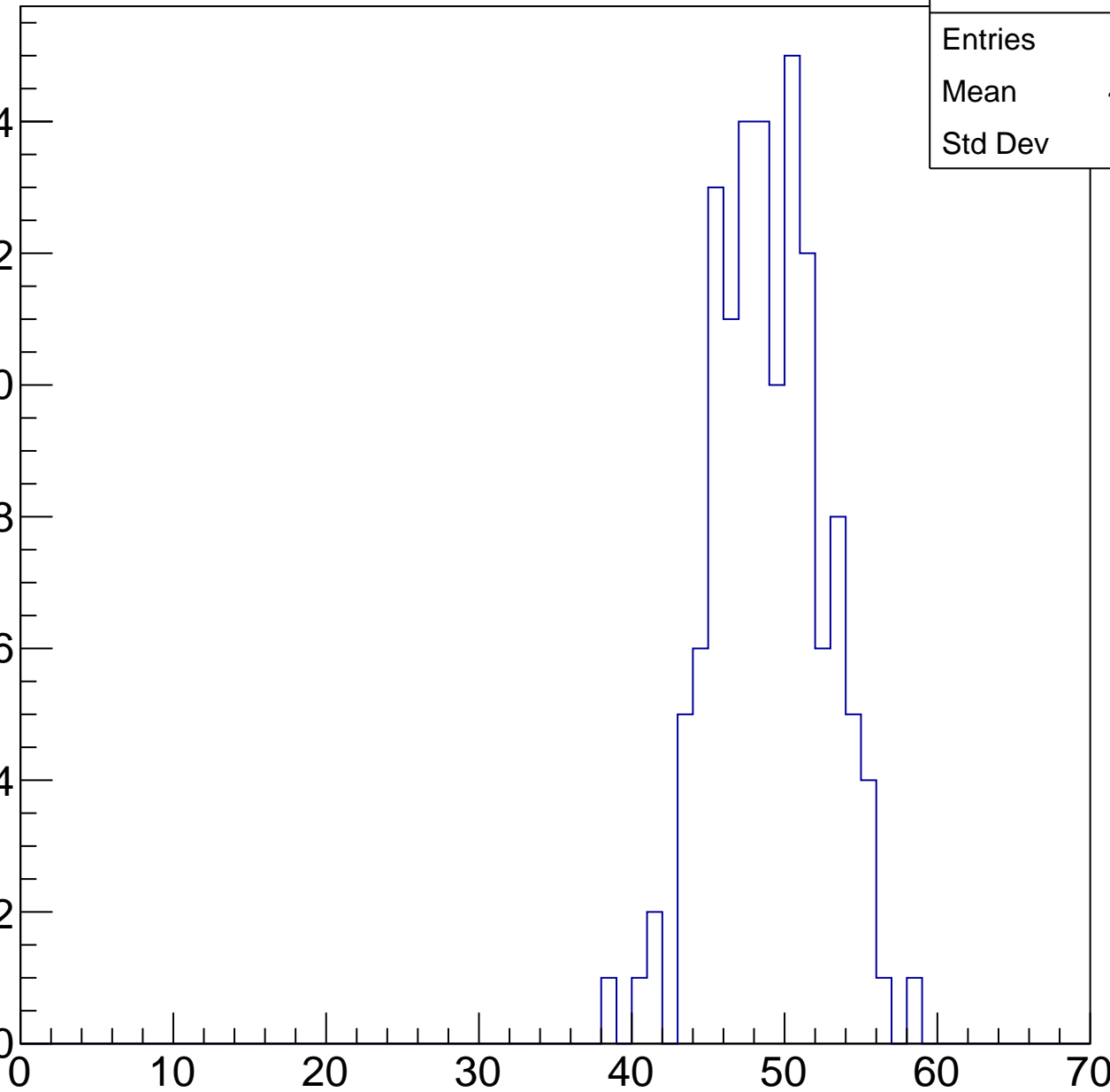
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	48.47
Std Dev	3.57

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch15, adc4

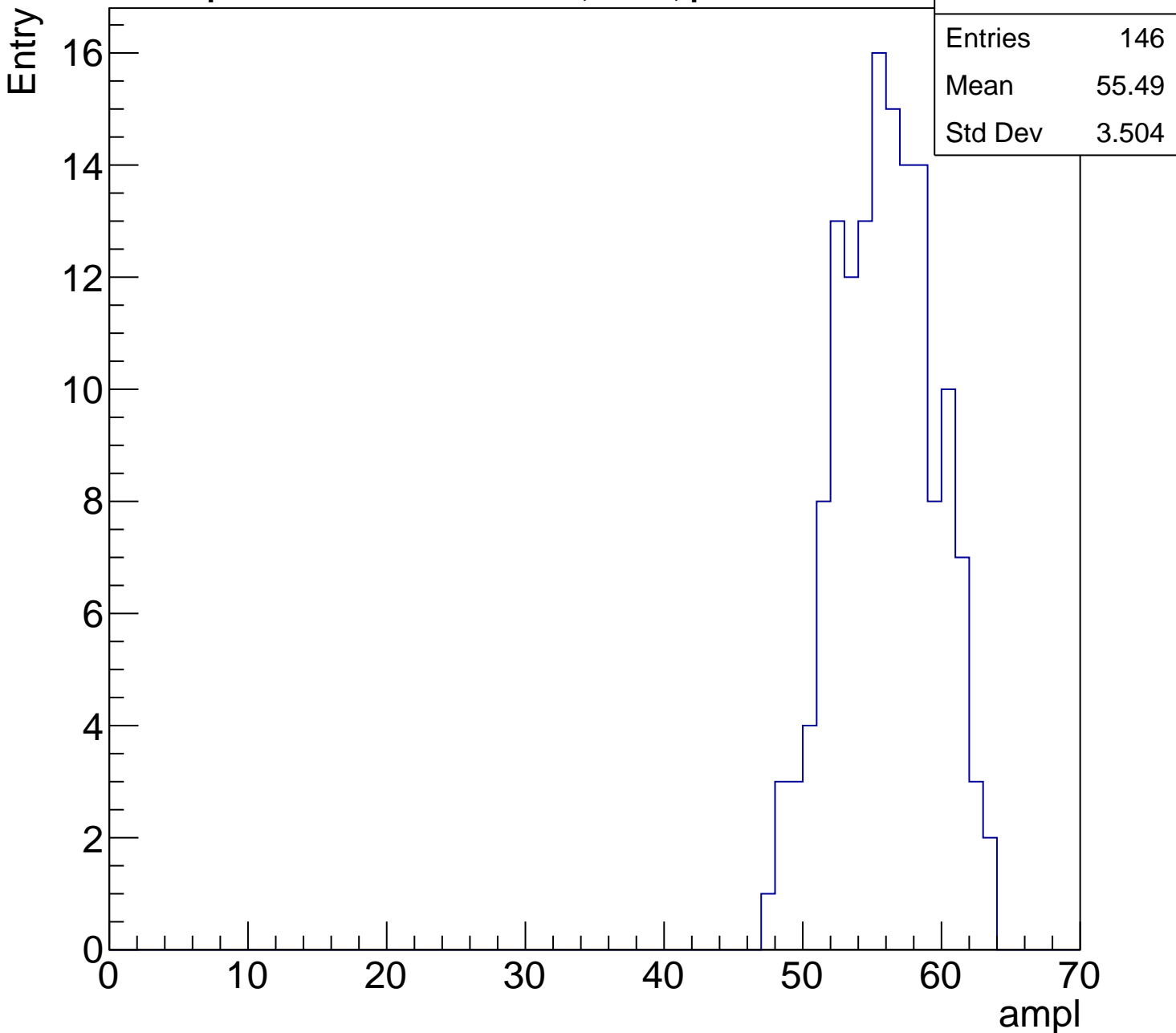
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	55.49
Std Dev	3.504

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch15, adc5

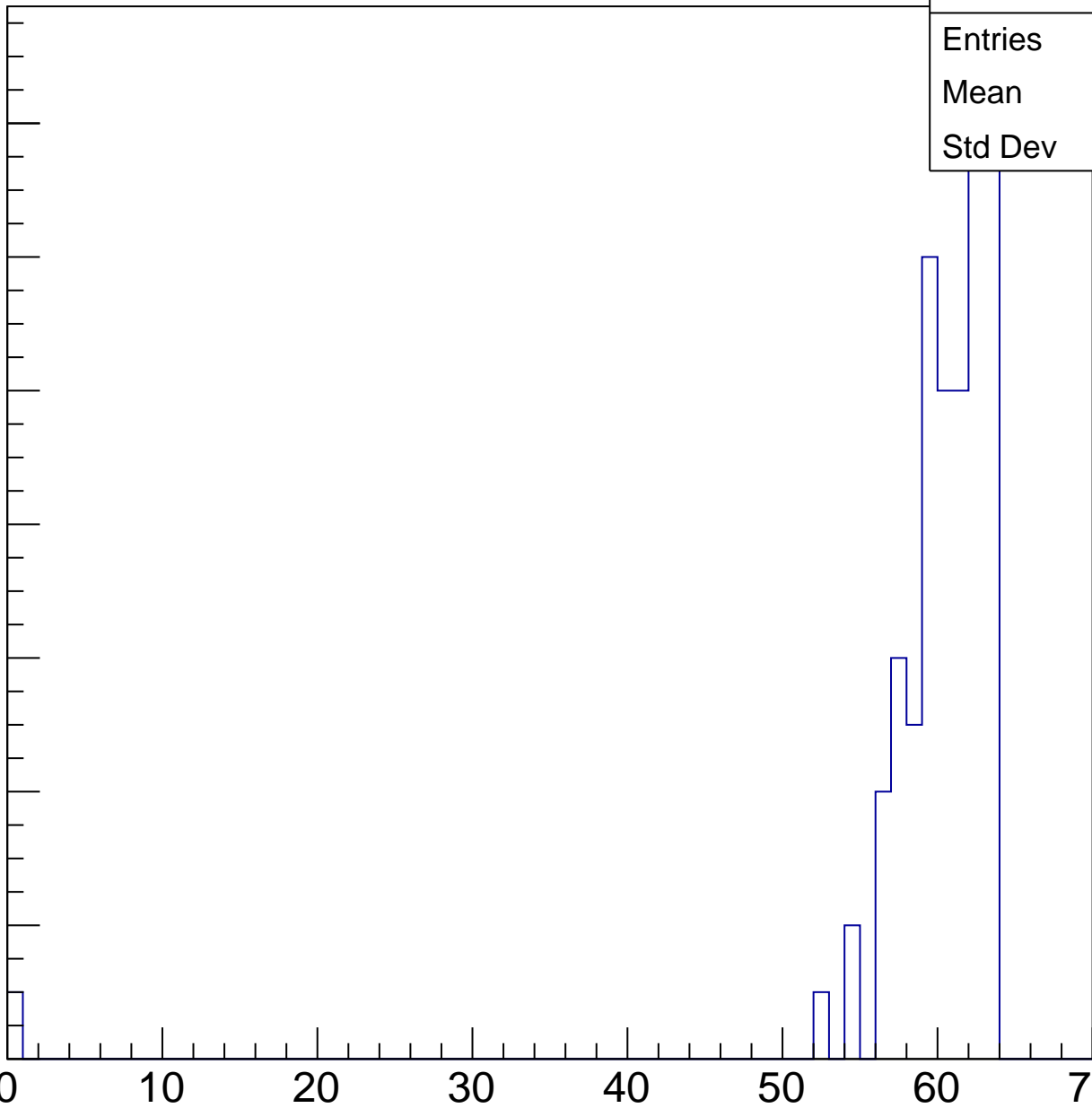
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	59.37
Std Dev	7.079

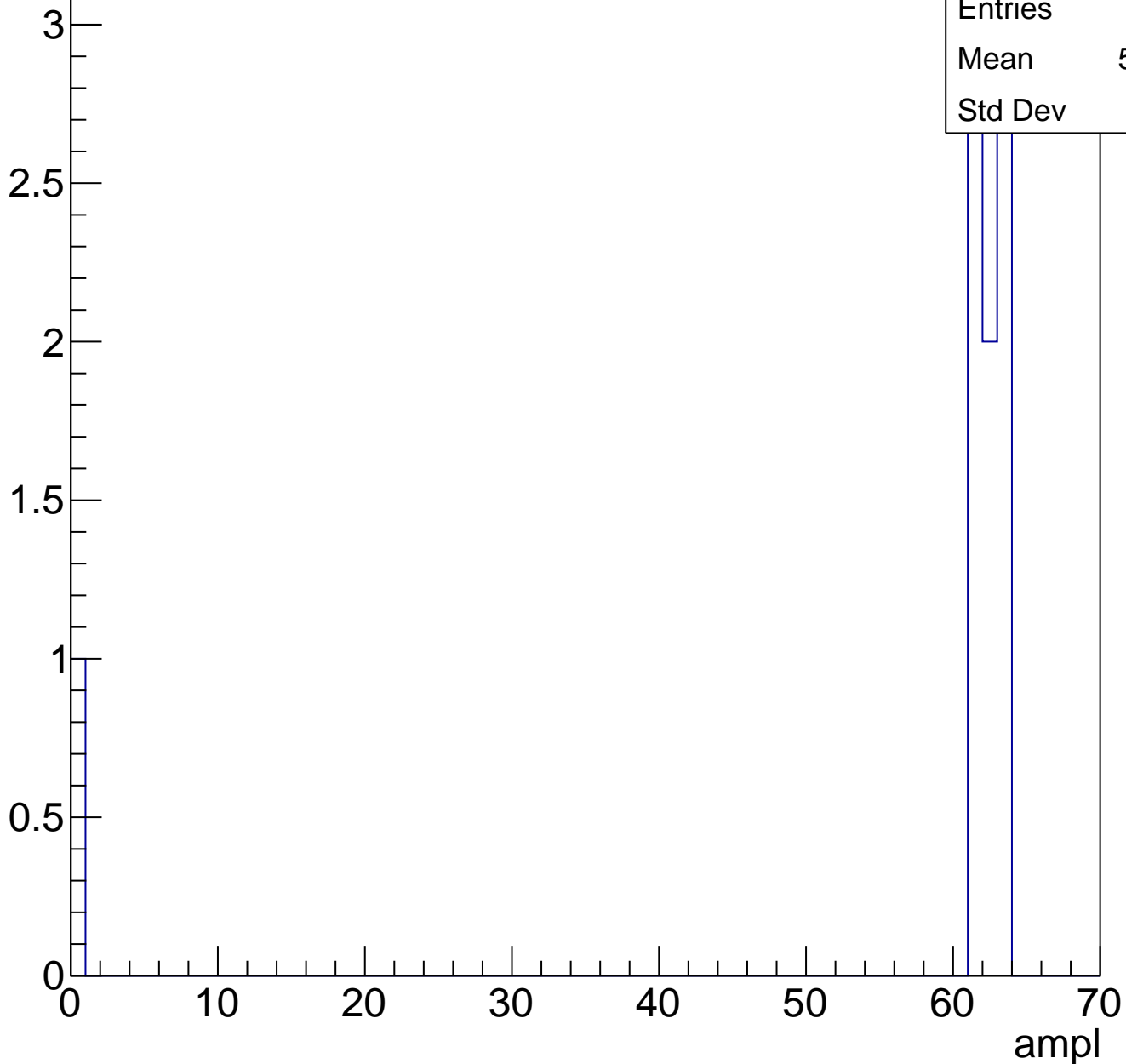
ampl



B0L101S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch16, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	29.01
Std Dev	4.199

Gaus mean : 29.7976

Gaus Width: 3.1878

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

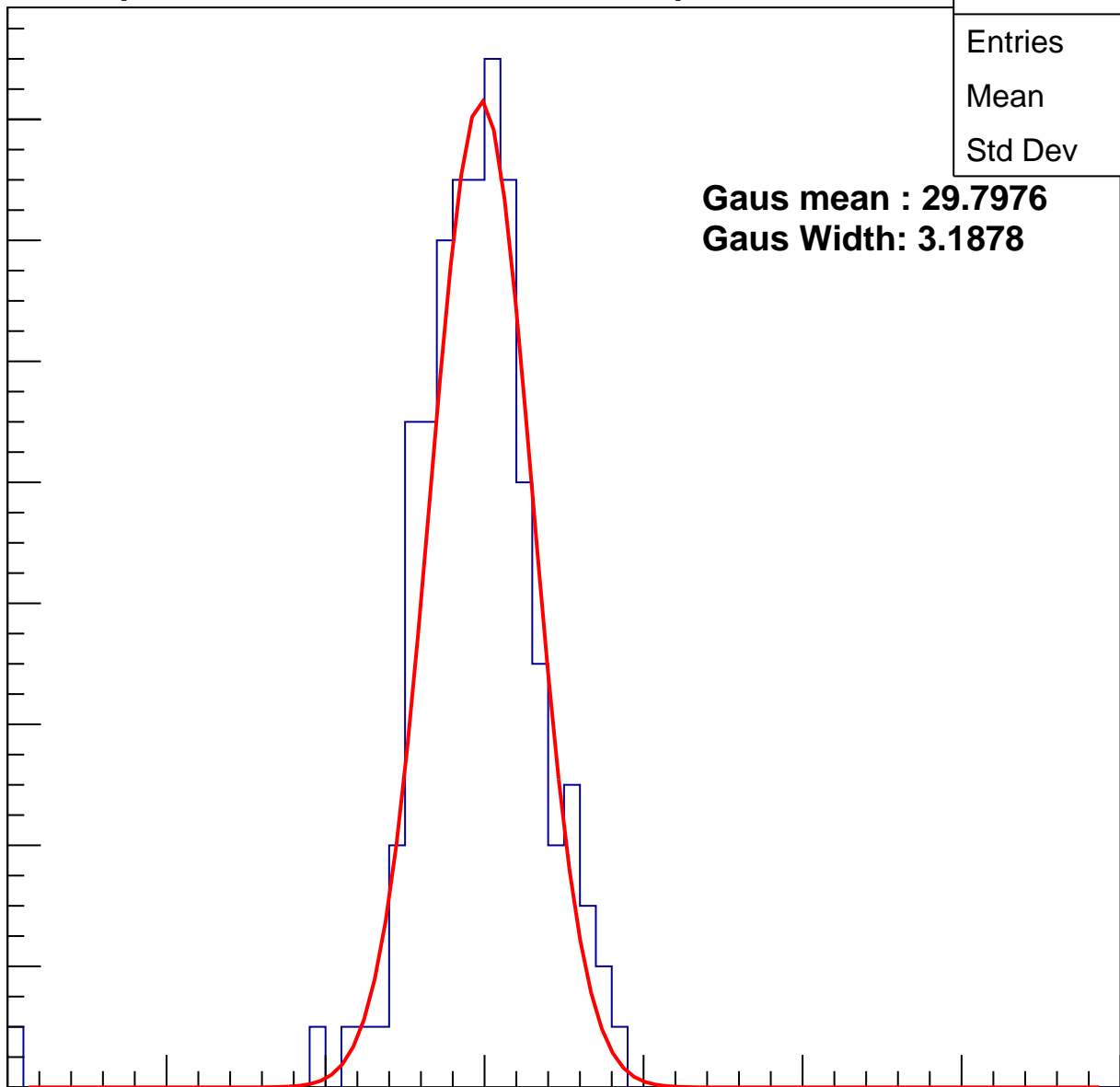
40

50

60

70

ampl



B0L101S, U15-ch16, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	35.05
Std Dev	3.268

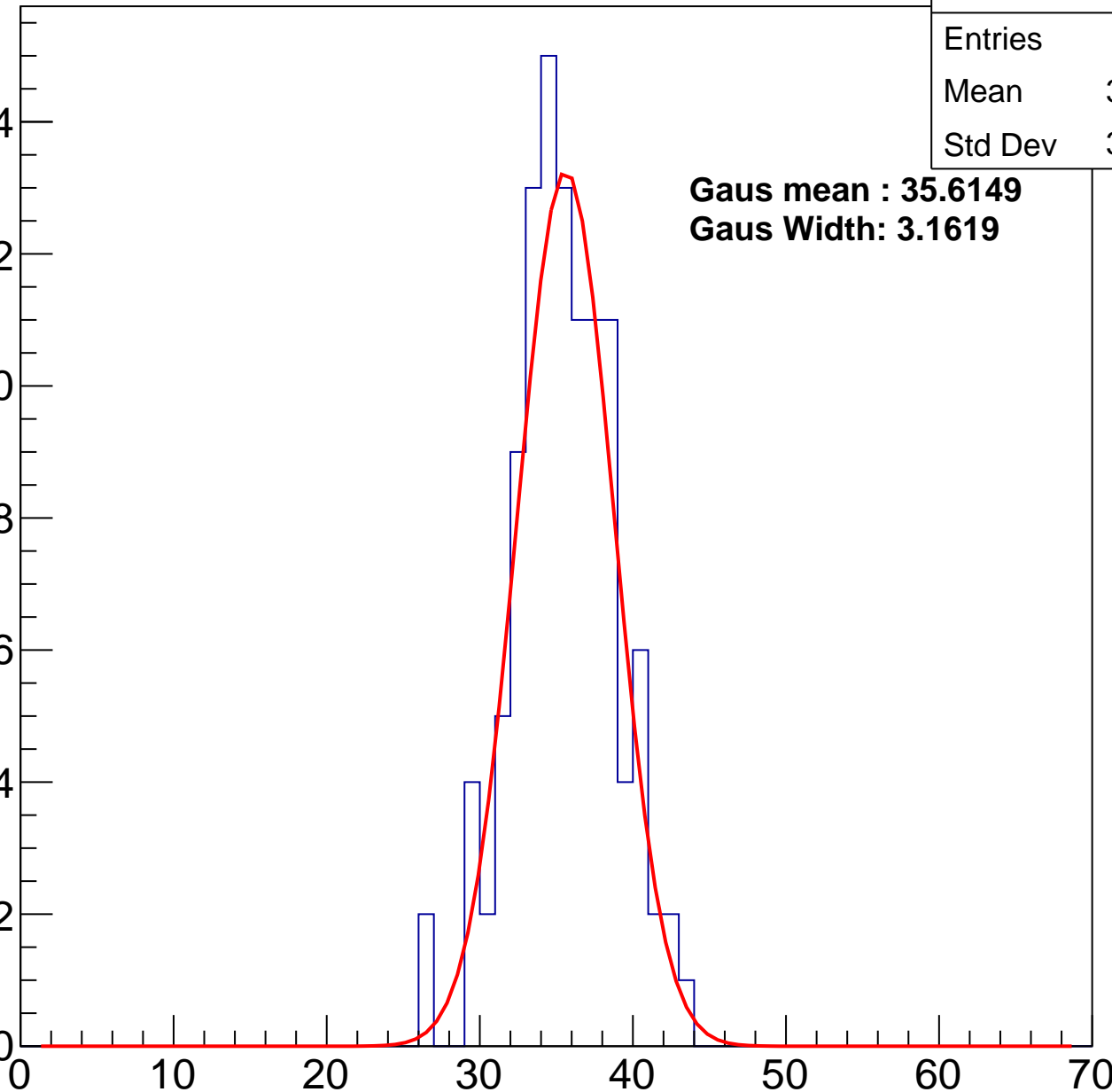
Gaus mean : 35.6149

Gaus Width: 3.1619

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch16, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

148

Mean

41.6

Std Dev

3.31

Gaus mean : 42.3211

Gaus Width: 3.5620

0

10

20

30

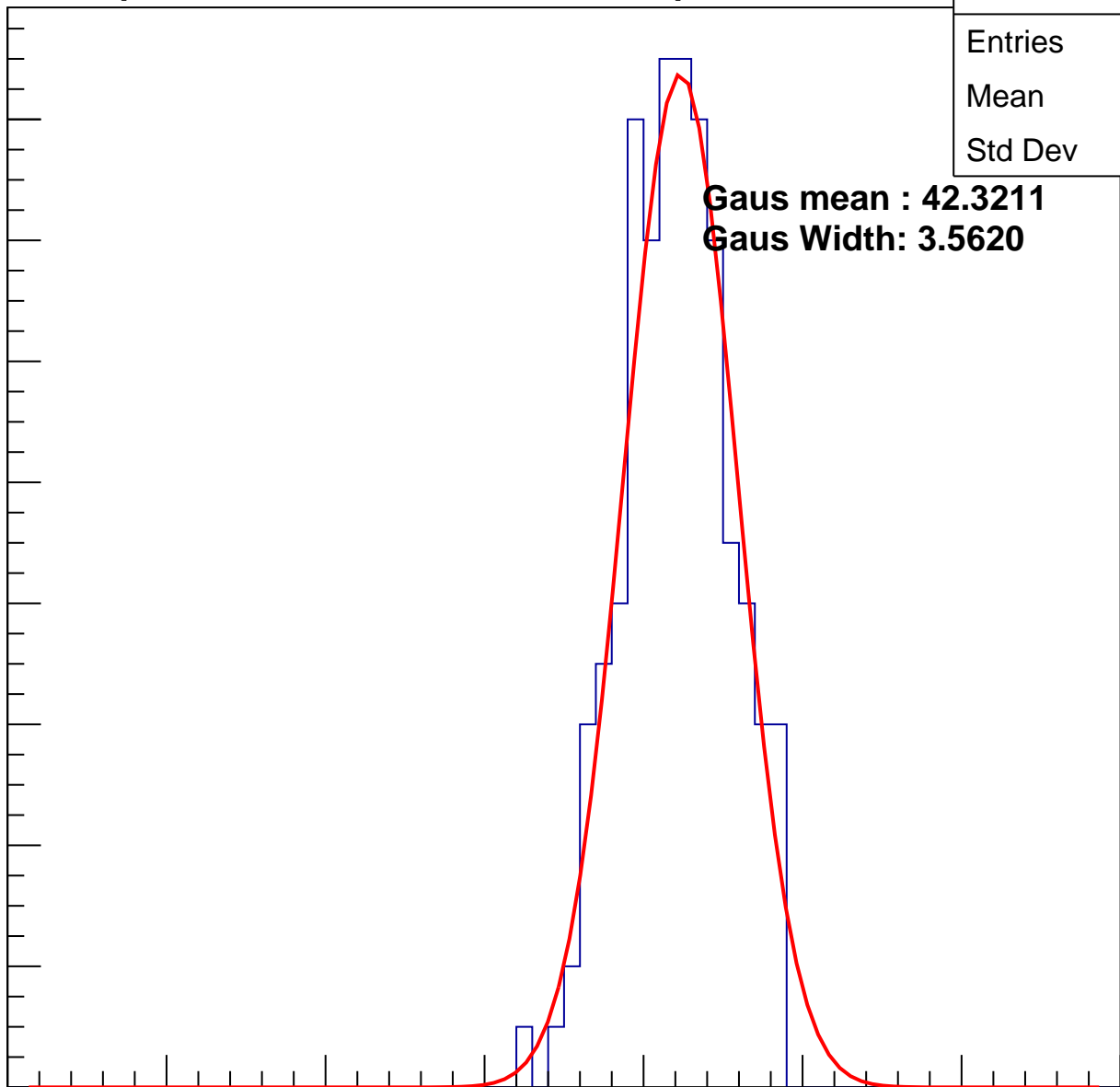
40

50

60

70

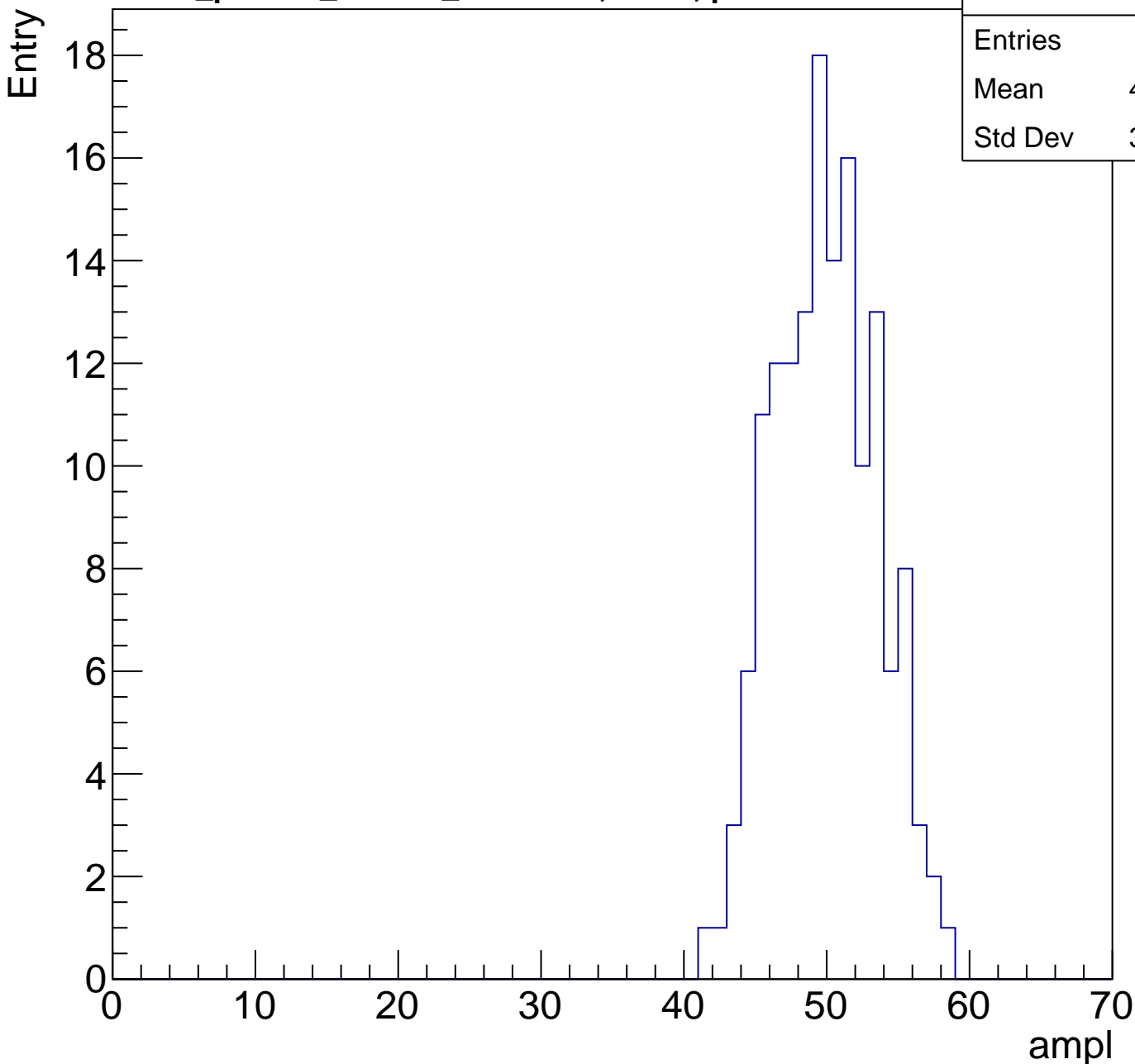
ampl



B0L101S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	49.48
Std Dev	3.513



B0L101S, U15-ch16, adc4

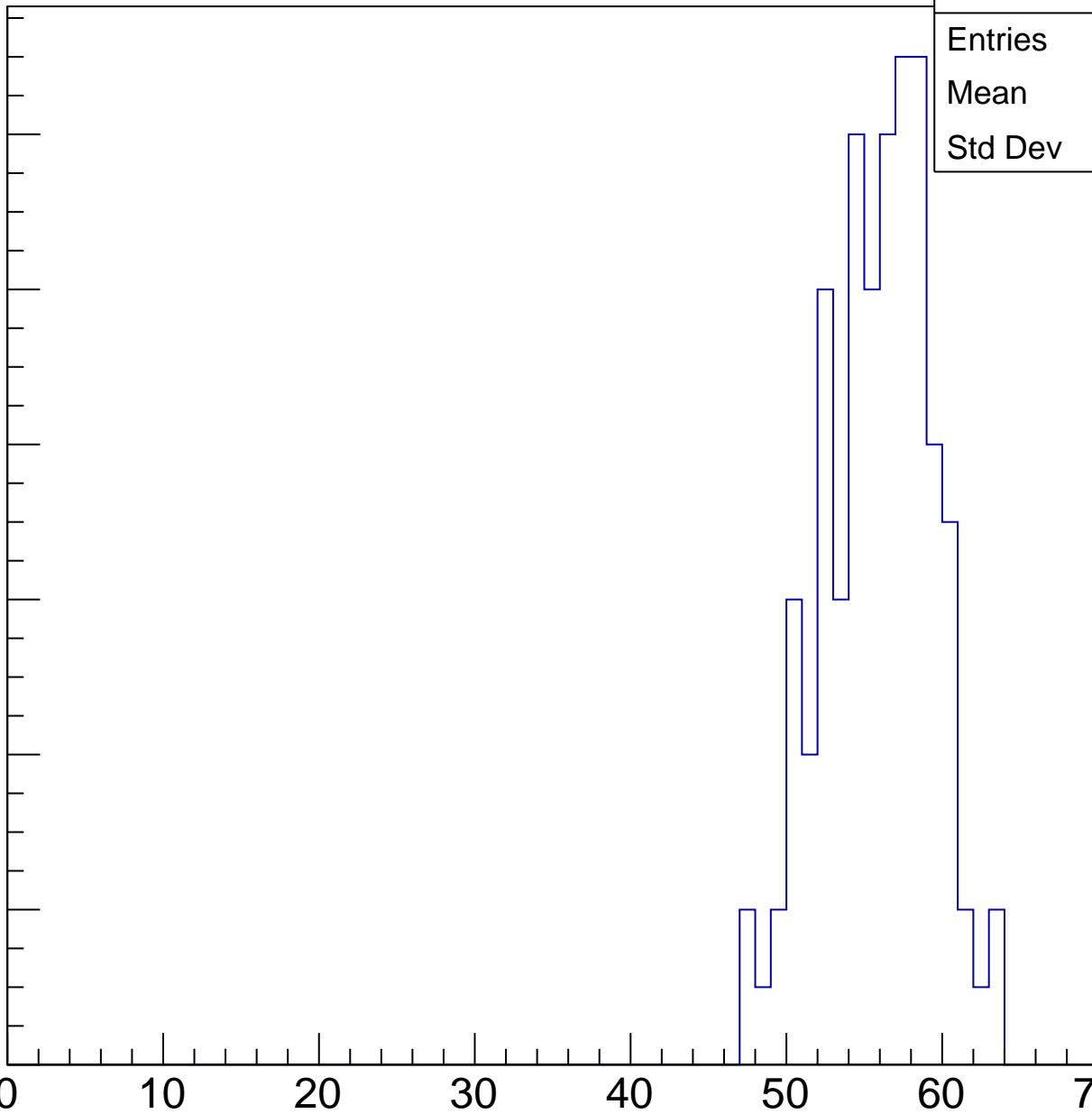
calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	55.4
Std Dev	3.428

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U15-ch16, adc5

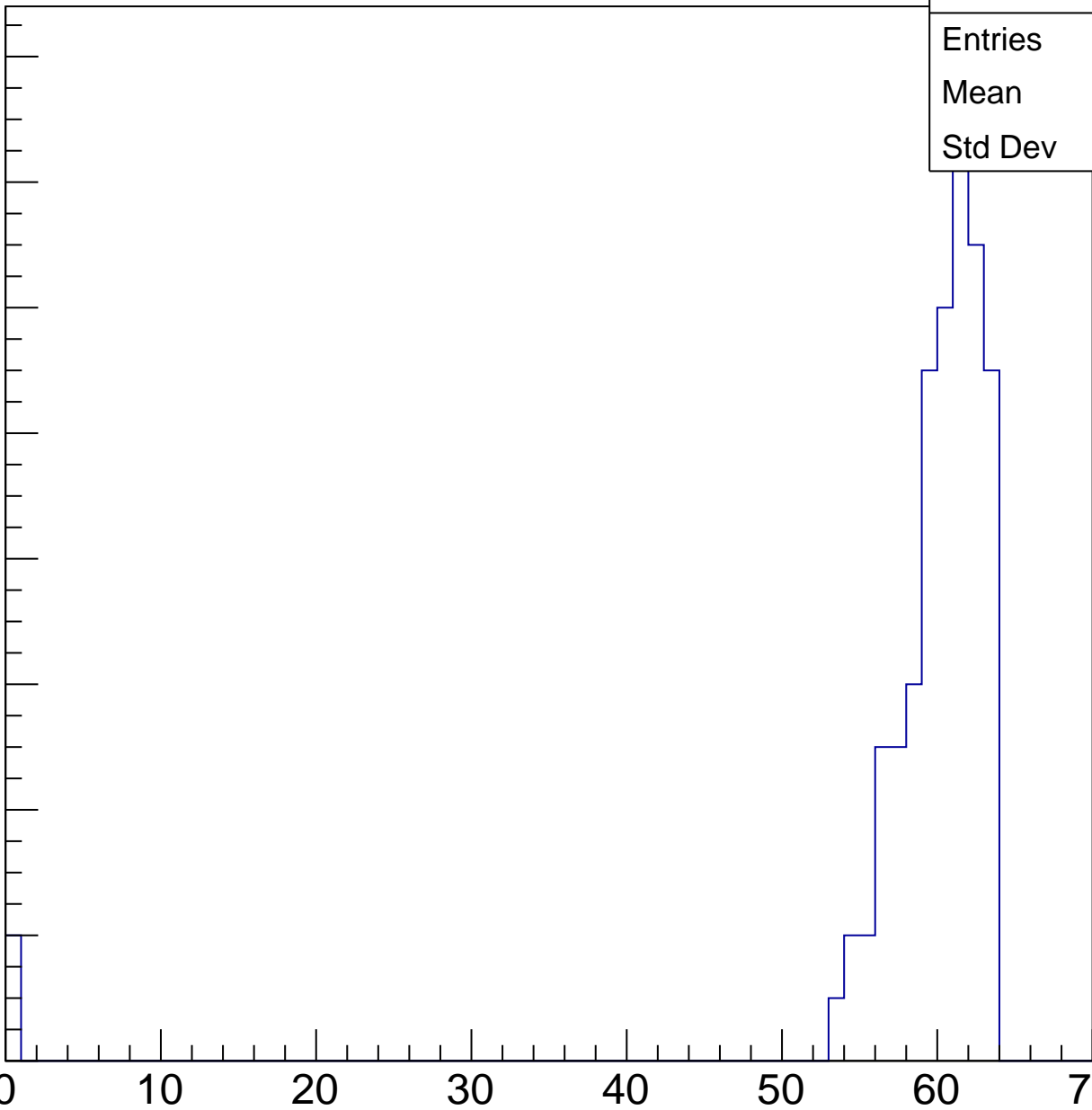
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	58.47
Std Dev	9.336

ampl

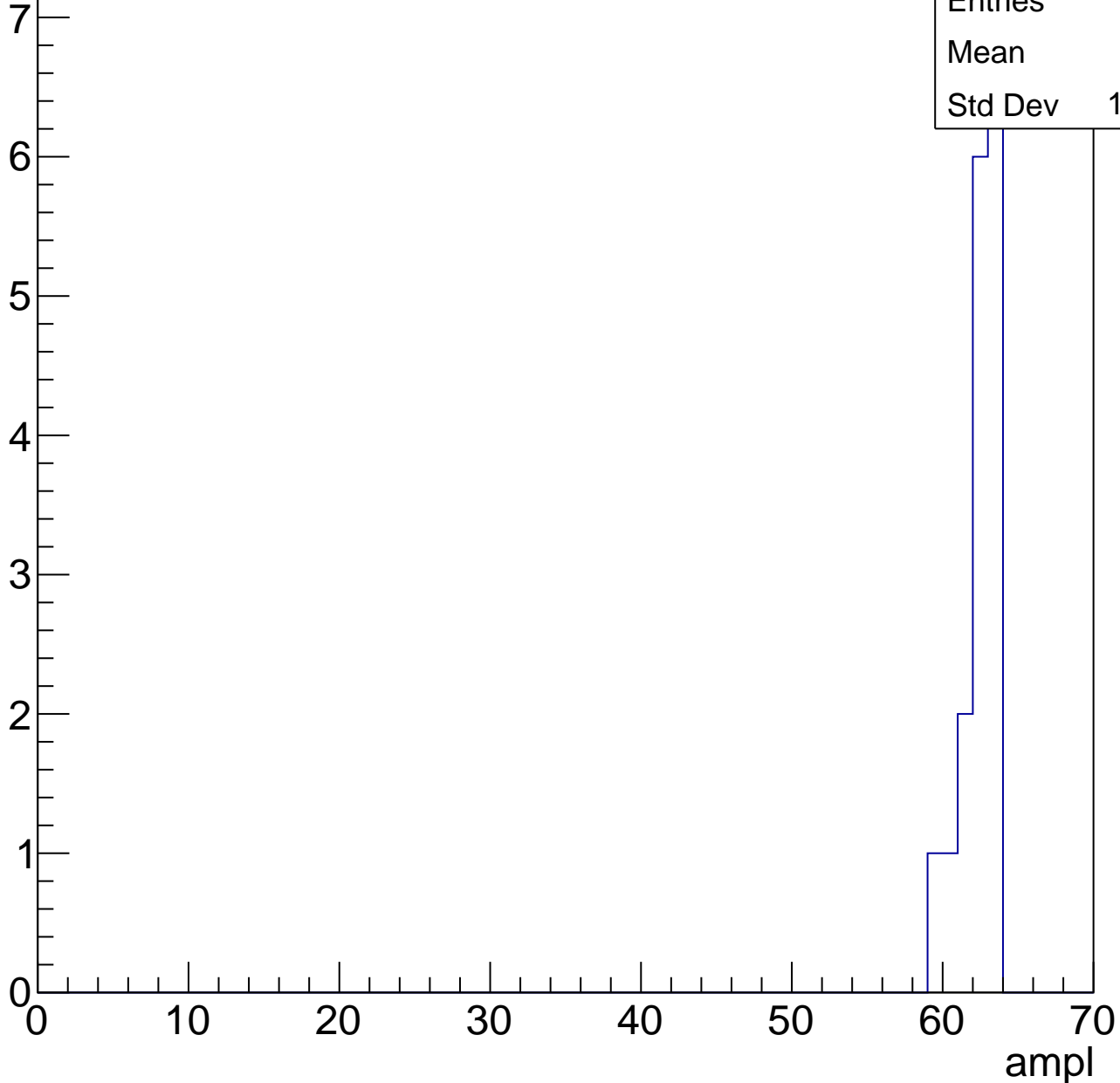


B0L101S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	62
Std Dev	1.138



B0L101S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch17, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	29.13
Std Dev	4.189

Gaus mean : 29.9311

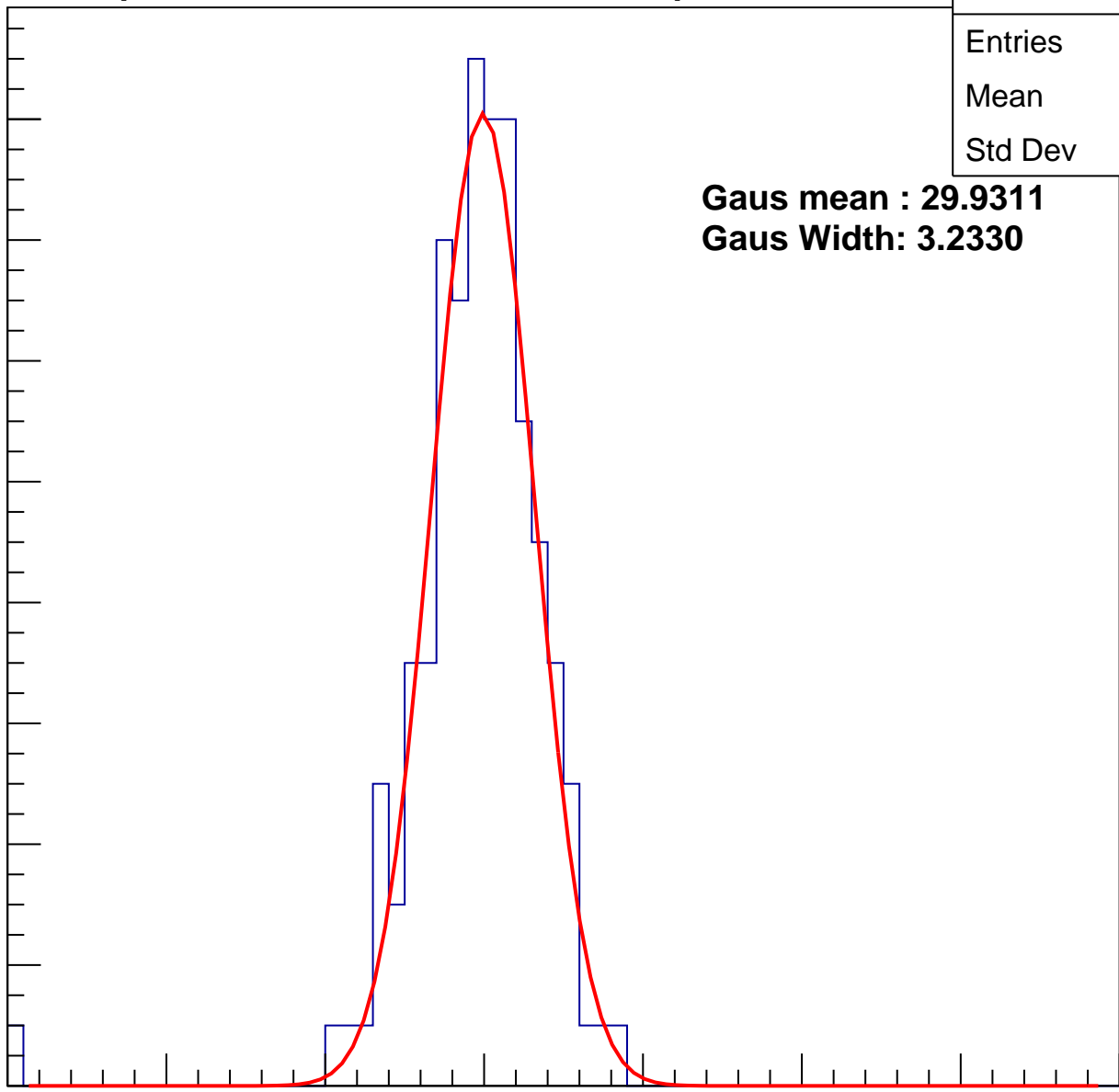
Gaus Width: 3.2330

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch17, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	36.43
Std Dev	3.514

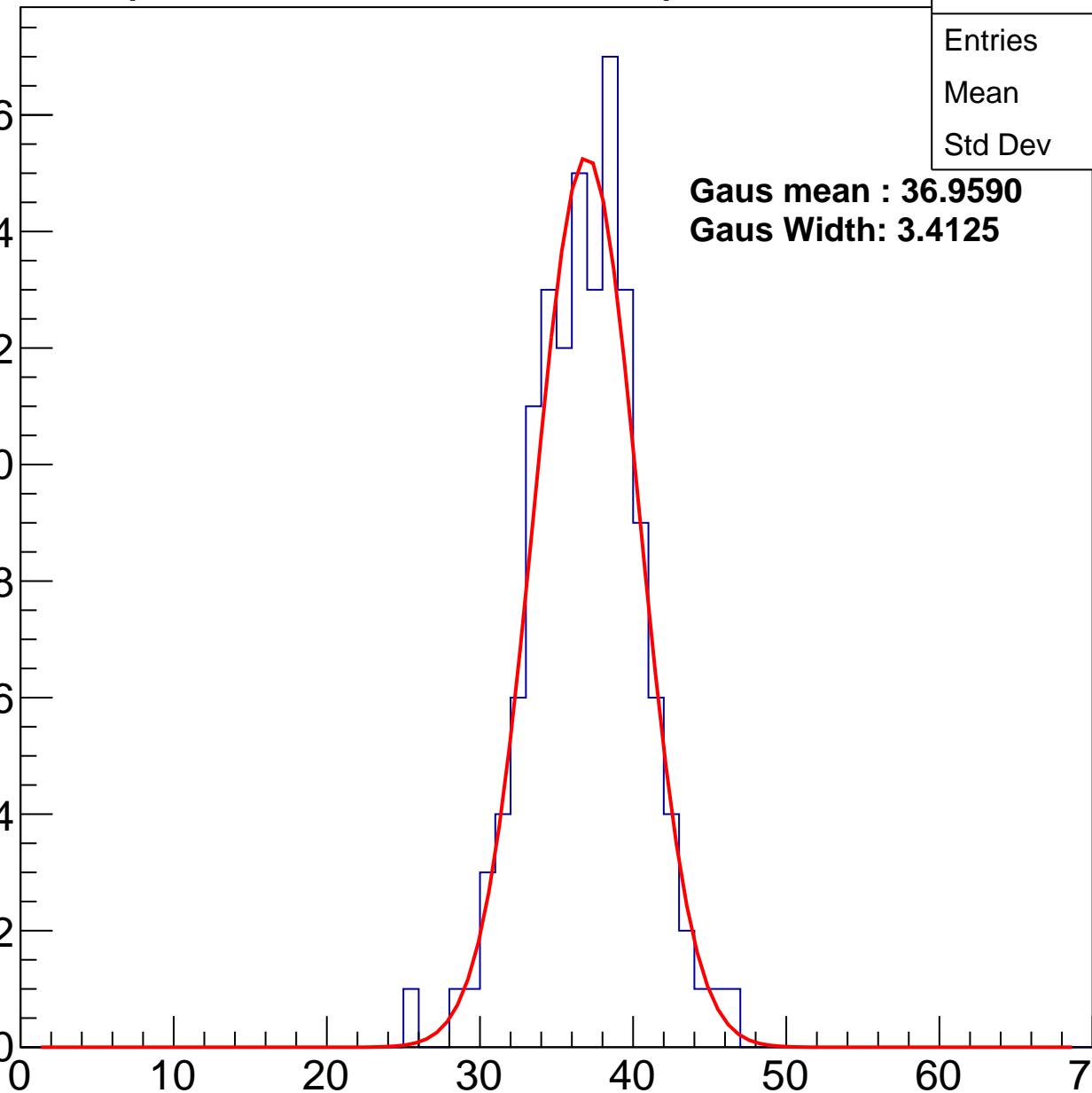
Gaus mean : 36.9590

Gaus Width: 3.4125

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch17, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

160

Mean

43.58

Std Dev

3.915

Gaus mean : 44.2105

Gaus Width: 3.6064

0

10

20

30

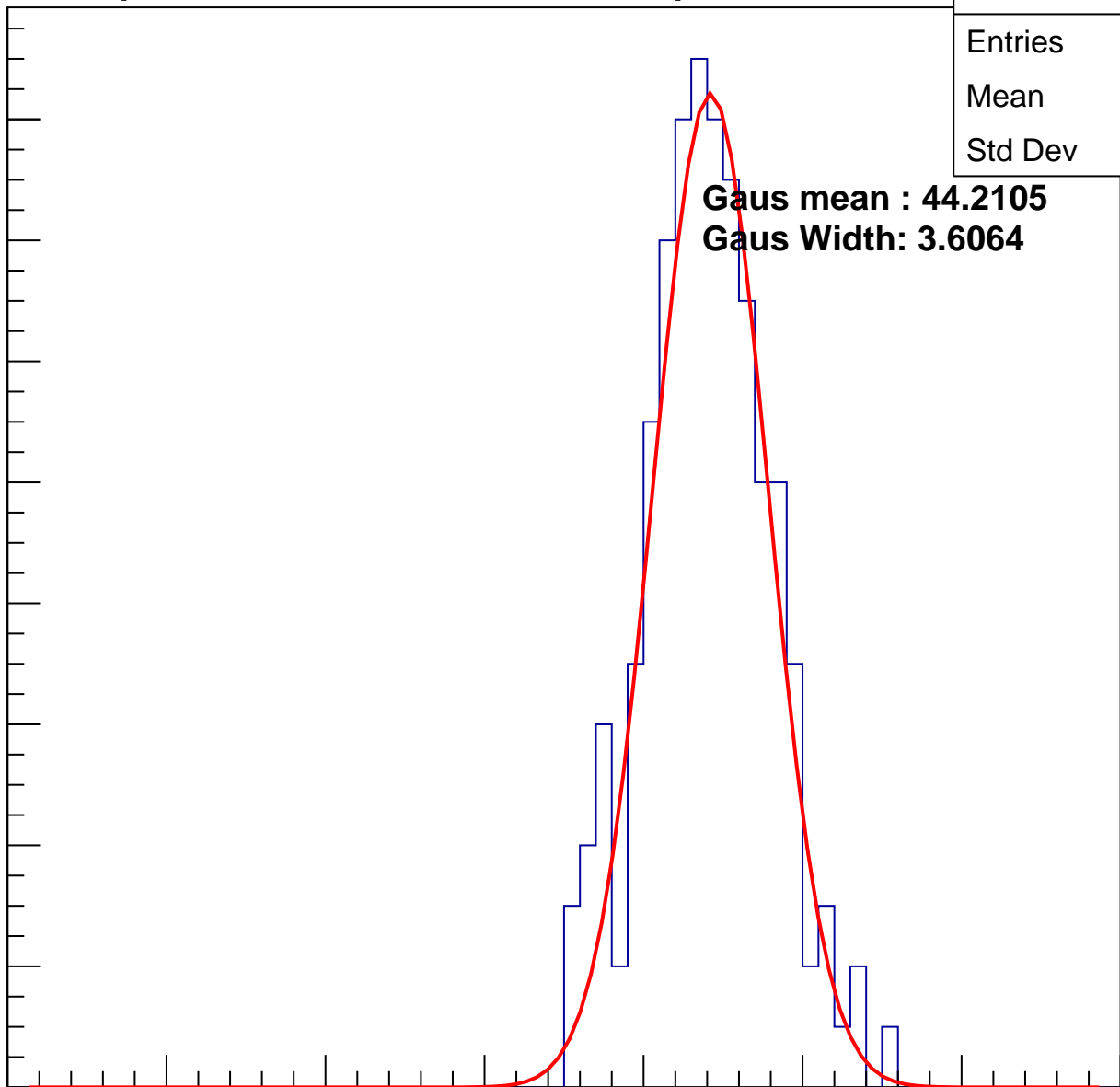
40

50

60

70

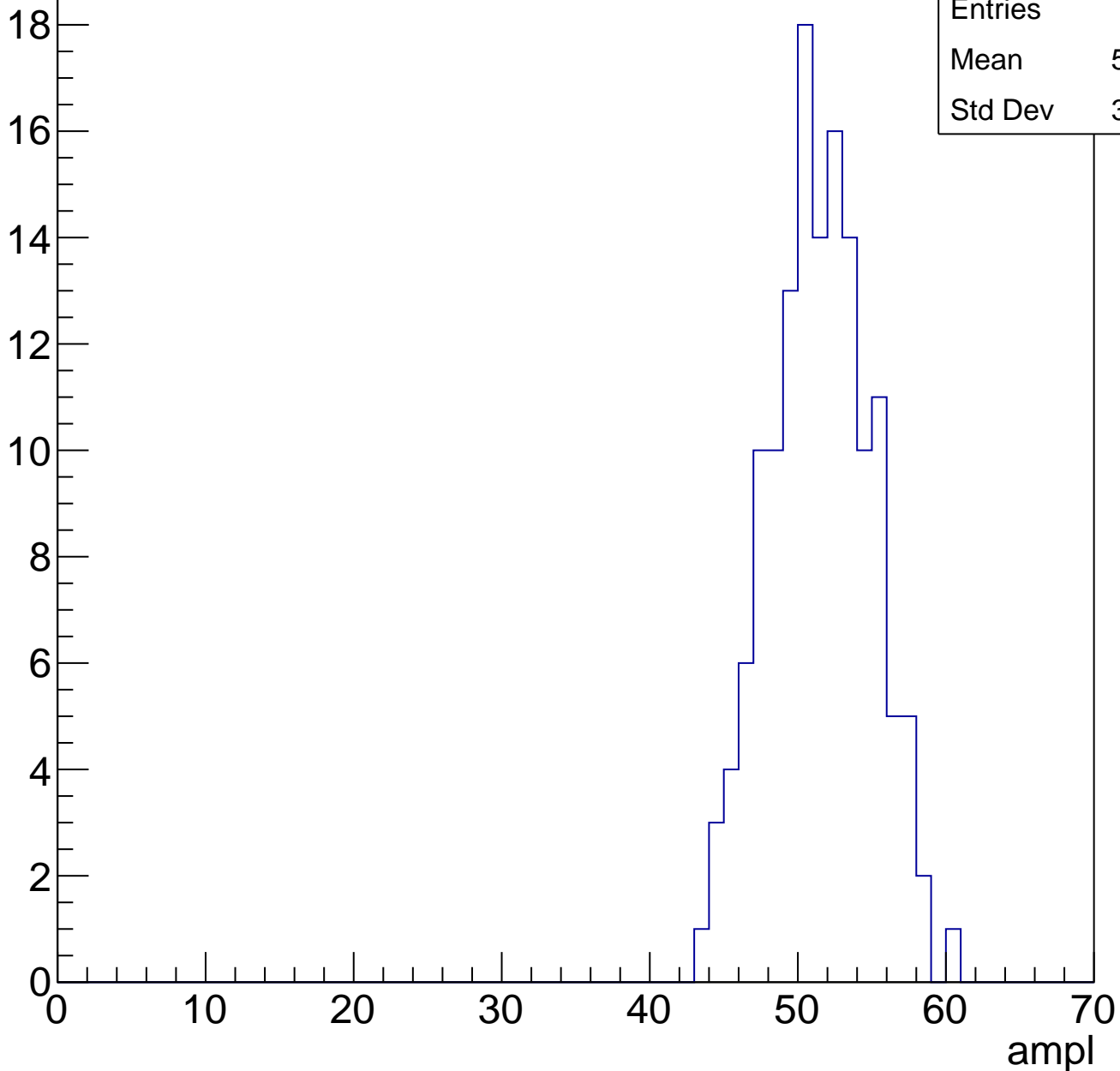
ampl



B0L101S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#1, port C1

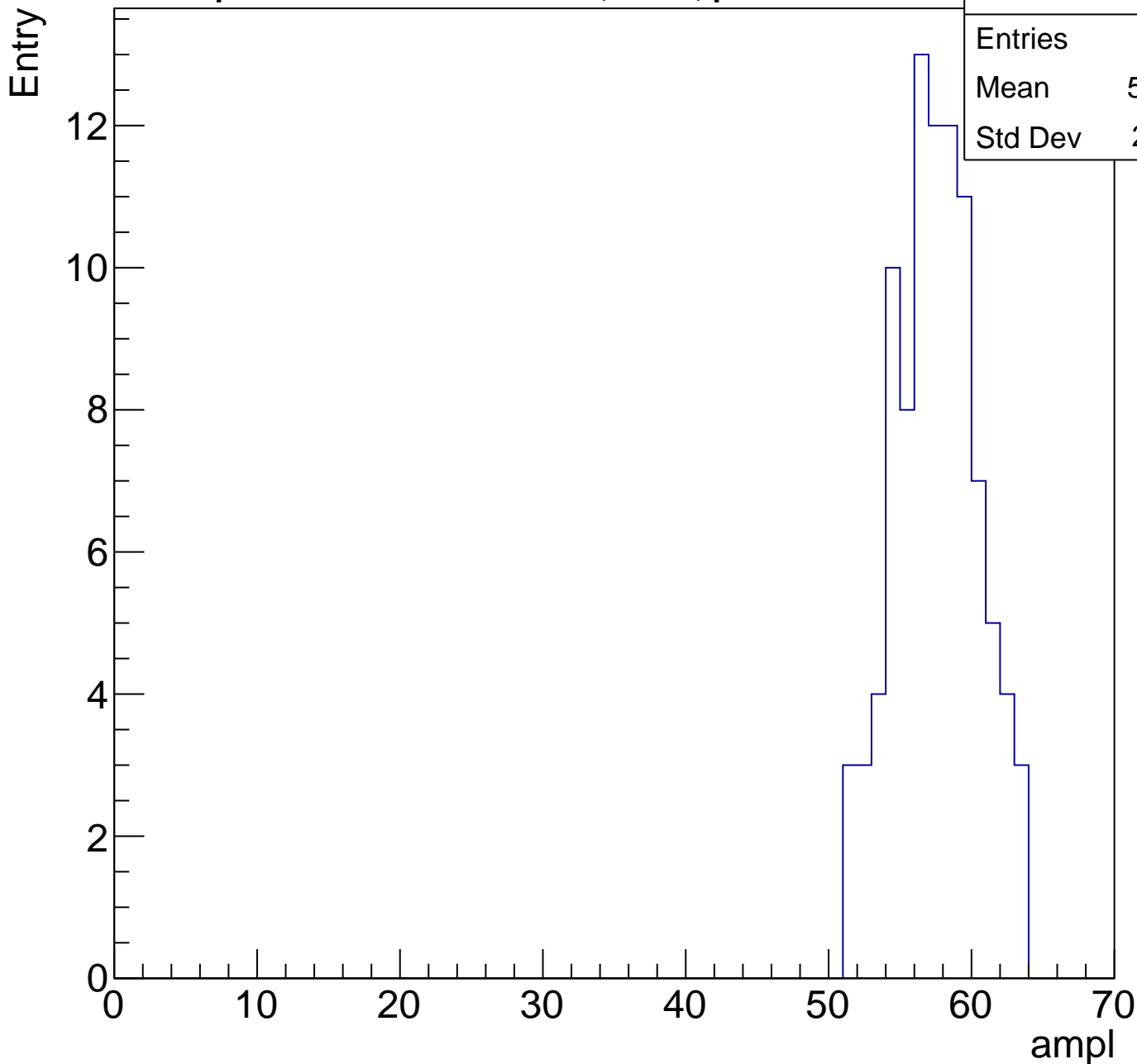
Entry



B0L101S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	95
Mean	57.05
Std Dev	2.881



B0L101S, U15-ch17, adc5

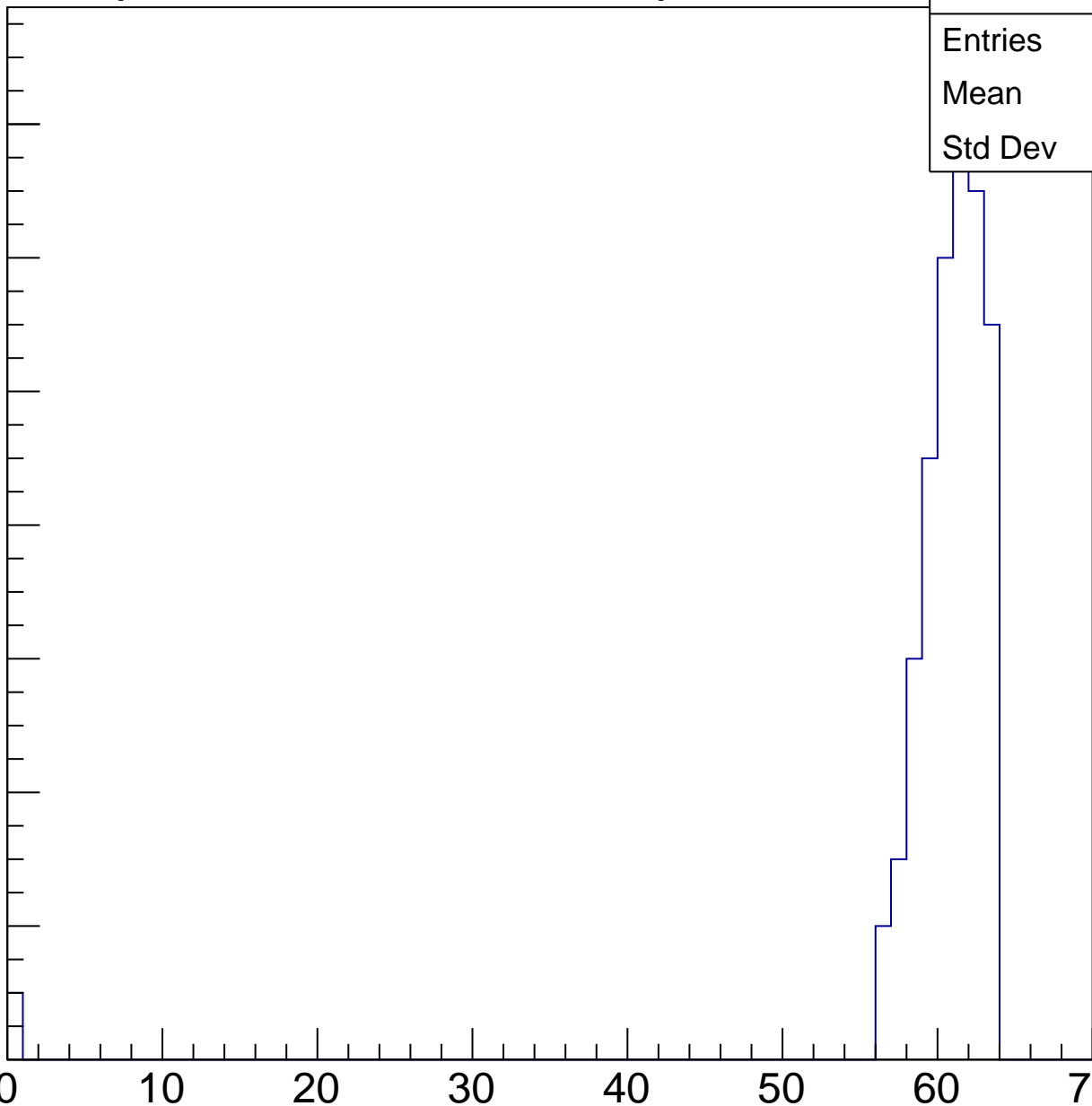
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.67
Std Dev	7.312

ampl

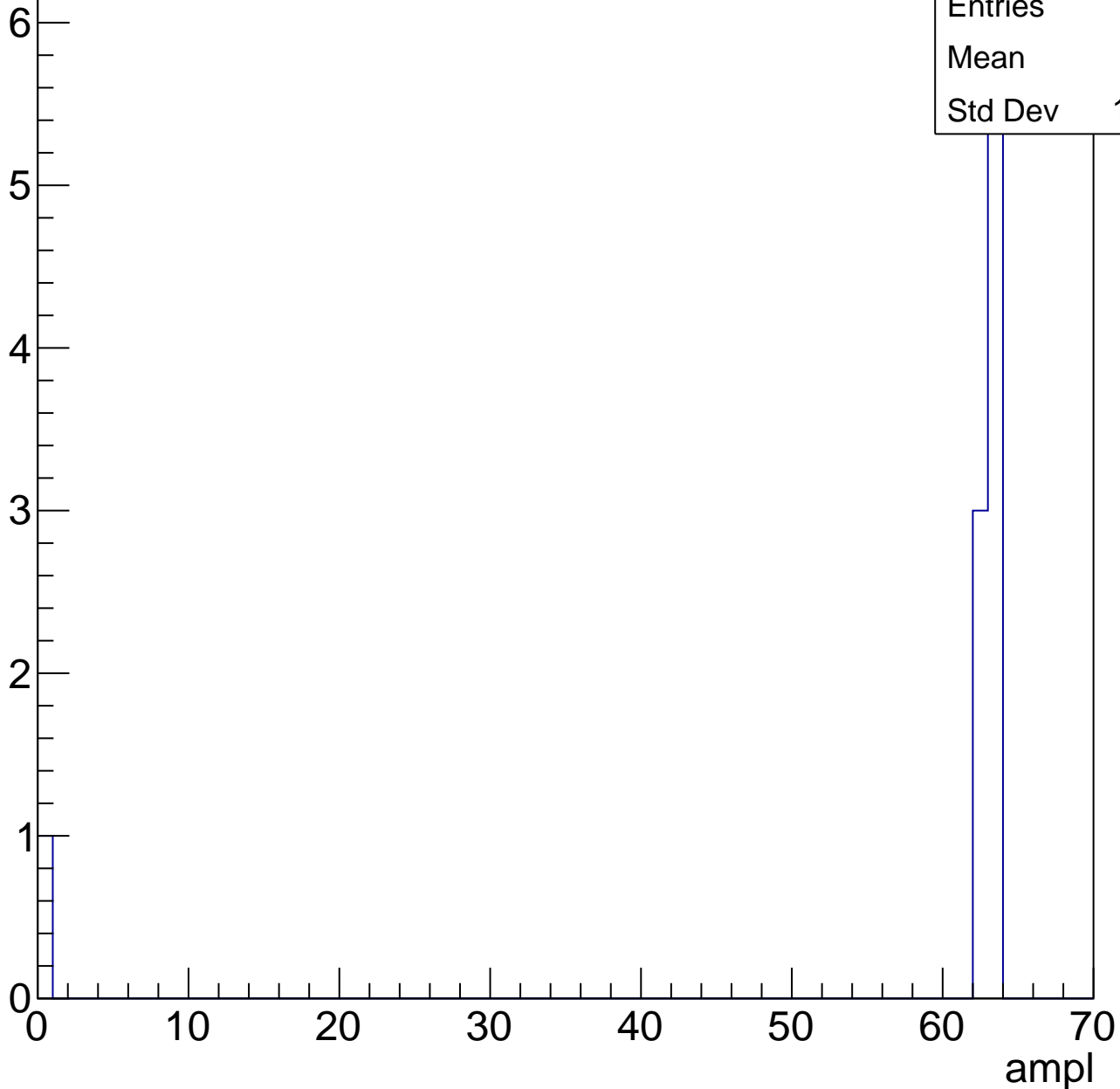


B0L101S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	56.4
Std Dev	18.81



B0L101S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

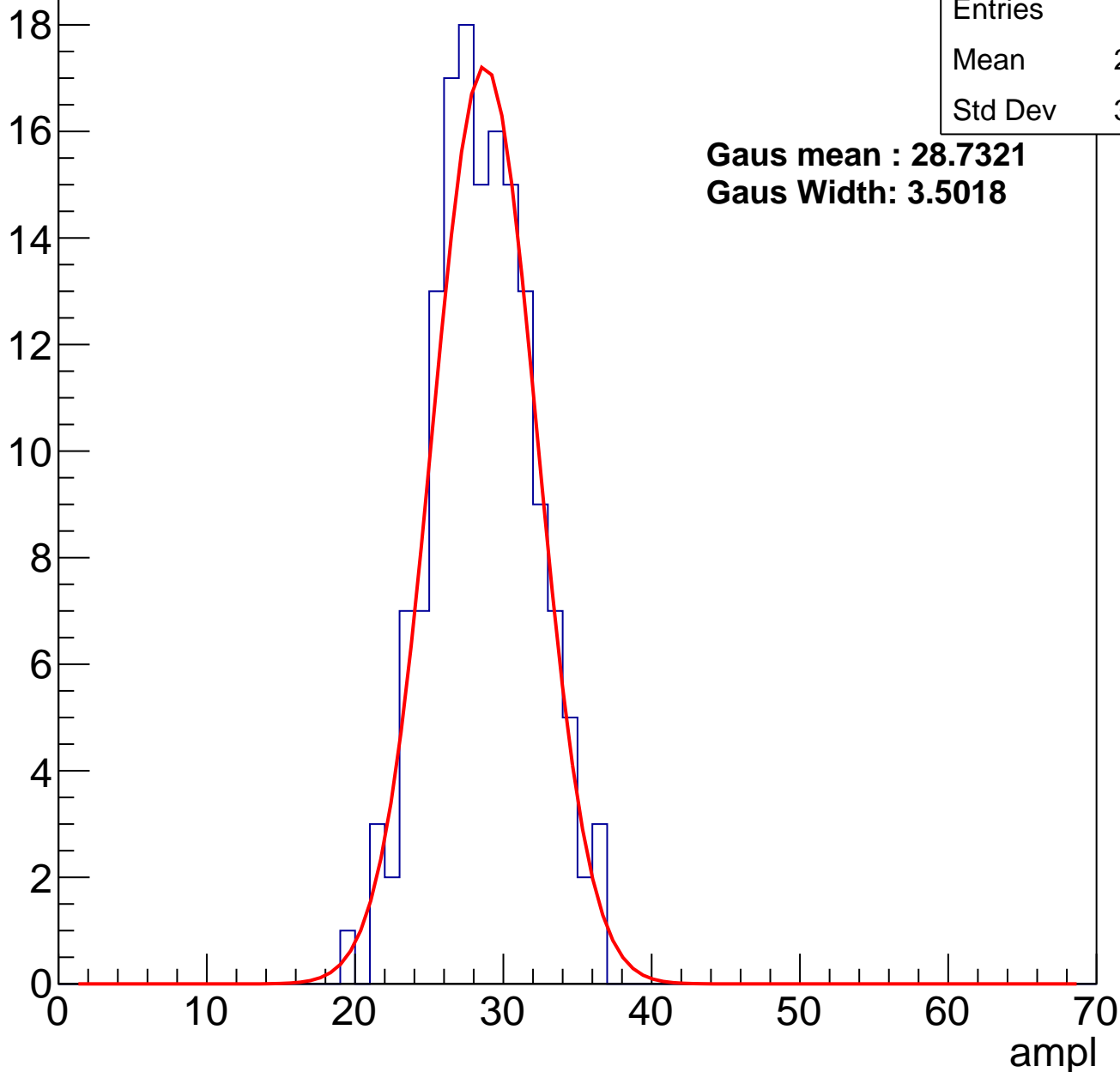


Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch18, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch18, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	35.58
Std Dev	3.617

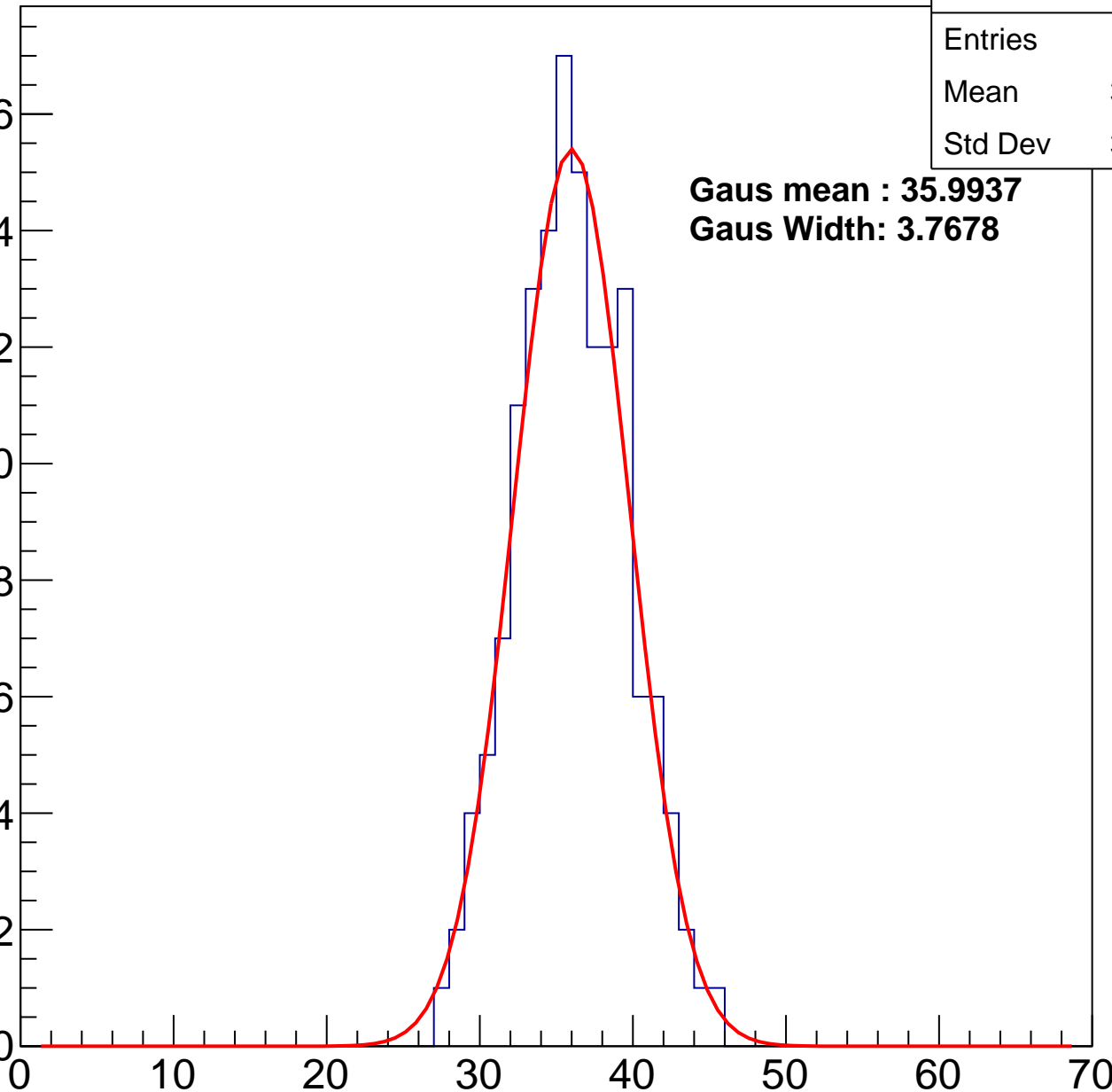
Gaus mean : 35.9937

Gaus Width: 3.7678

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch18, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

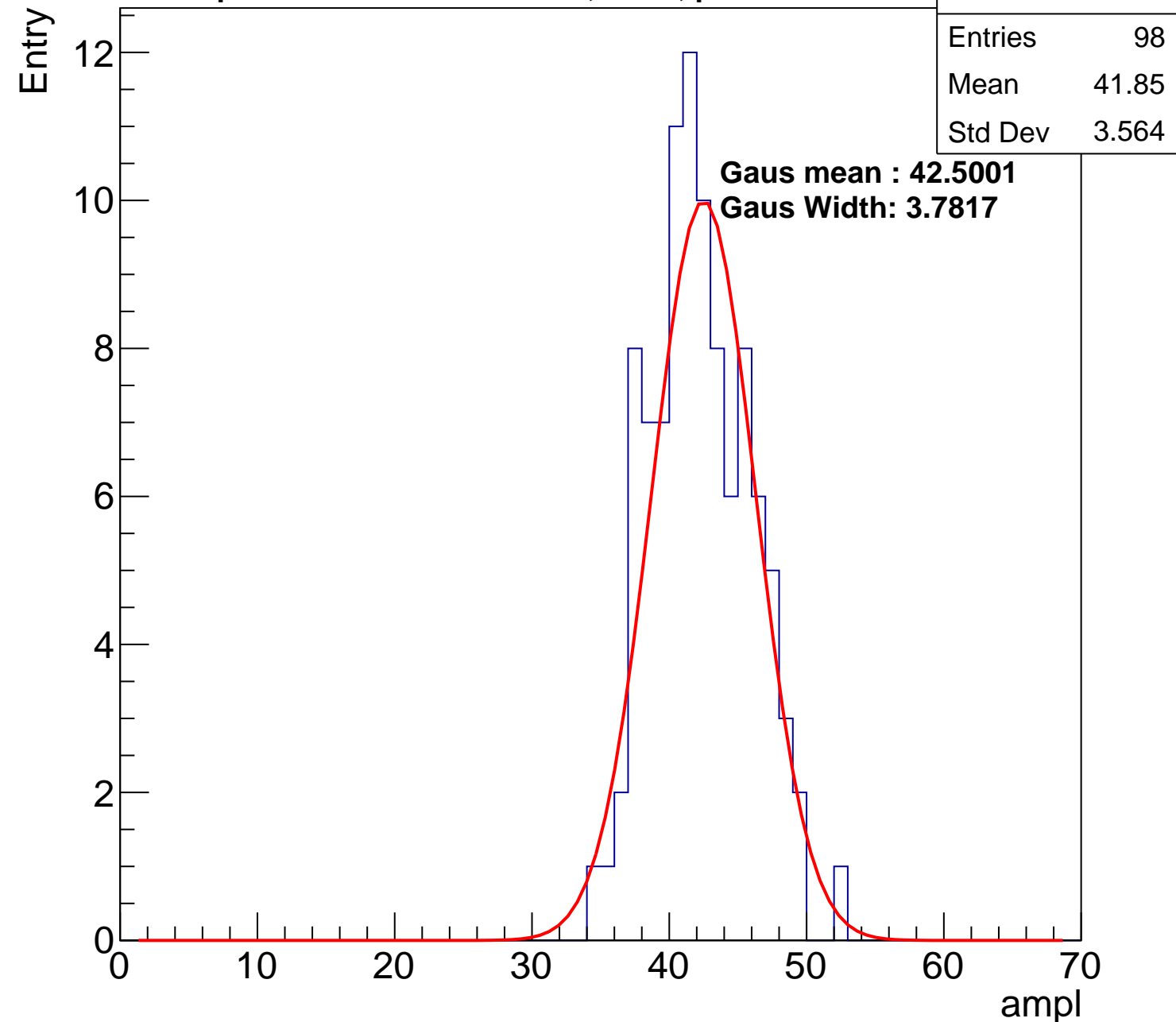
60

ampl

Entries	98
Mean	41.85
Std Dev	3.564

Gaus mean : 42.5001

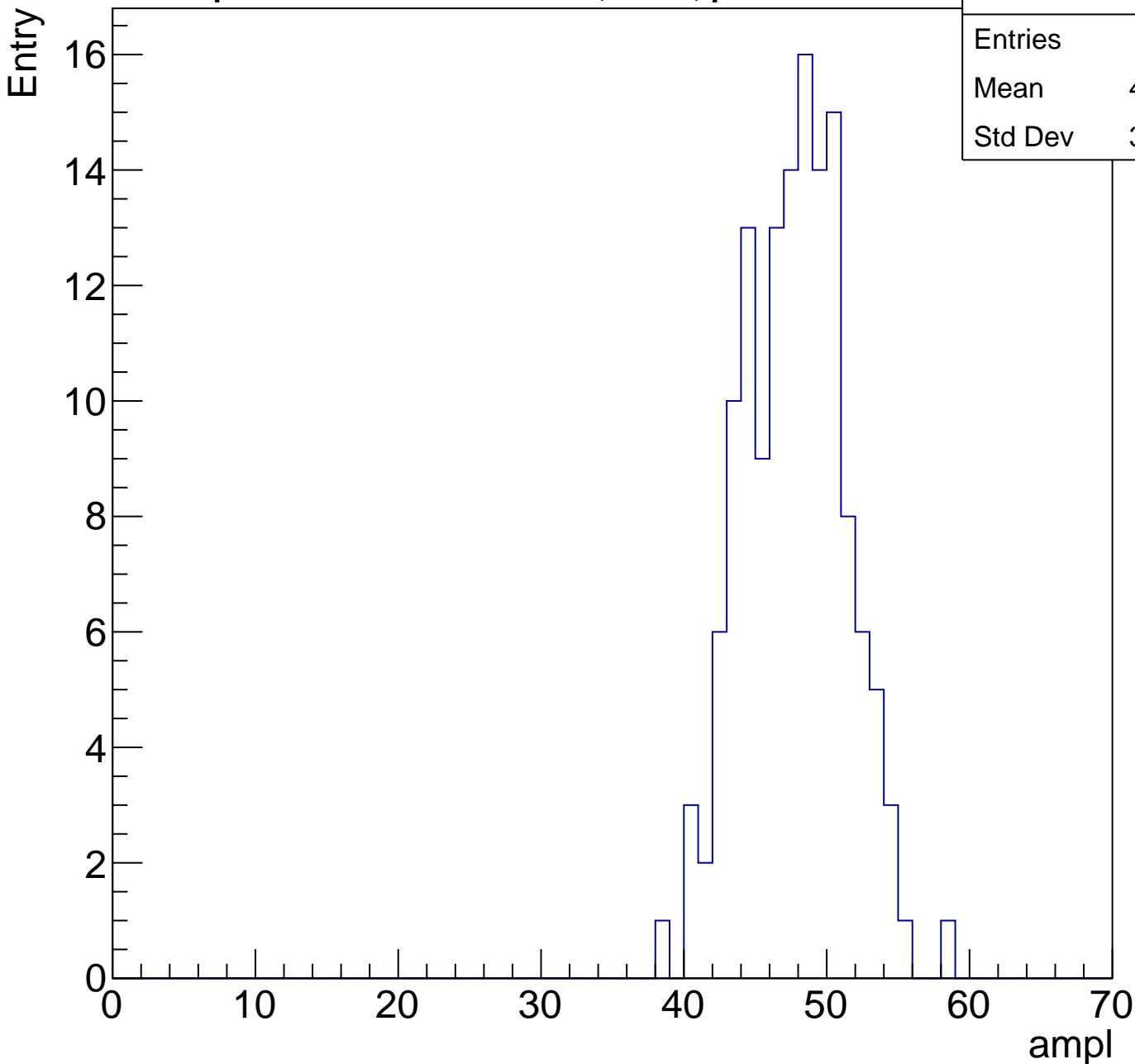
Gaus Width: 3.7817



B0L101S, U15-ch18, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	47.28
Std Dev	3.548



B0L101S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#1, port C1

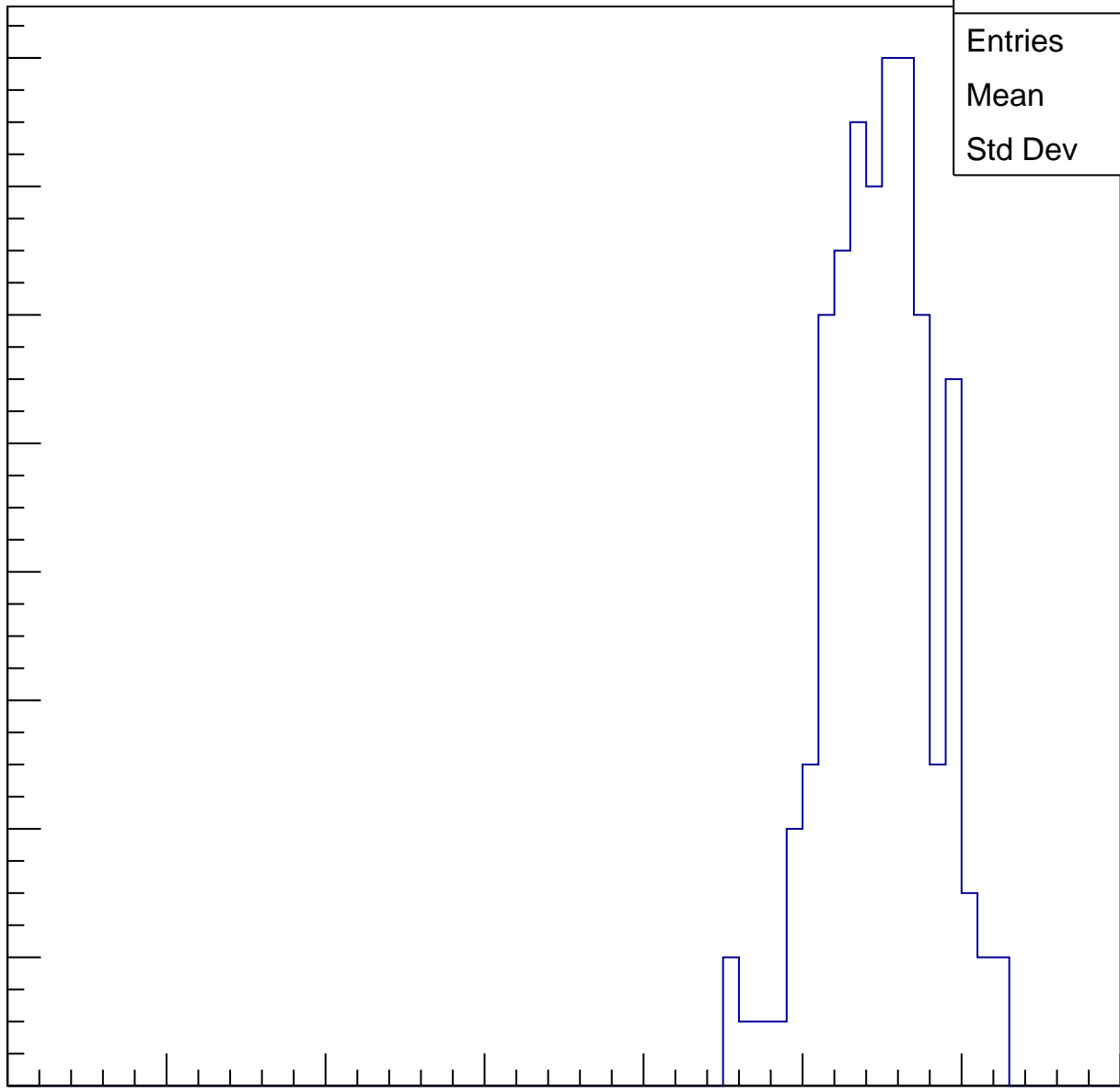
Entries	135
Mean	54.38
Std Dev	3.36

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

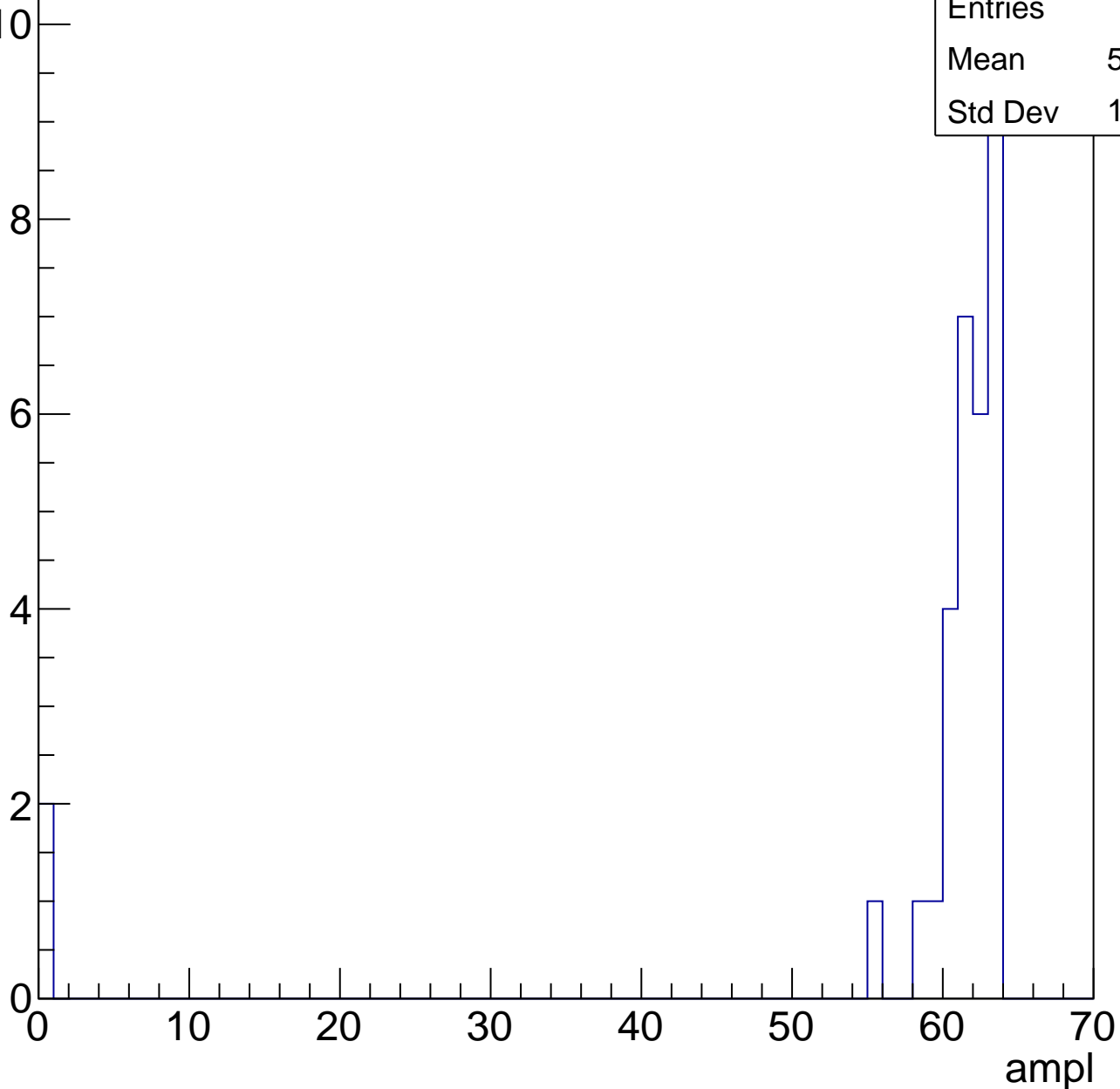
Entries	84
Mean	59.56
Std Dev	2.407

B0L101S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

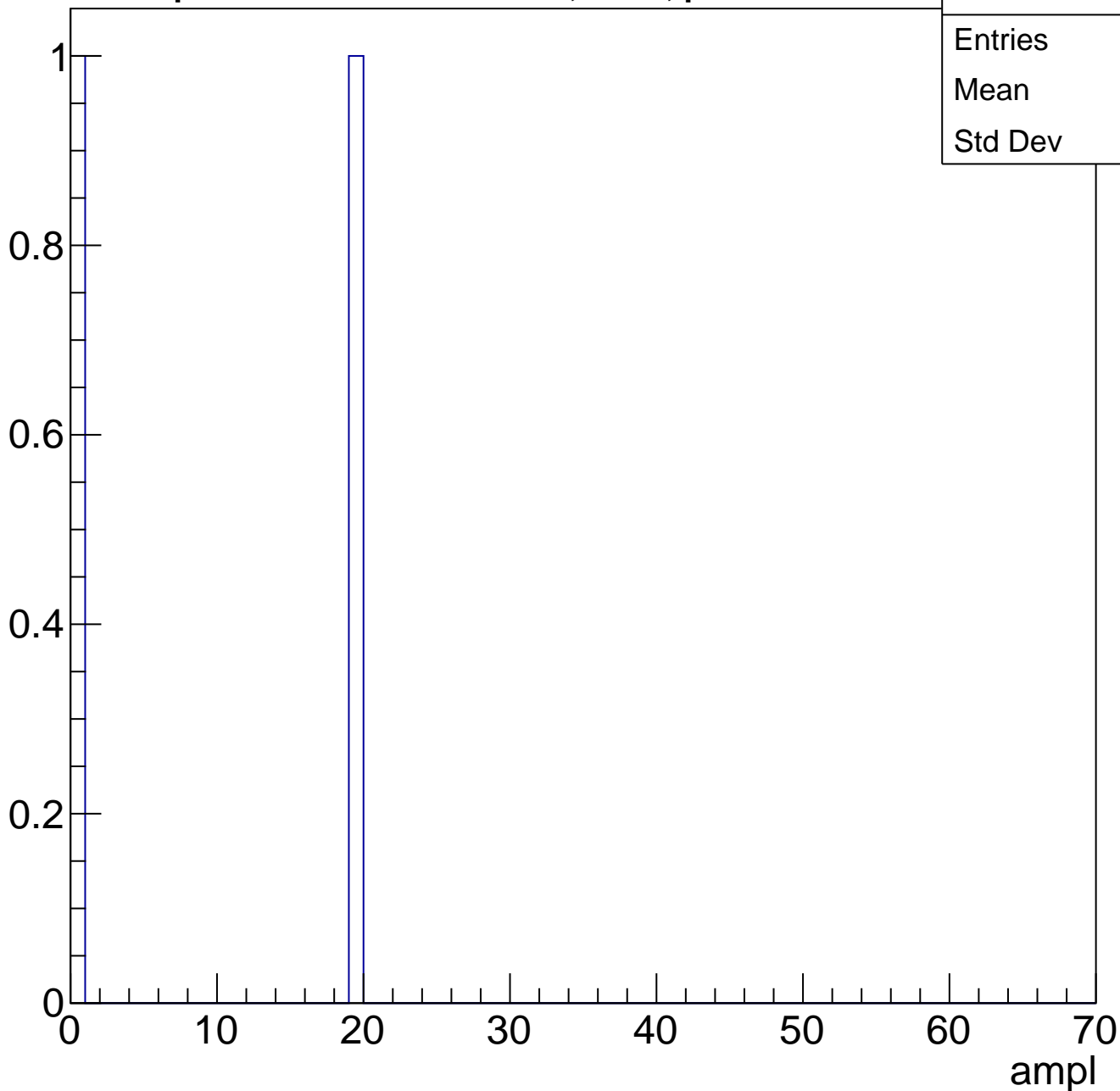
Entries	32
Mean	57.53
Std Dev	14.95



B0L101S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L101S, U15-ch19, adc0

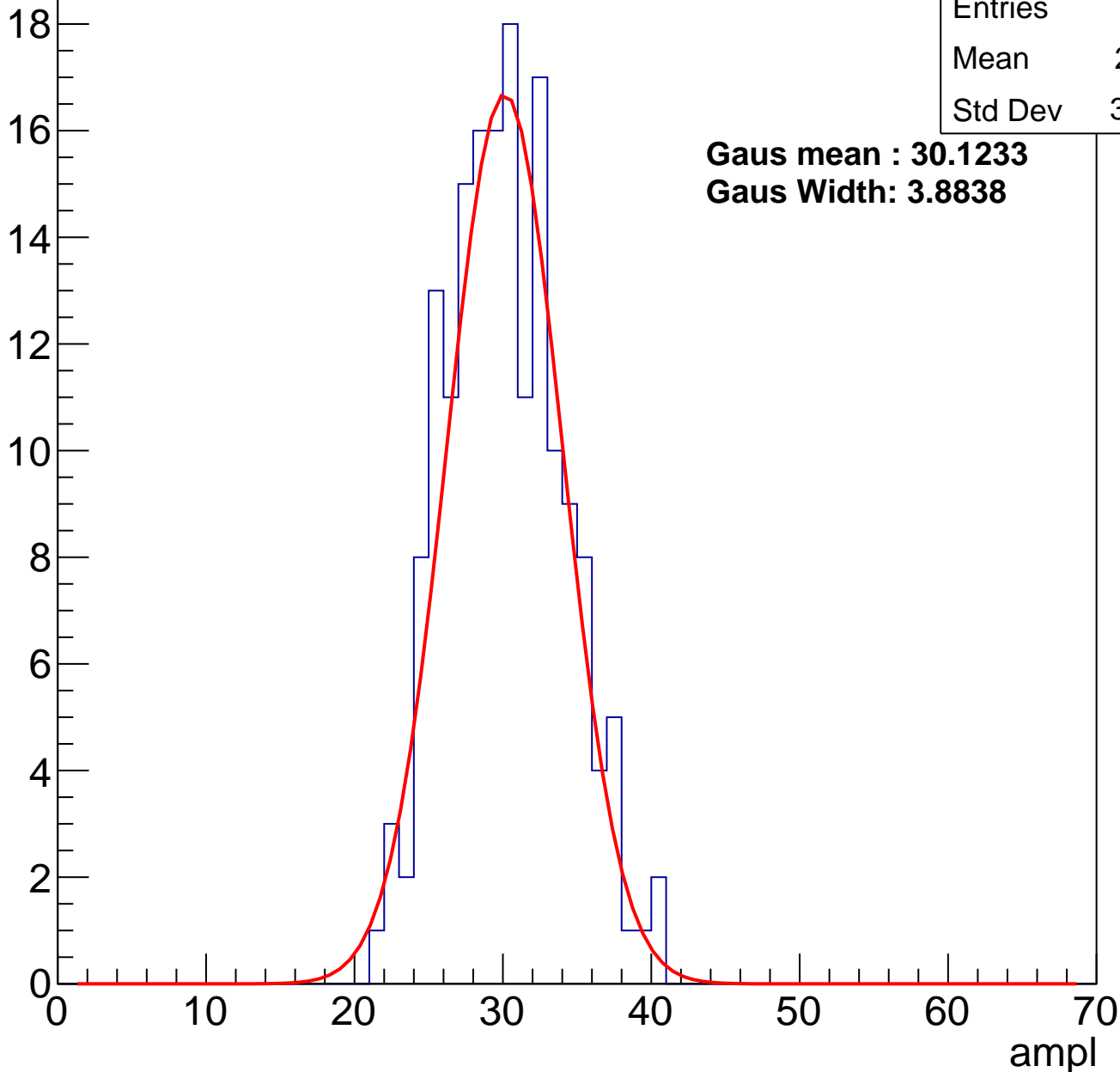
calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	29.71
Std Dev	3.905

Gaus mean : 30.1233

Gaus Width: 3.8838

Entry



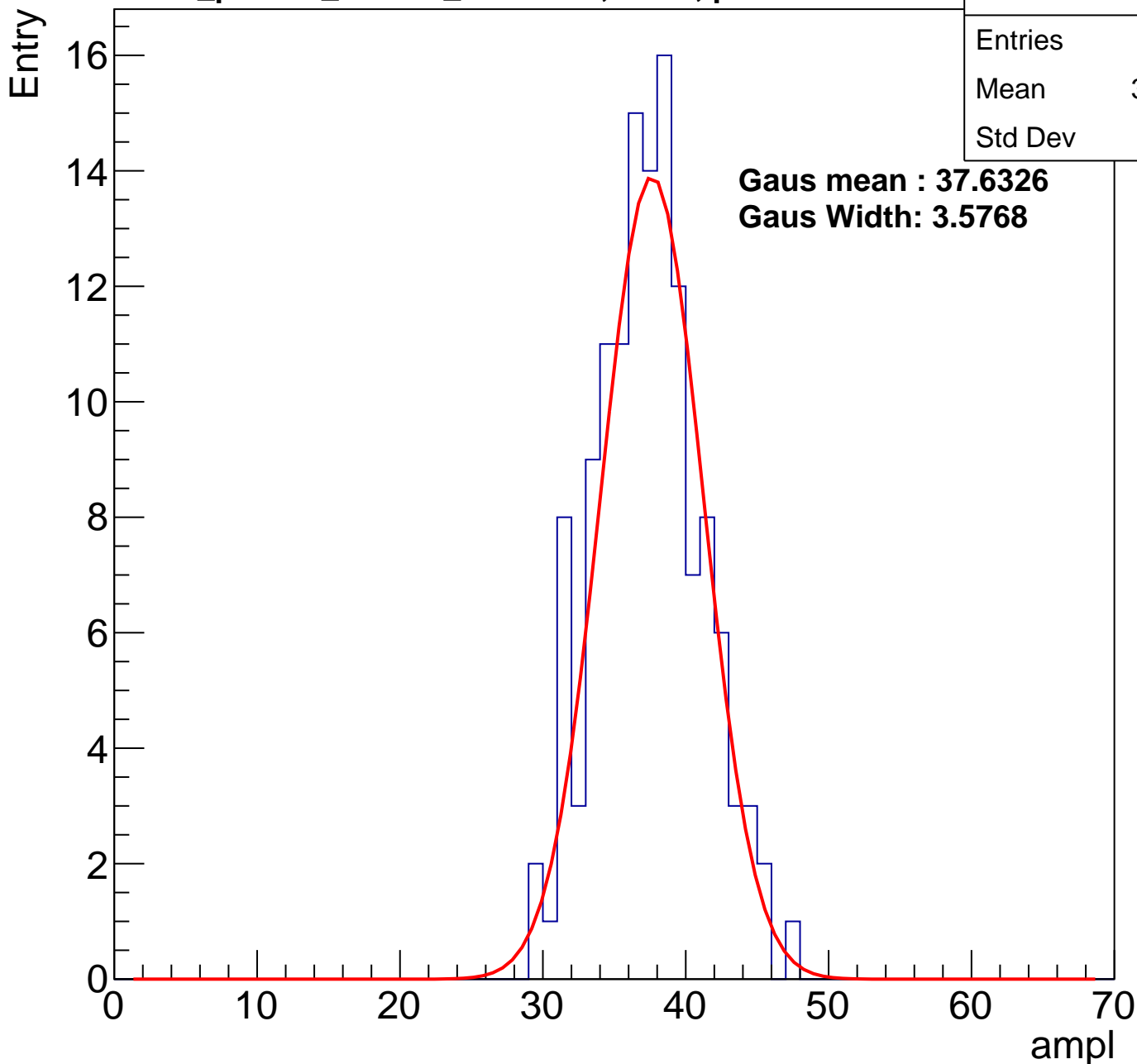
B0L101S, U15-ch19, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	36.97
Std Dev	3.62

Gaus mean : 37.6326

Gaus Width: 3.5768



B0L101S, U15-ch19, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

112

Mean

42.89

Std Dev

3.054

Gaus mean : 43.5529

Gaus Width: 2.9479

0

10

20

30

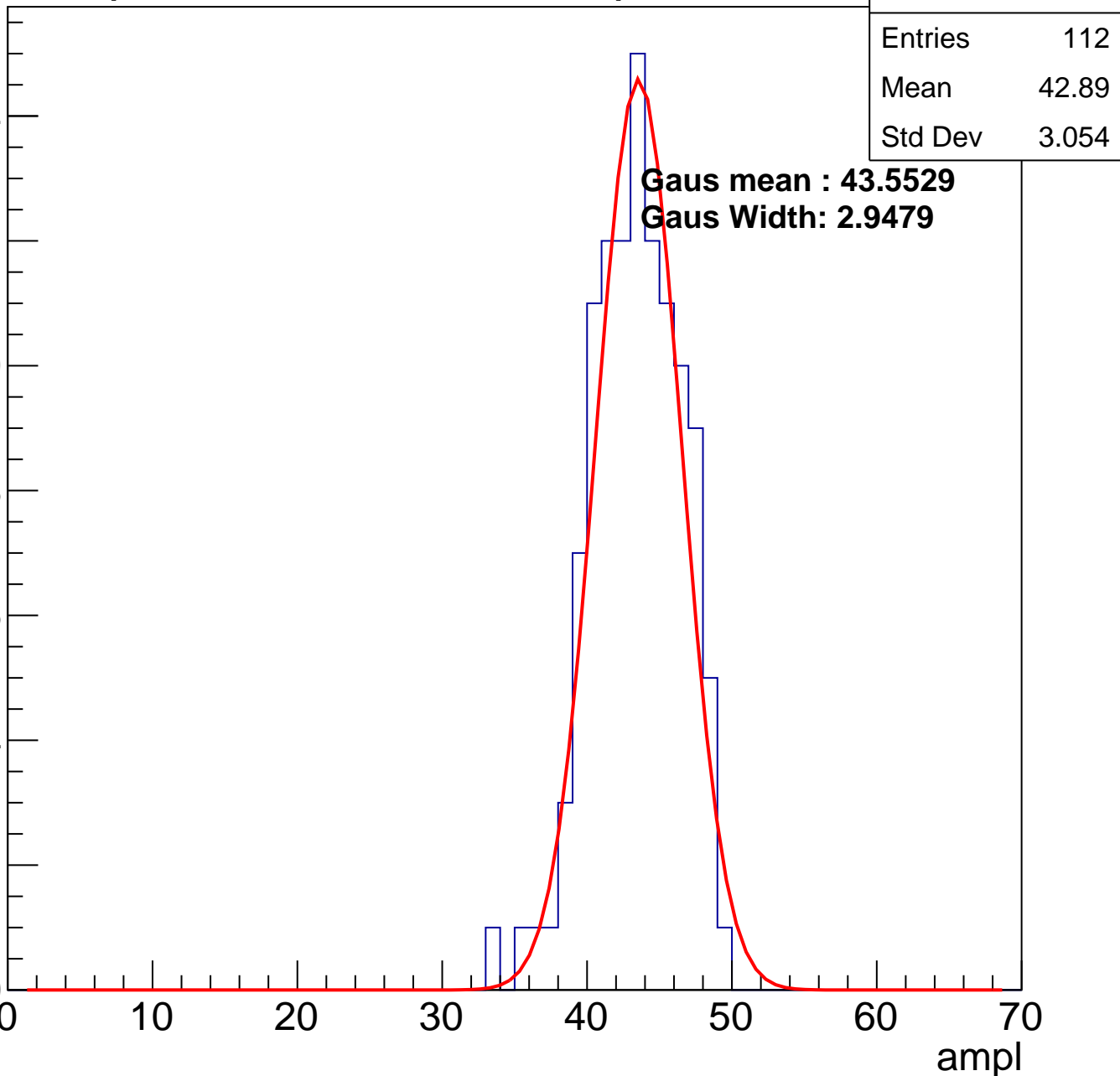
40

50

60

70

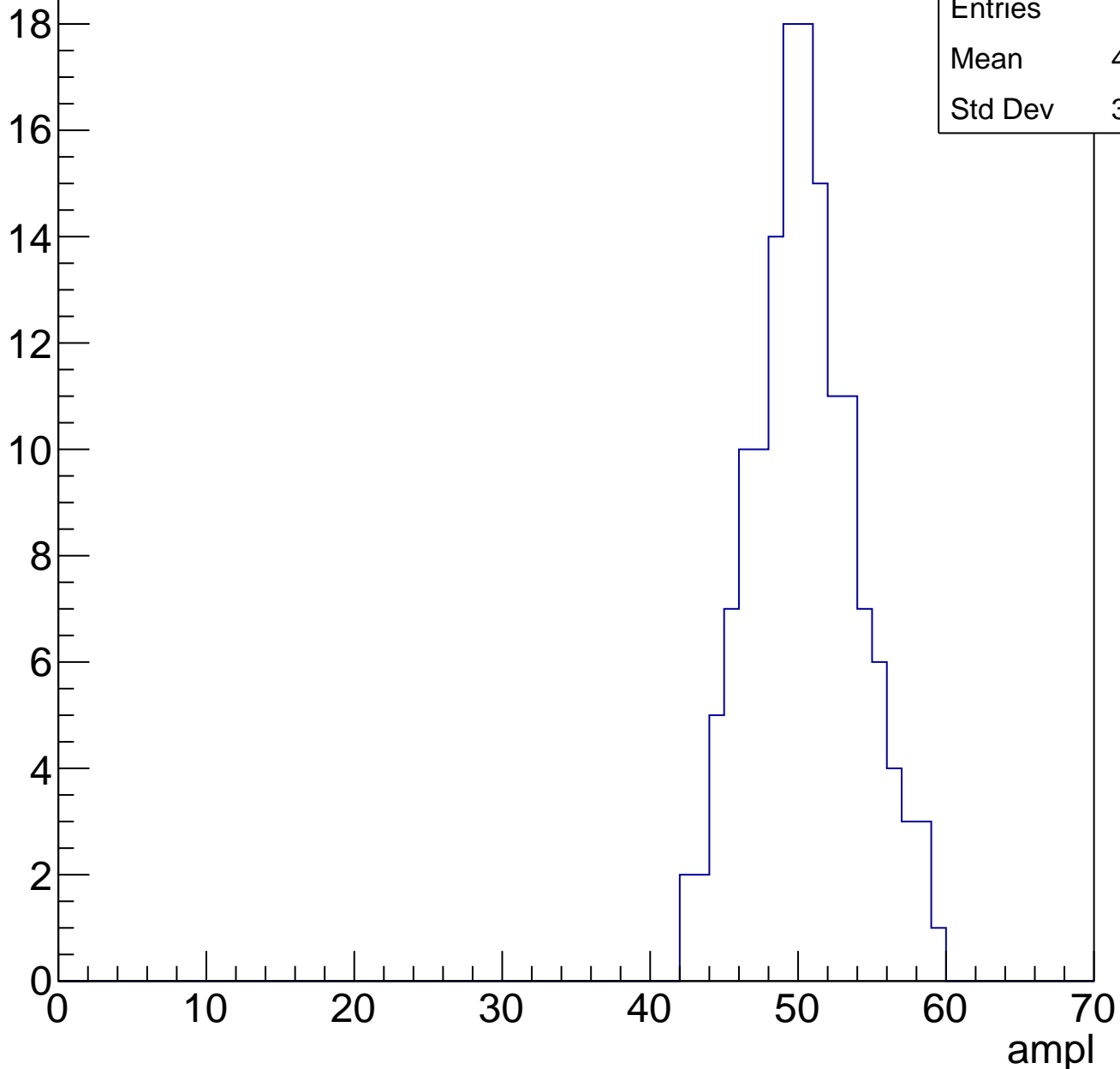
ampl



B0L101S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

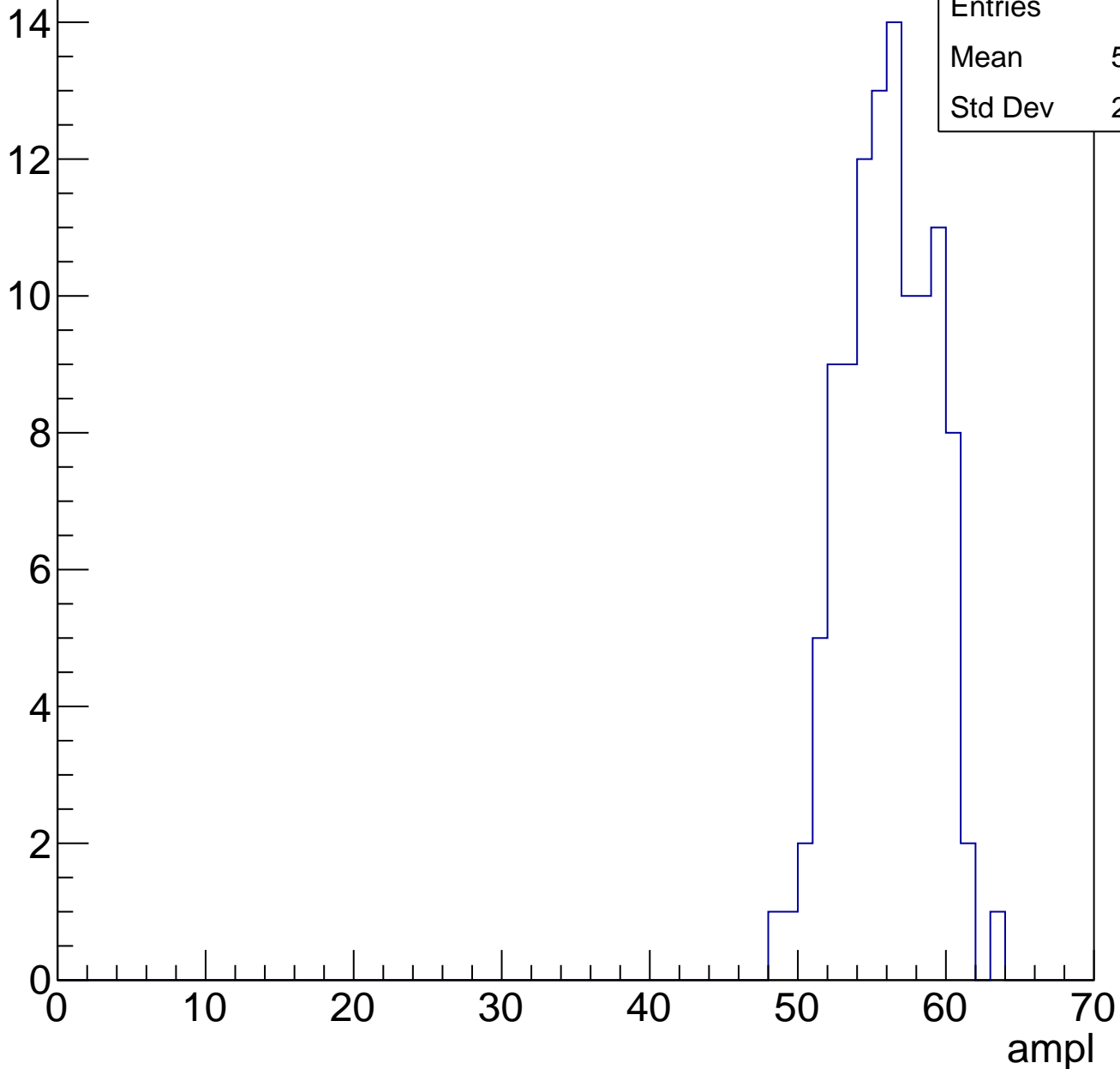


B0L101S, U15-ch19, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	55.63
Std Dev	2.977

Entry



B0L101S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

70

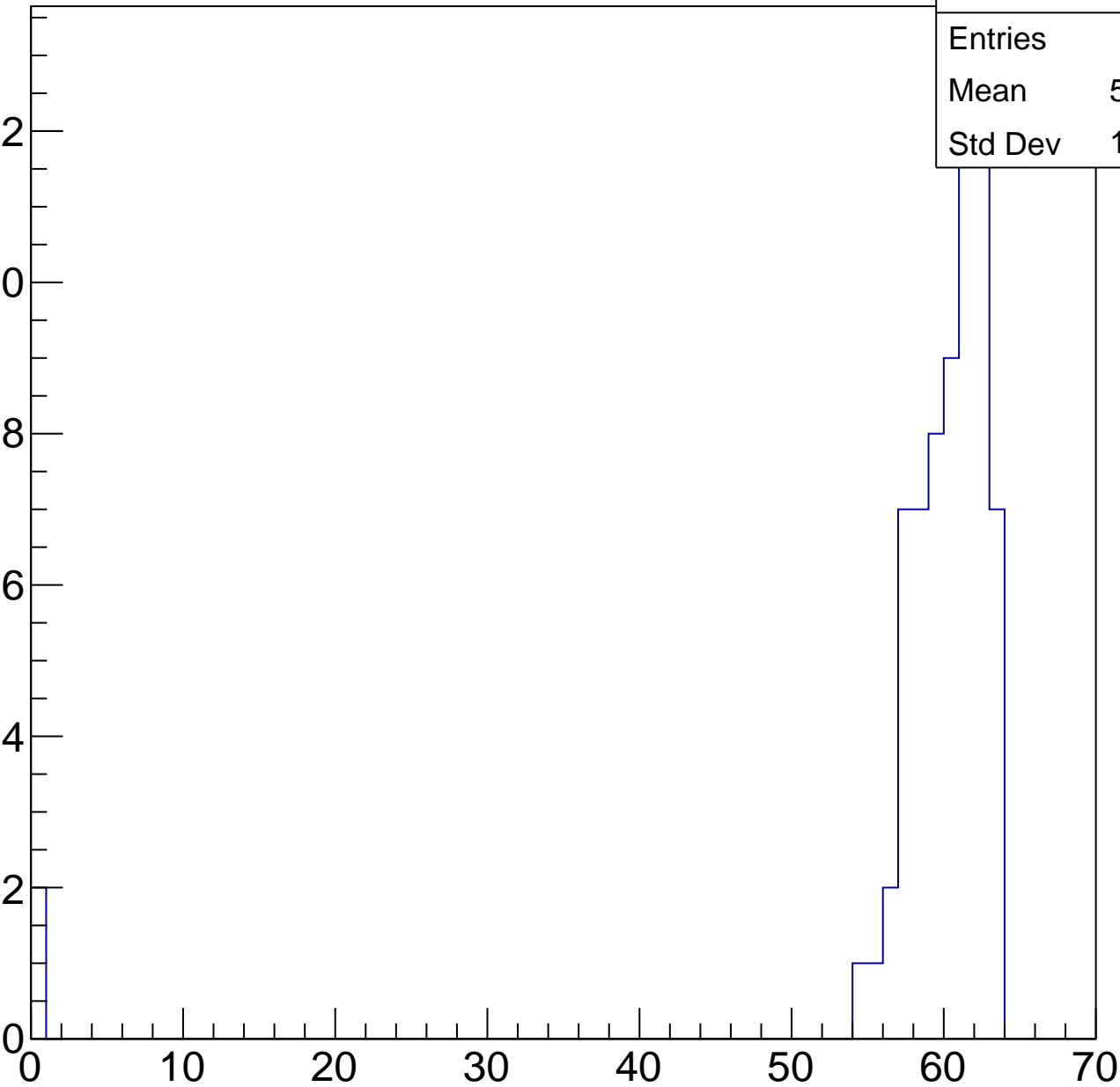
Mean

58.26

Std Dev

10.22

ampl



B0L101S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	27
Mean	62.04
Std Dev	1.036

B0L101S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



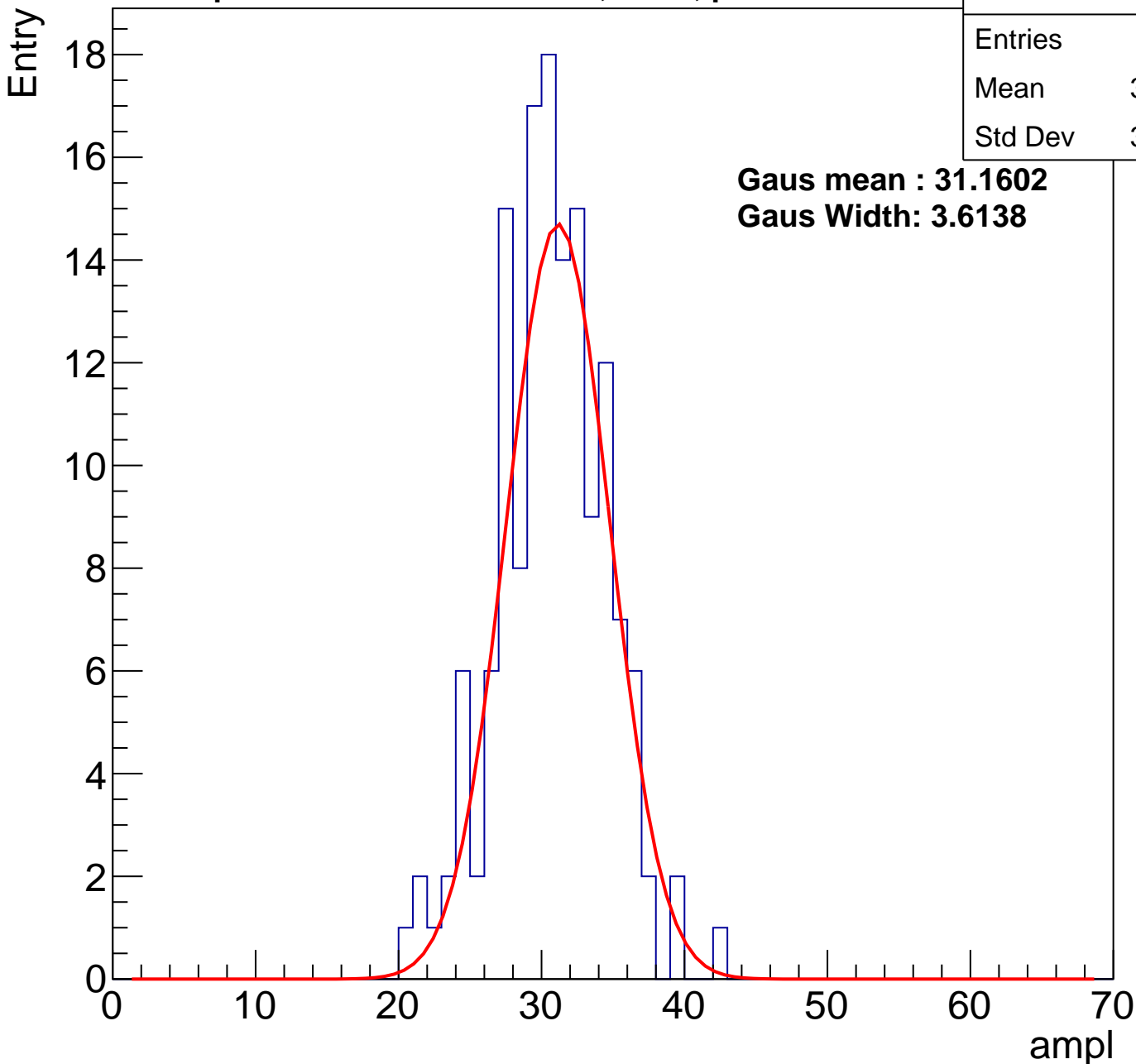
Entries	3
Mean	6.667
Std Dev	9.428

B0L101S, U15-ch20, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	30.25
Std Dev	3.795

Gaus mean : 31.1602
Gaus Width: 3.6138



B0L101S, U15-ch20, adc1

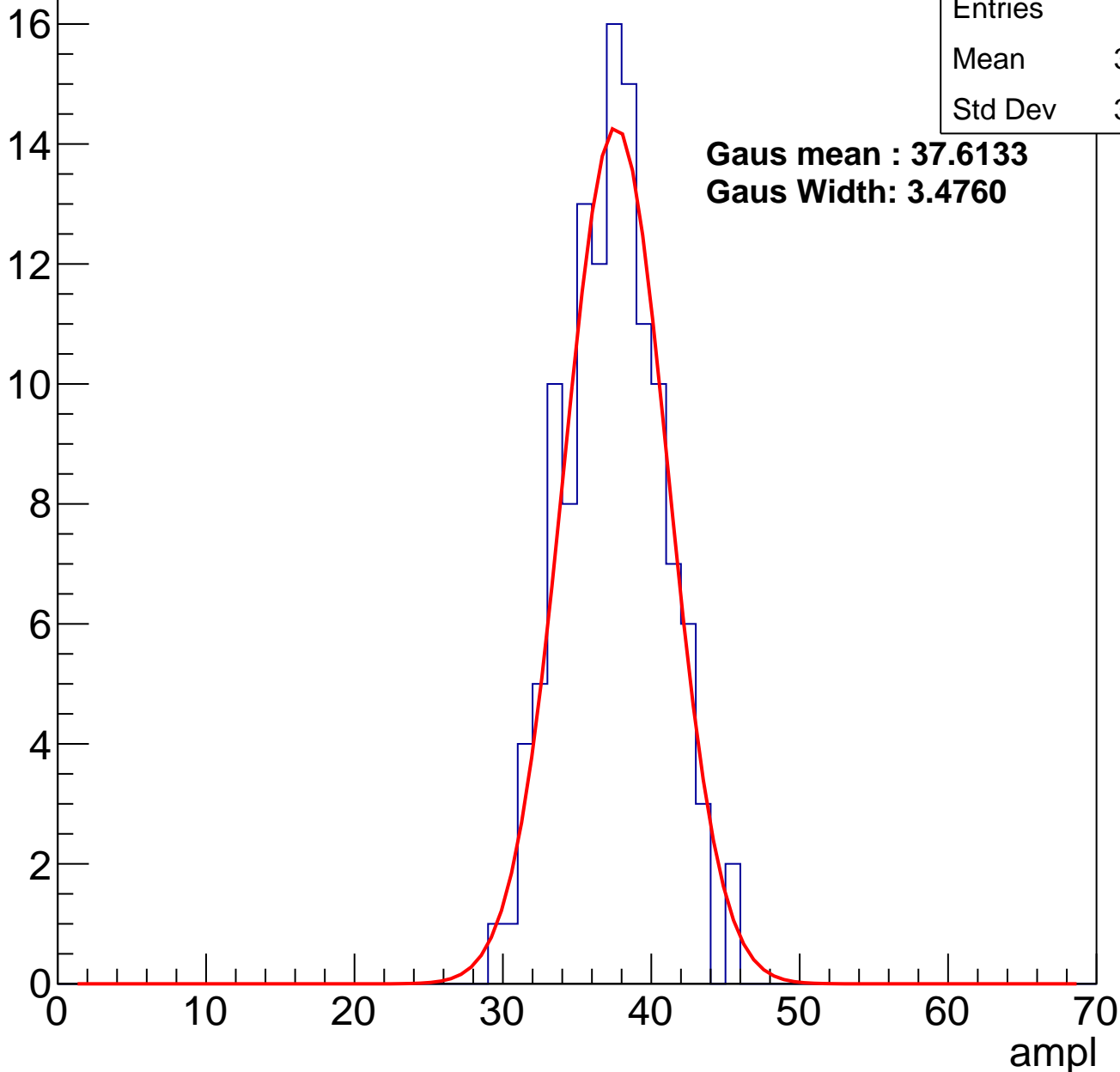
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	36.94
Std Dev	3.268

Gaus mean : 37.6133

Gaus Width: 3.4760

Entry



B0L101S, U15-ch20, adc2

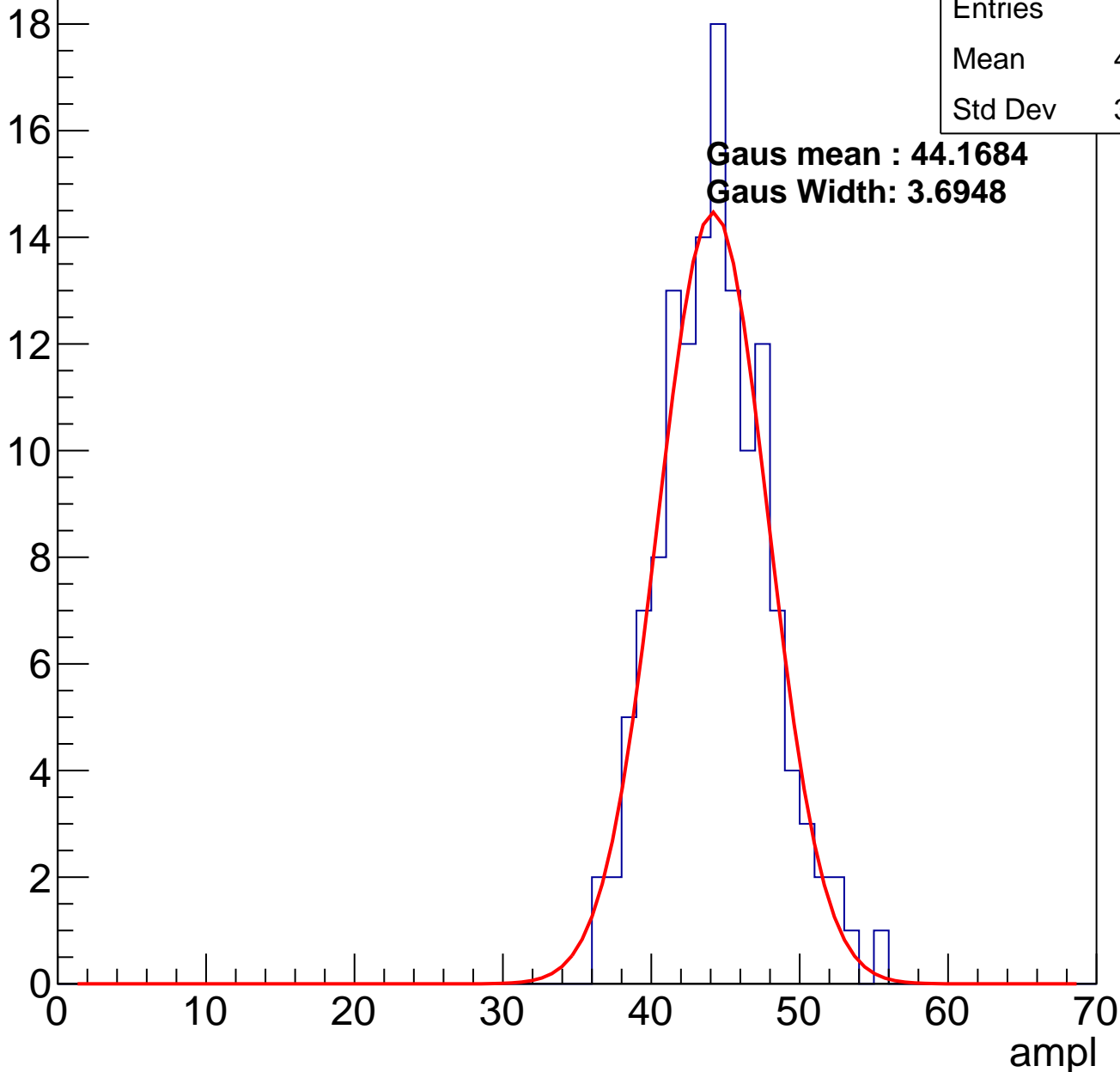
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	43.86
Std Dev	3.632

Gaus mean : 44.1684

Gaus Width: 3.6948

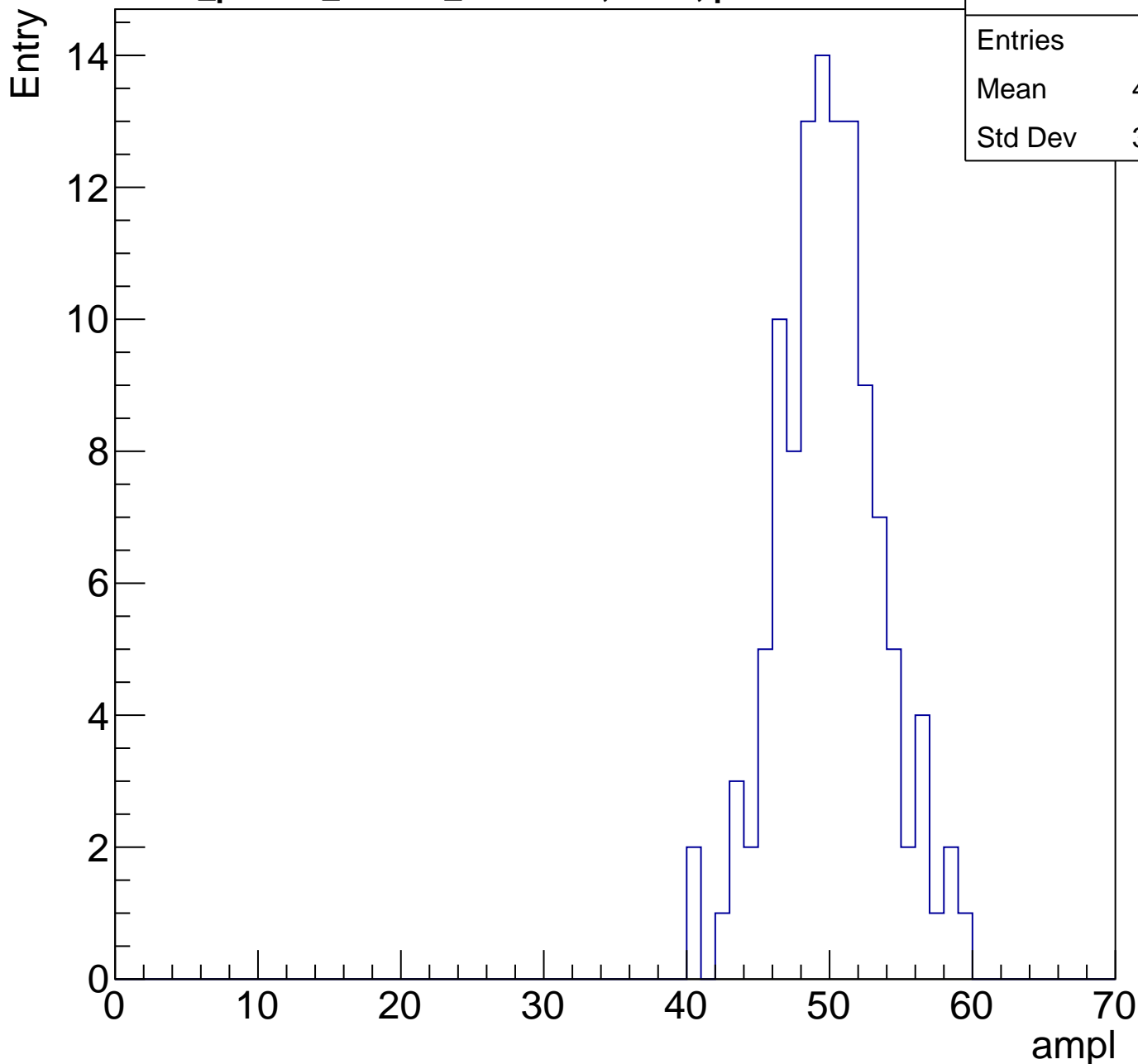
Entry



B0L101S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	49.55
Std Dev	3.643



B0L101S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	55.56
Std Dev	3.172

Entry

12

10

8

6

4

2

0

0

10

20

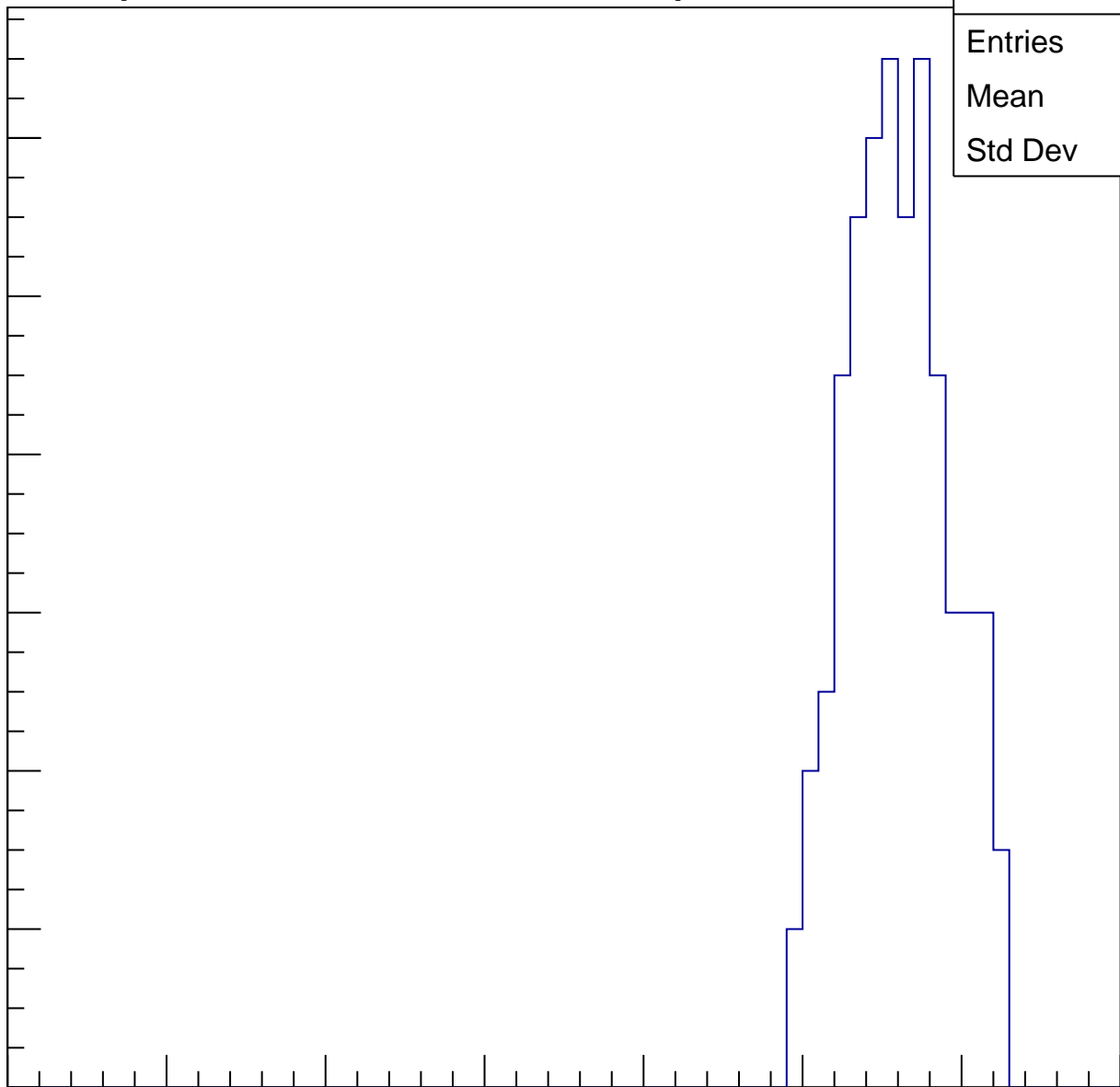
30

40

50

60

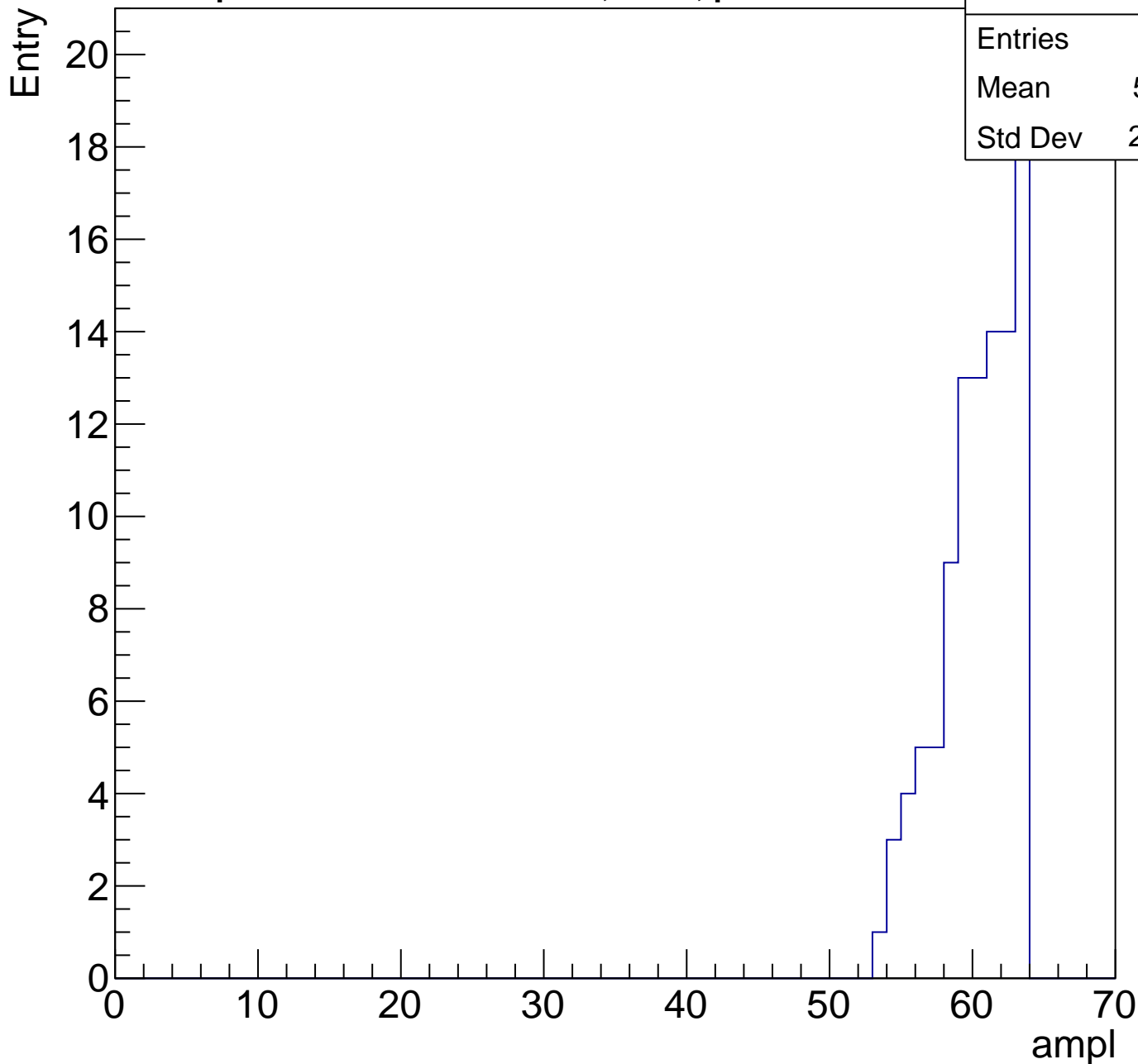
ampl



B0L101S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#1, port C1

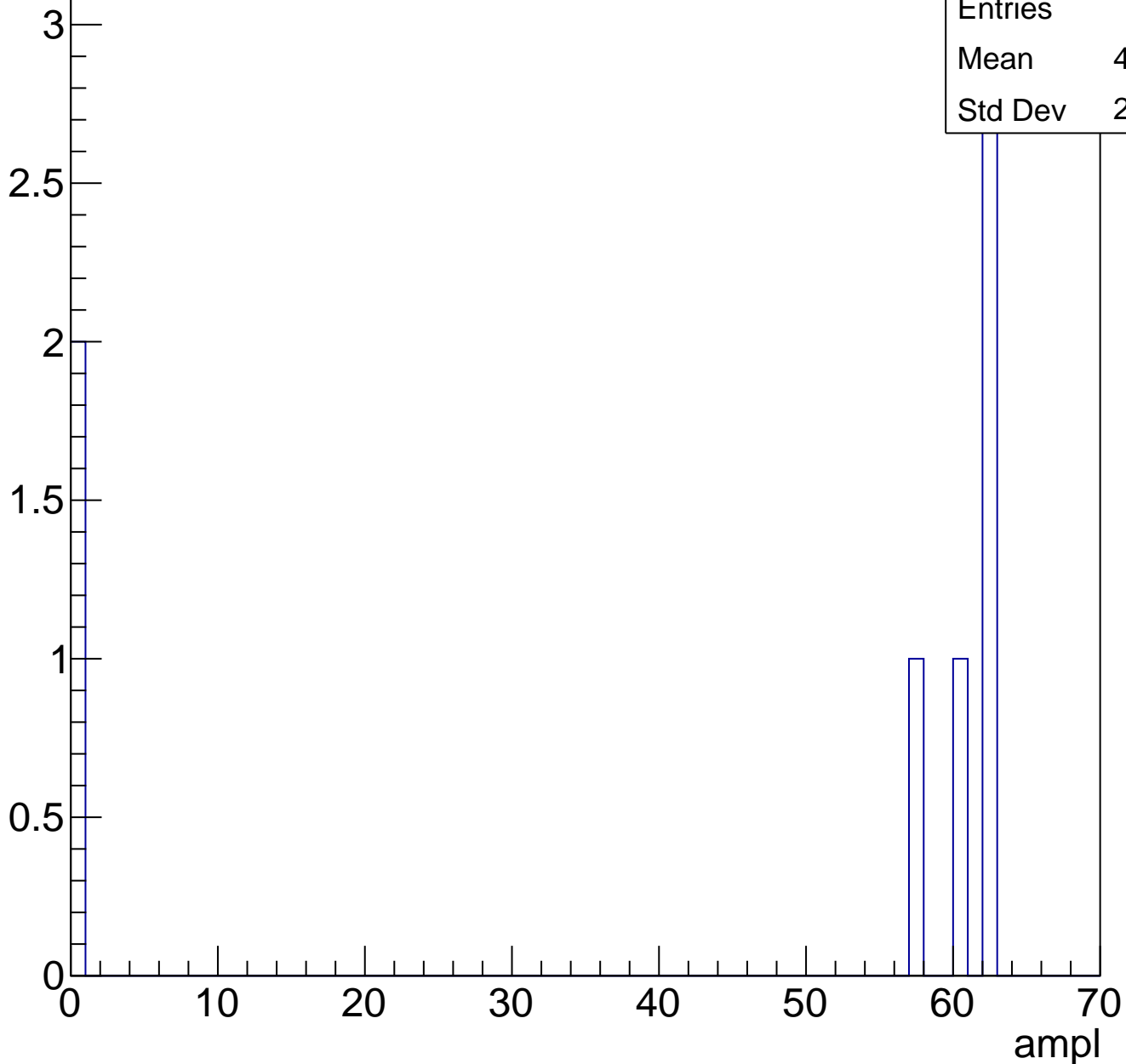
Entries	101
Mean	59.91
Std Dev	2.595



B0L101S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

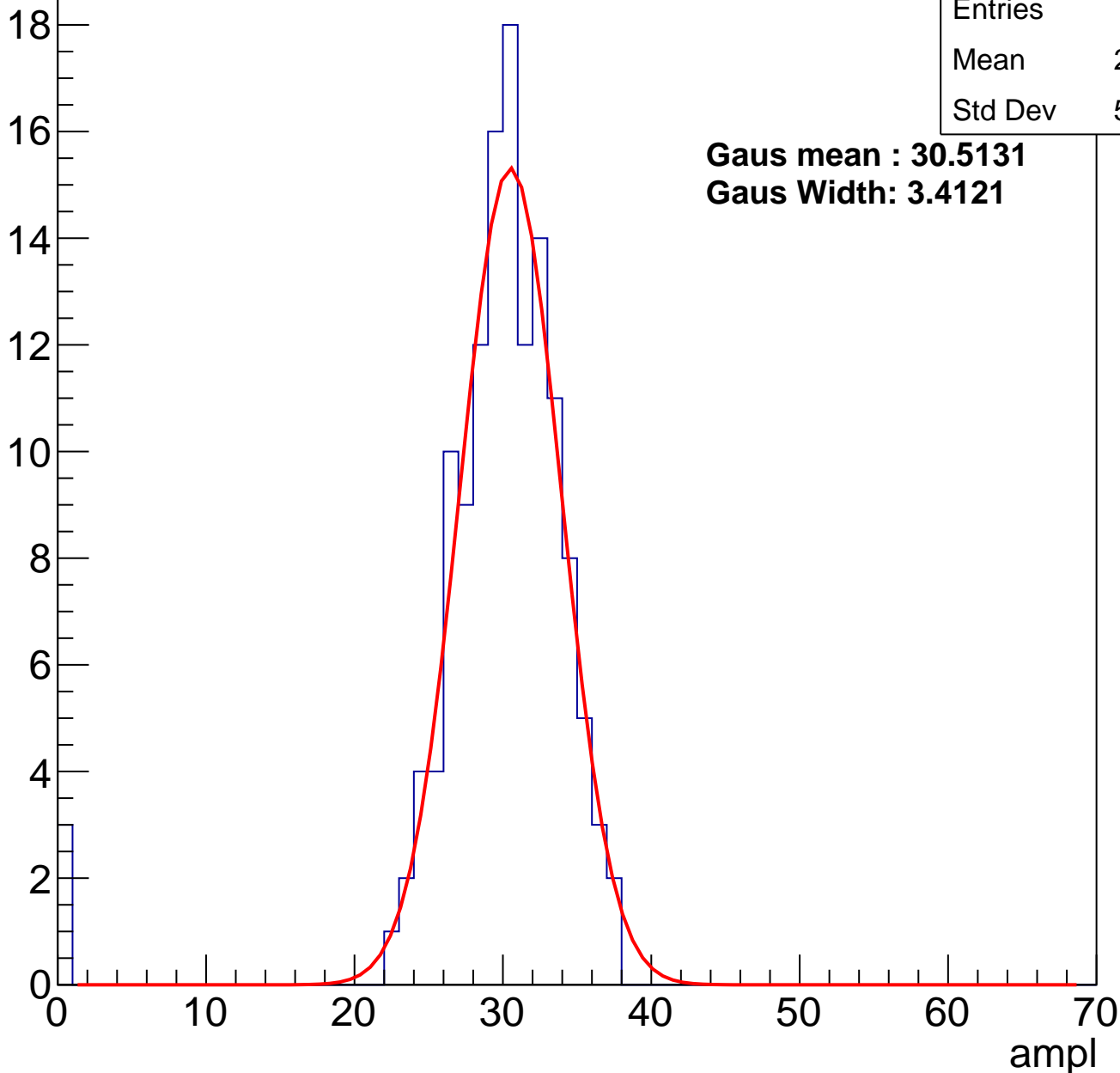


Entries	1
Mean	23
Std Dev	0

B0L101S, U15-ch21, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch21, adc1

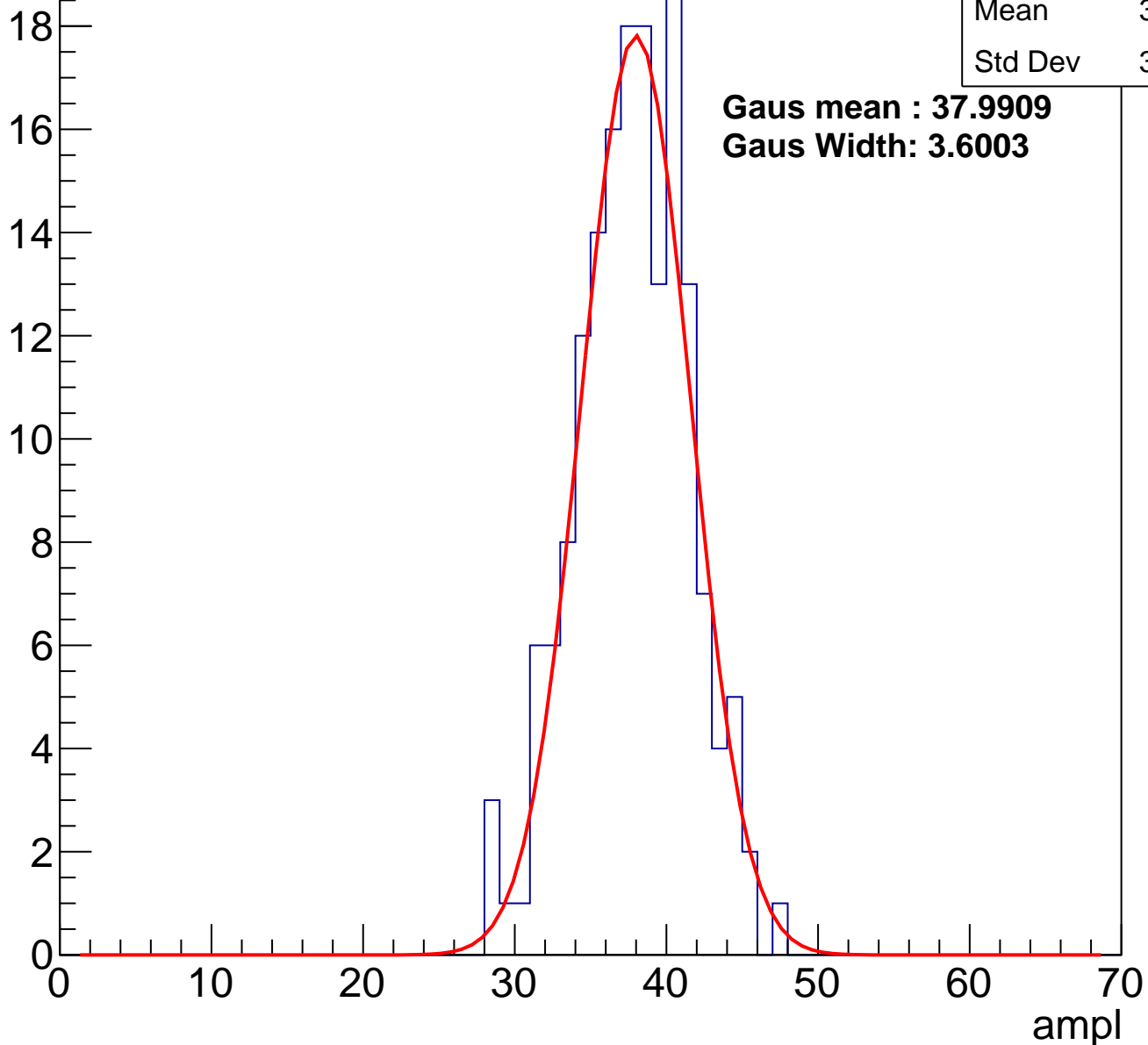
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	37.32
Std Dev	3.674

Gaus mean : 37.9909

Gaus Width: 3.6003

Entry



B0L101S, U15-ch21, adc2

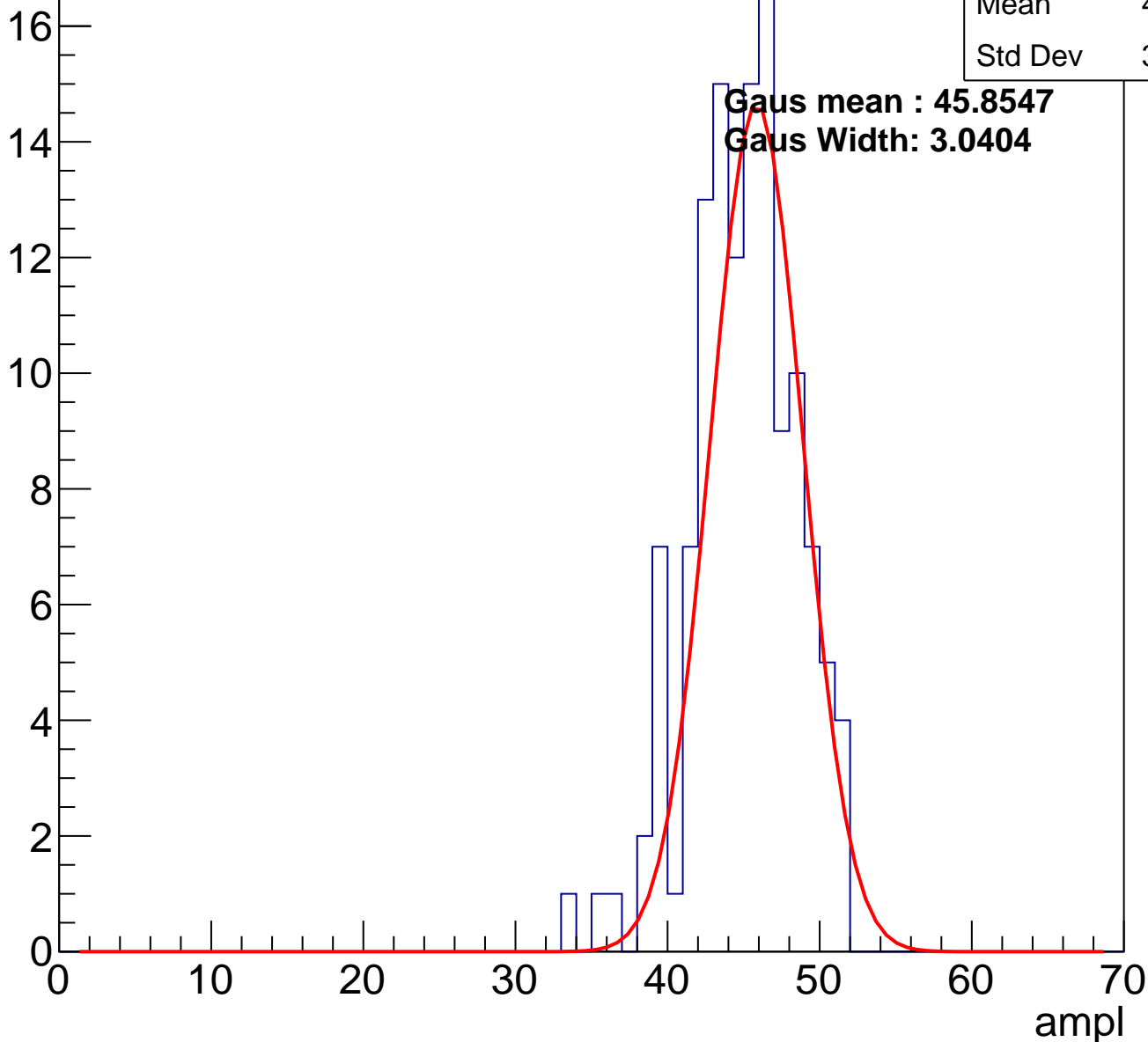
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	127
Mean	44.54
Std Dev	3.434

Gaus mean : 45.8547

Gaus Width: 3.0404



B0L101S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	51.37
Std Dev	3.536

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

B0L101S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

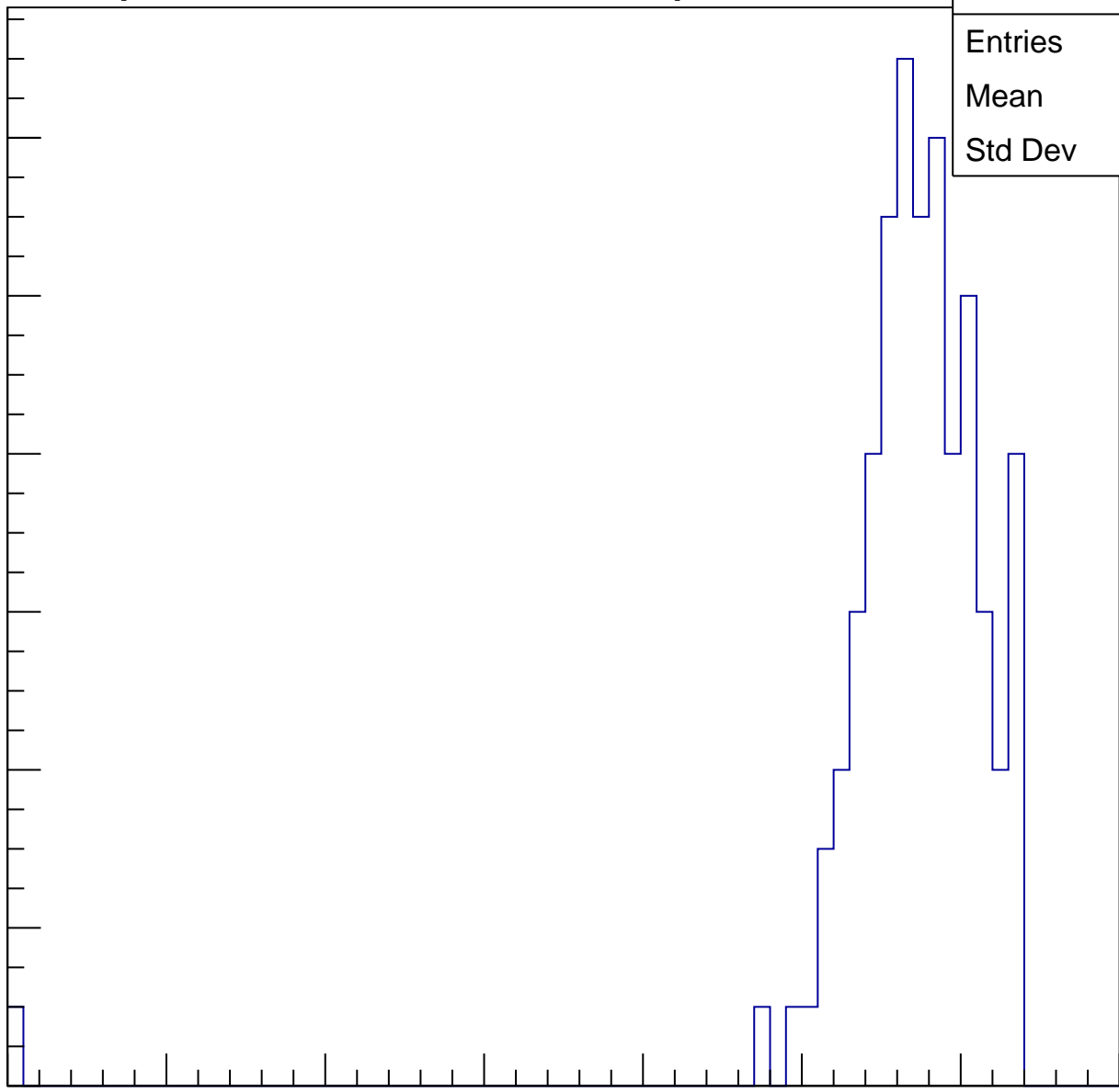
50

60

70

ampl

Entries	108
Mean	56.51
Std Dev	6.446



B0L101S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	67
Mean	59.42
Std Dev	7.579

ampl

0

10

20

30

40

50

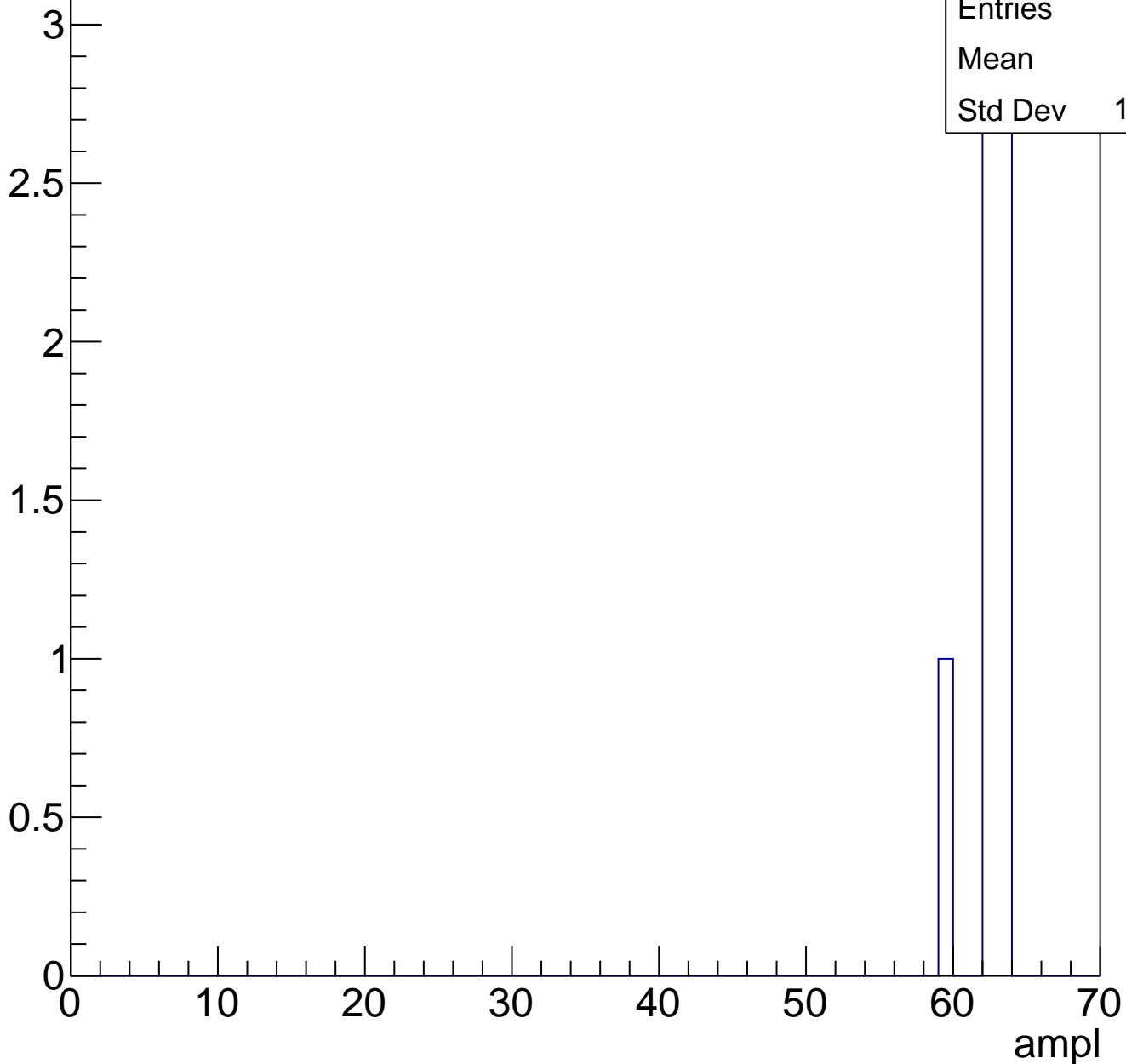
60

70

B0L101S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L101S, U15-ch22, adc0

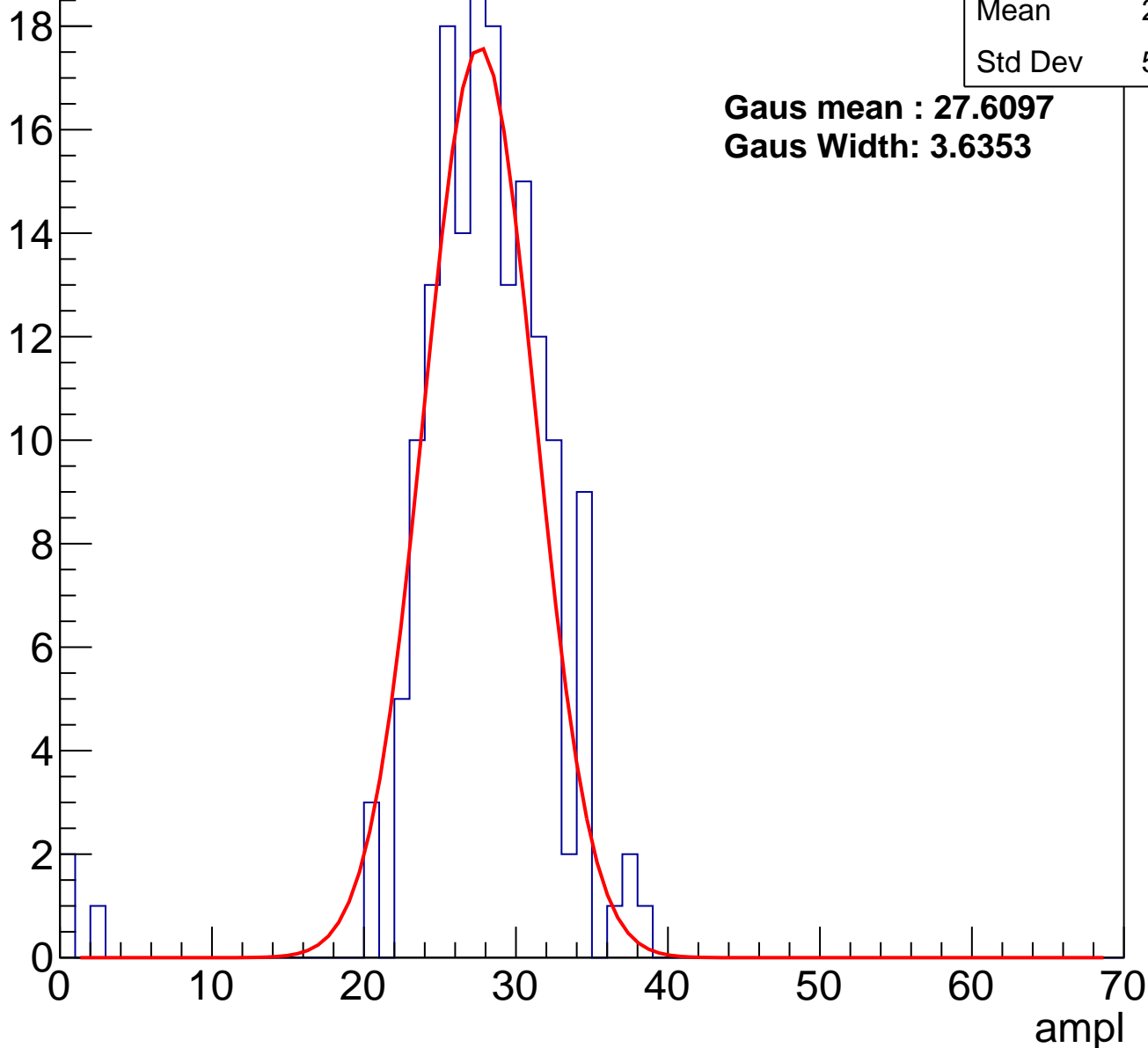
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	27.29
Std Dev	5.039

Gaus mean : 27.6097

Gaus Width: 3.6353

Entry



B0L101S, U15-ch22, adc1

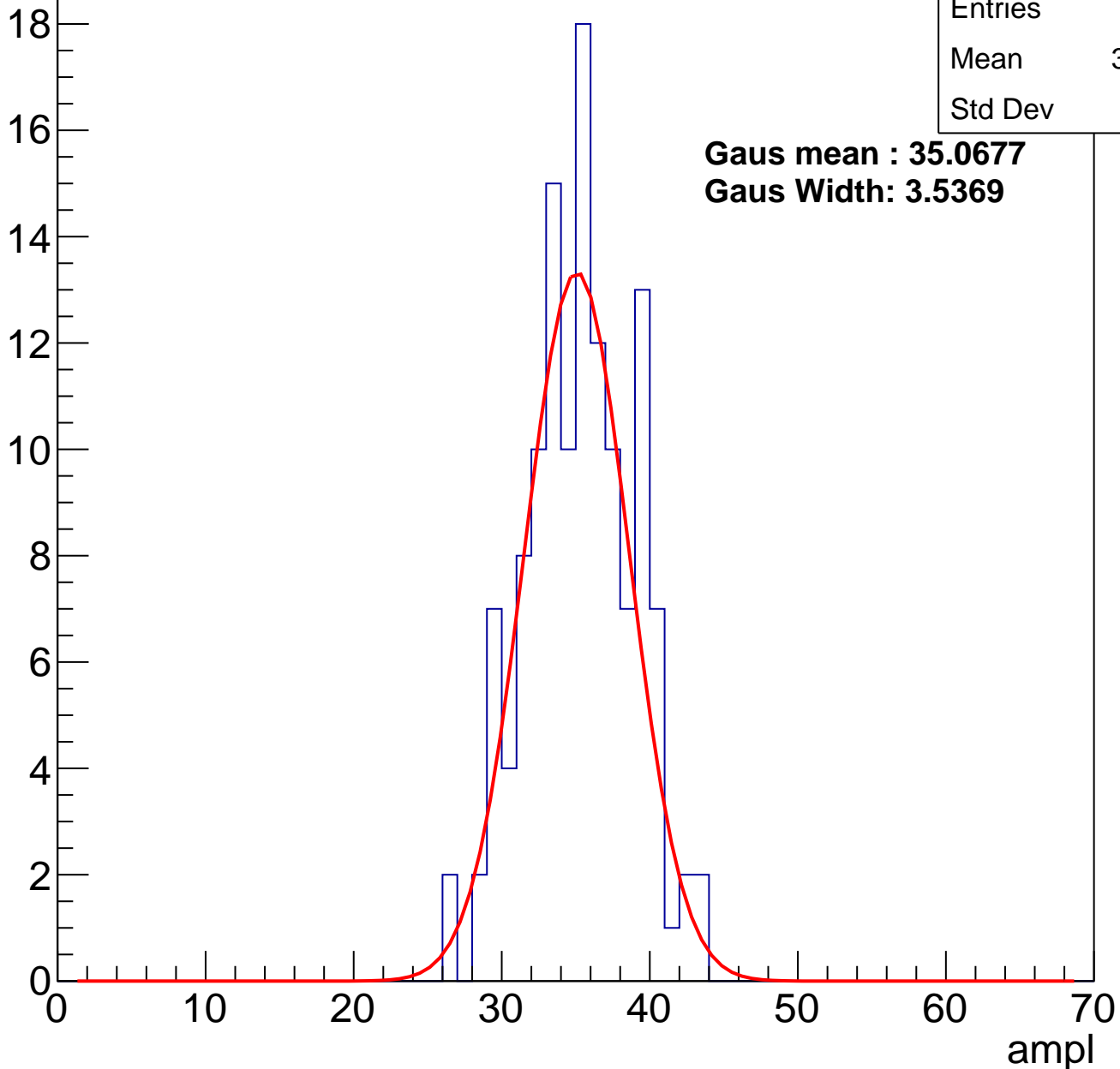
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	34.85
Std Dev	3.57

Gaus mean : 35.0677

Gaus Width: 3.5369

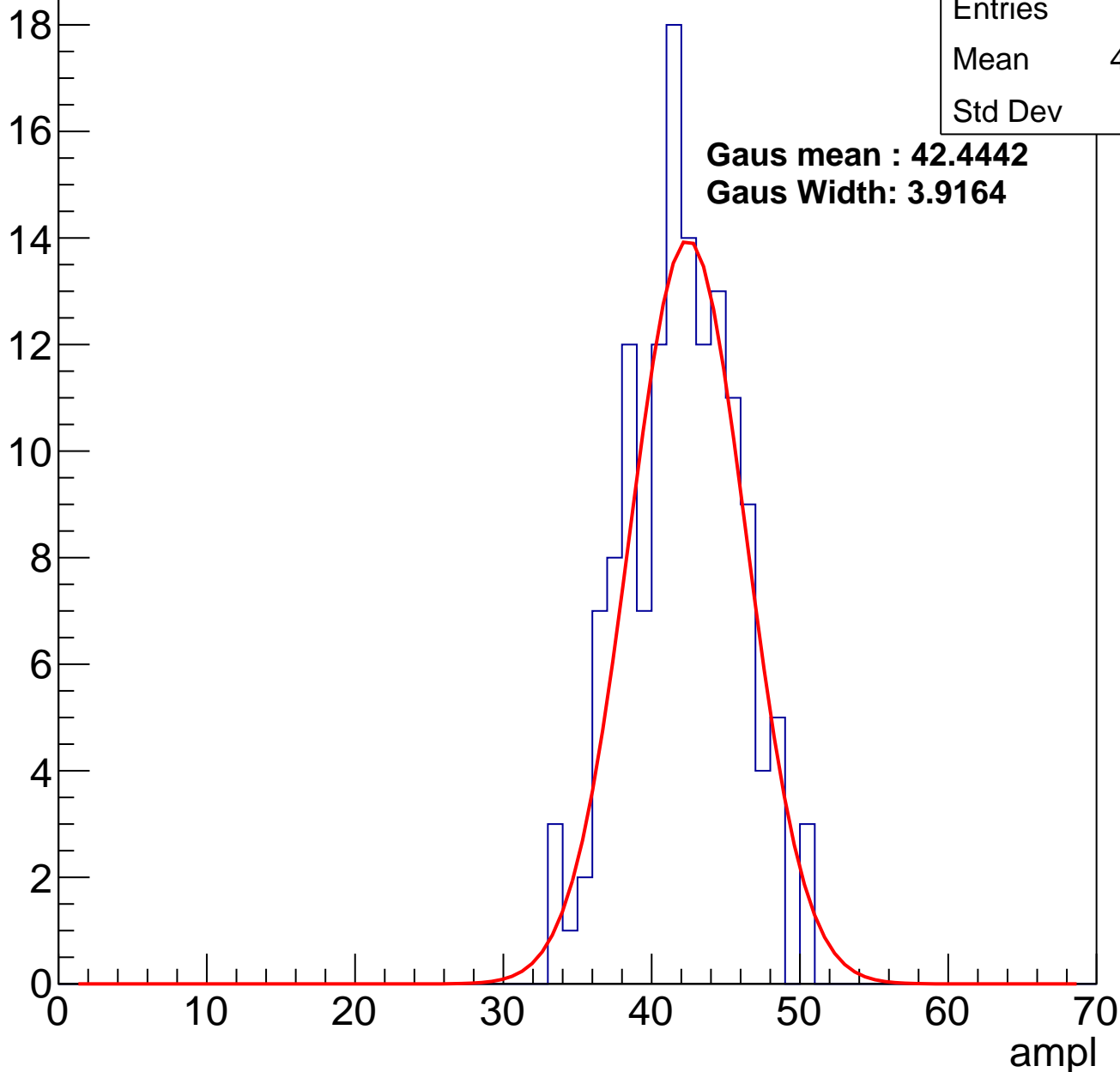
Entry



B0L101S, U15-ch22, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch22, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

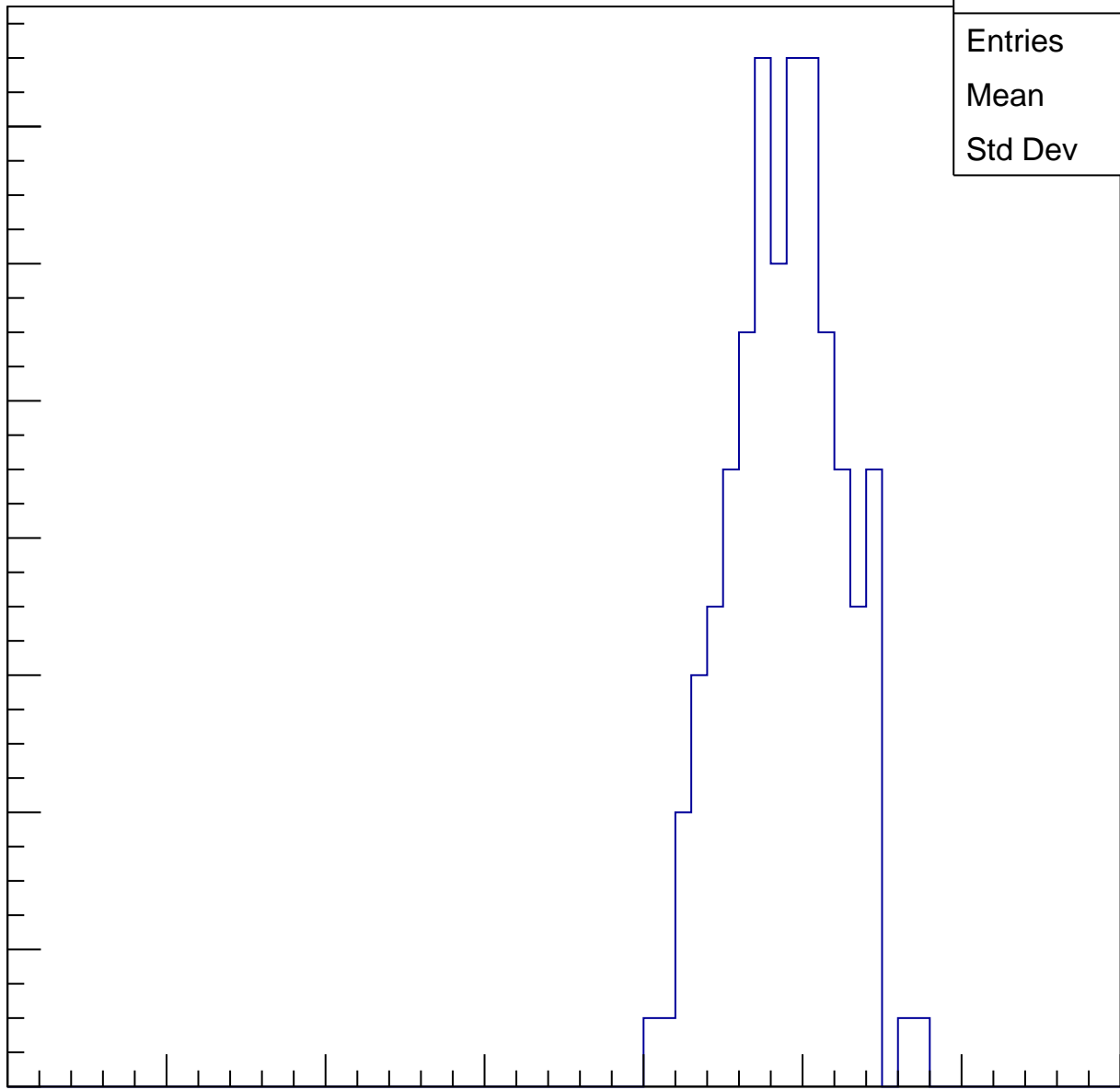
2

0

Entries	134
Mean	48.44
Std Dev	3.45

ampl

0 10 20 30 40 50 60 70

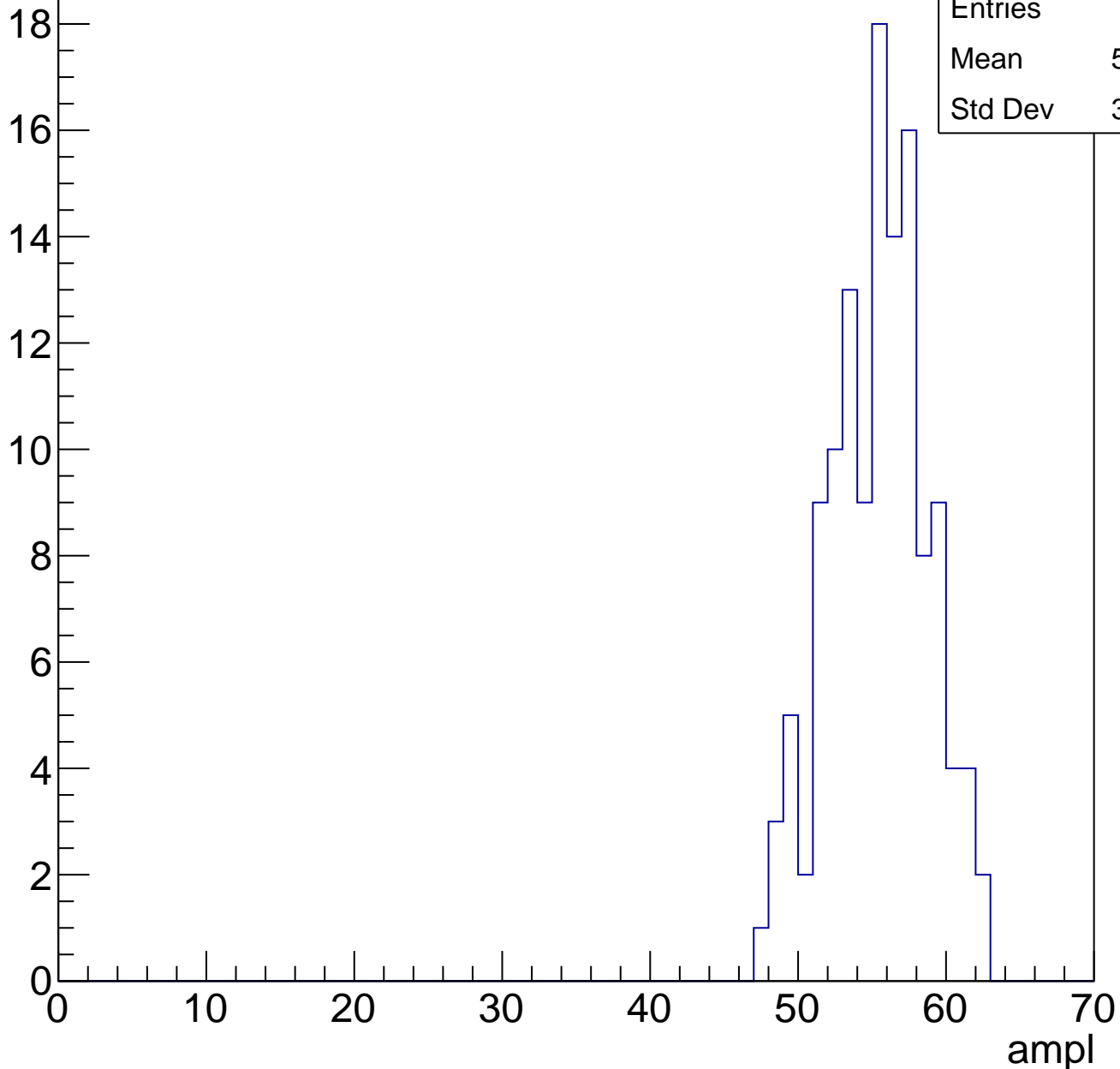


B0L101S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	54.95
Std Dev	3.286

Entry



B0L101S, U15-ch22, adc5

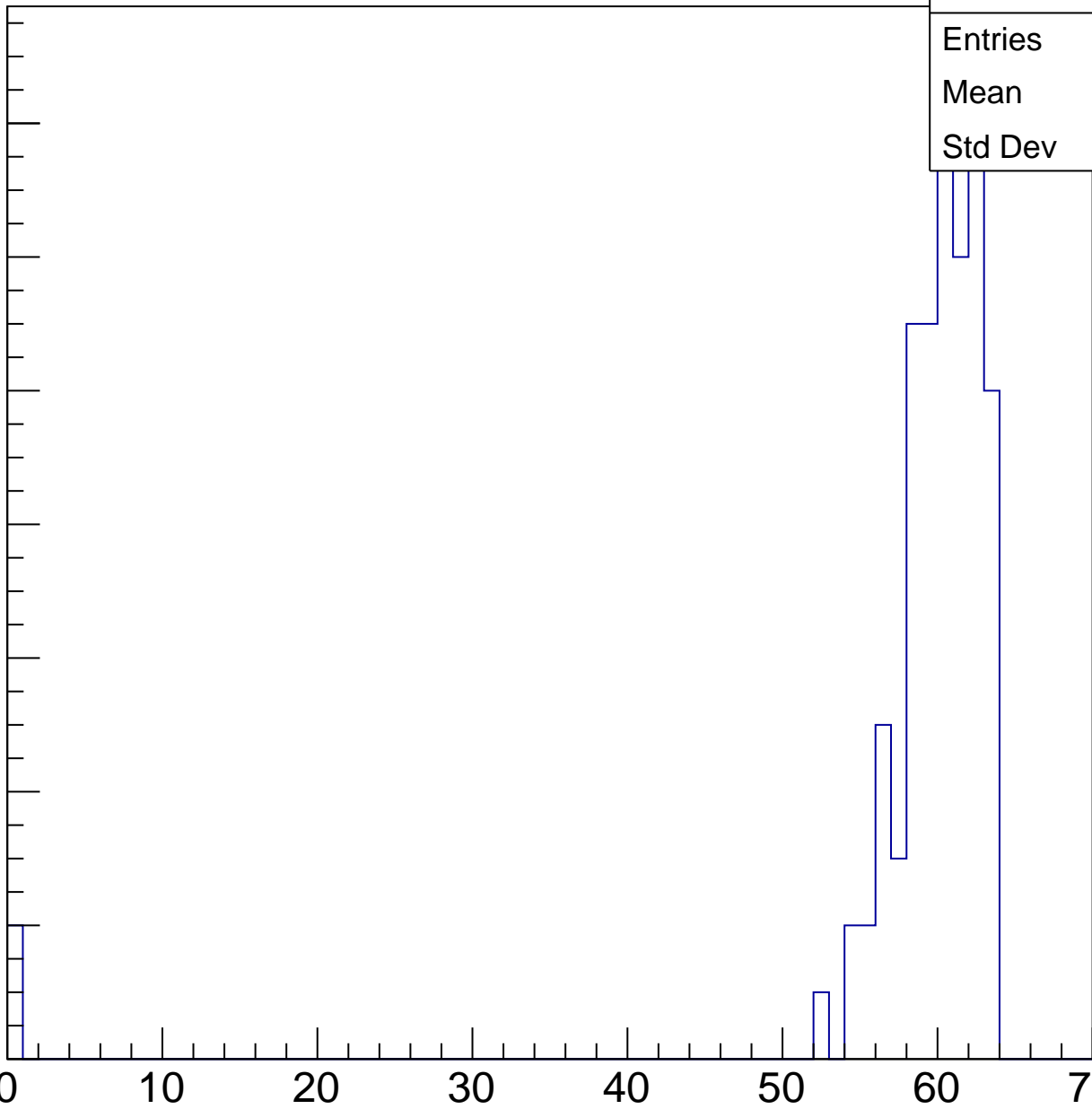
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	88
Mean	58.39
Std Dev	9.218

ampl

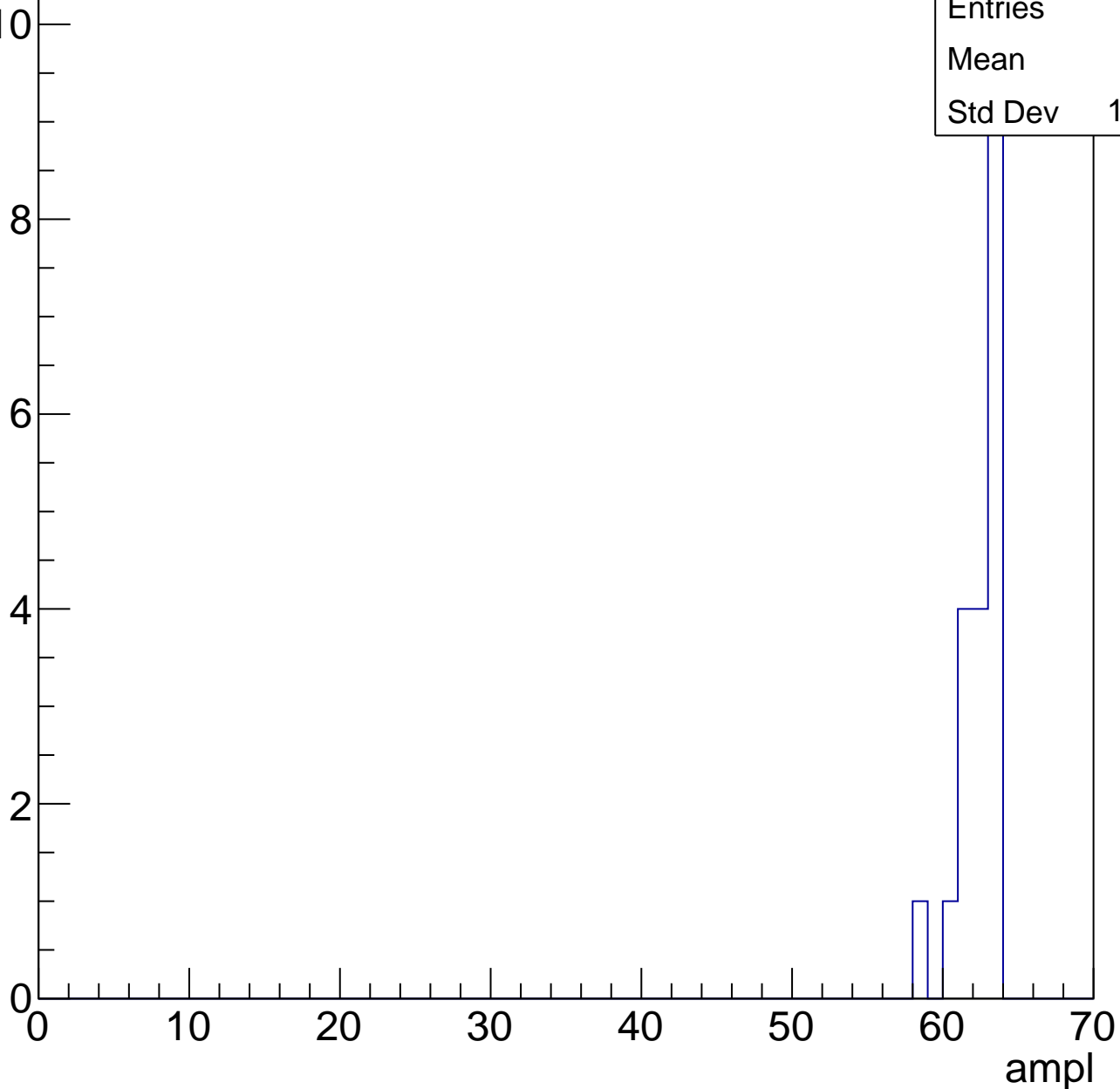


B0L101S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

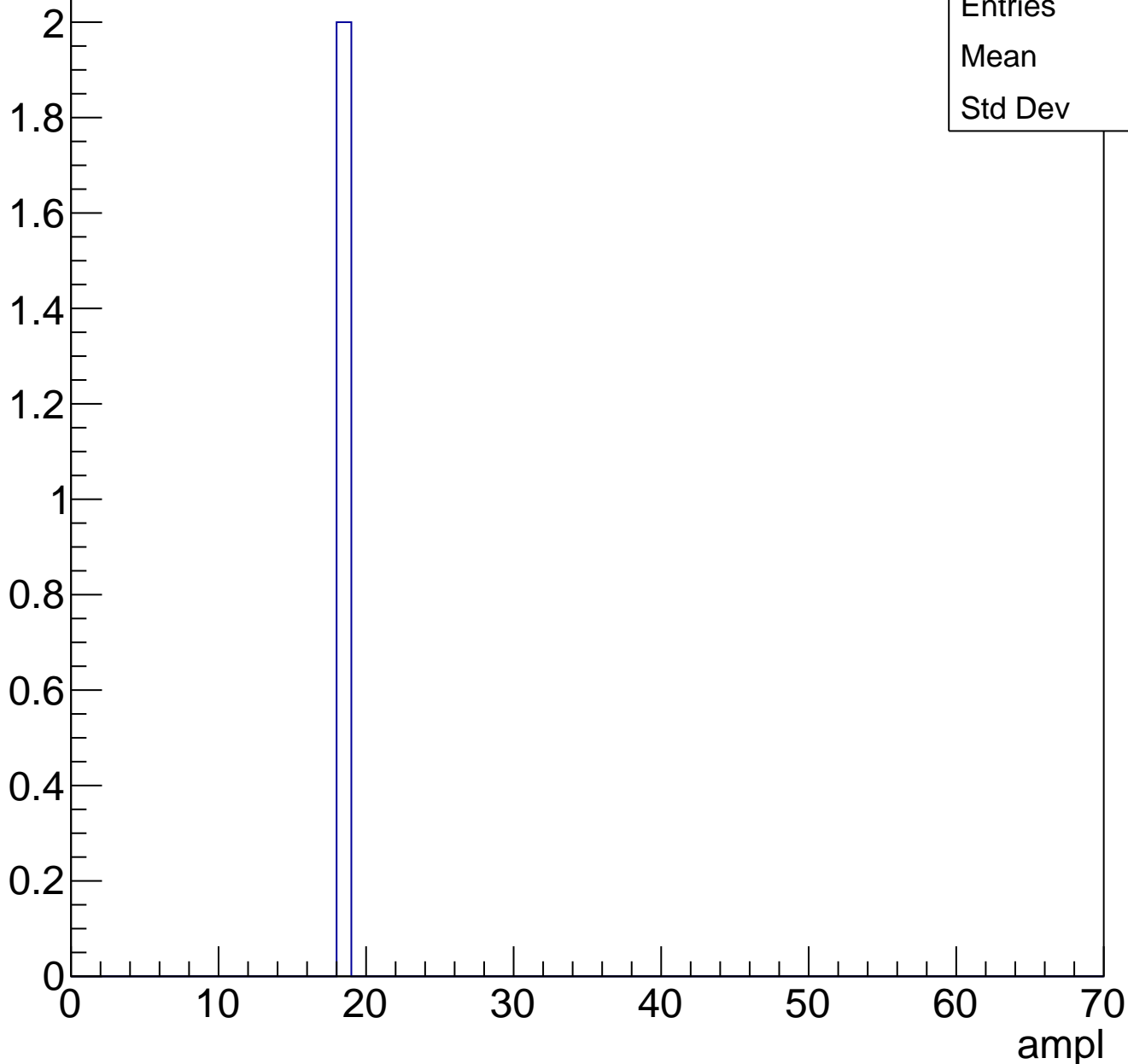
Entries	20
Mean	62
Std Dev	1.304



B0L101S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch23, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	30.73
Std Dev	3.644

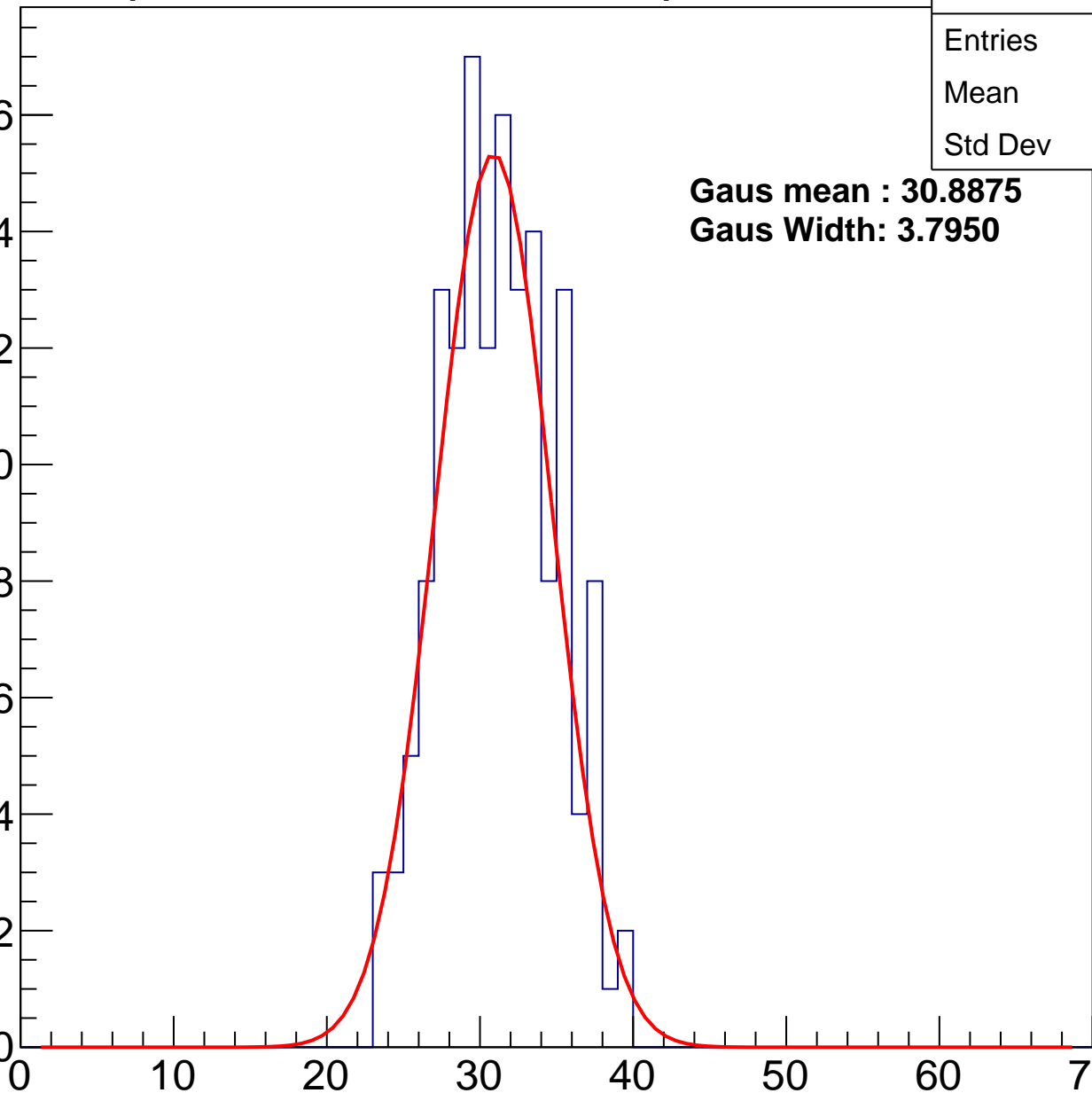
Gaus mean : 30.8875

Gaus Width: 3.7950

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch23, adc1

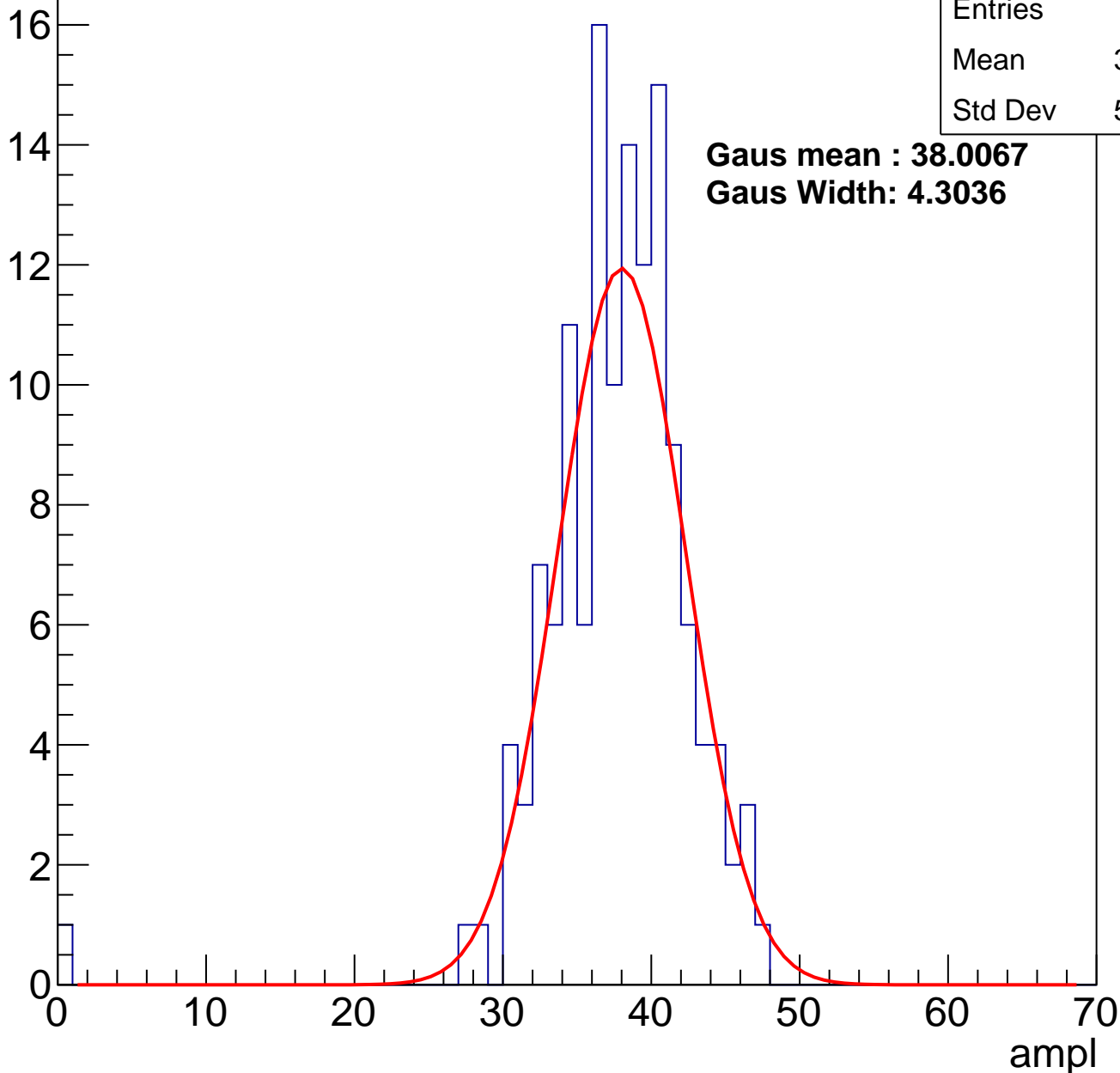
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	37.24
Std Dev	5.115

Gaus mean : 38.0067

Gaus Width: 4.3036

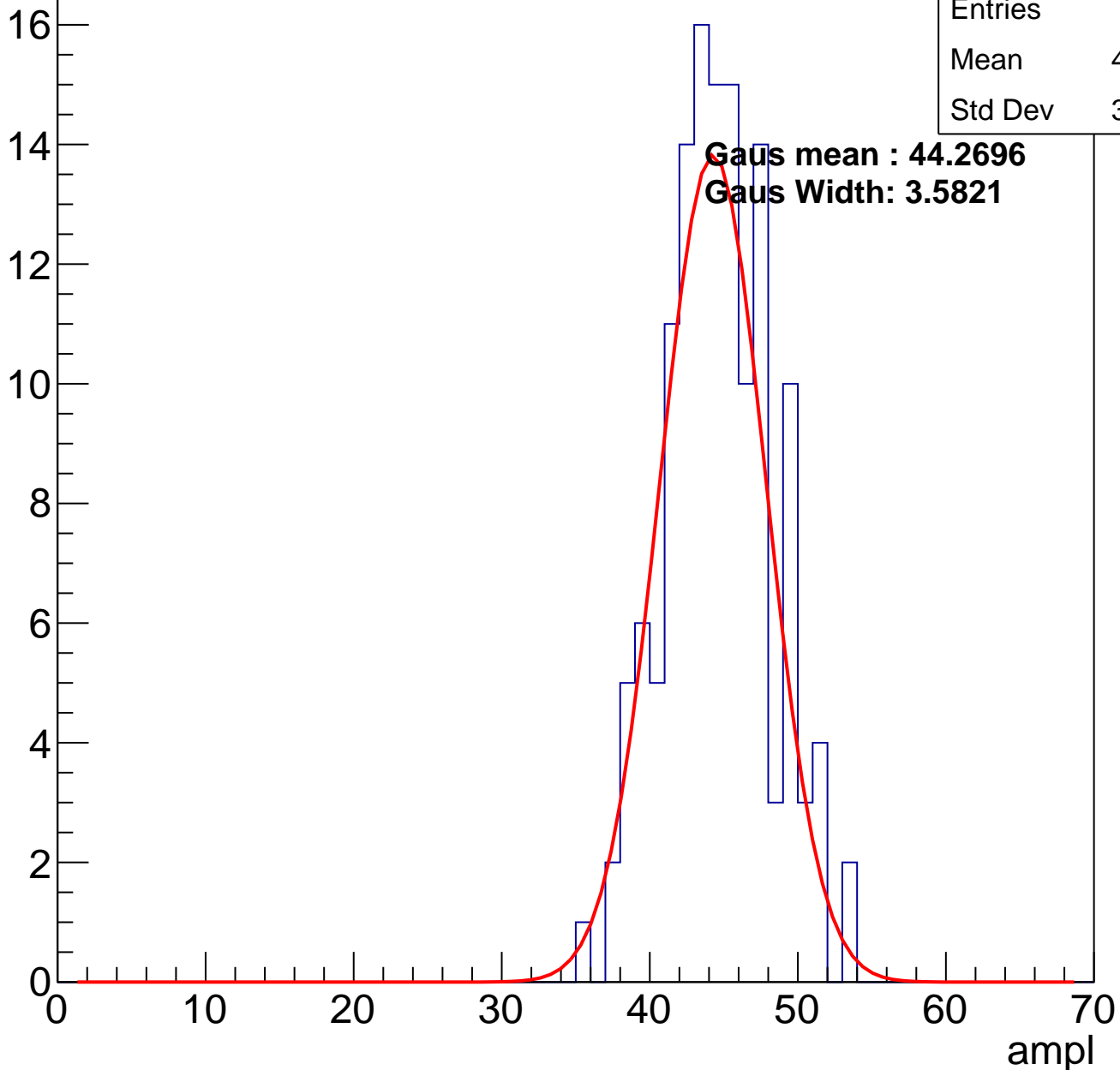
Entry



B0L101S, U15-ch23, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	50.83
Std Dev	2.979

Entry

16

14

12

10

8

6

4

2

0

0

10

20

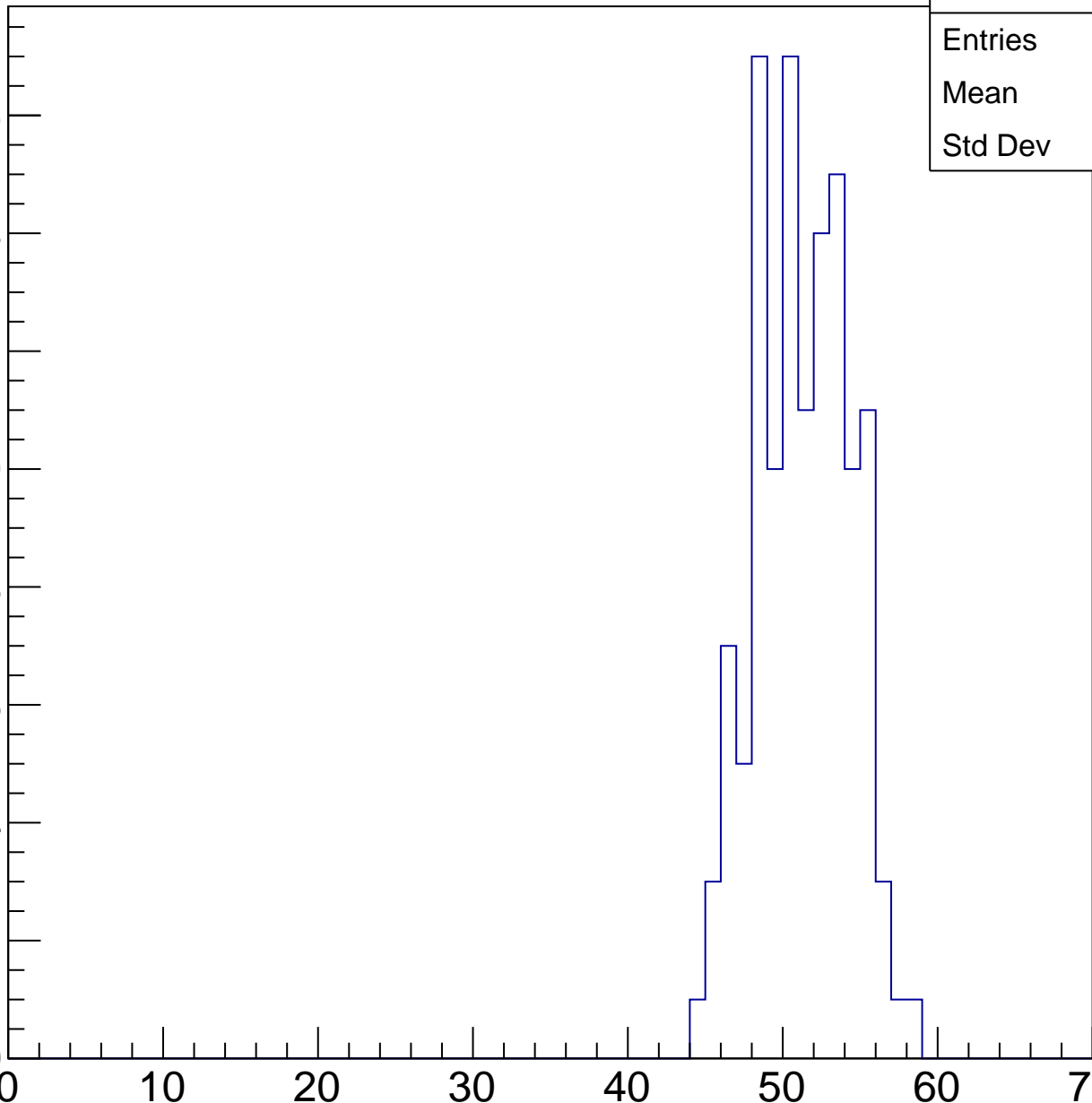
30

40

50

60

ampl

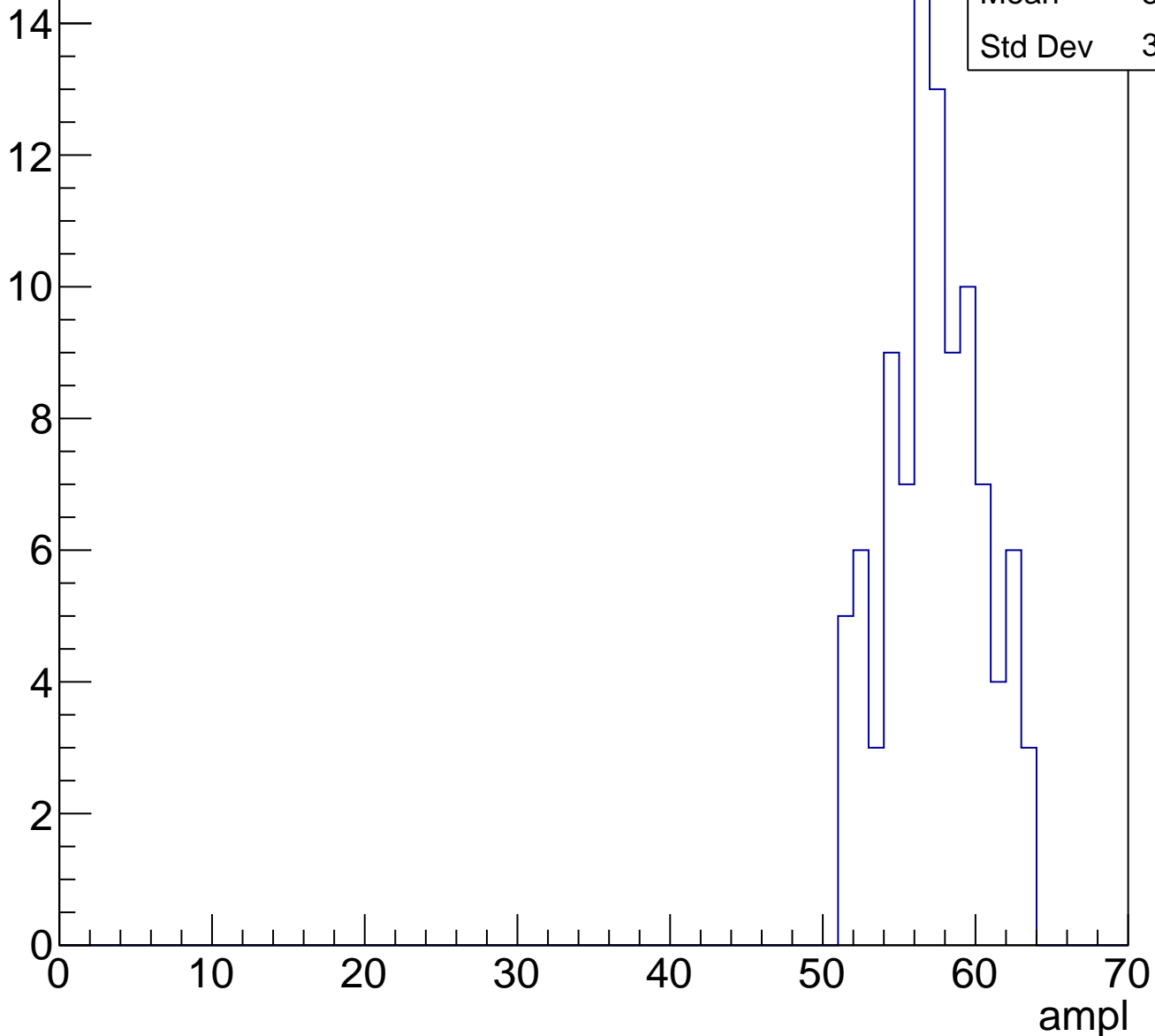


B0L101S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	56.86
Std Dev	3.103

Entry



B0L101S, U15-ch23, adc5

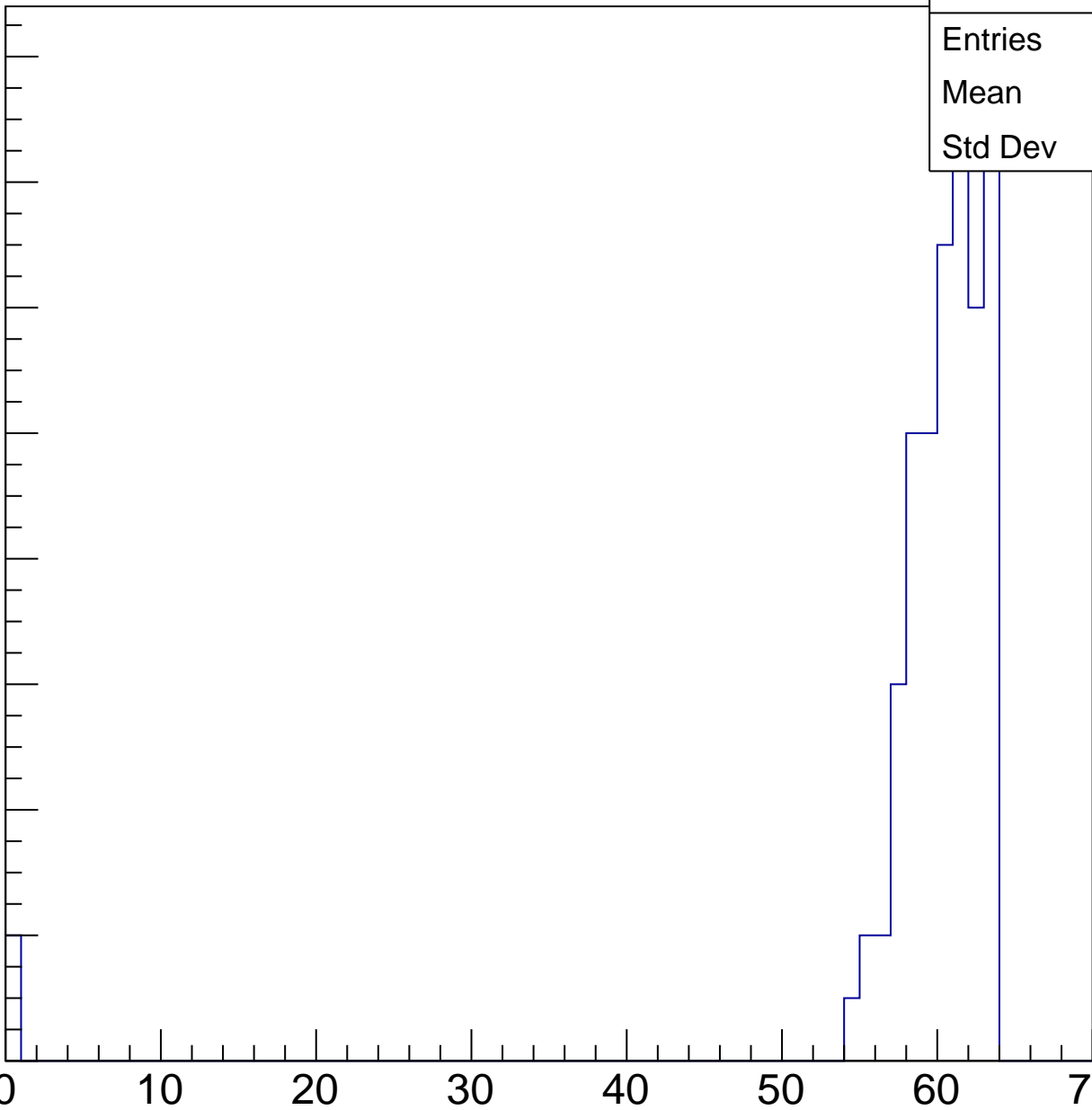
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	58.8
Std Dev	9.175

ampl



B0L101S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch24, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.27
Std Dev	3.706

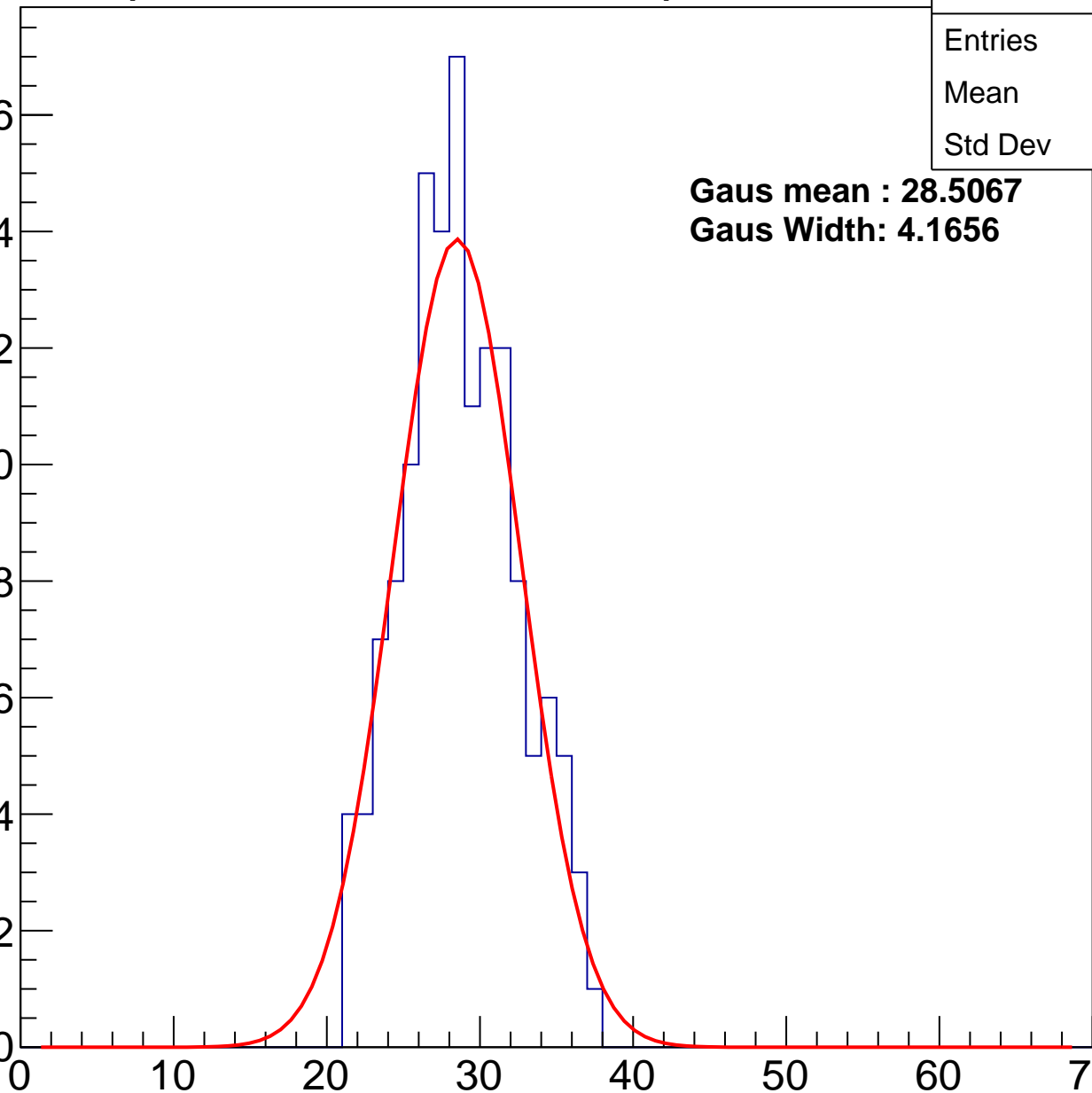
Gaus mean : 28.5067

Gaus Width: 4.1656

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch24, adc1

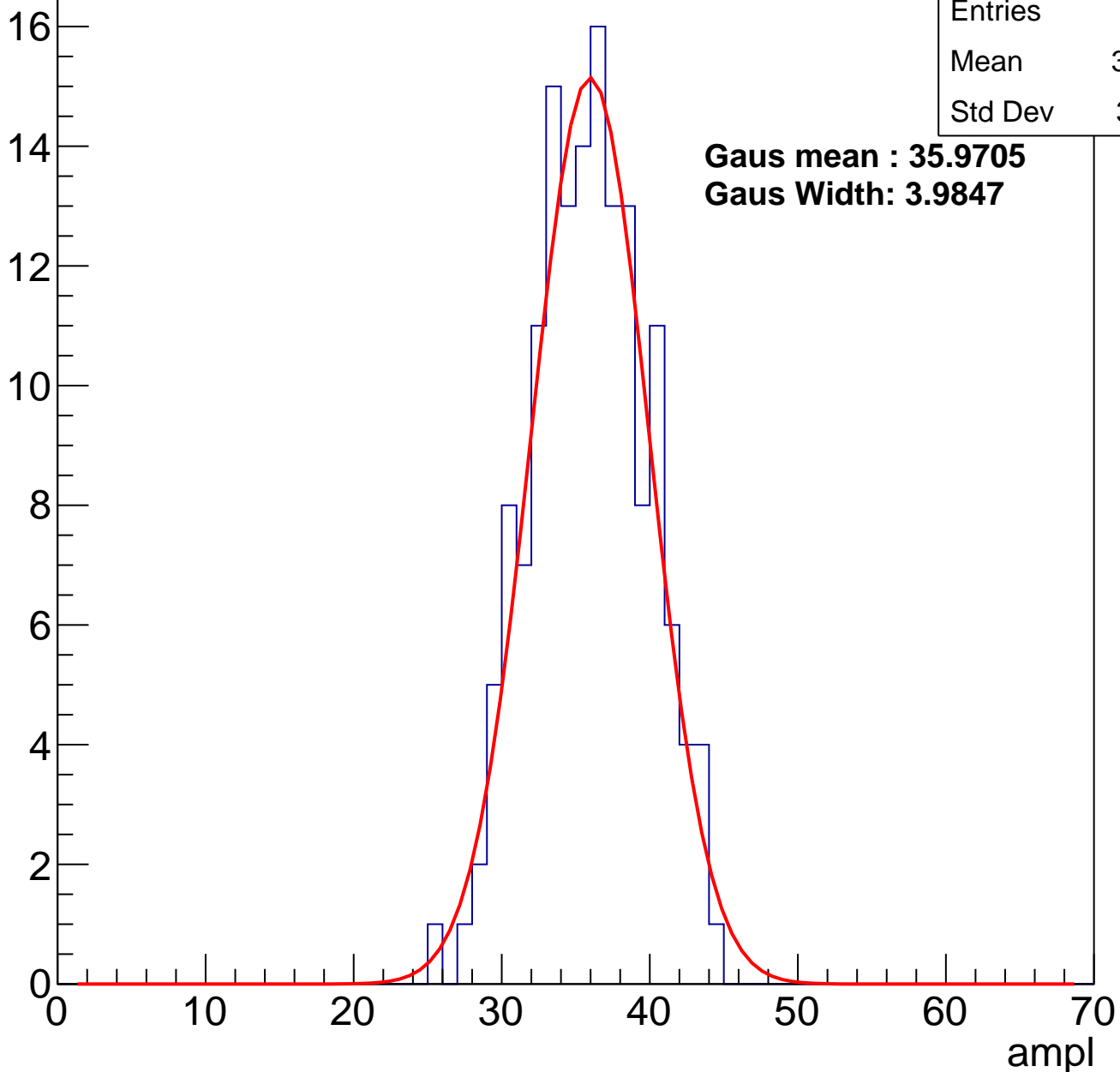
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	35.44
Std Dev	3.821

Gaus mean : 35.9705

Gaus Width: 3.9847

Entry



B0L101S, U15-ch24, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

128

Mean

42.77

Std Dev

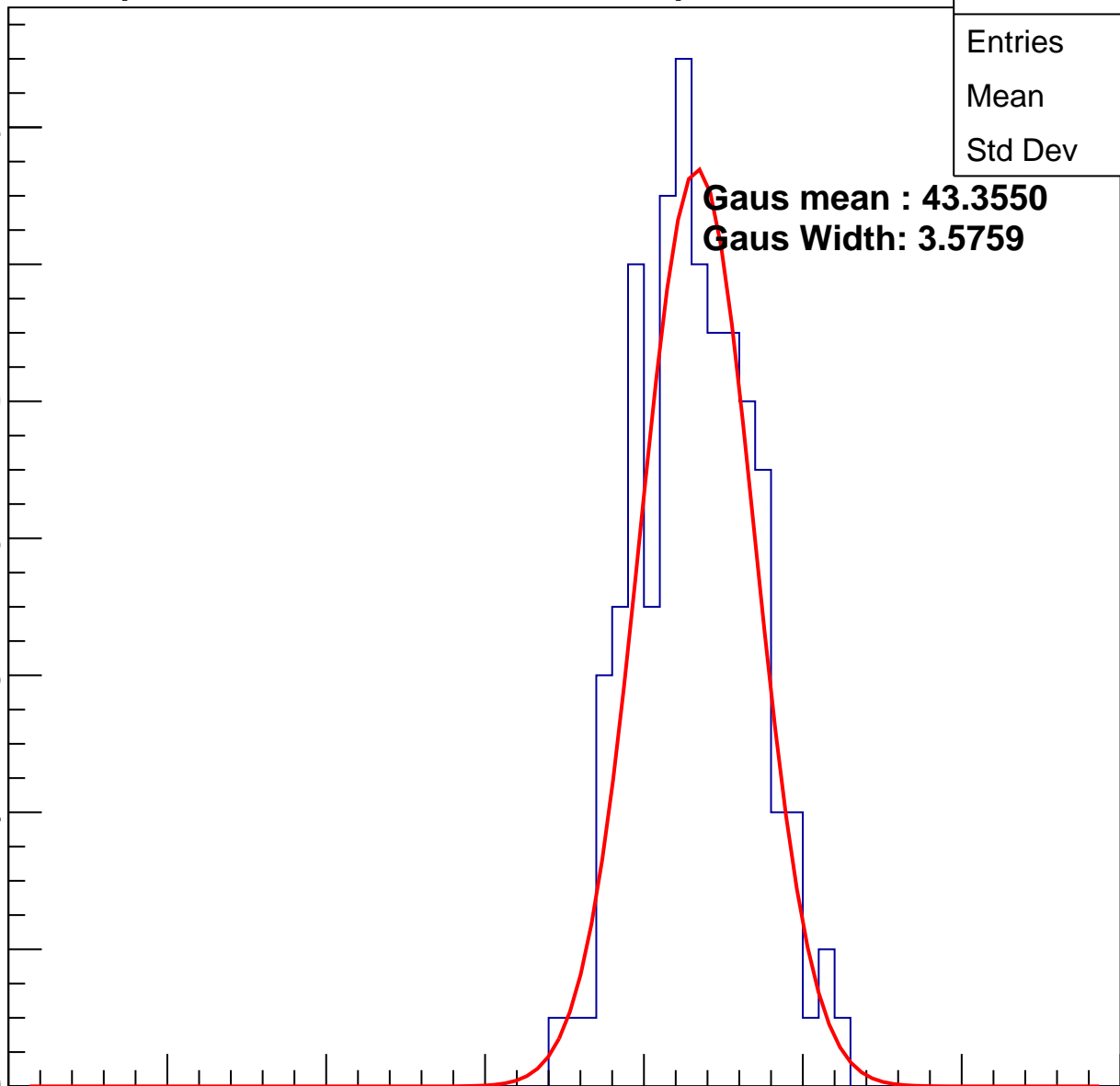
3.635

Gaus mean : 43.3550

Gaus Width: 3.5759

0 10 20 30 40 50 60 70

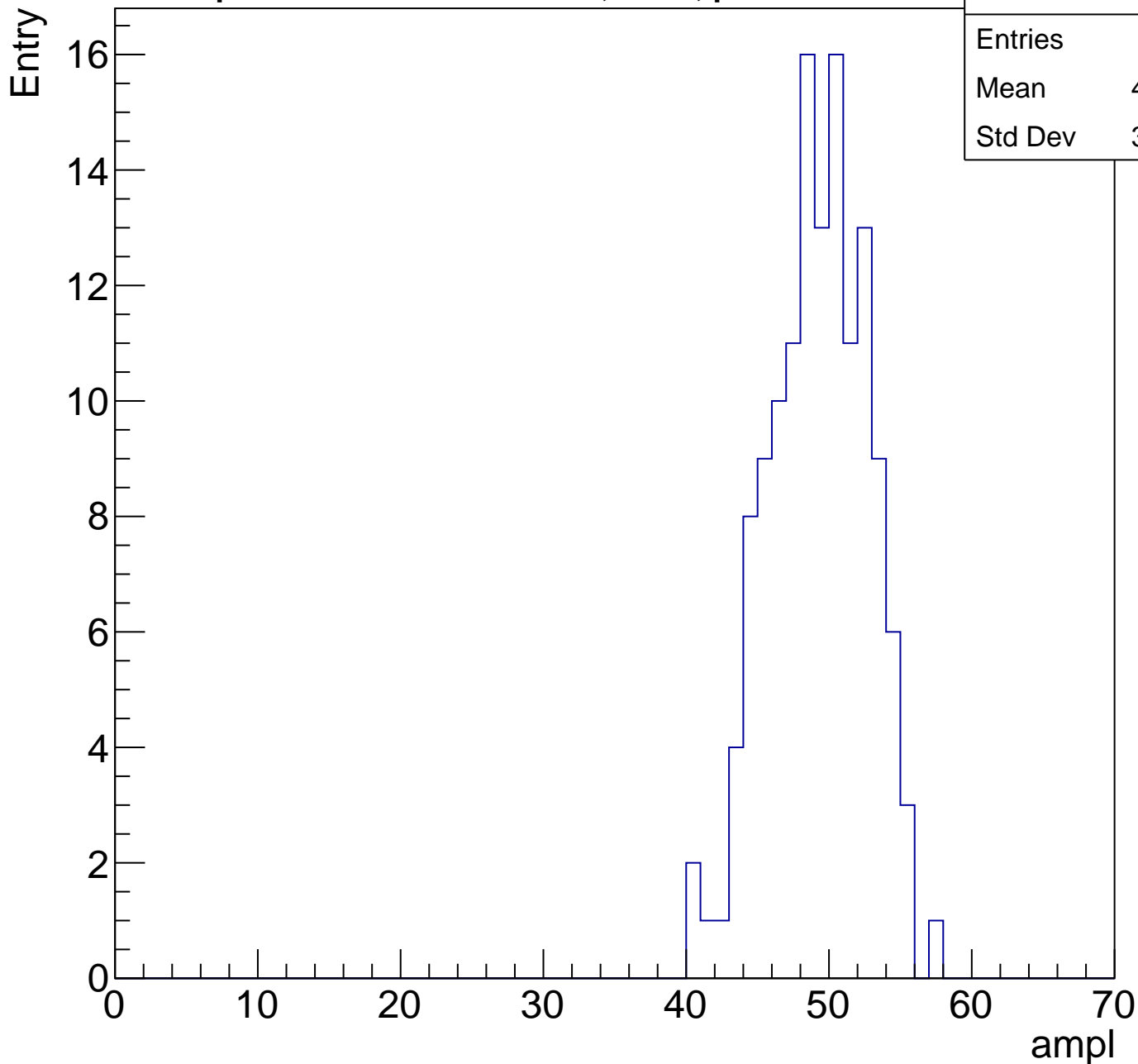
ampl



B0L101S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#1, port C1

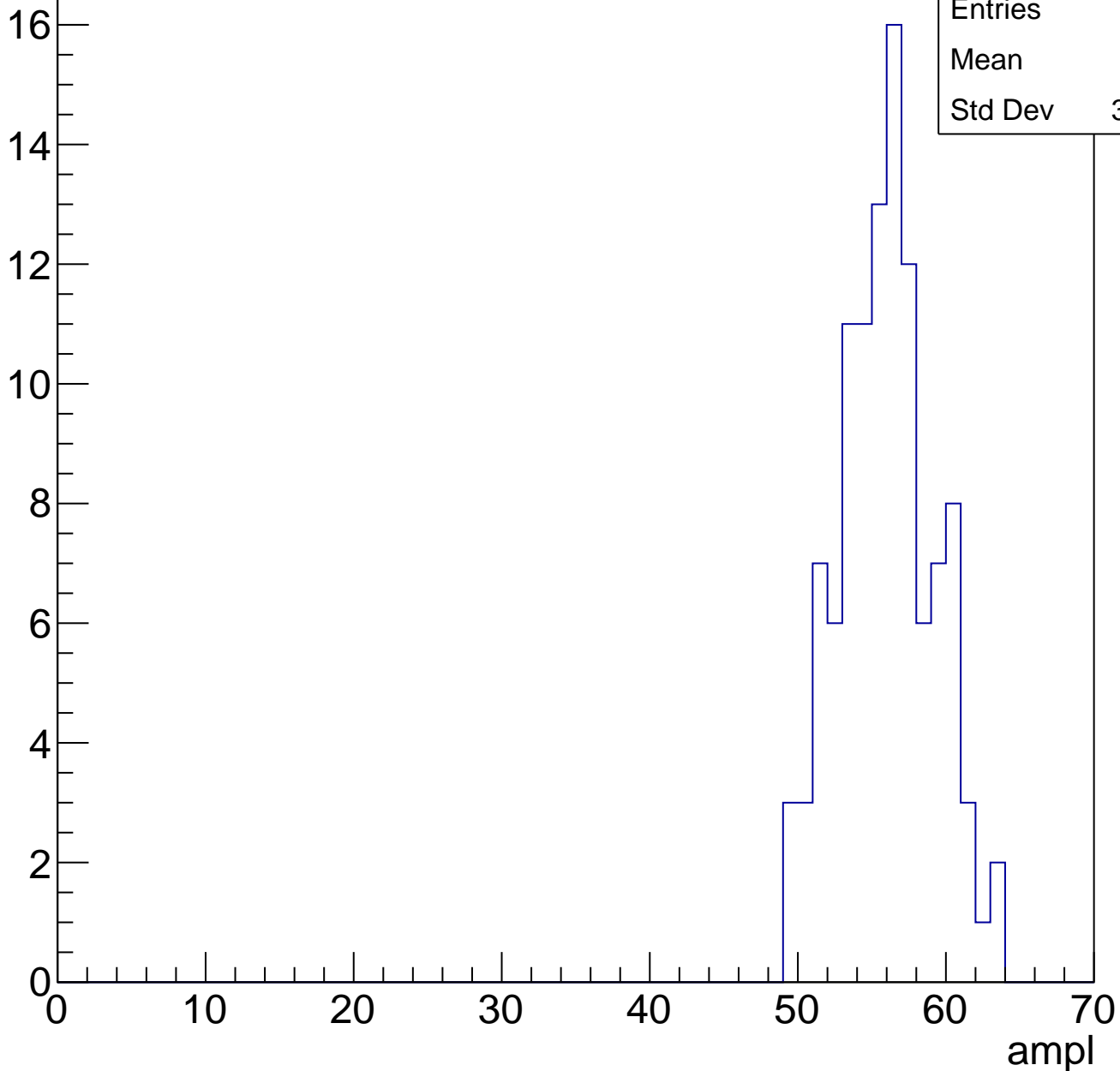
Entries	134
Mean	48.76
Std Dev	3.399



B0L101S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch24, adc5

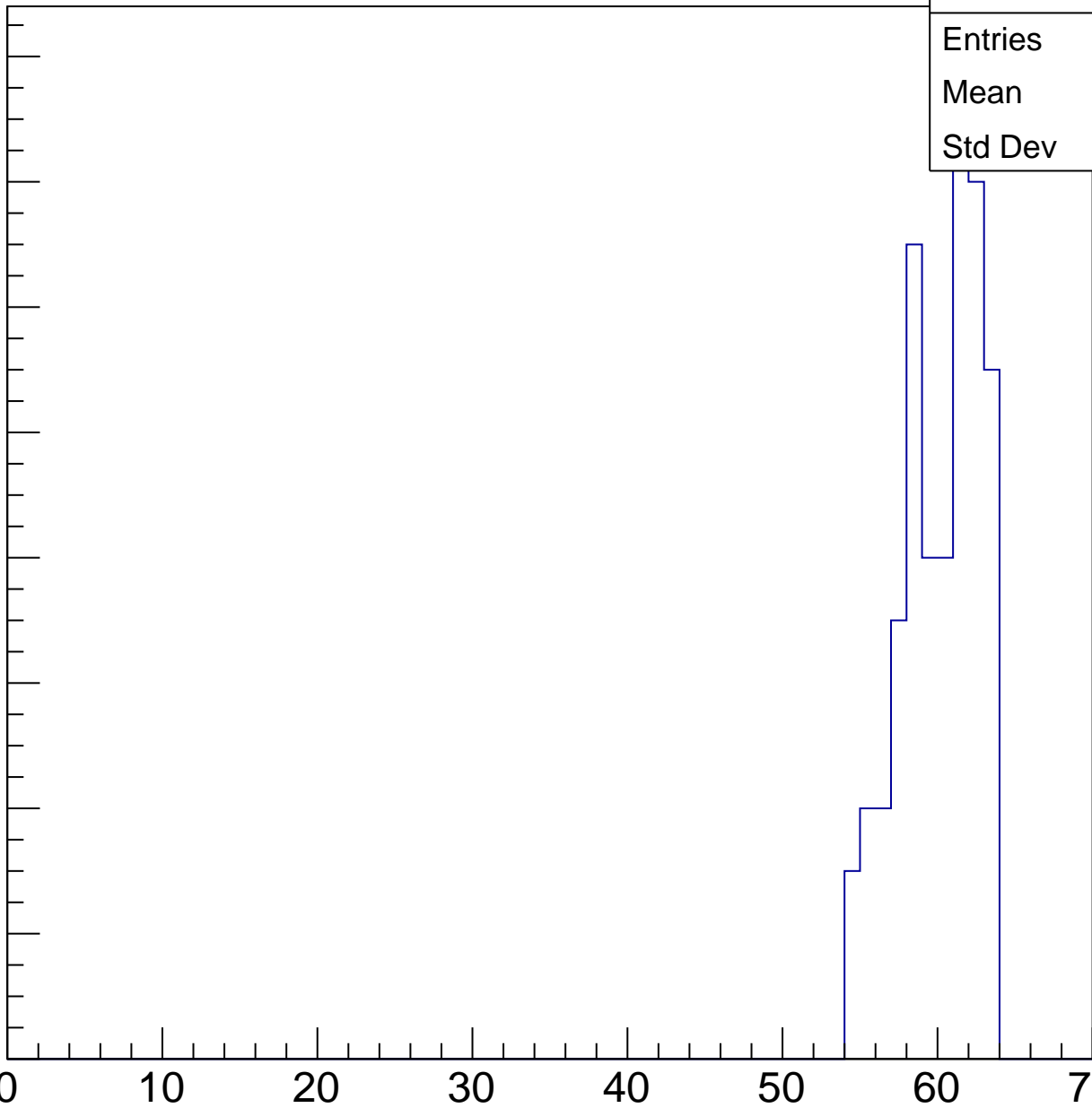
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	88
Mean	59.64
Std Dev	2.51

ampl

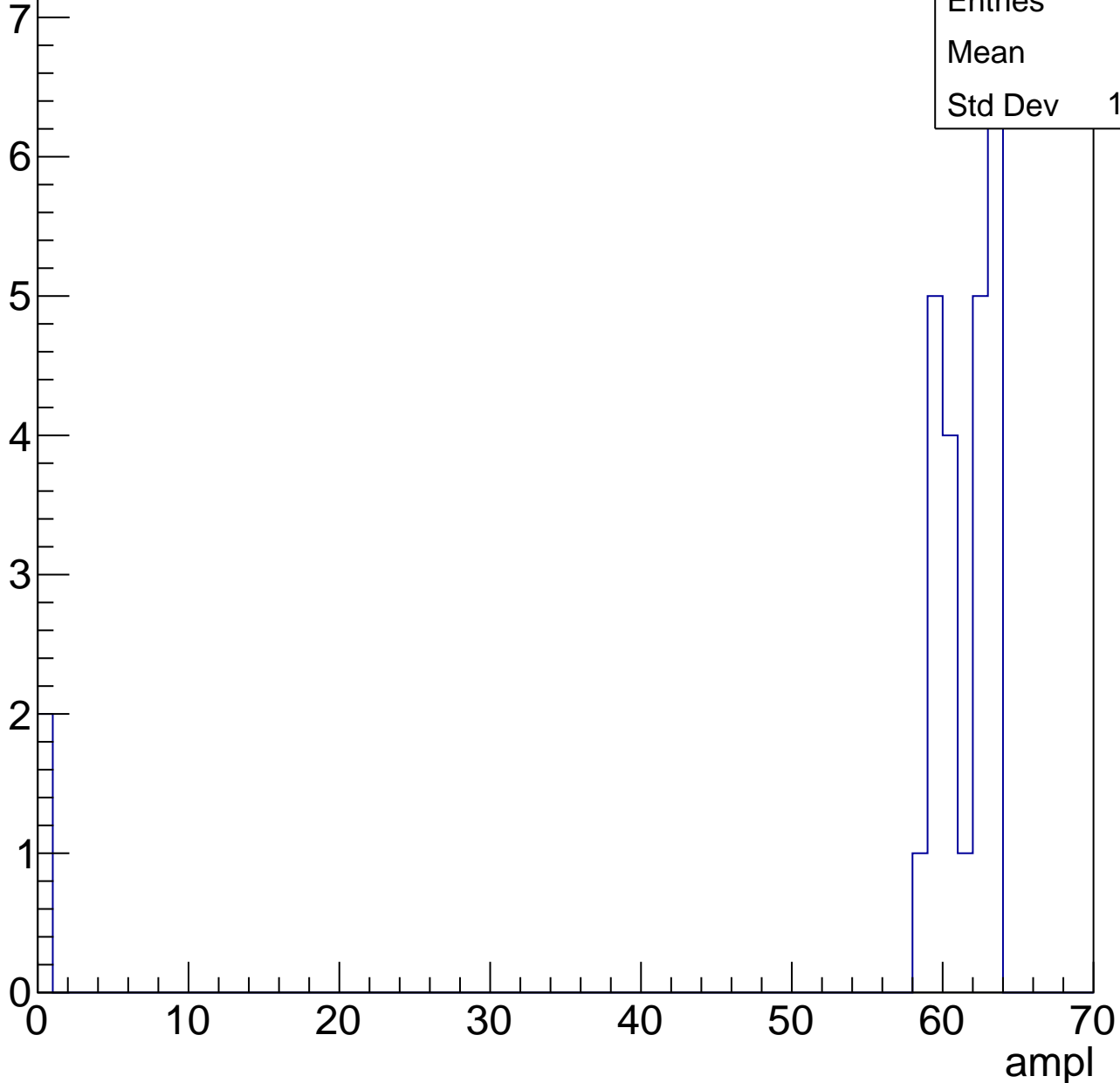


B0L101S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

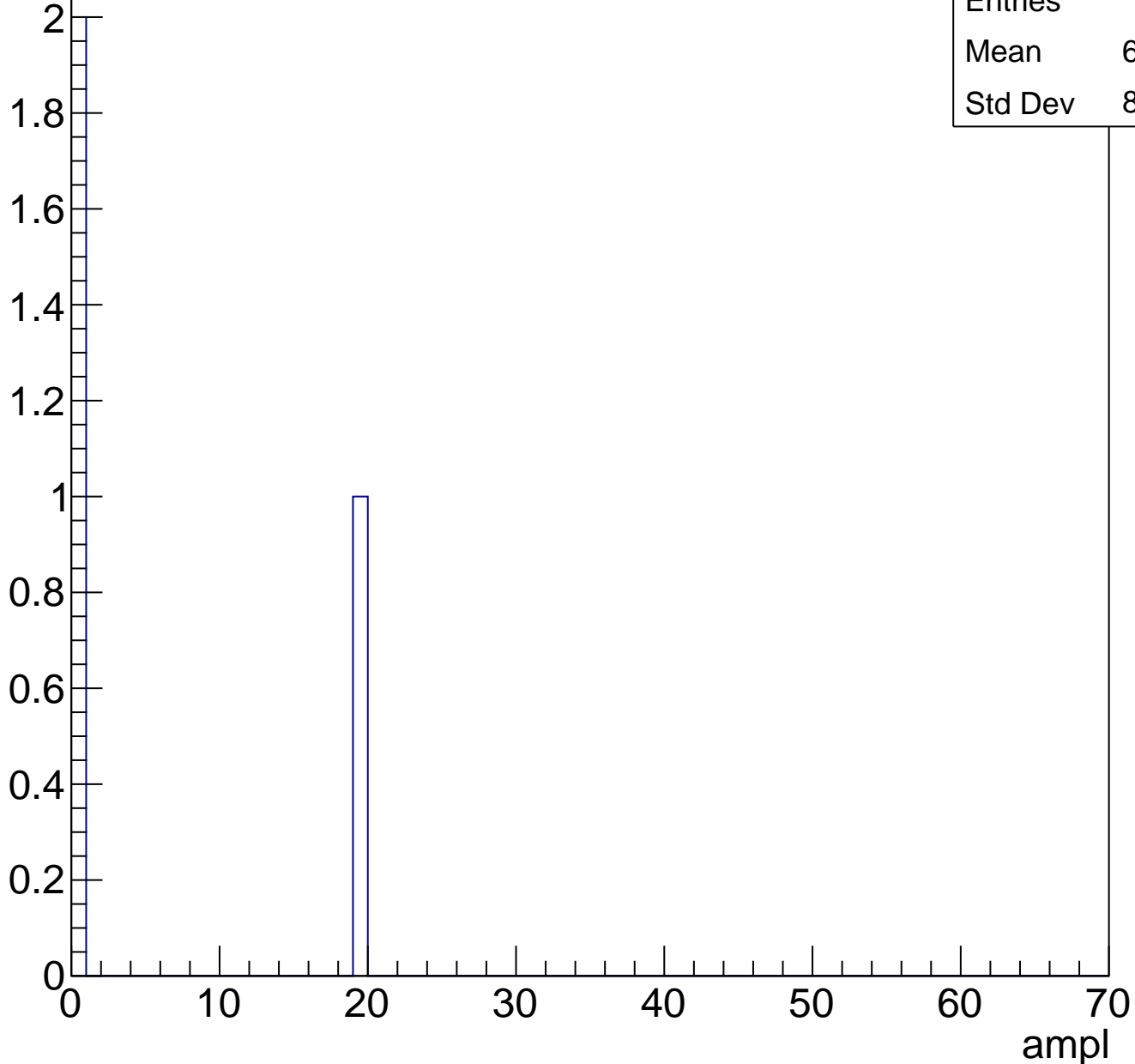
Entries	25
Mean	56.2
Std Dev	16.65



B0L101S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B0L101S, U15-ch25, adc0

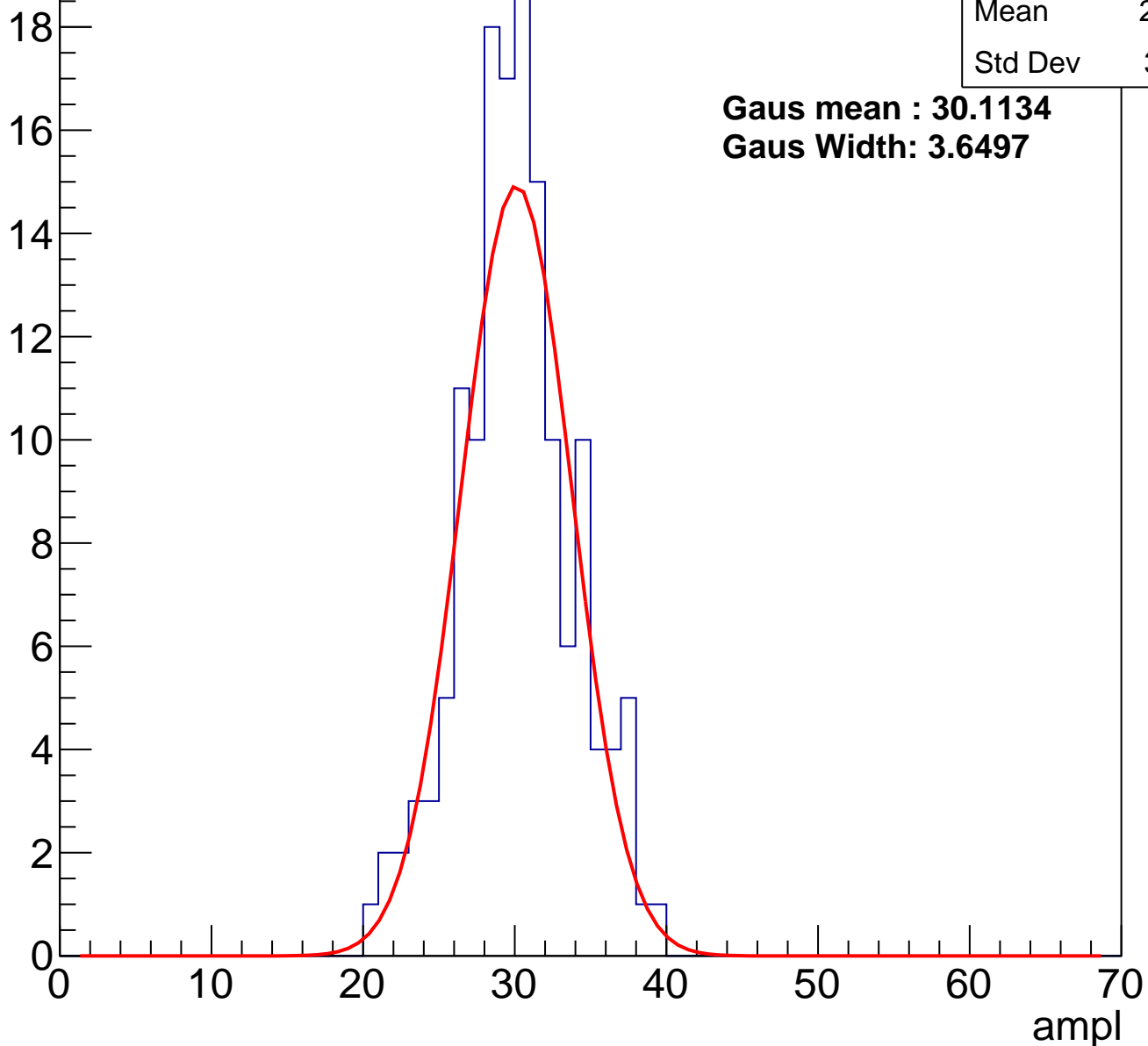
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	29.69
Std Dev	3.701

Gaus mean : 30.1134

Gaus Width: 3.6497

Entry



B0L101S, U15-ch25, adc1

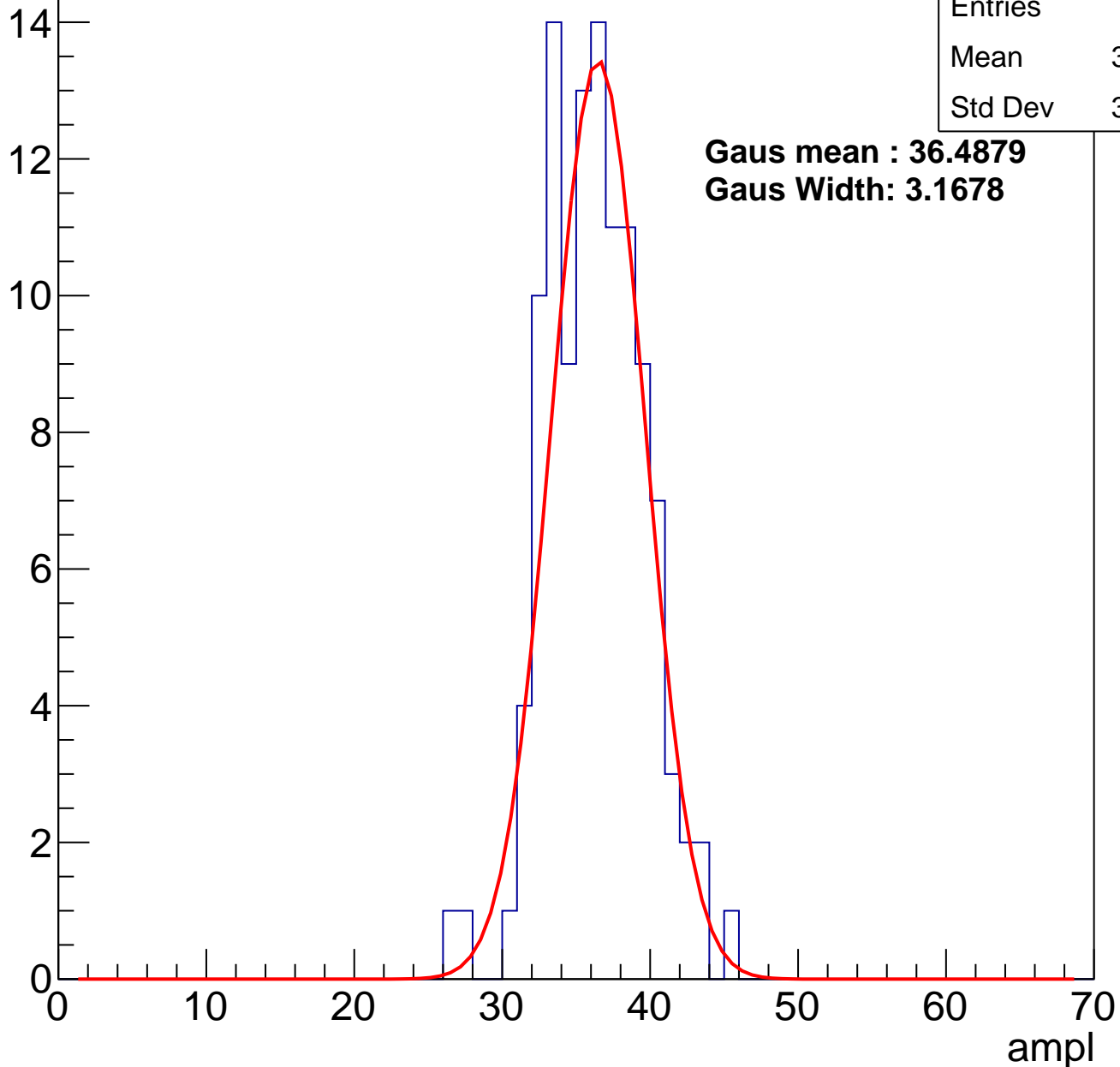
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.82
Std Dev	3.289

Gaus mean : 36.4879

Gaus Width: 3.1678

Entry



B0L101S, U15-ch25, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

164

Mean

43.04

Std Dev

3.604

Gaus mean : 43.3950

Gaus Width: 3.5552

0

10

20

30

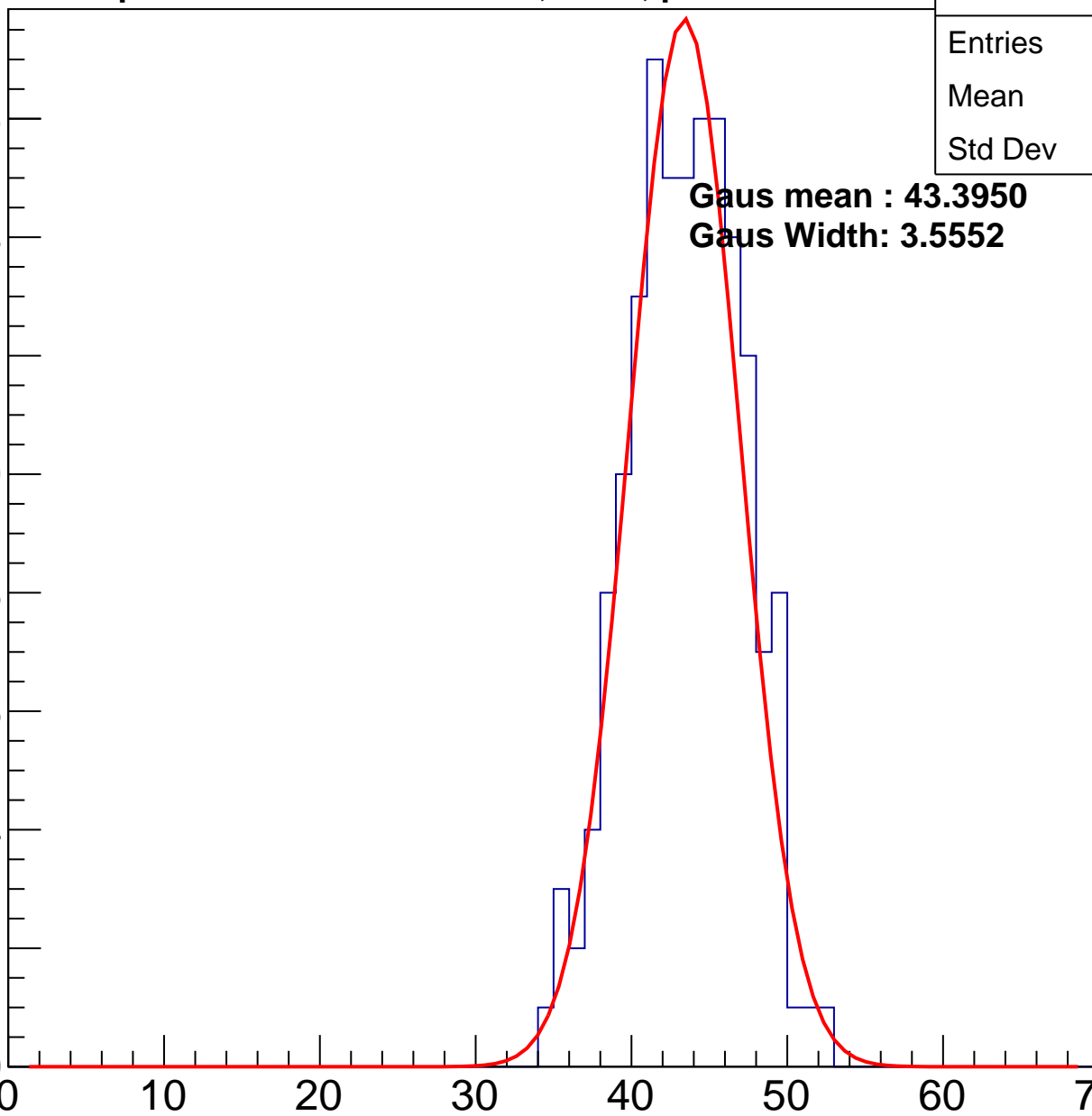
40

50

60

70

ampl



B0L101S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#1, port C1

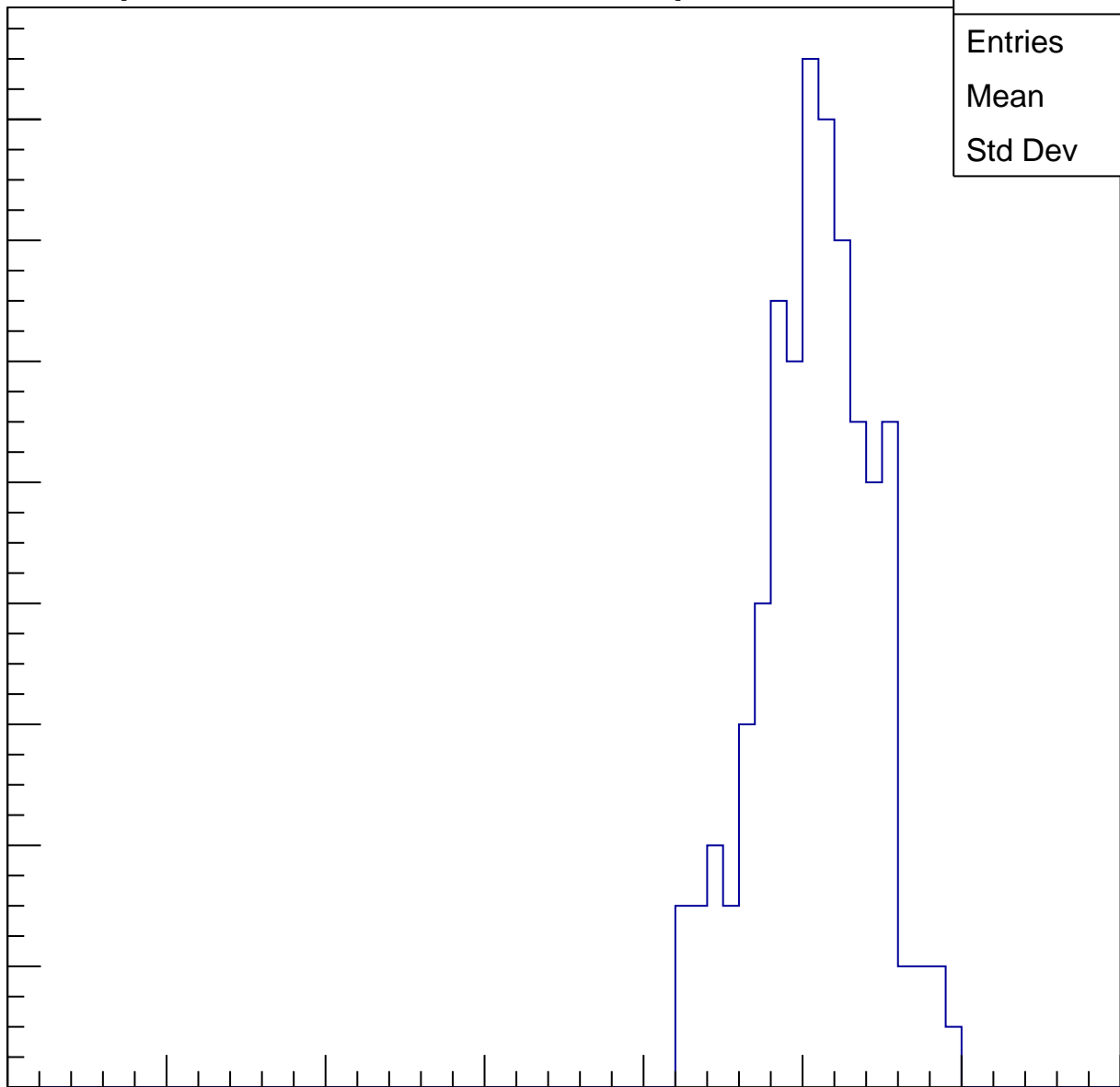
Entries	138
Mean	50.38
Std Dev	3.566

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch25, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	83
Mean	56.13
Std Dev	2.944

Entry

10

8

6

4

2

0

0

10

20

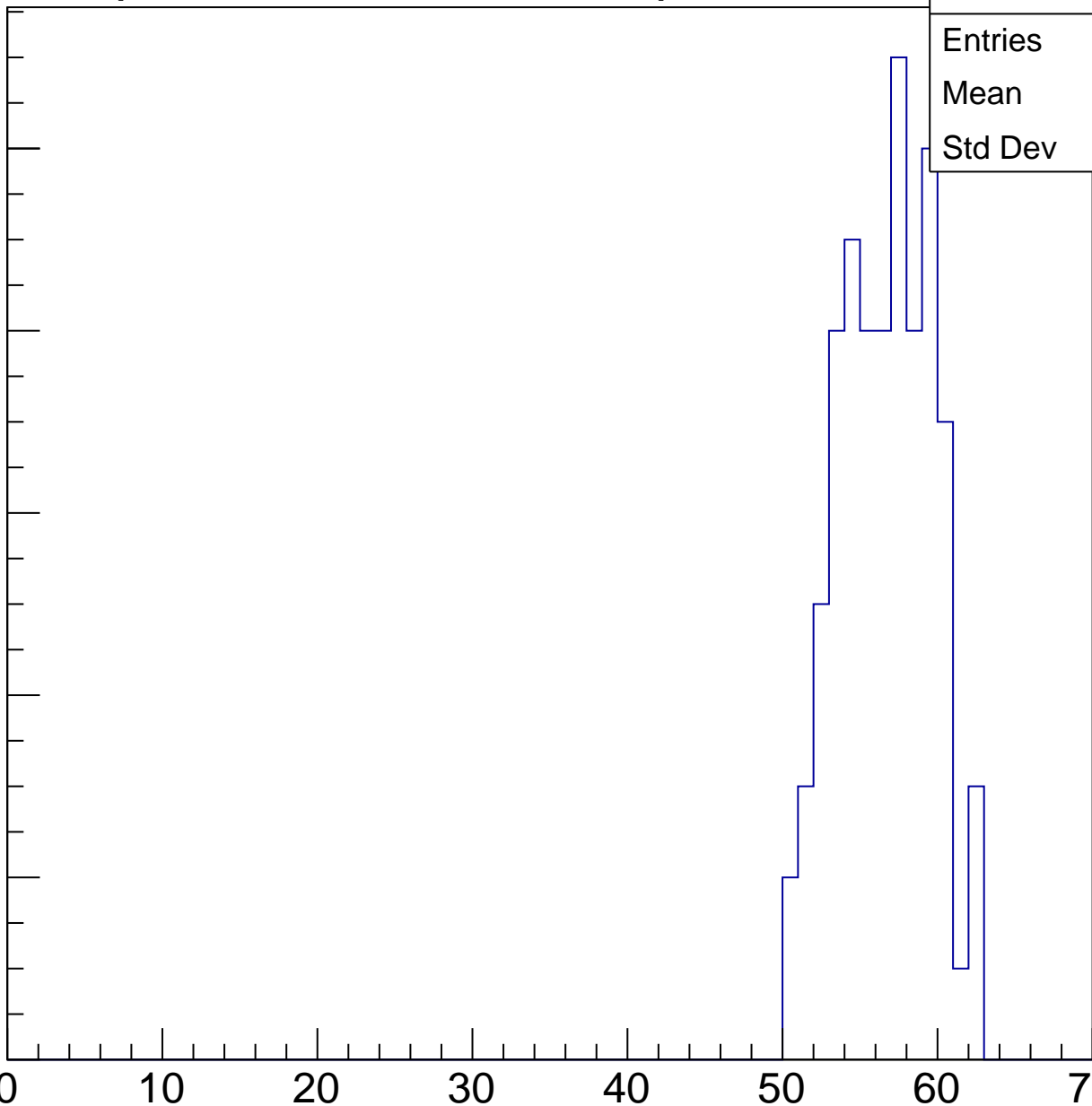
30

40

50

60

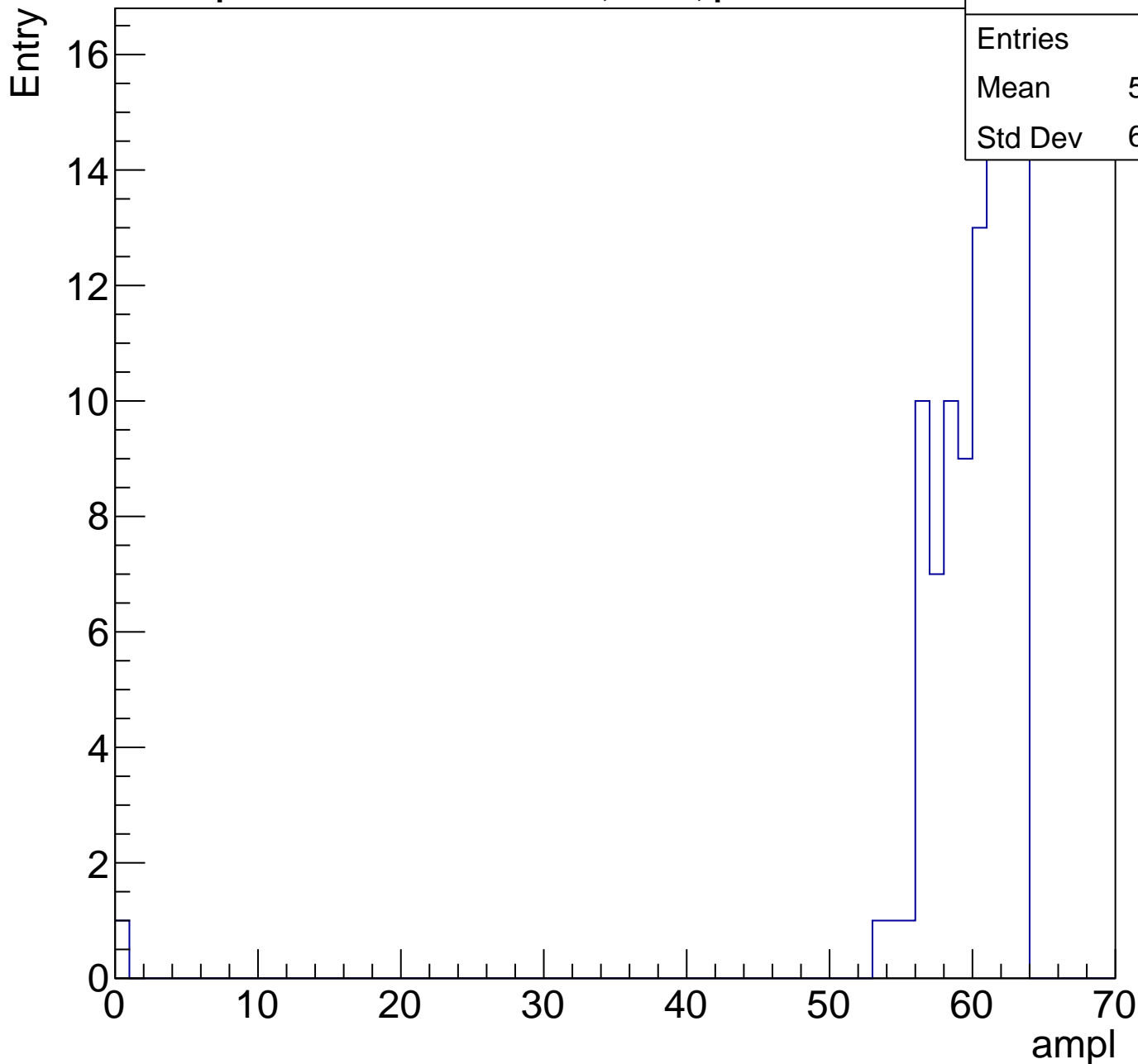
ampl



B0L101S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#1, port C1

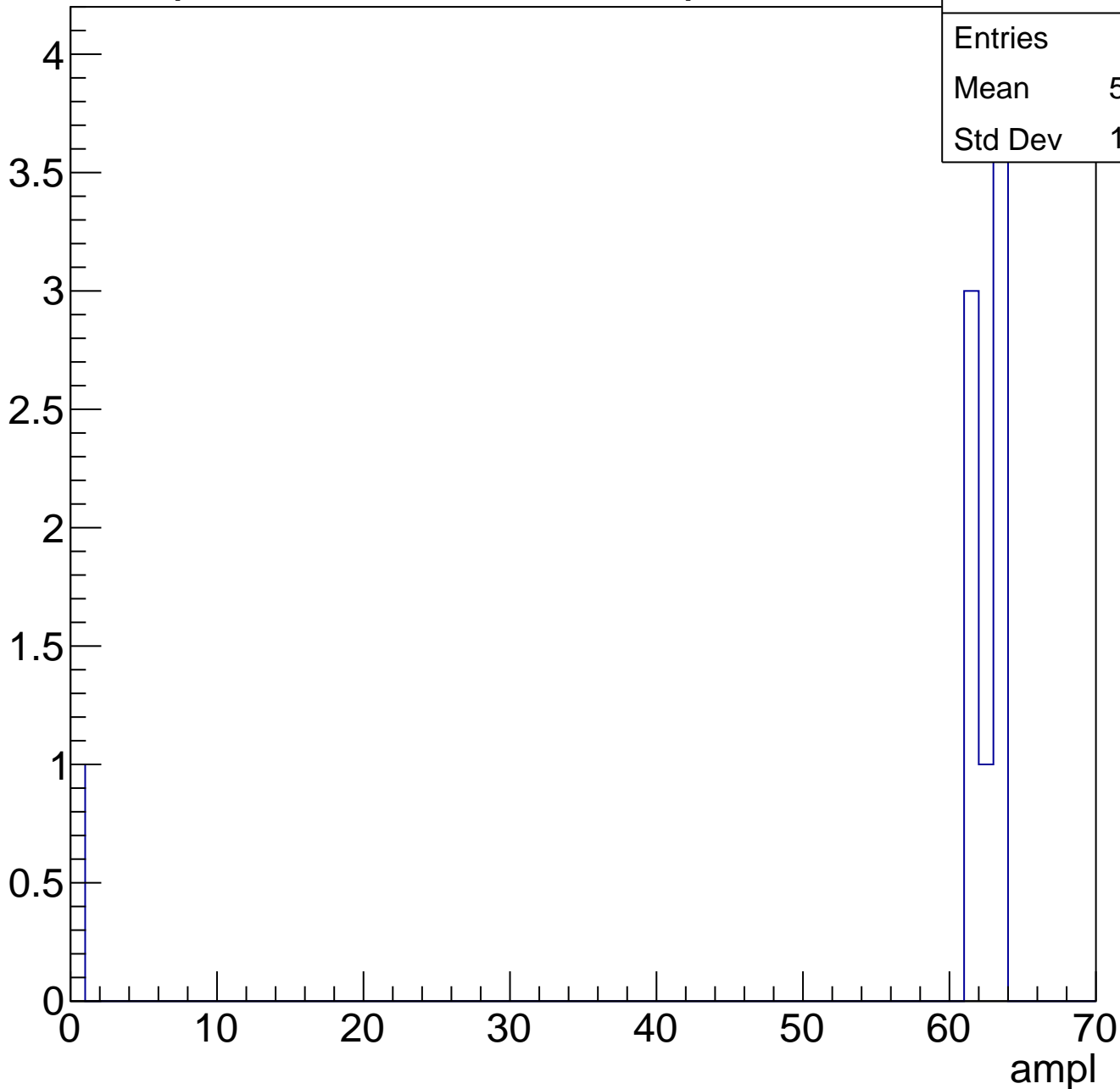
Entries	101
Mean	59.29
Std Dev	6.414



B0L101S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch26, adc0

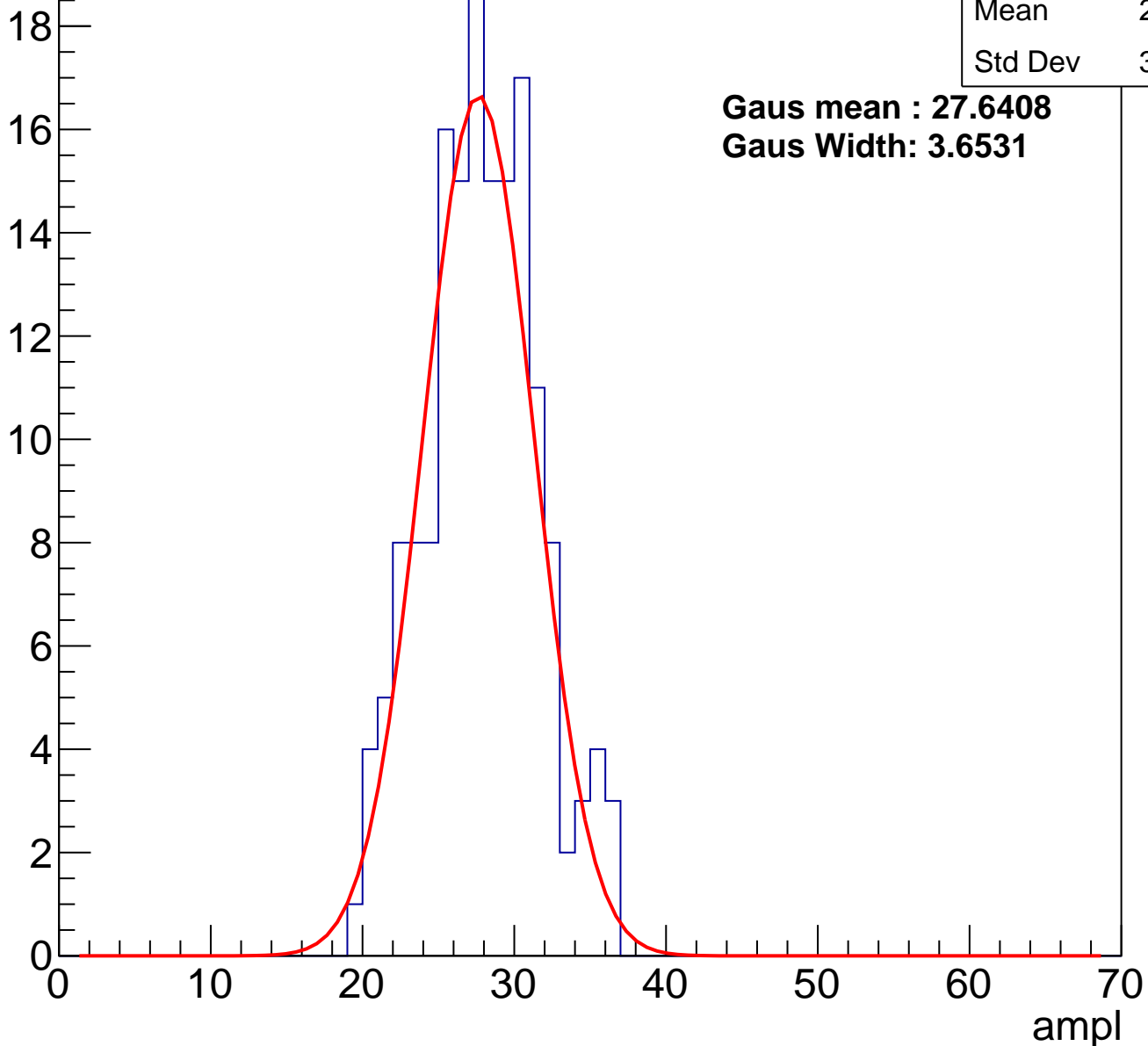
calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	27.39
Std Dev	3.699

Gaus mean : 27.6408

Gaus Width: 3.6531

Entry

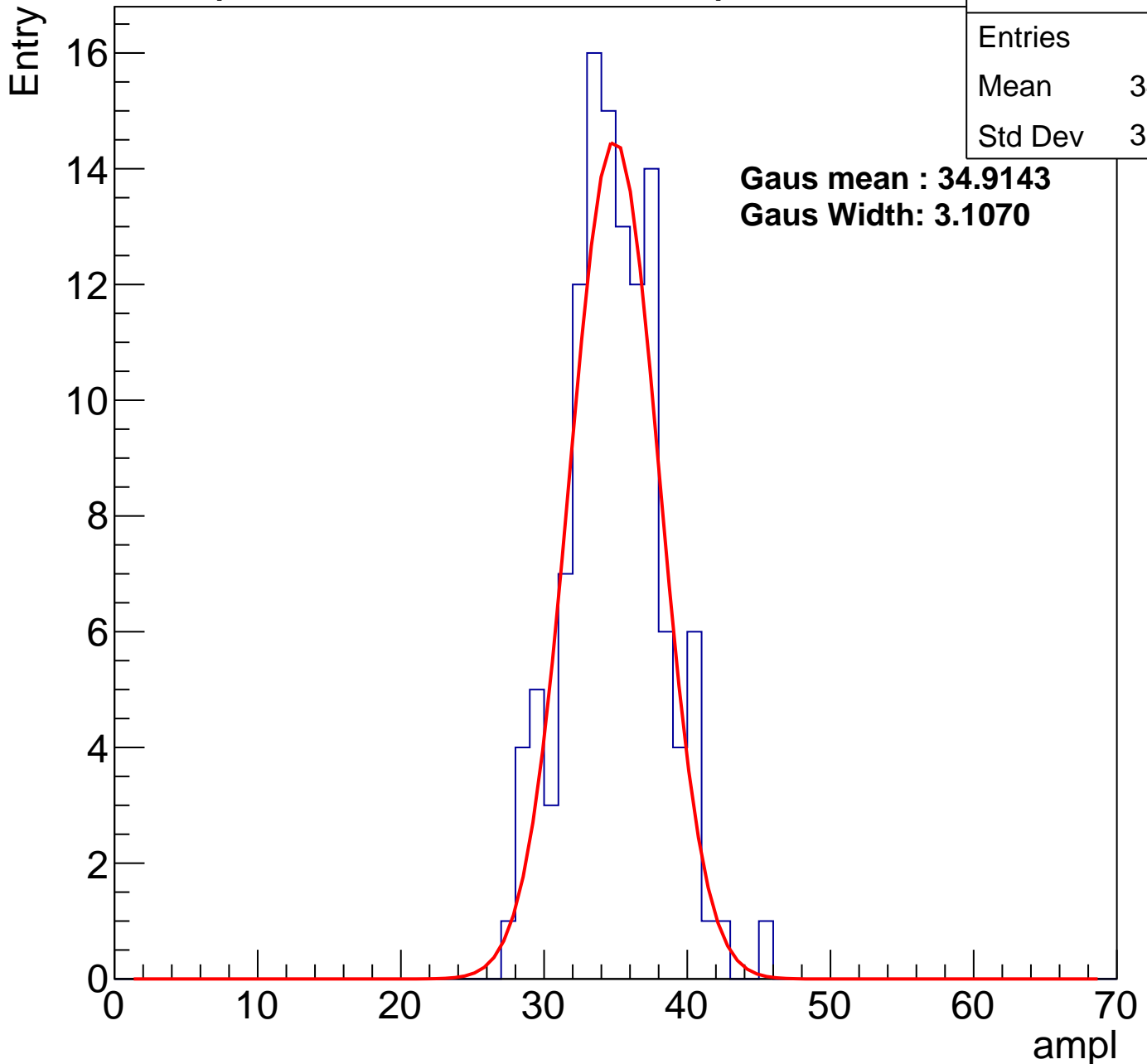


B0L101S, U15-ch26, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	34.46
Std Dev	3.278

Gaus mean : 34.9143
Gaus Width: 3.1070



B0L101S, U15-ch26, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

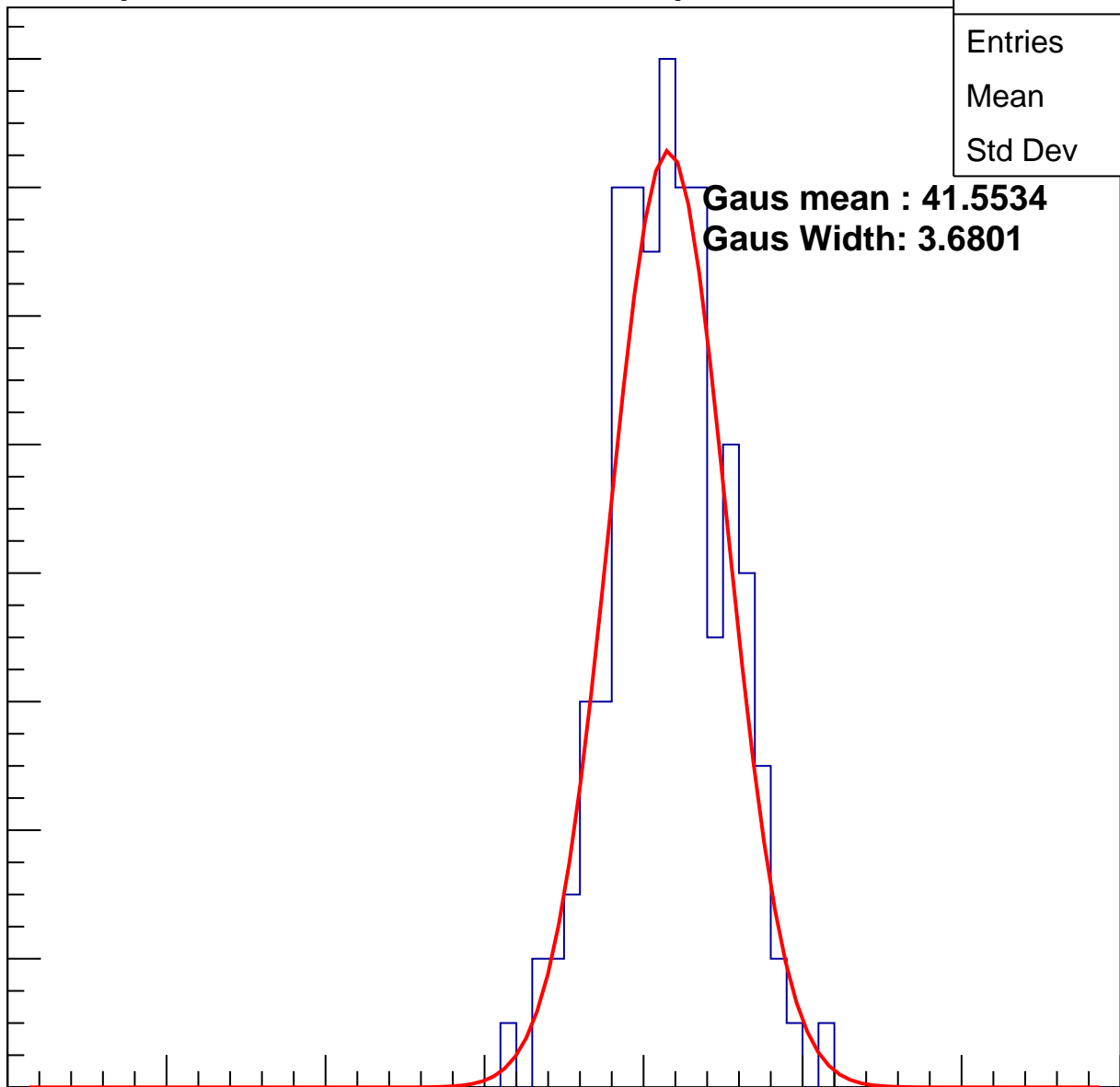
Entries	139
Mean	41.07
Std Dev	3.608

Gaus mean : 41.5534

Gaus Width: 3.6801

0 10 20 30 40 50 60 70

ampl

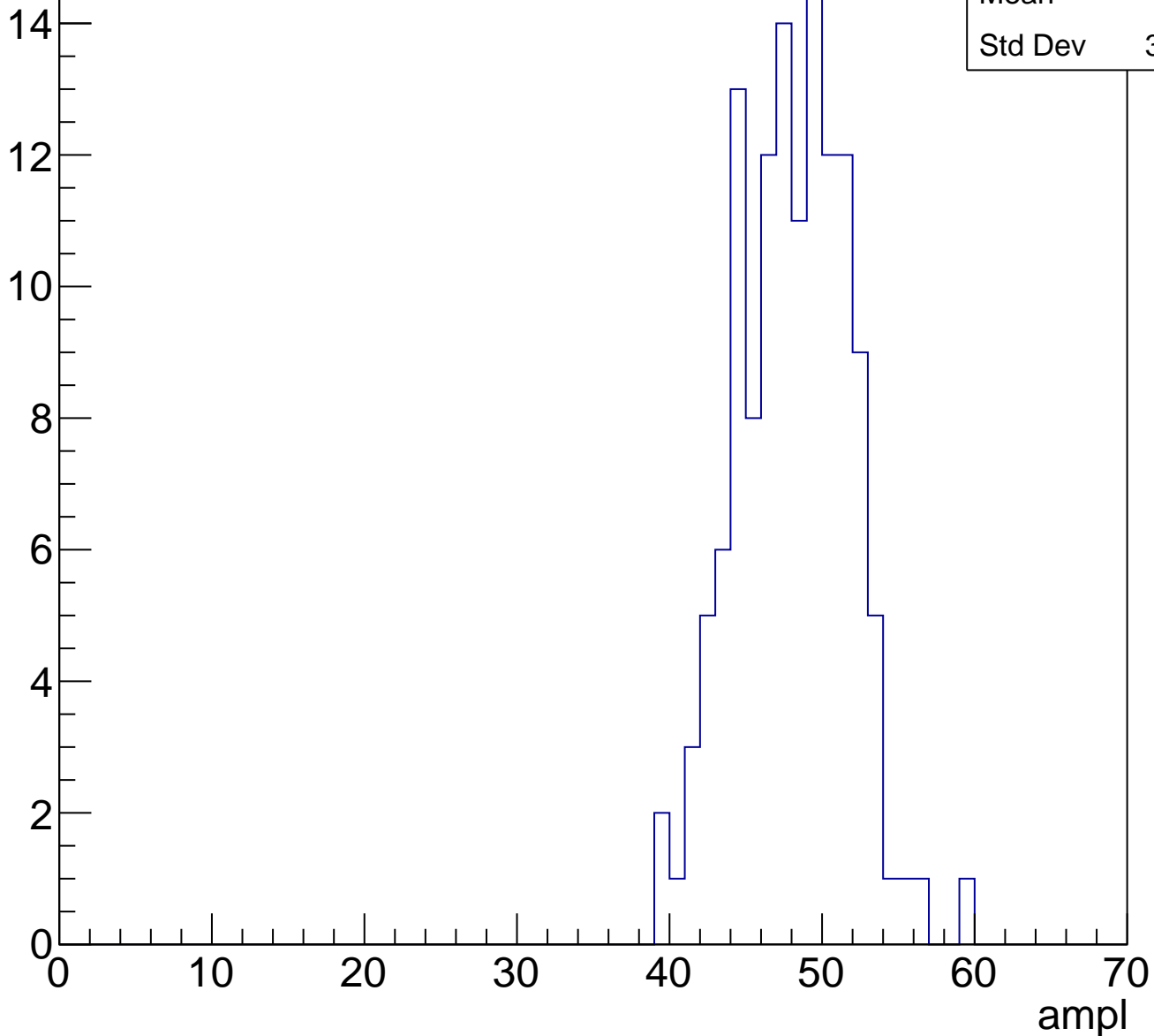


B0L101S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	47.6
Std Dev	3.624

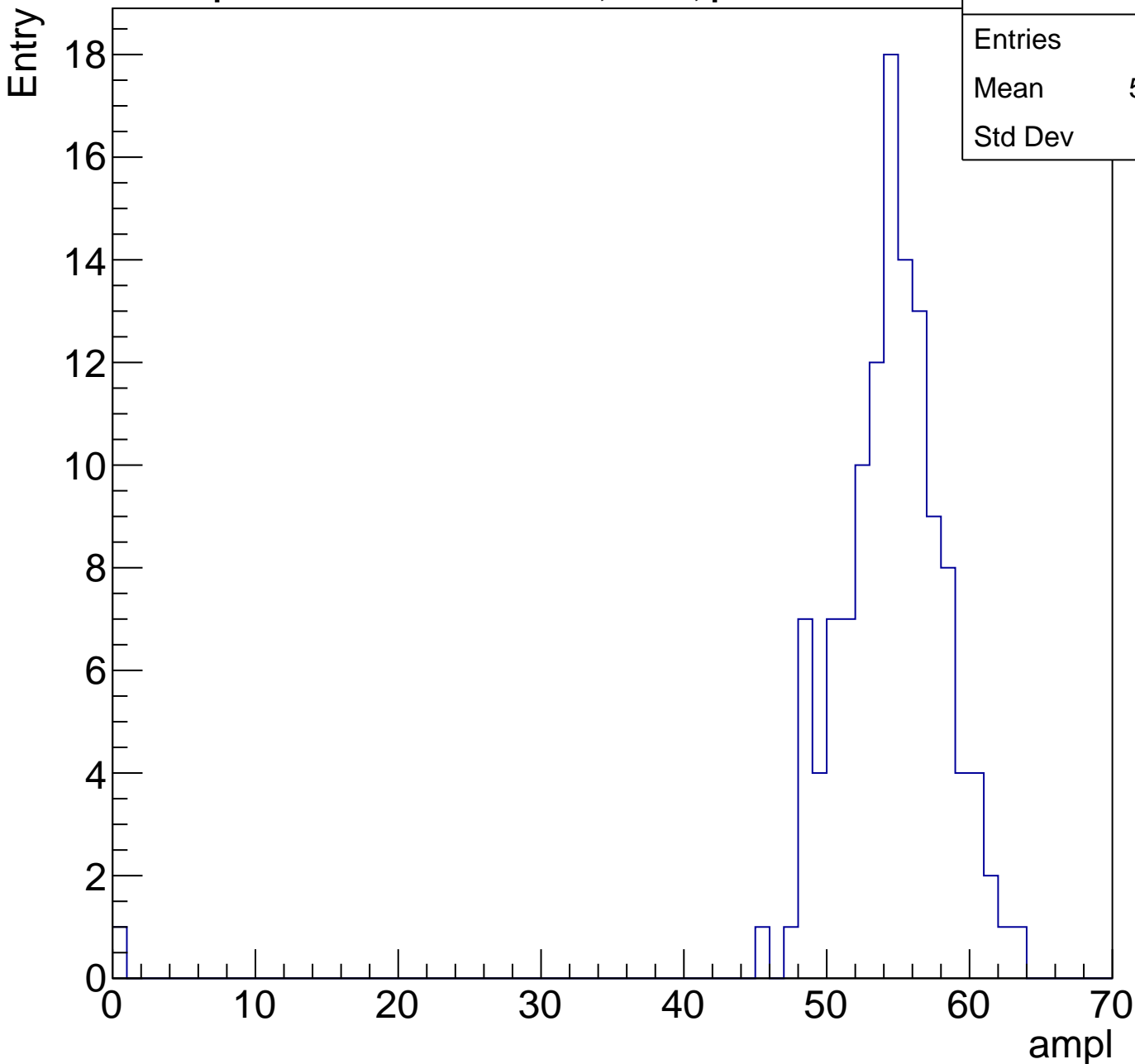
Entry



B0L101S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	53.69
Std Dev	5.94



B0L101S, U15-ch26, adc5

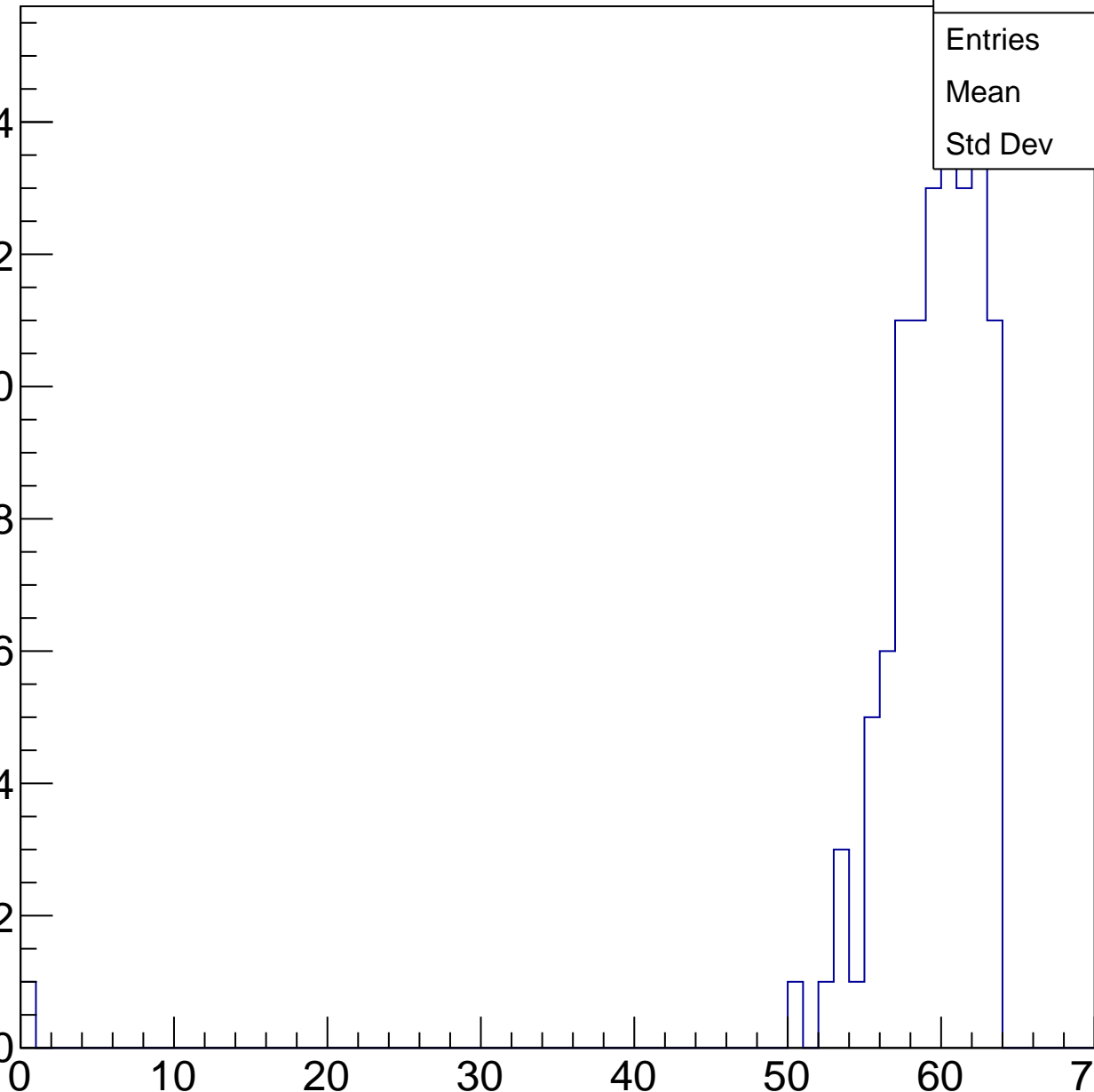
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	58.62
Std Dev	6.365

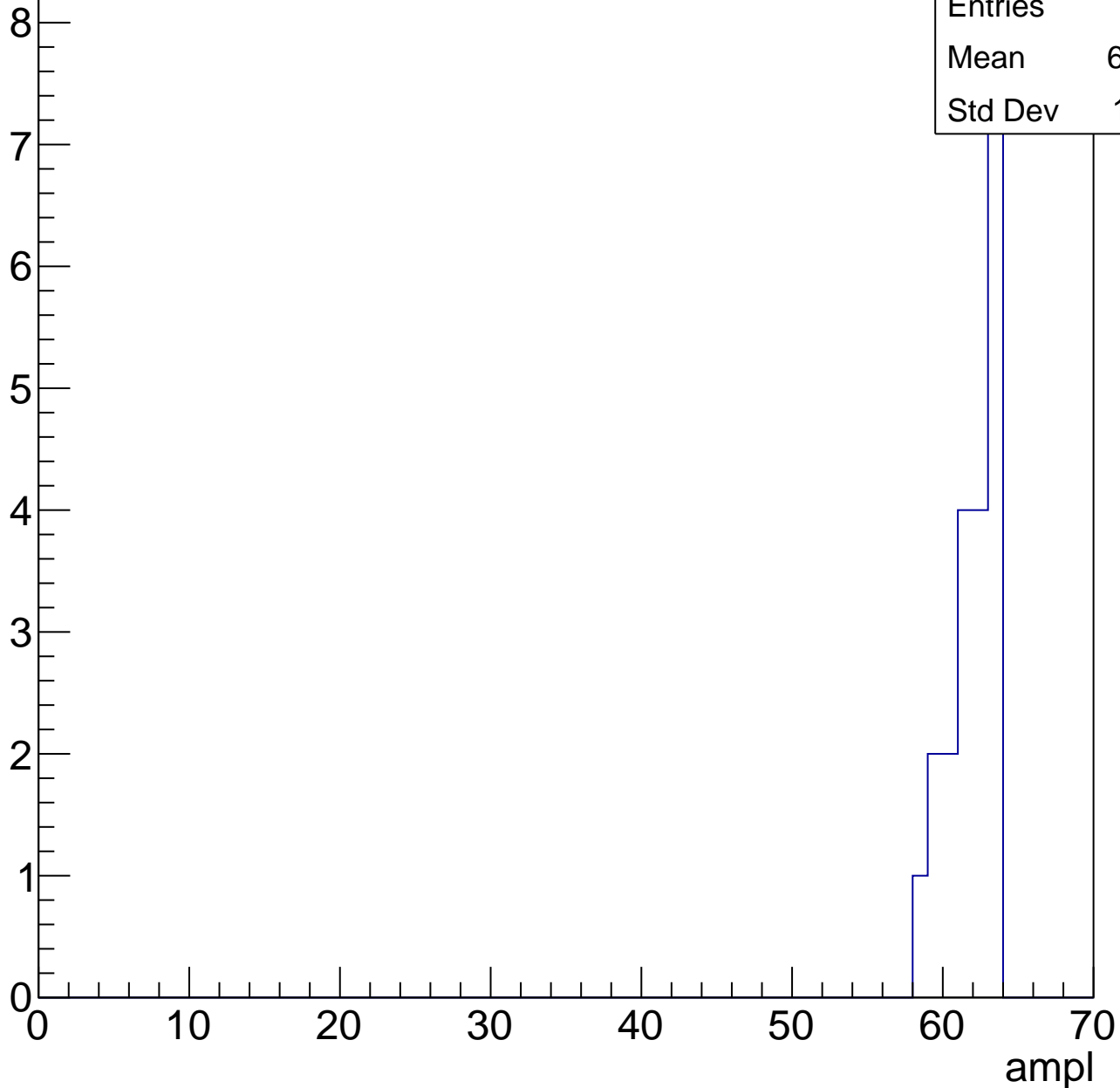
ampl



B0L101S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#1, port C1

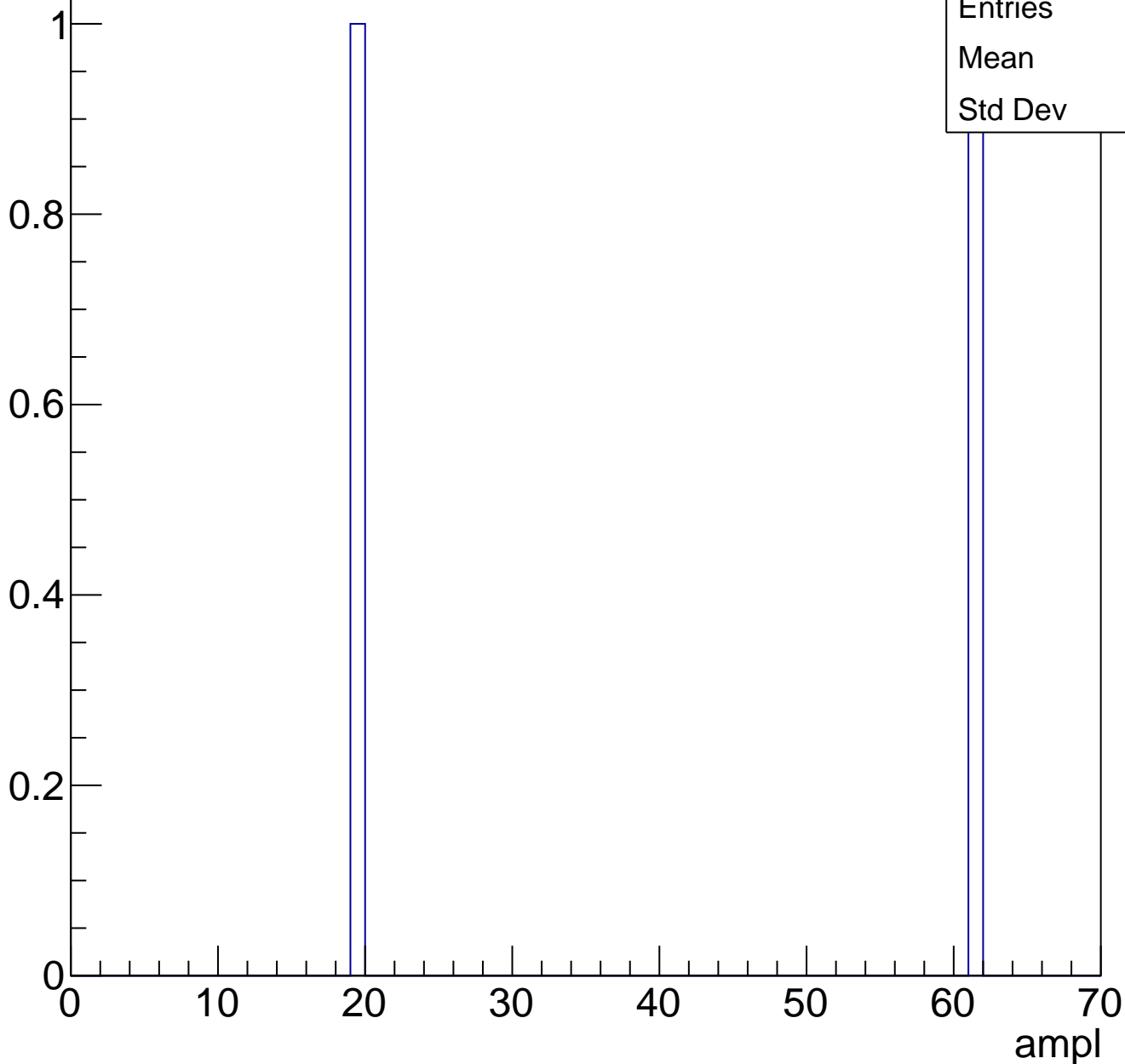
Entry



B0L101S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch27, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	182
Mean	28.42
Std Dev	4.202

Gaus mean : 28.9477

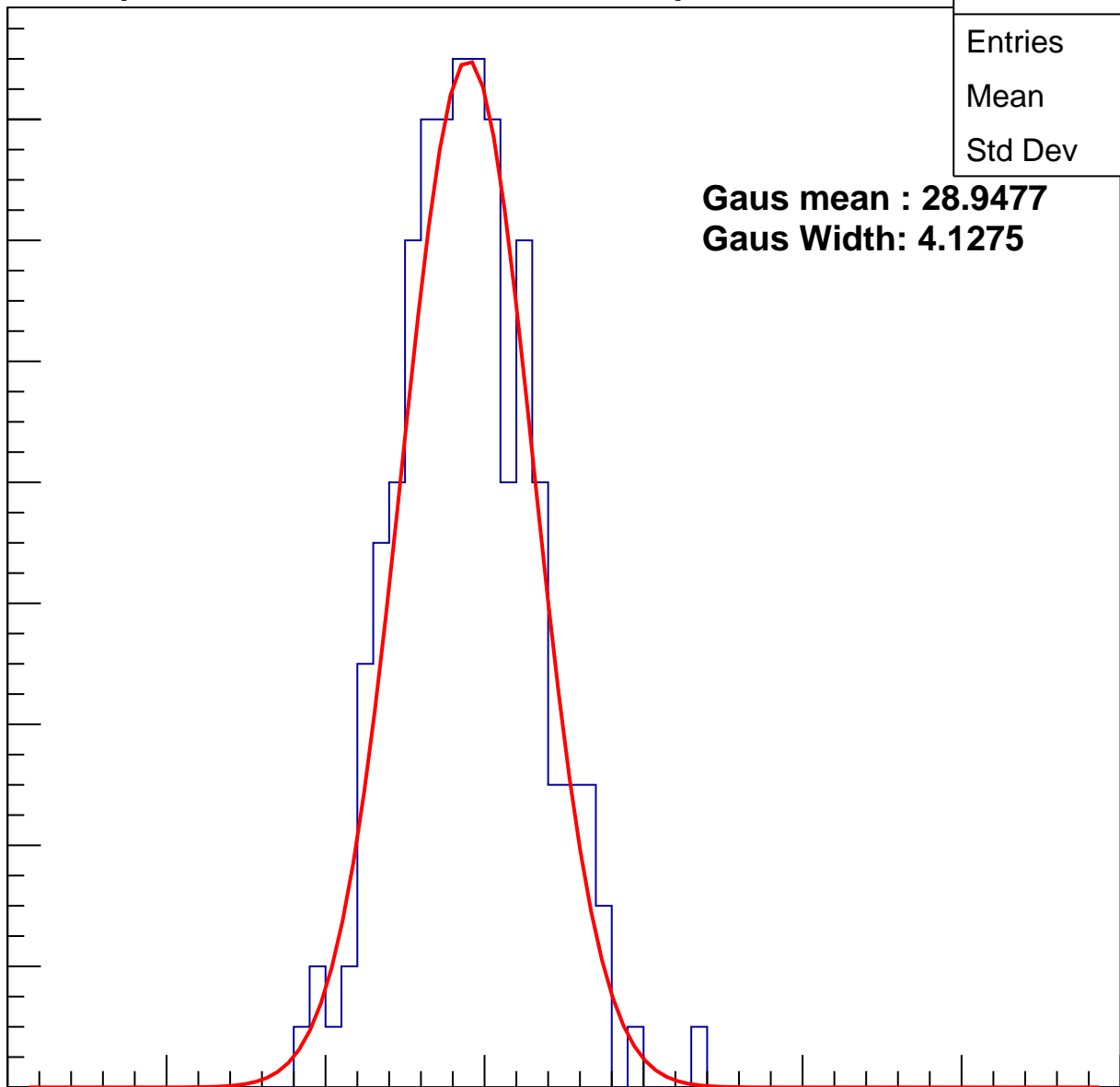
Gaus Width: 4.1275

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

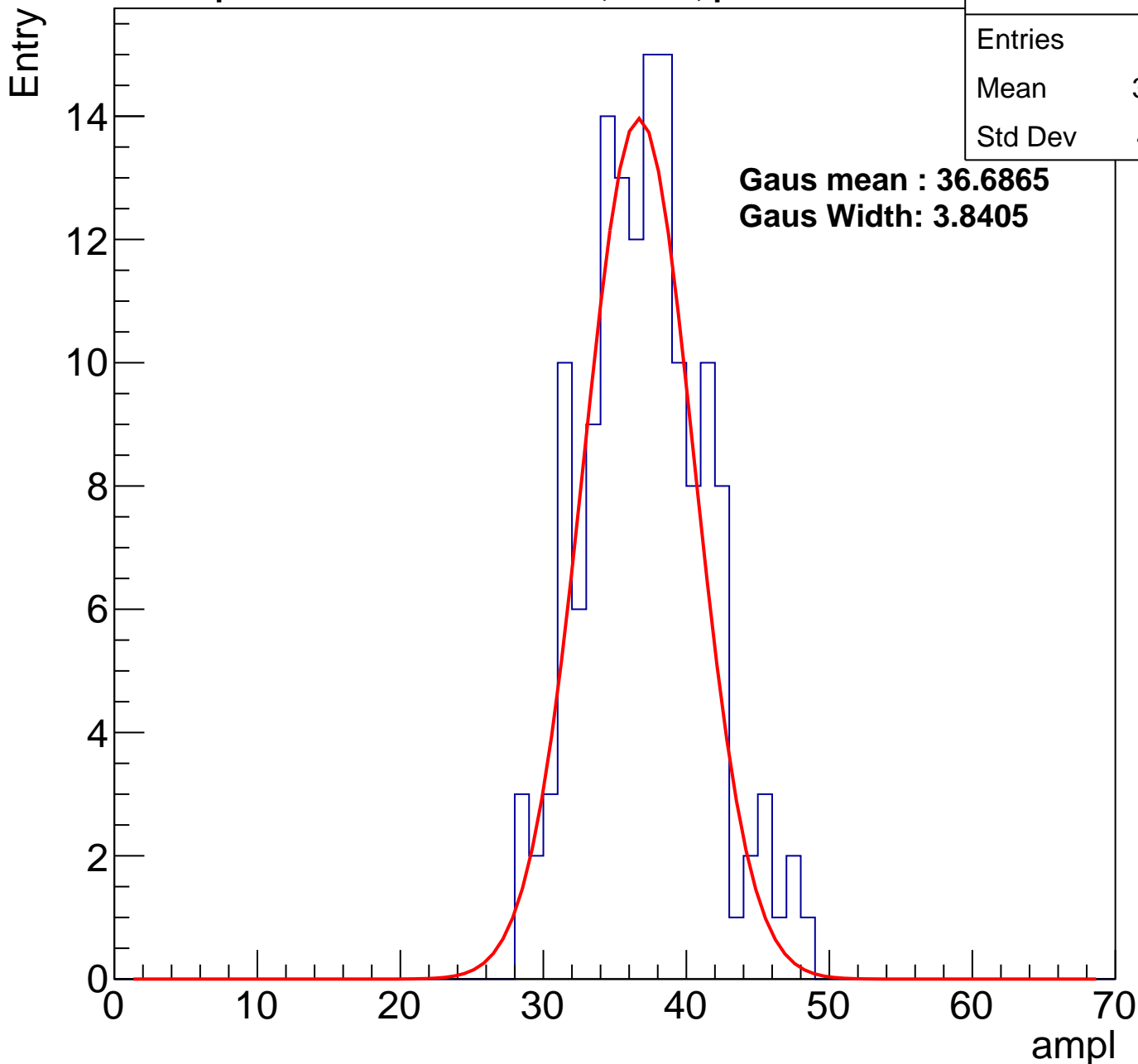


B0L101S, U15-ch27, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.68
Std Dev	4.161

Gaus mean : 36.6865
Gaus Width: 3.8405



B0L101S, U15-ch27, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	88
Mean	41.99
Std Dev	3.423

Gaus mean : 42.6593

Gaus Width: 3.6407

Entry

12

10

8

6

4

2

0

0

10

20

30

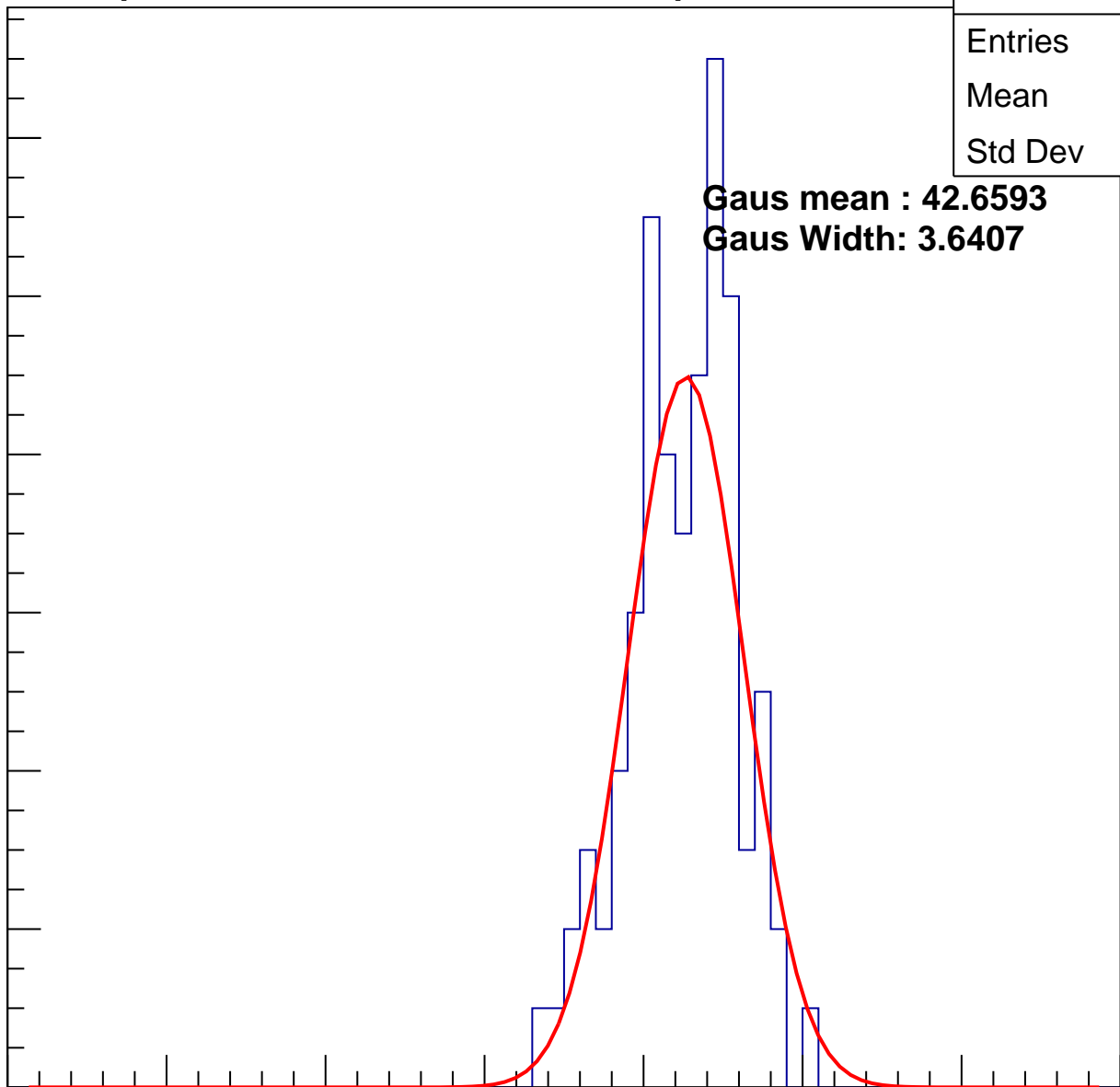
40

50

60

70

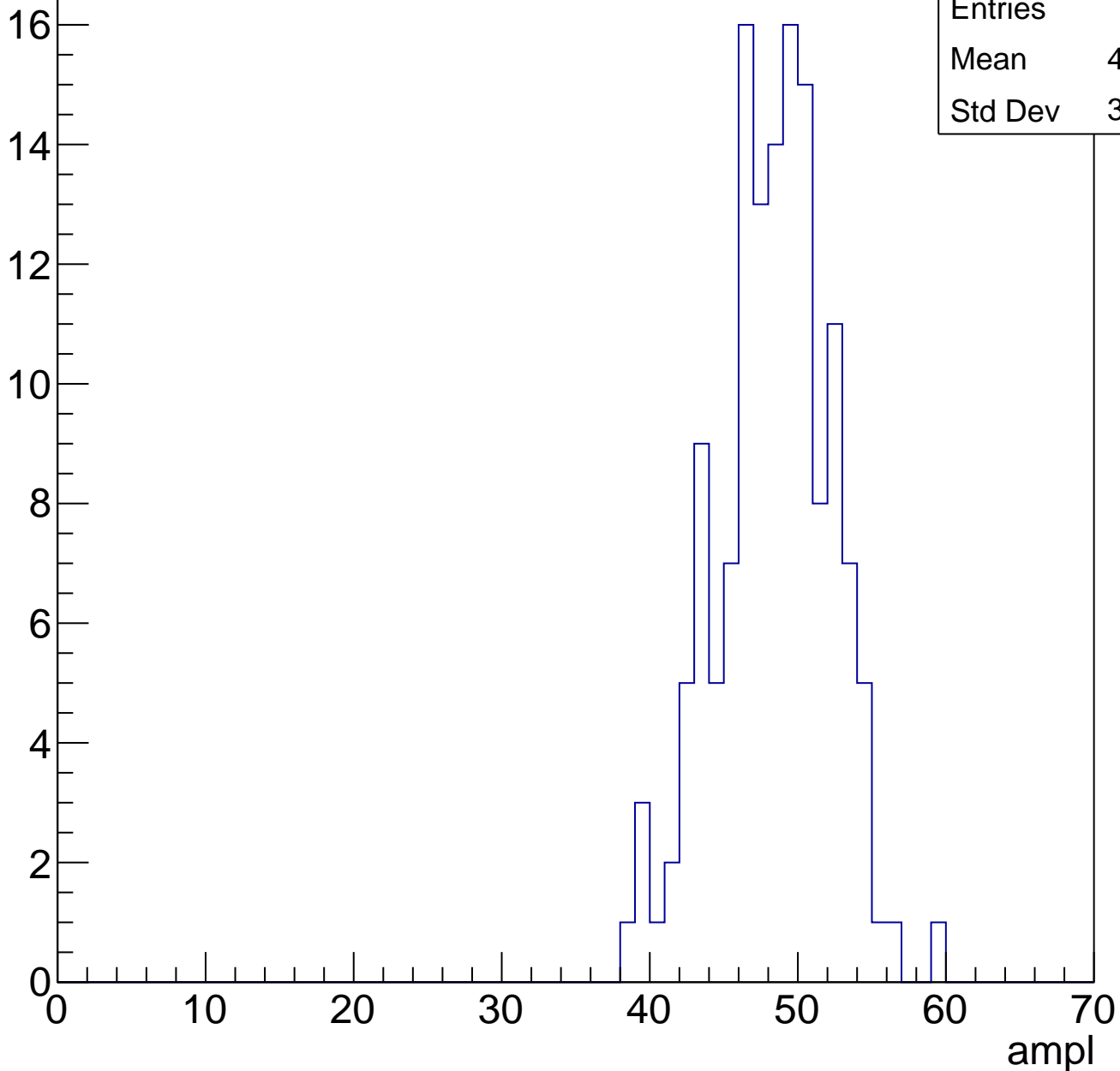
ampl



B0L101S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#1, port C1

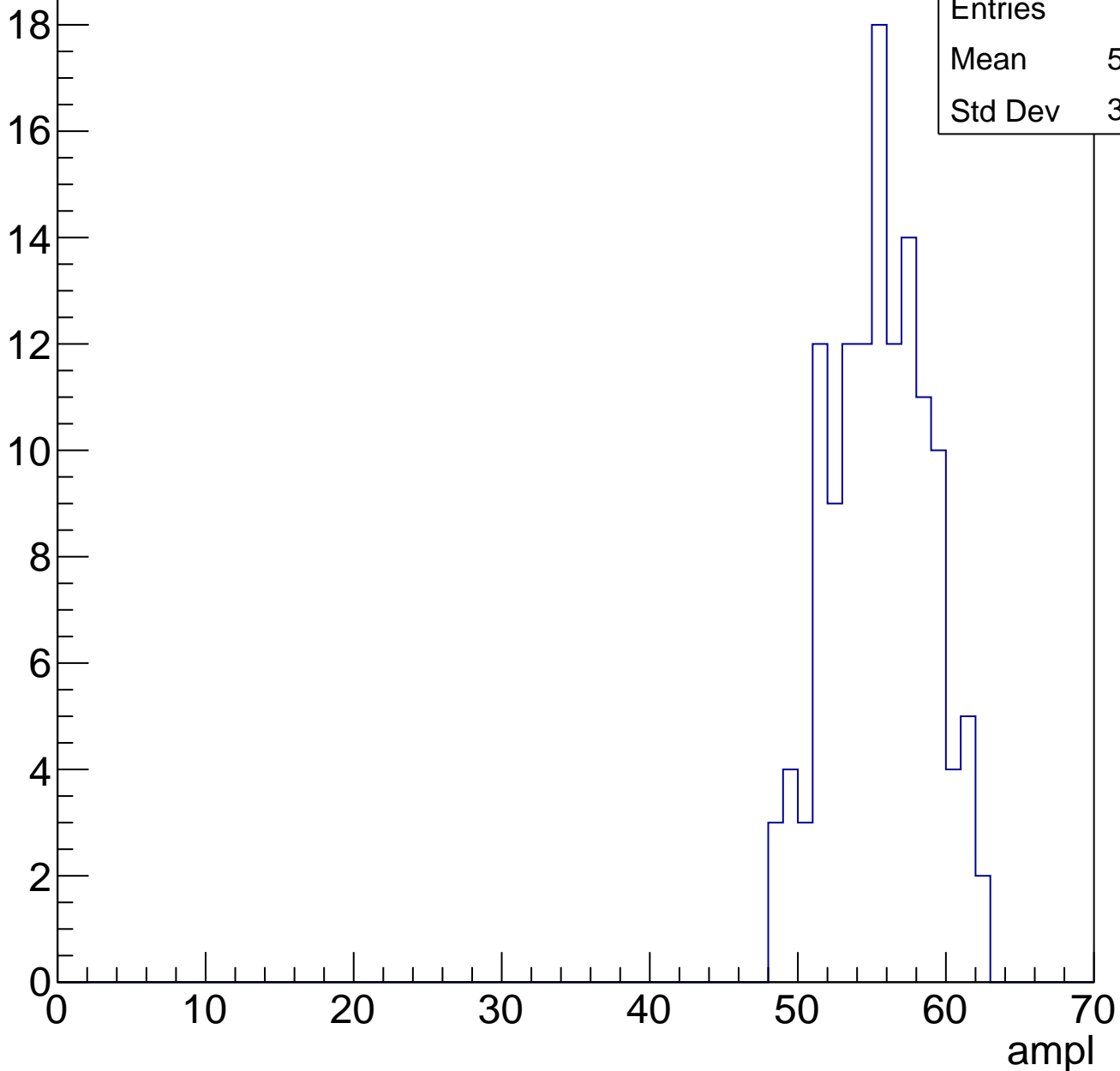
Entry



B0L101S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch27, adc5

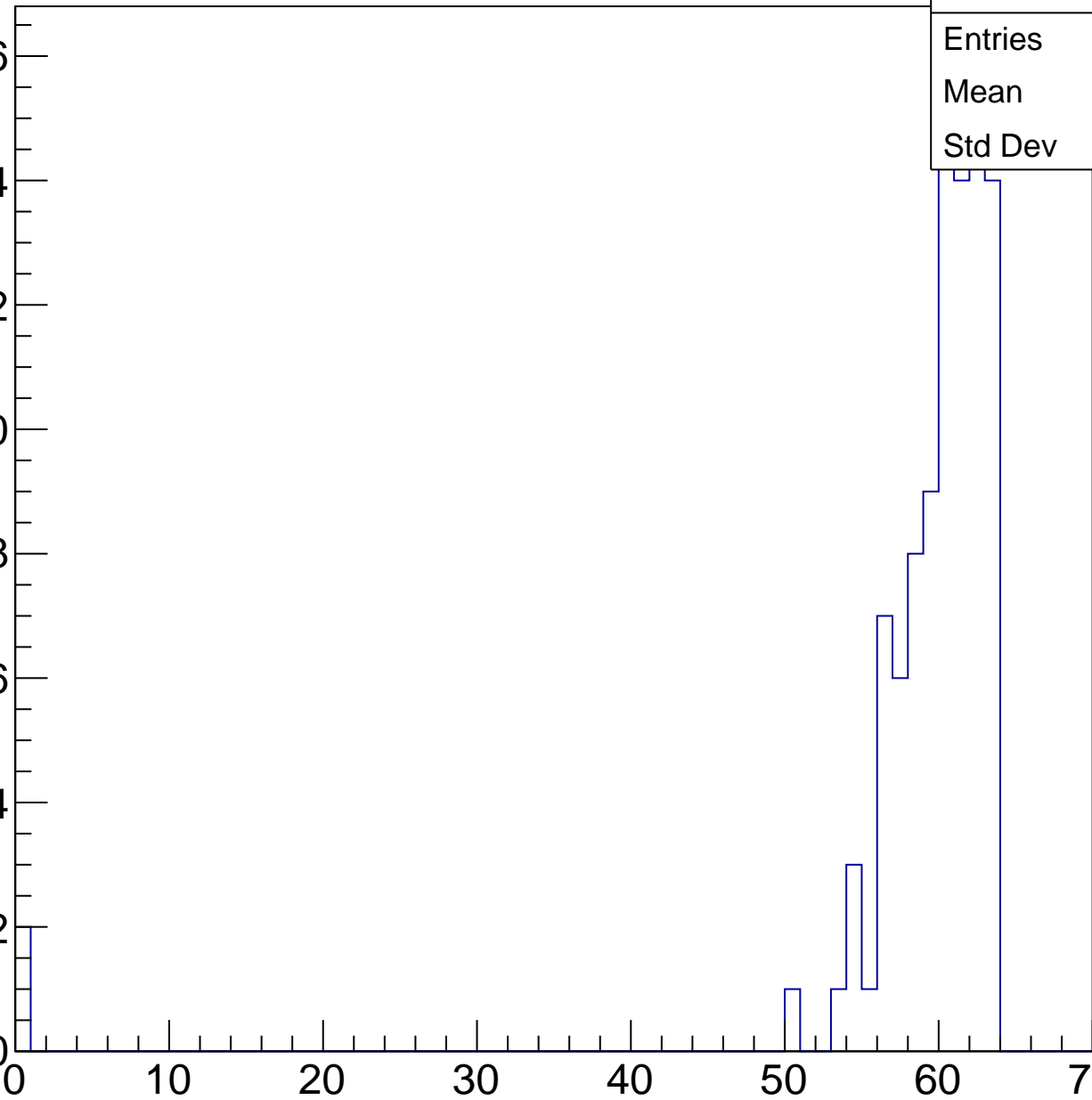
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.53
Std Dev	8.899

ampl

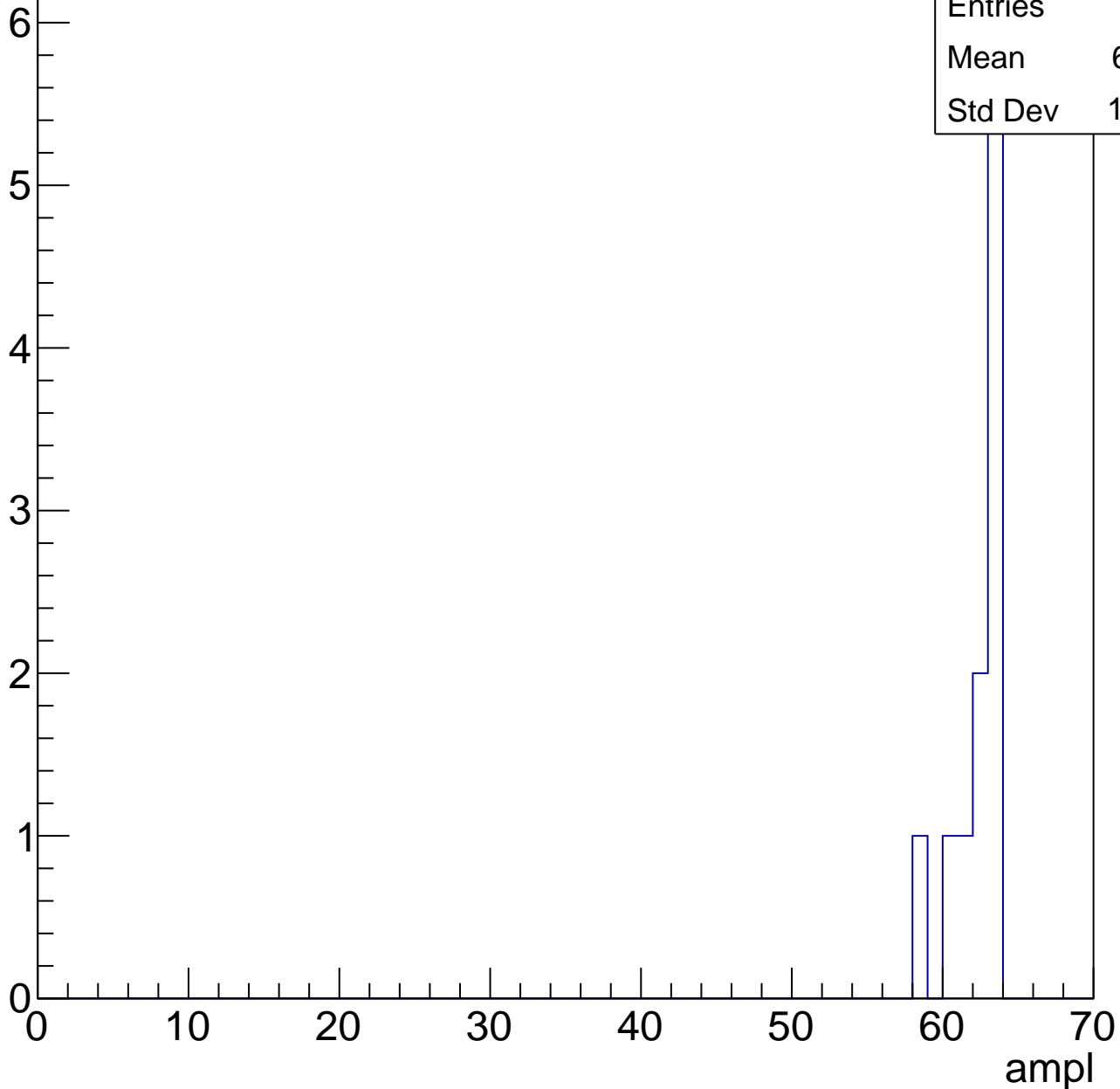


B0L101S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

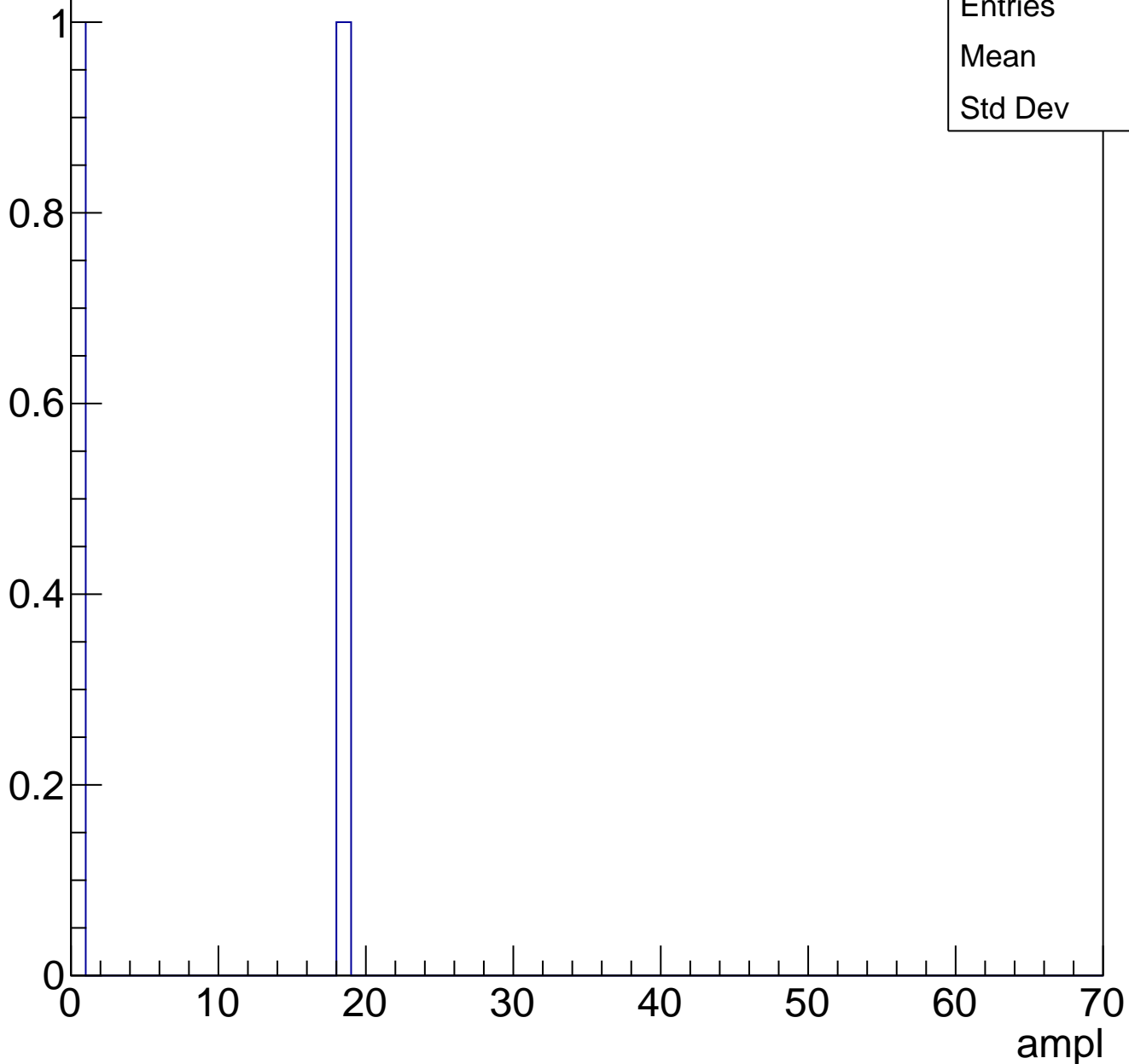
Entries	11
Mean	61.91
Std Dev	1.564



B0L101S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch28, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	27.53
Std Dev	4.342

Gaus mean : 28.1655

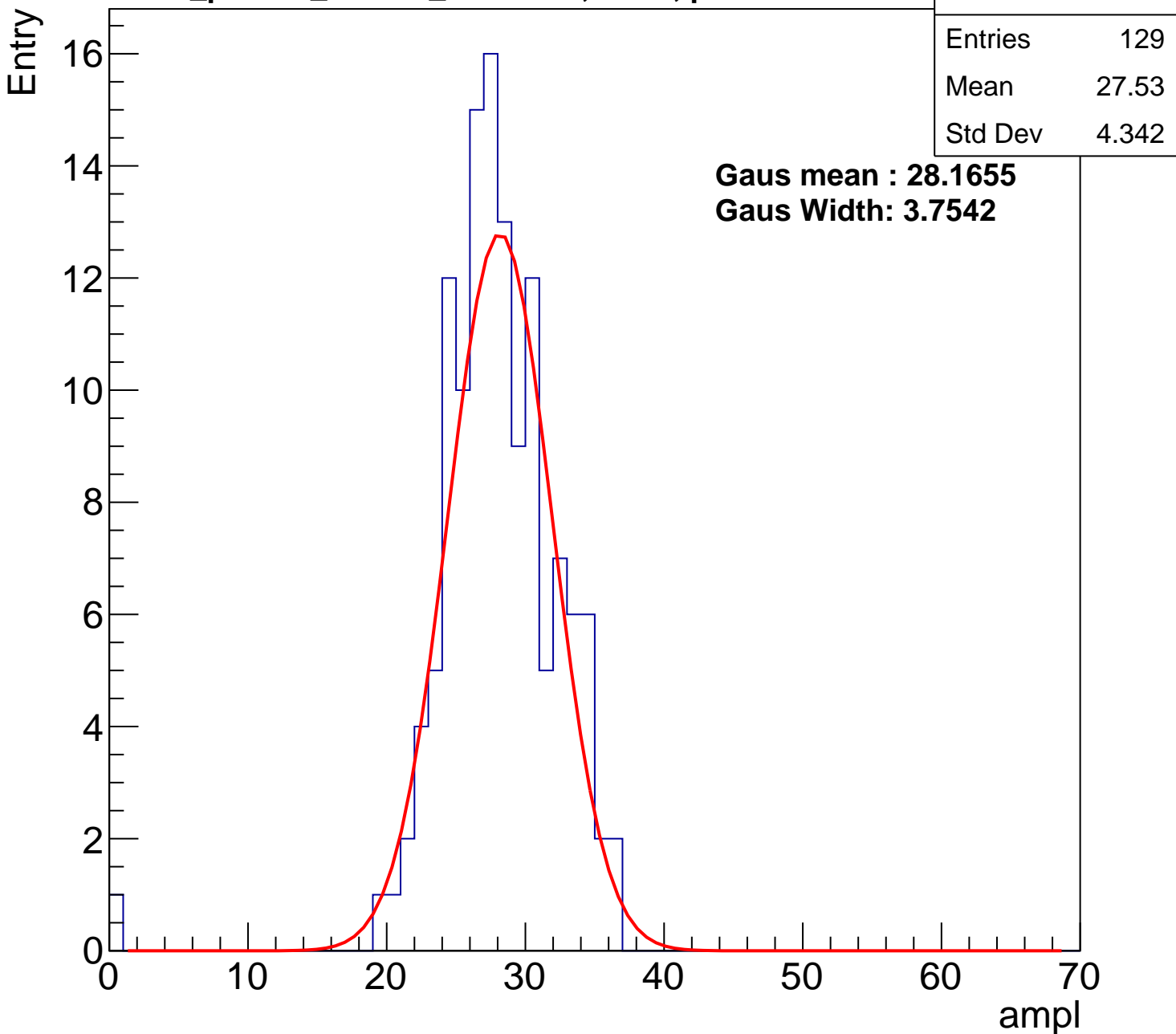
Gaus Width: 3.7542

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch28, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

144

Mean

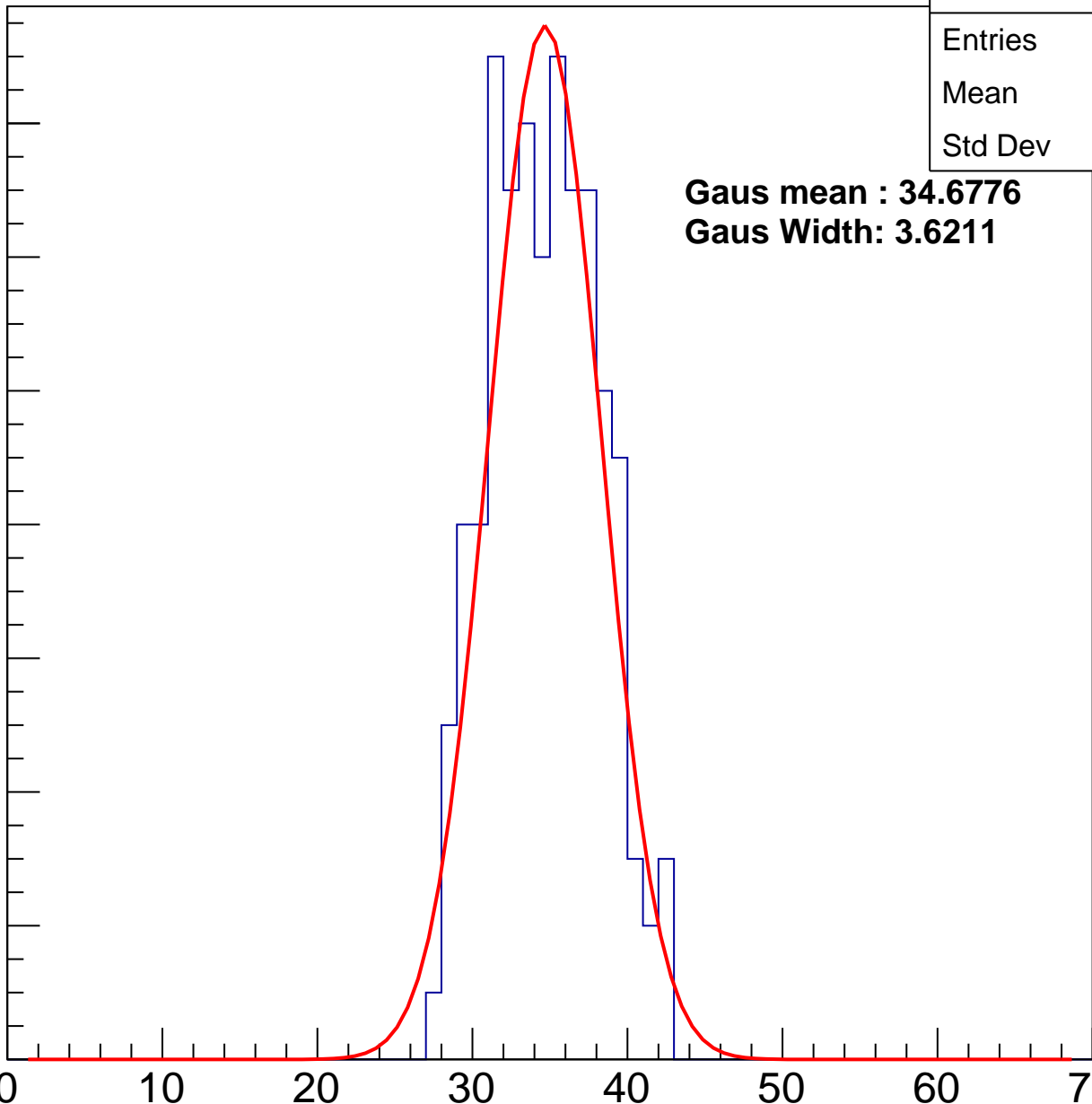
34.19

Std Dev

3.456

Gaus mean : 34.6776

Gaus Width: 3.6211



B0L101S, U15-ch28, adc2

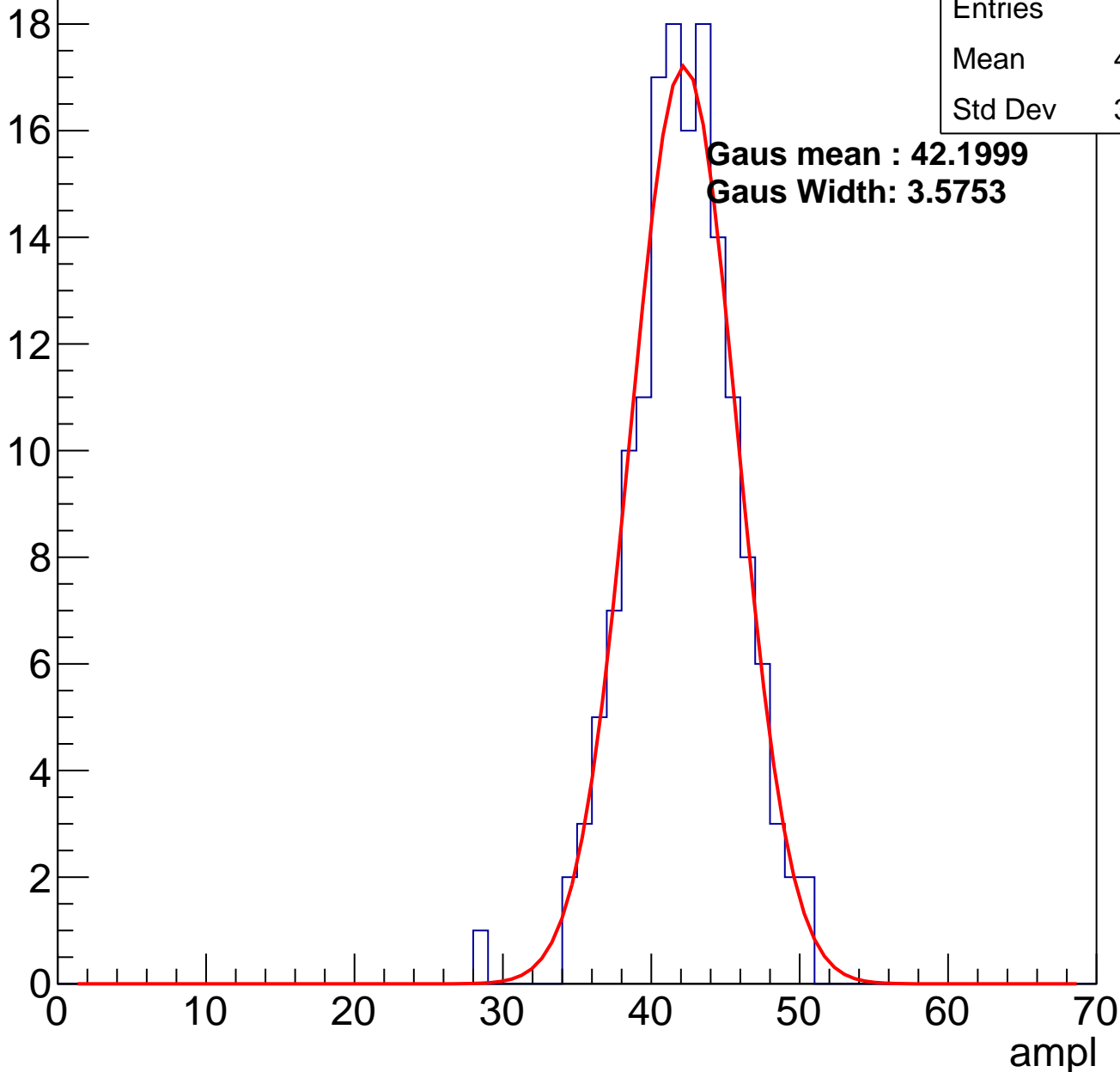
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	41.66
Std Dev	3.548

Gaus mean : 42.1999

Gaus Width: 3.5753

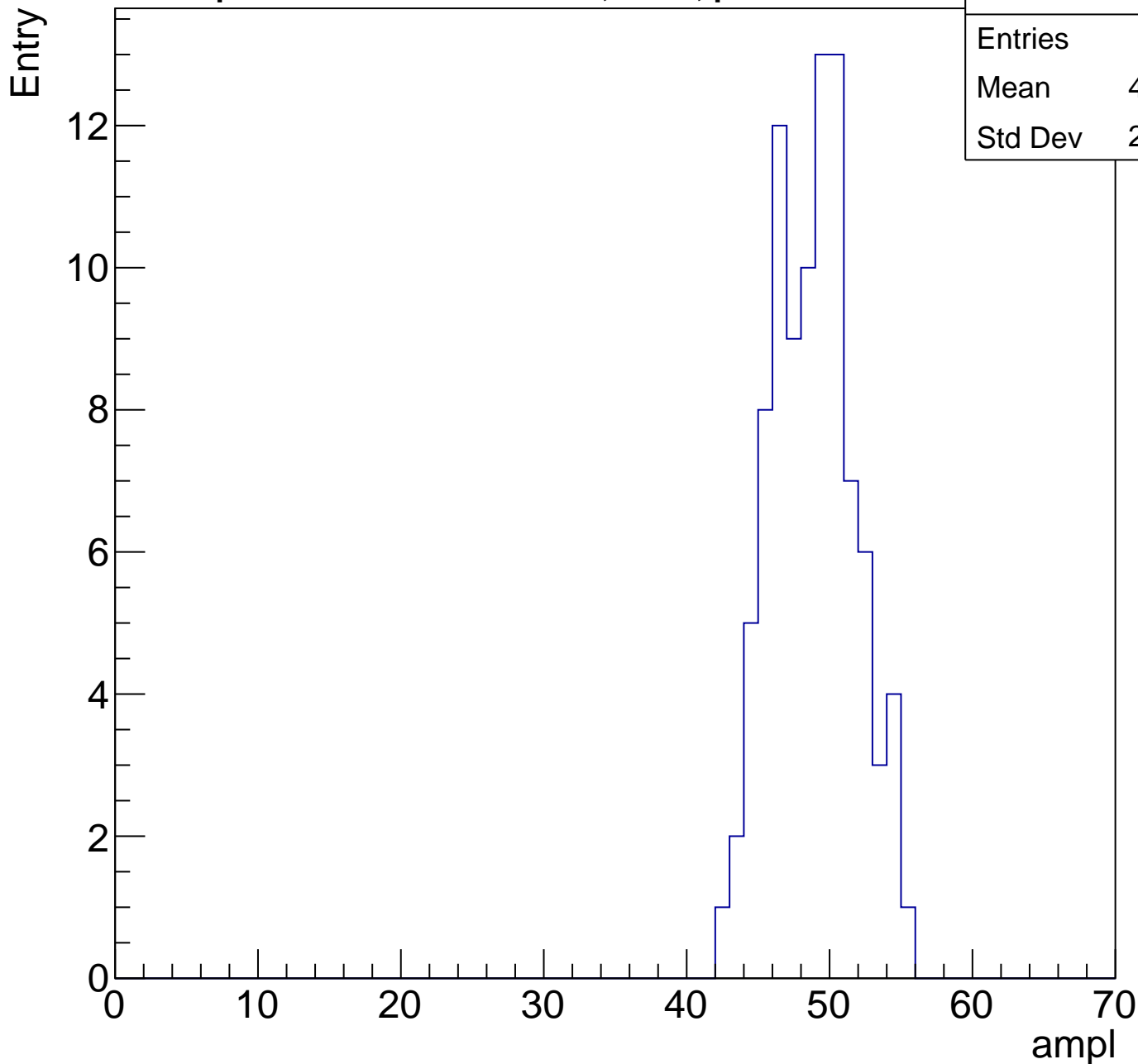
Entry



B0L101S, U15-ch28, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	48.39
Std Dev	2.867



B0L101S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#1, port C1

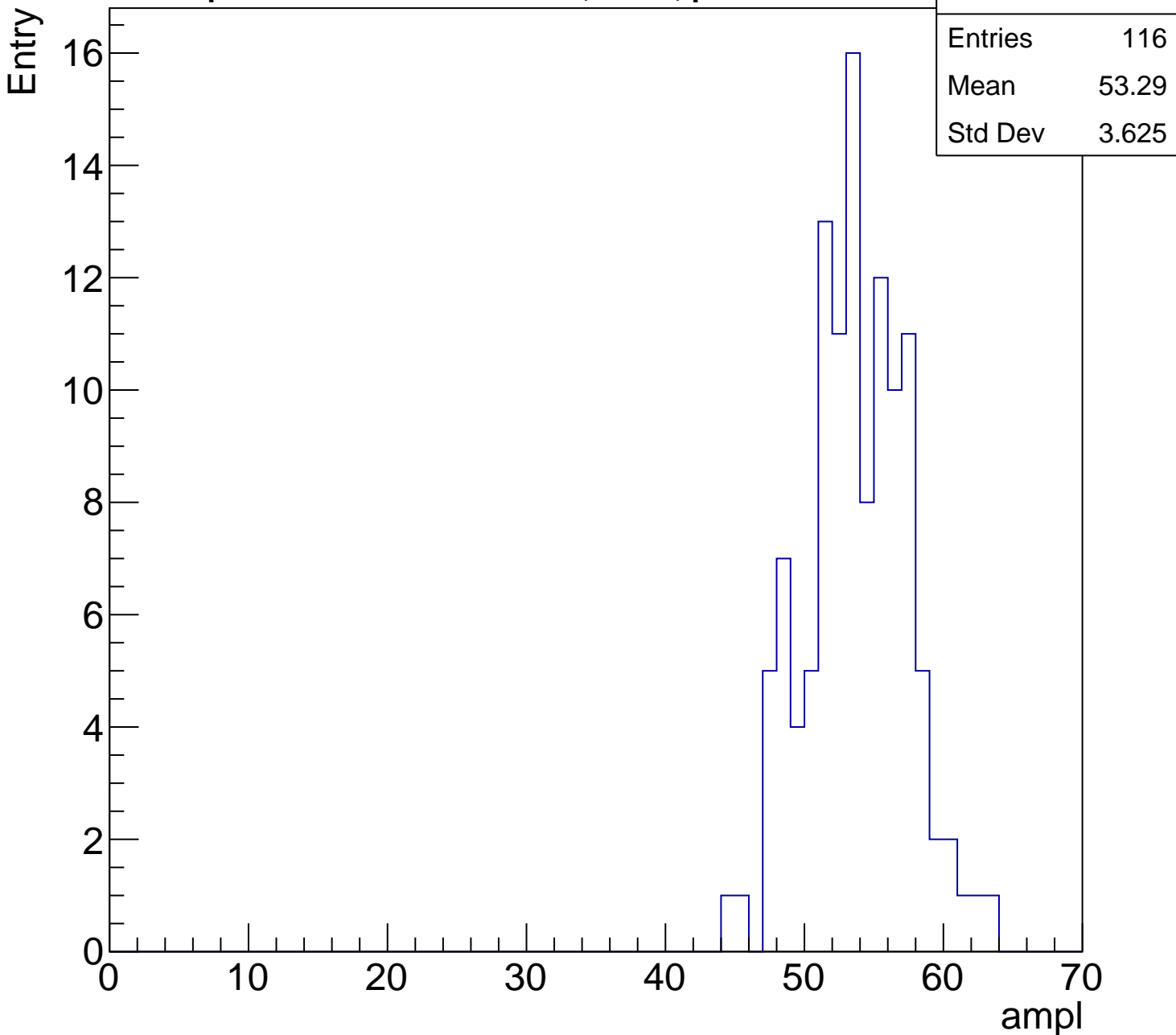
Entries	116
Mean	53.29
Std Dev	3.625

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch28, adc5

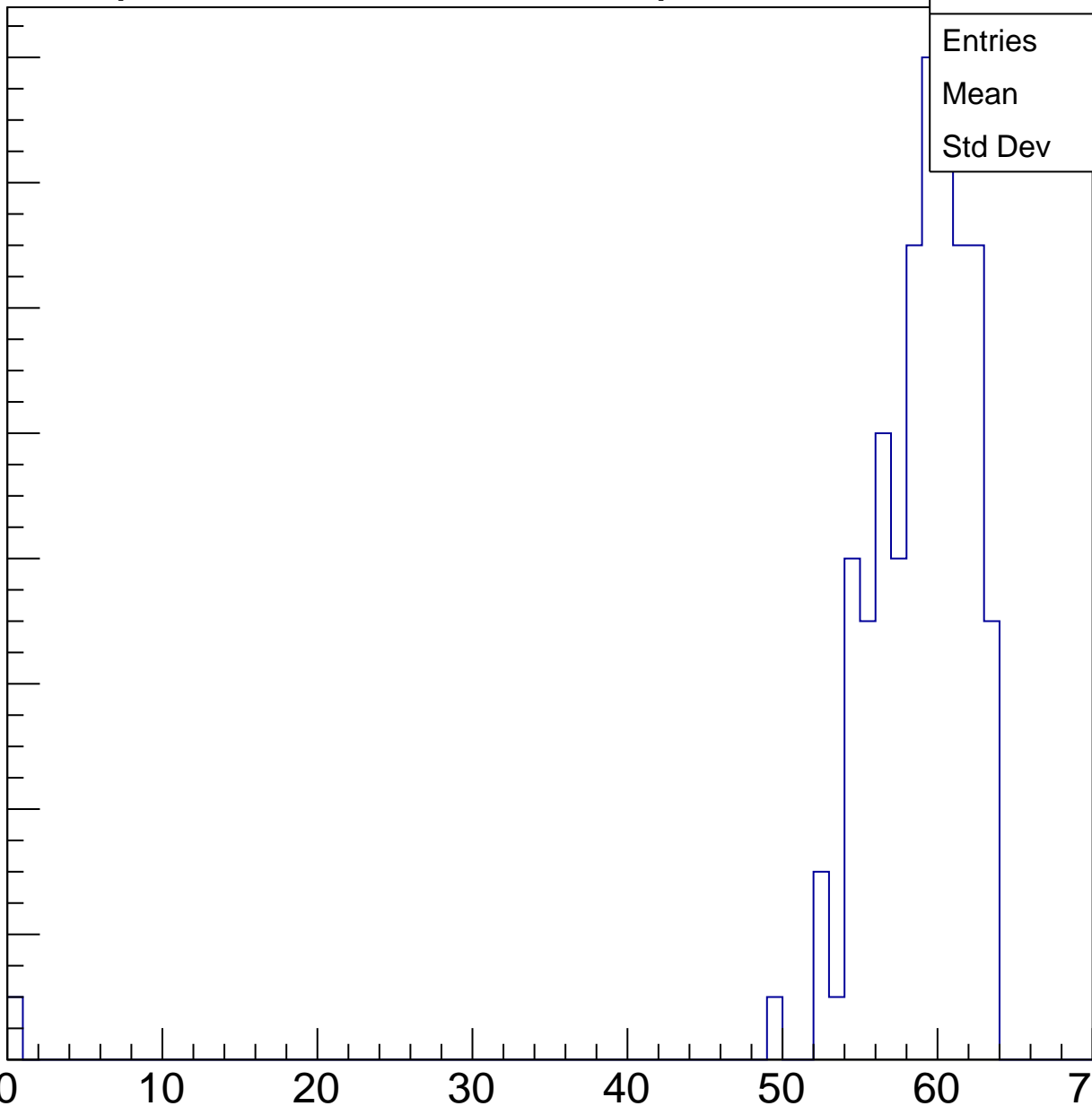
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	58.01
Std Dev	6.154

ampl



B0L101S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	32
Mean	59.44
Std Dev	10.81

Entry

10

8

6

4

2

0

0

10

20

30

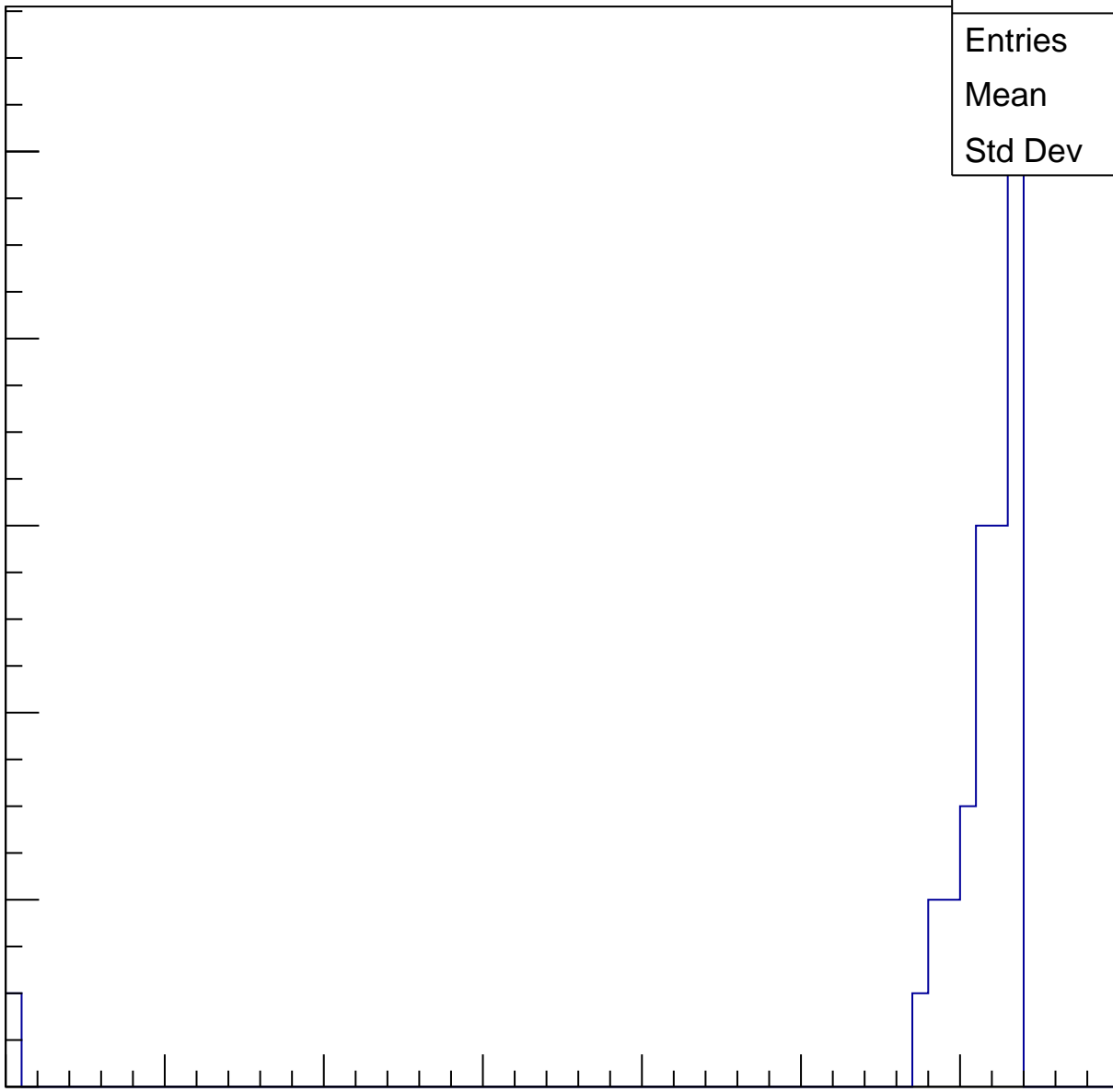
40

50

60

70

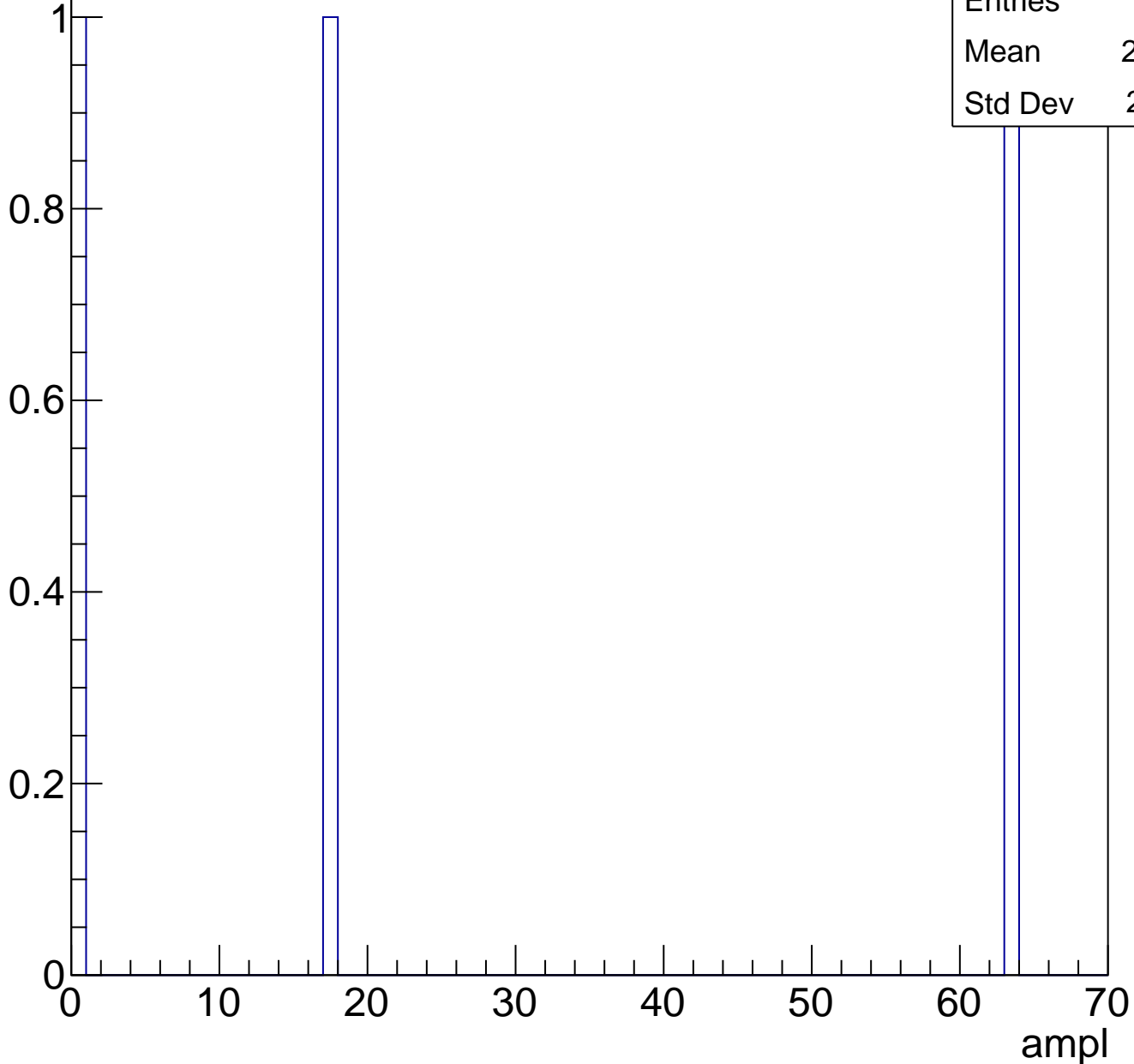
ampl



B0L101S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.67
Std Dev	26.61

B0L101S, U15-ch29, adc0

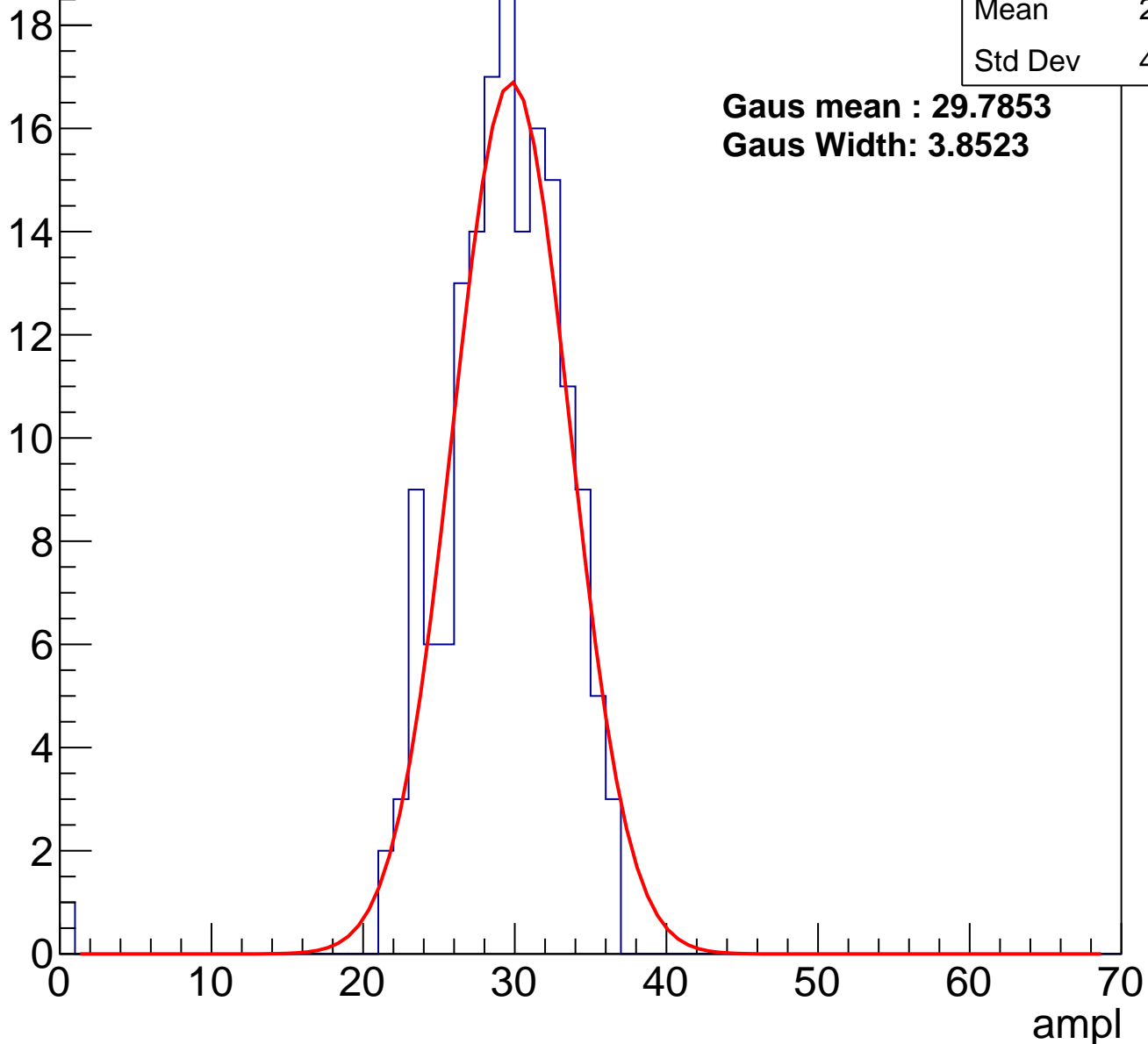
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.83
Std Dev	4.154

Gaus mean : 29.7853

Gaus Width: 3.8523

Entry



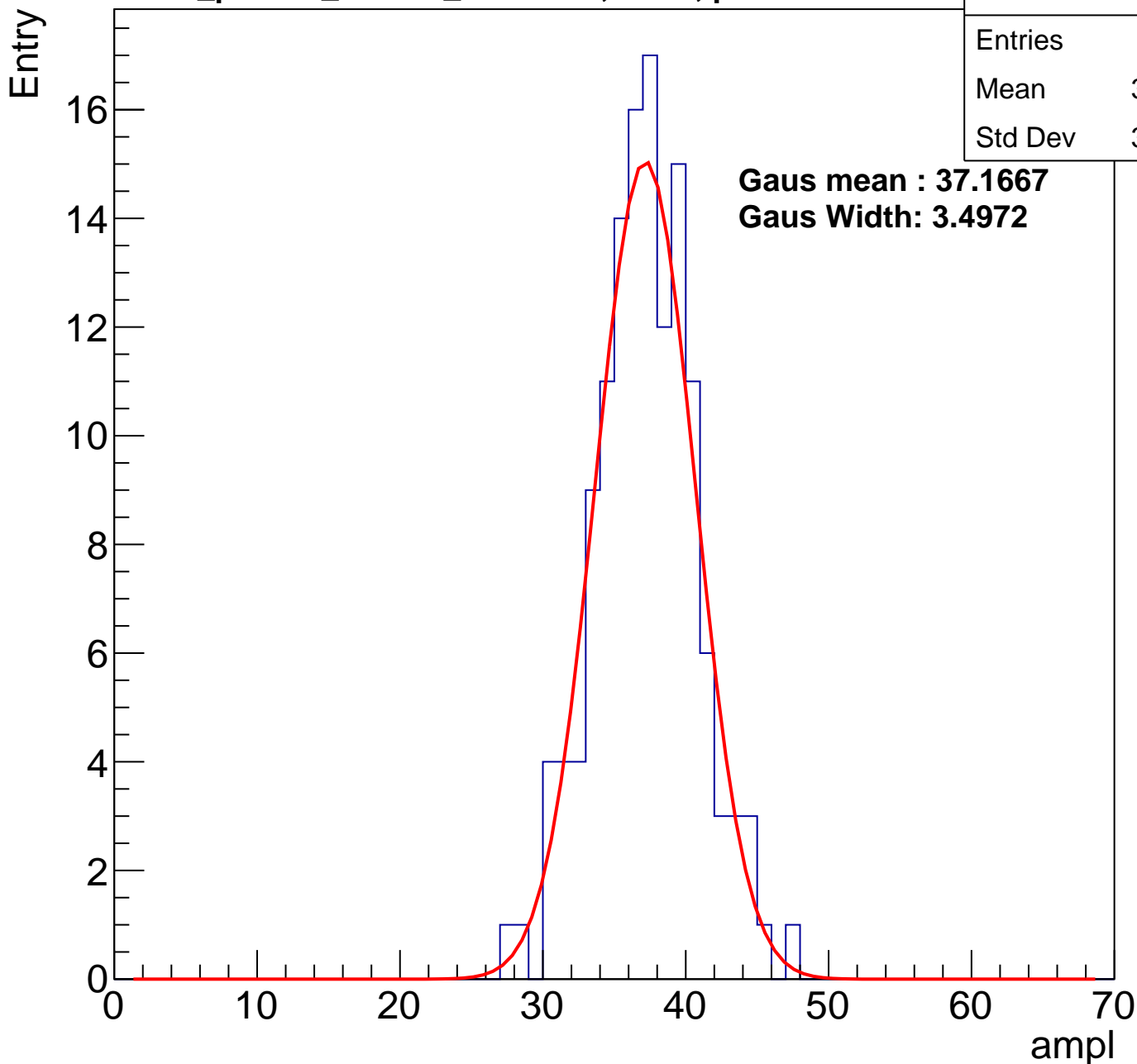
B0L101S, U15-ch29, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	36.76
Std Dev	3.524

Gaus mean : 37.1667

Gaus Width: 3.4972



B0L101S, U15-ch29, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	43.16
Std Dev	3.344

Gaus mean : 43.7093

Gaus Width: 3.3656

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

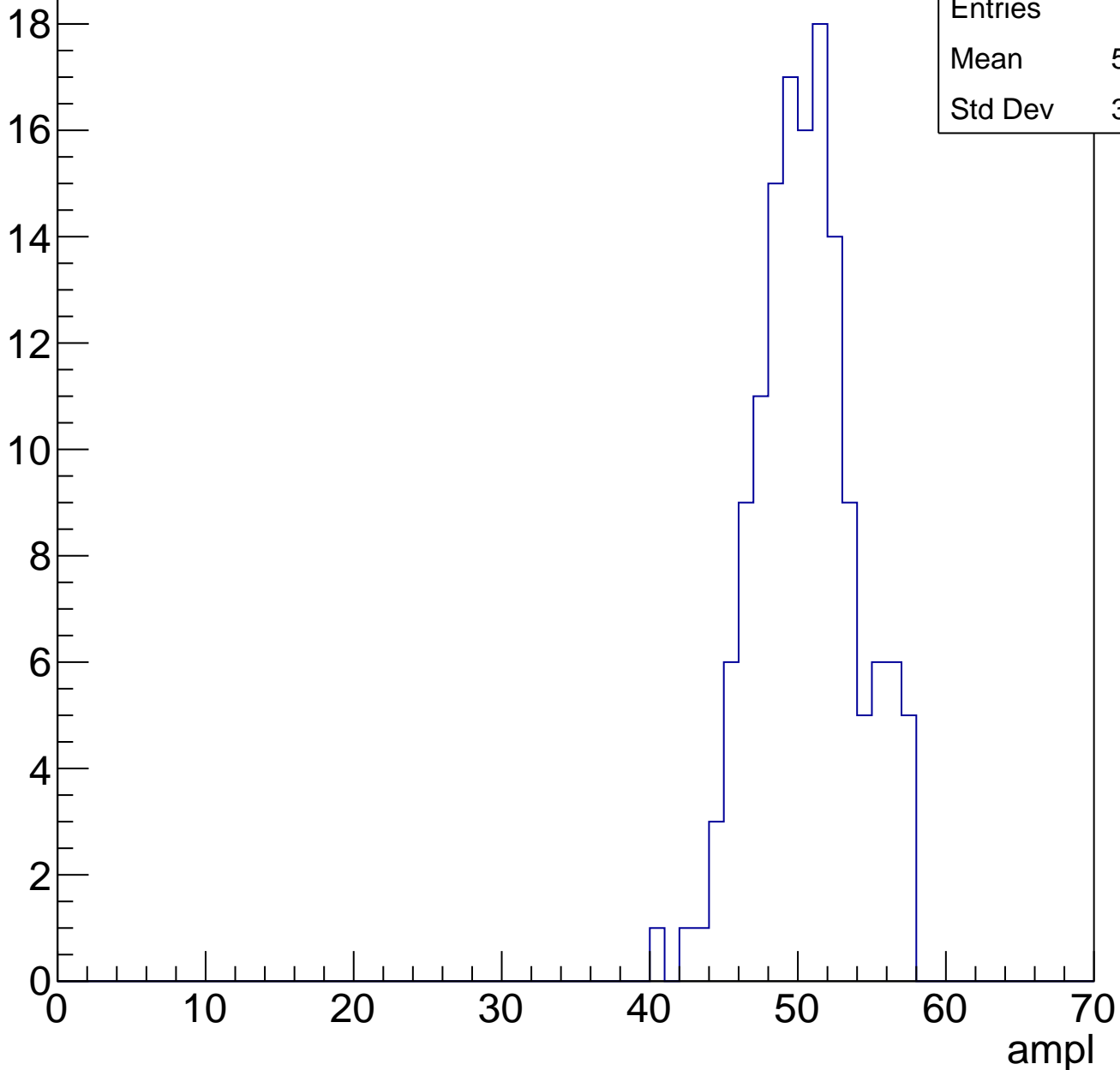
70

B0L101S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	50.03
Std Dev	3.383

Entry



B0L101S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#1, port C1

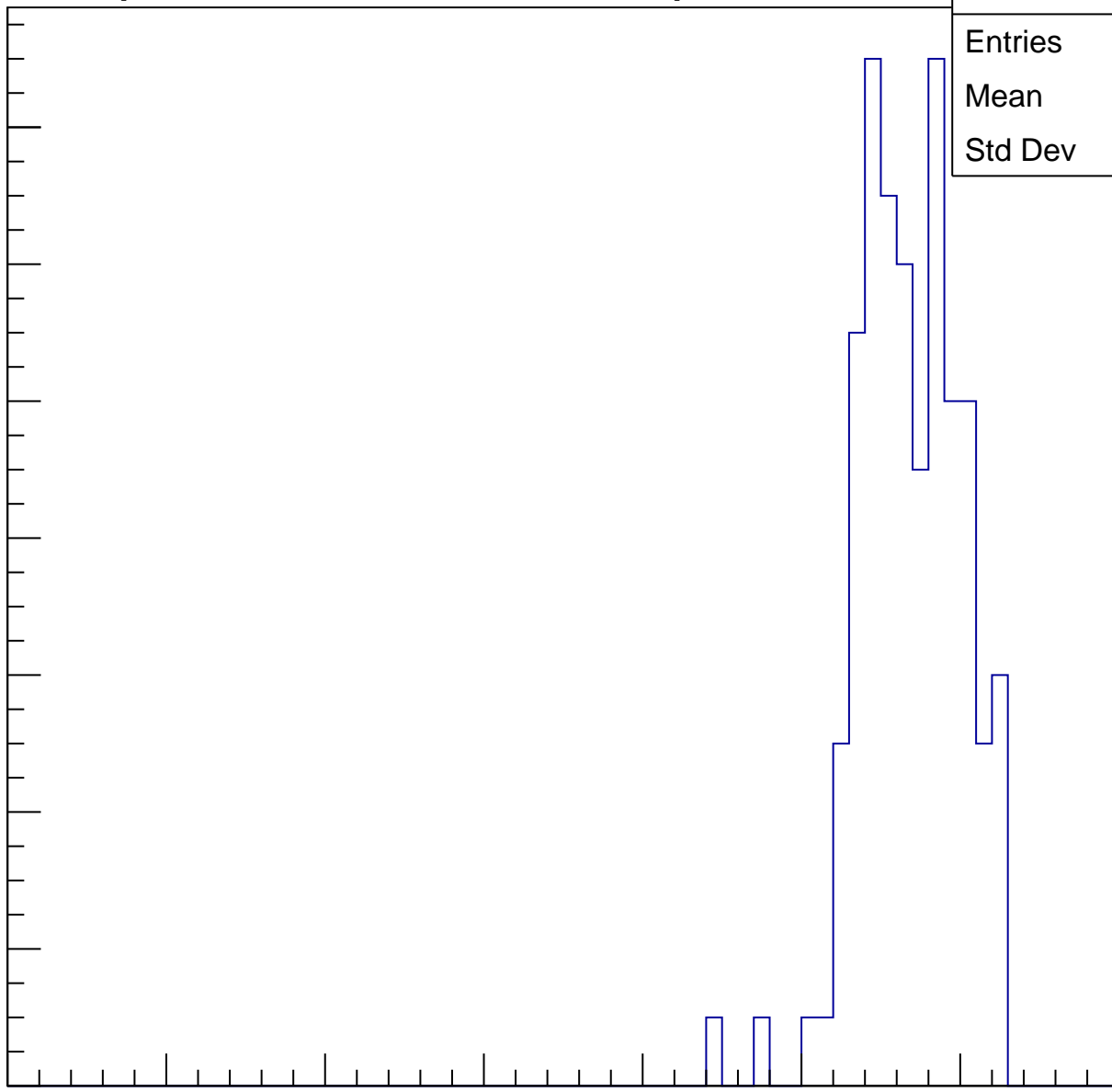
Entries	115
Mean	56.37
Std Dev	3.207

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch29, adc5

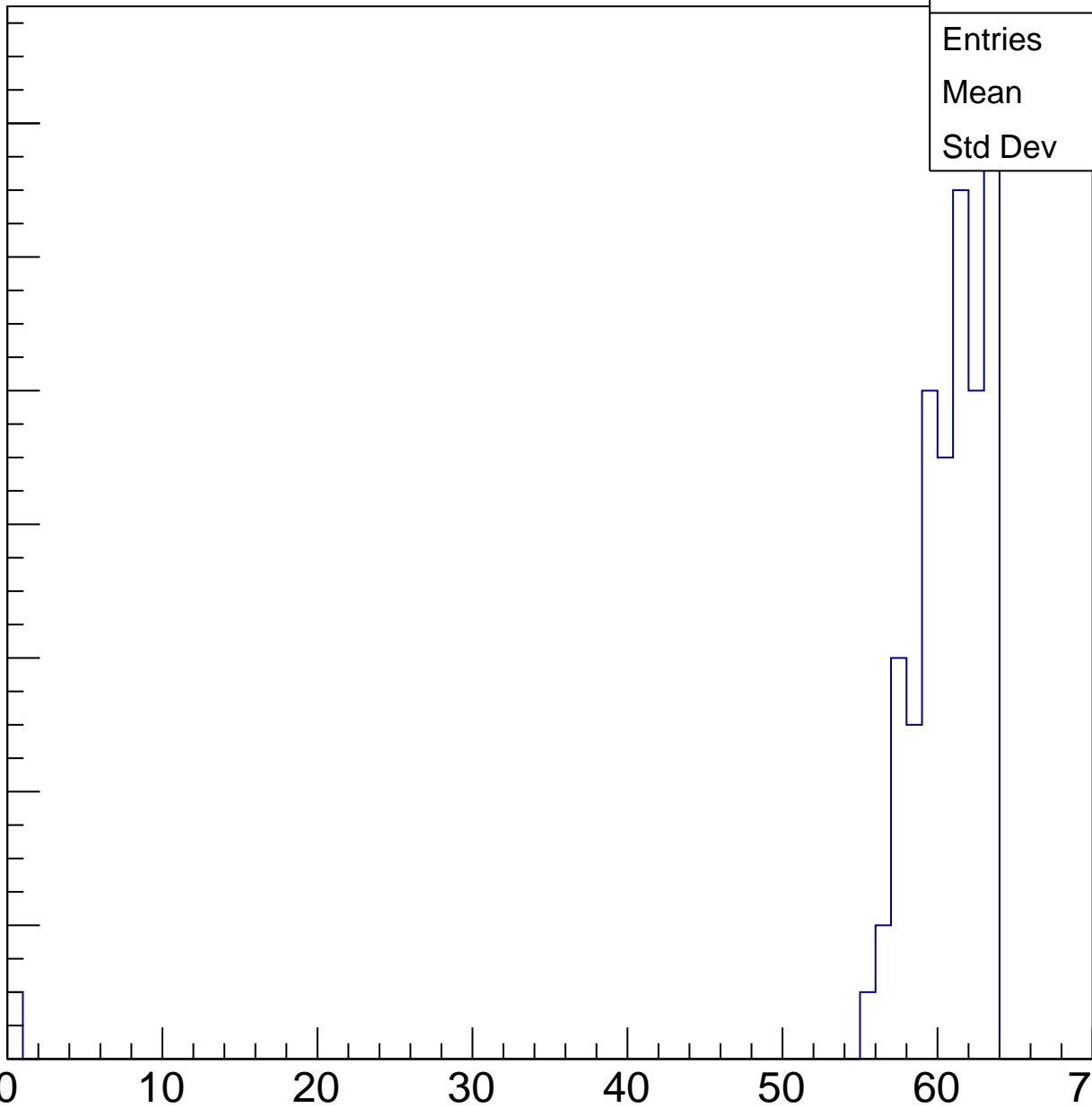
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	59.54
Std Dev	7.373

ampl

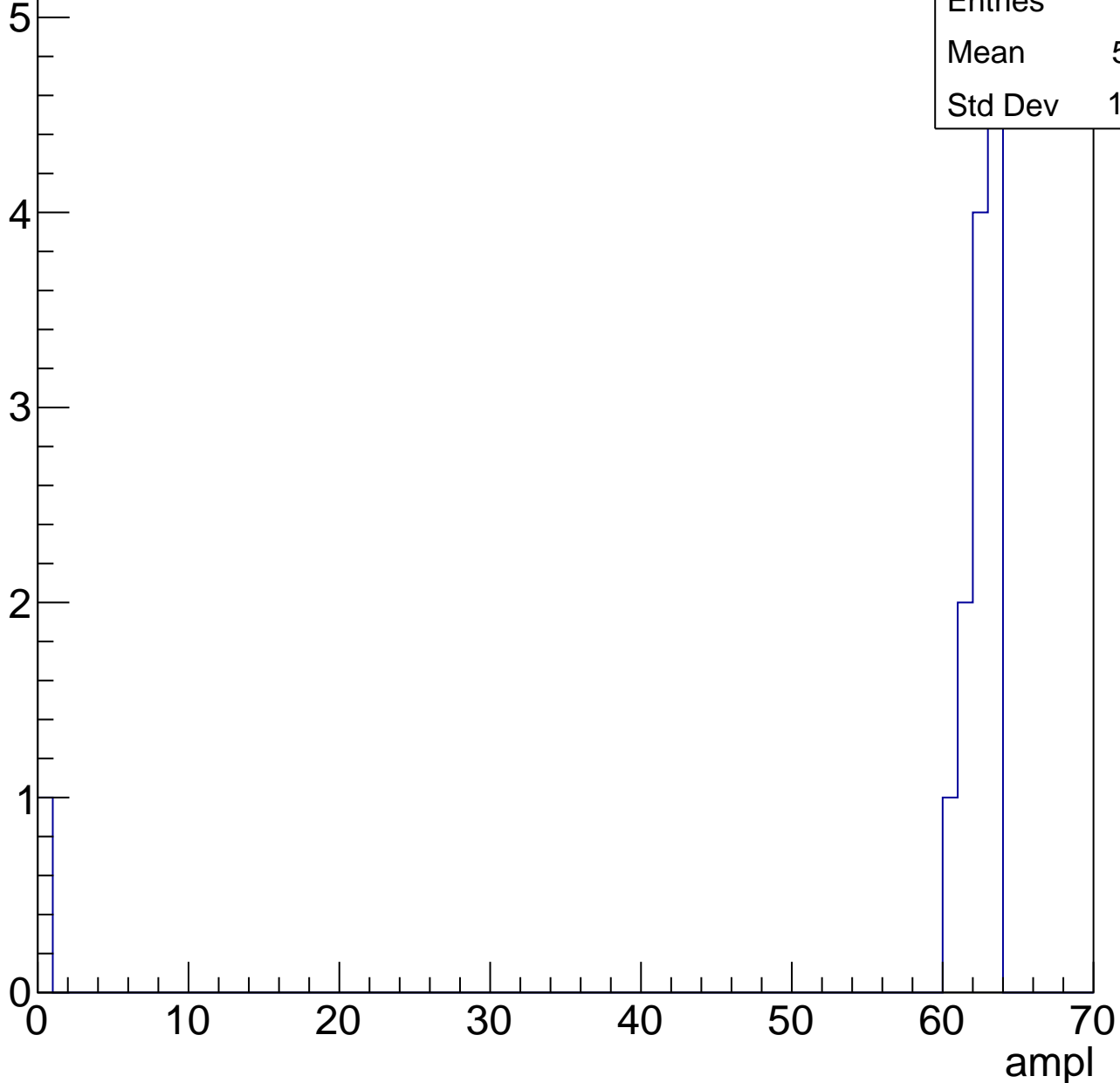


B0L101S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.31
Std Dev	16.57



B0L101S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

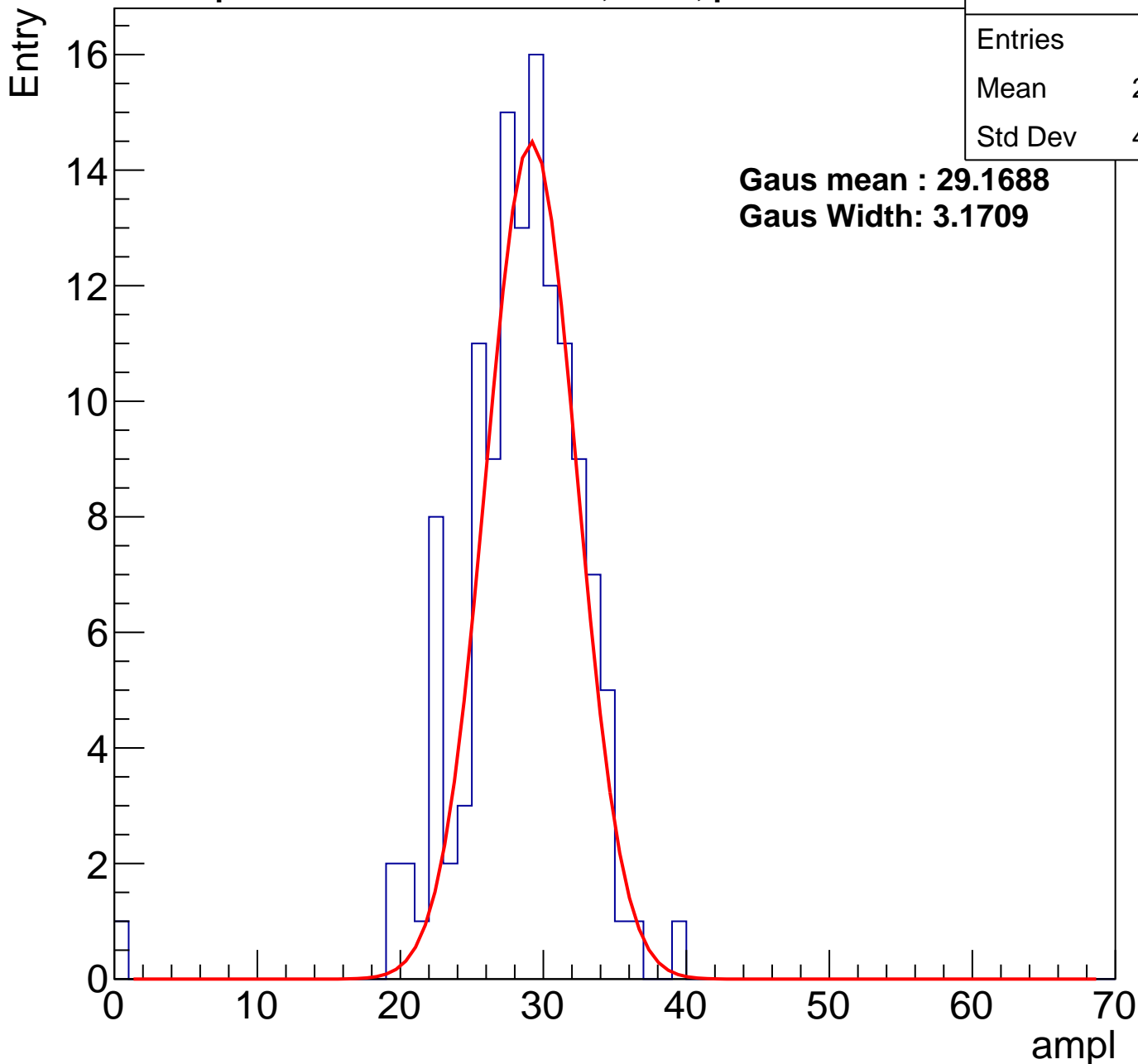
B0L101S, U15-ch30, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	27.96
Std Dev	4.433

Gaus mean : 29.1688

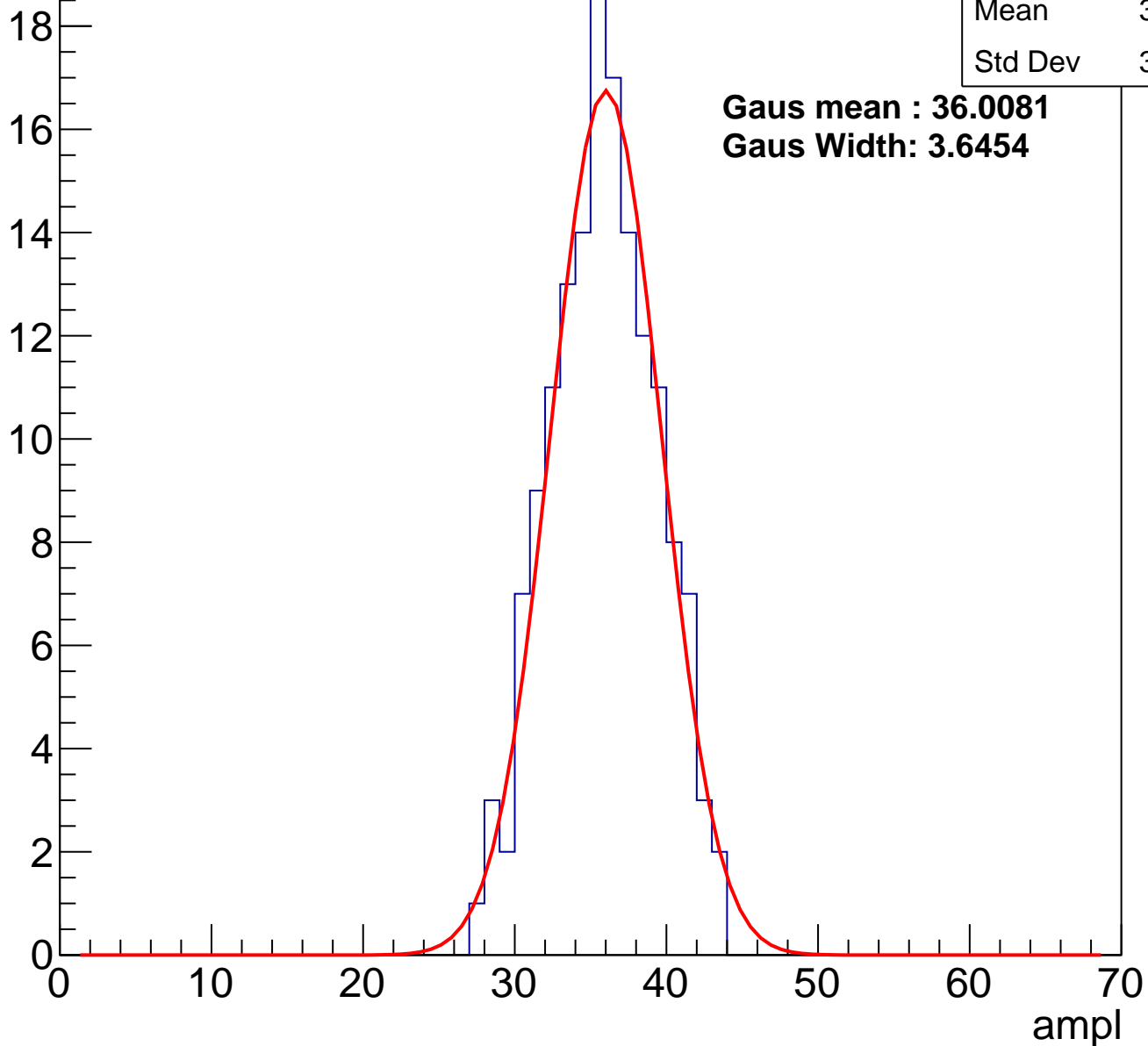
Gaus Width: 3.1709



B0L101S, U15-ch30, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch30, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	42.36
Std Dev	3.308

Gaus mean : 42.7943

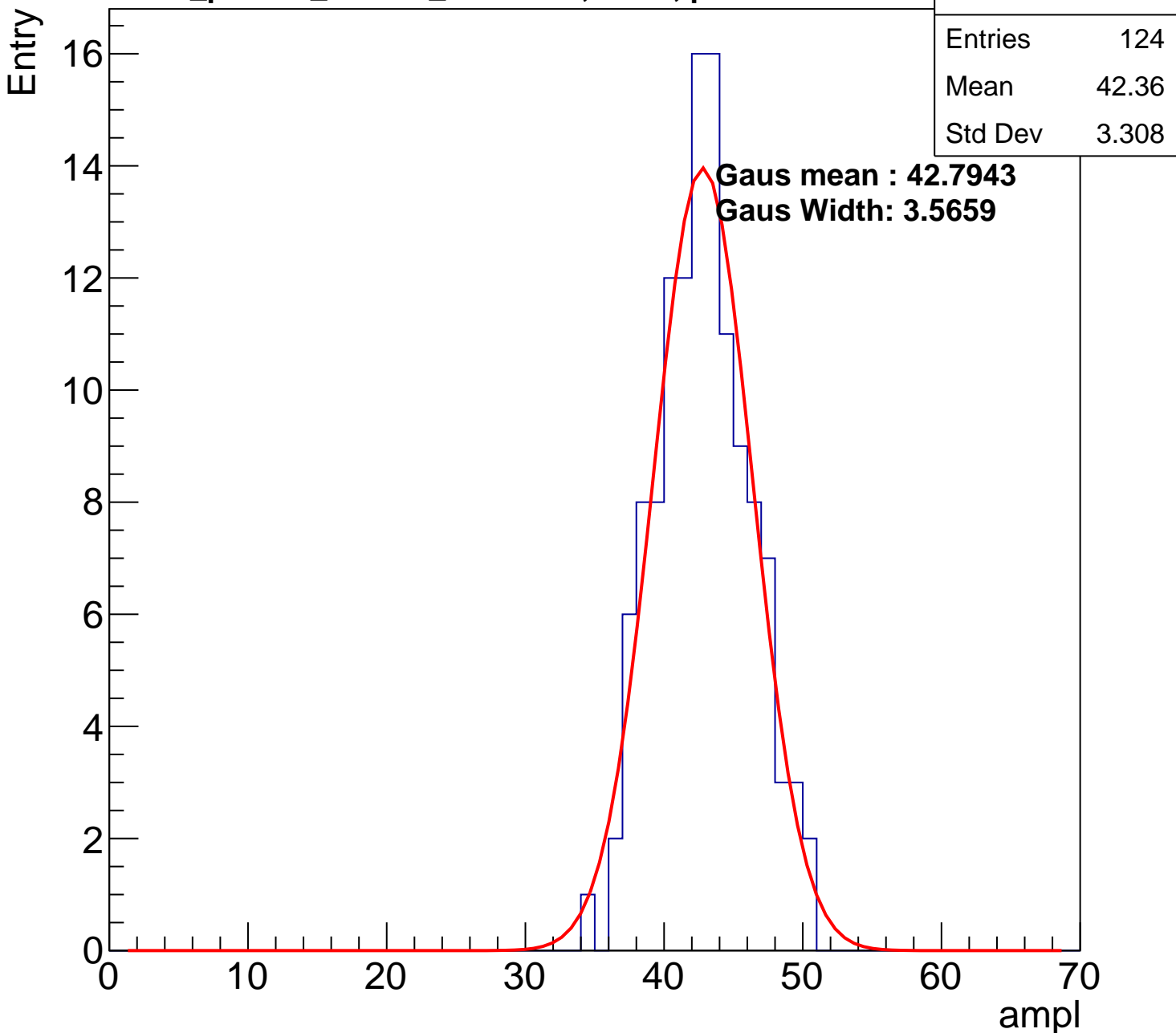
Gaus Width: 3.5659

Entry

16
14
12
10
8
6
4
2
0

ampl

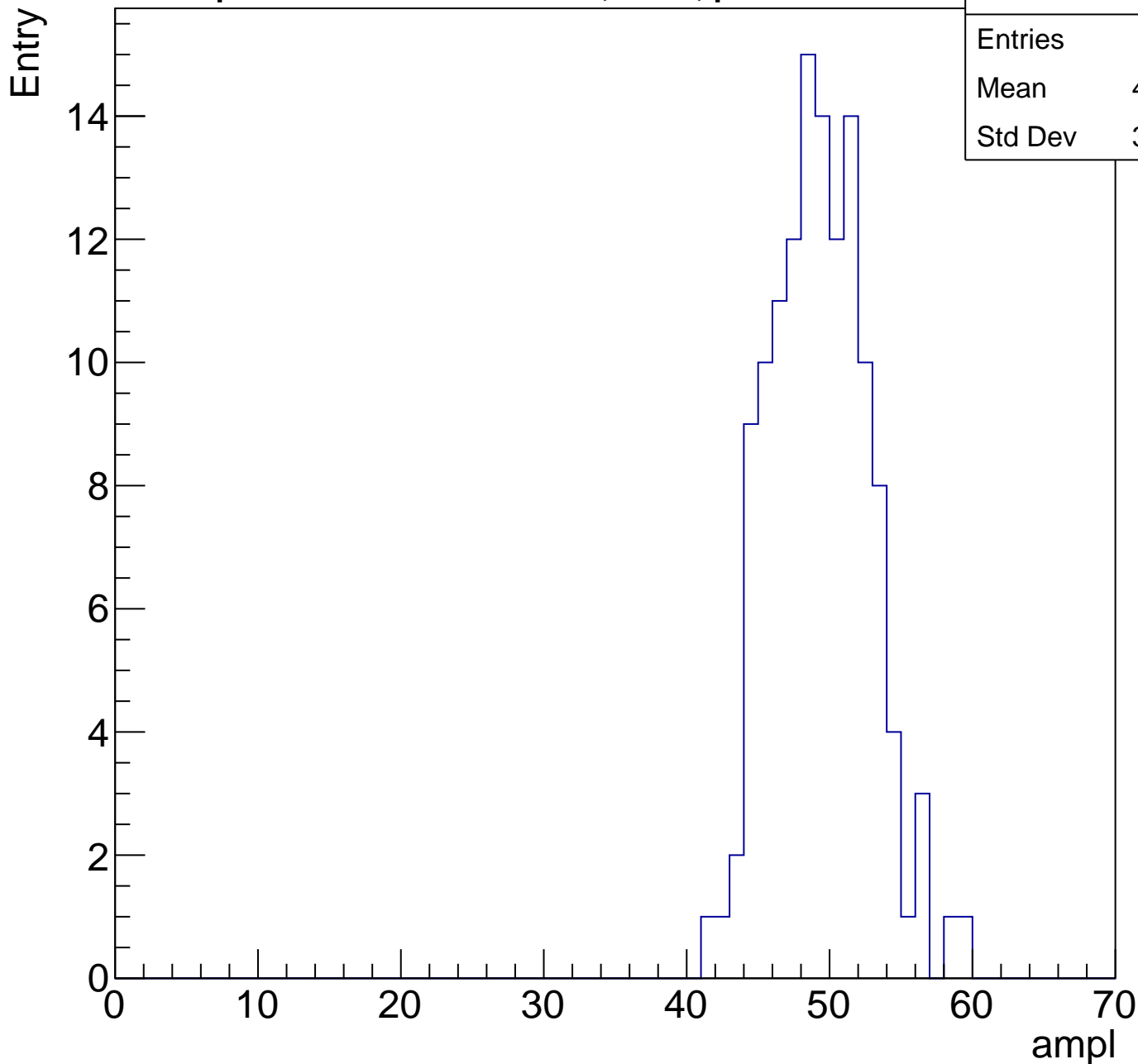
0 10 20 30 40 50 60 70



B0L101S, U15-ch30, adc3

calib_packv5_042523_0143.root, FC#1, port C1

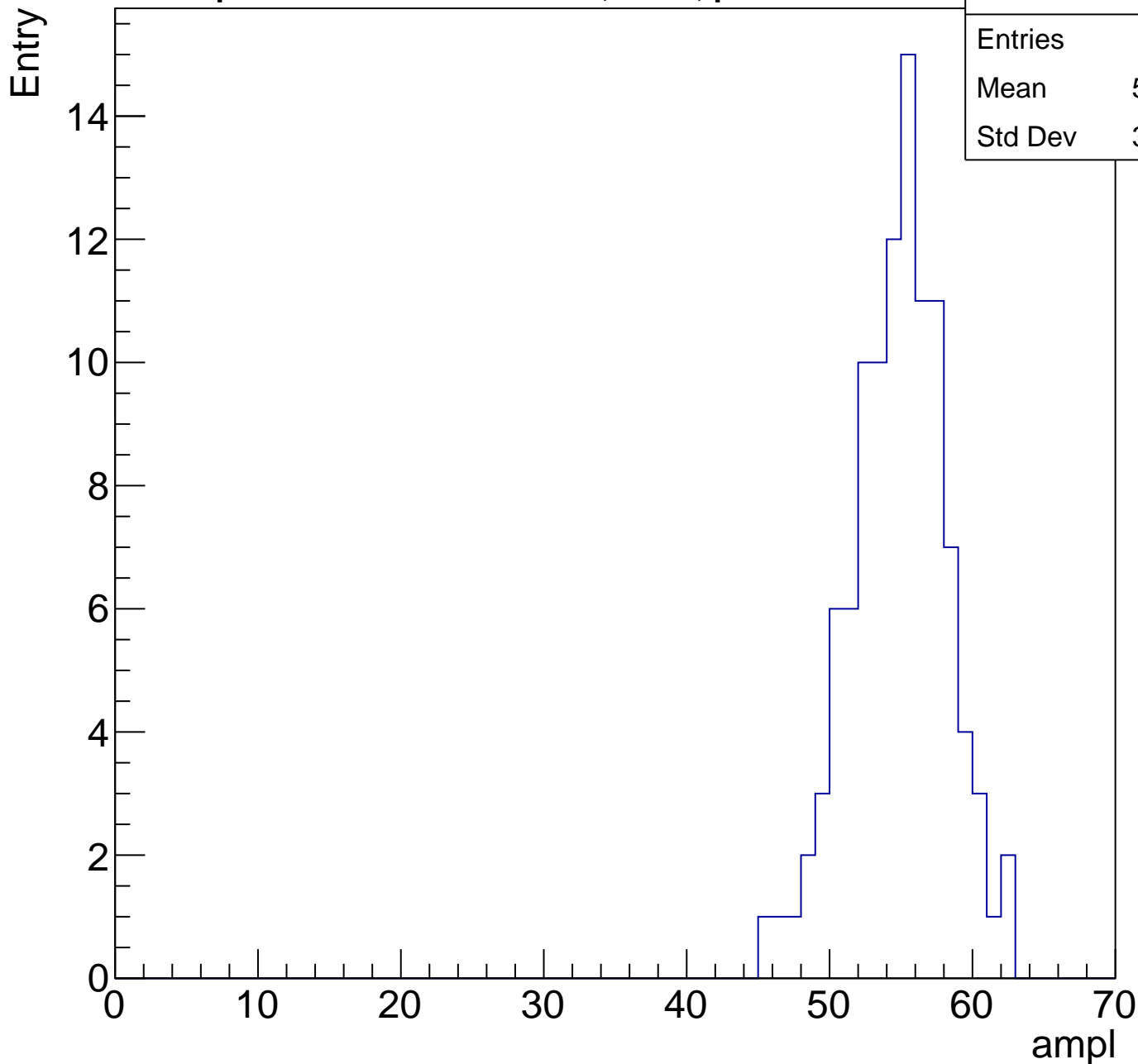
Entries	129
Mean	48.88
Std Dev	3.387



B0L101S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	54.34
Std Dev	3.342



B0L101S, U15-ch30, adc5

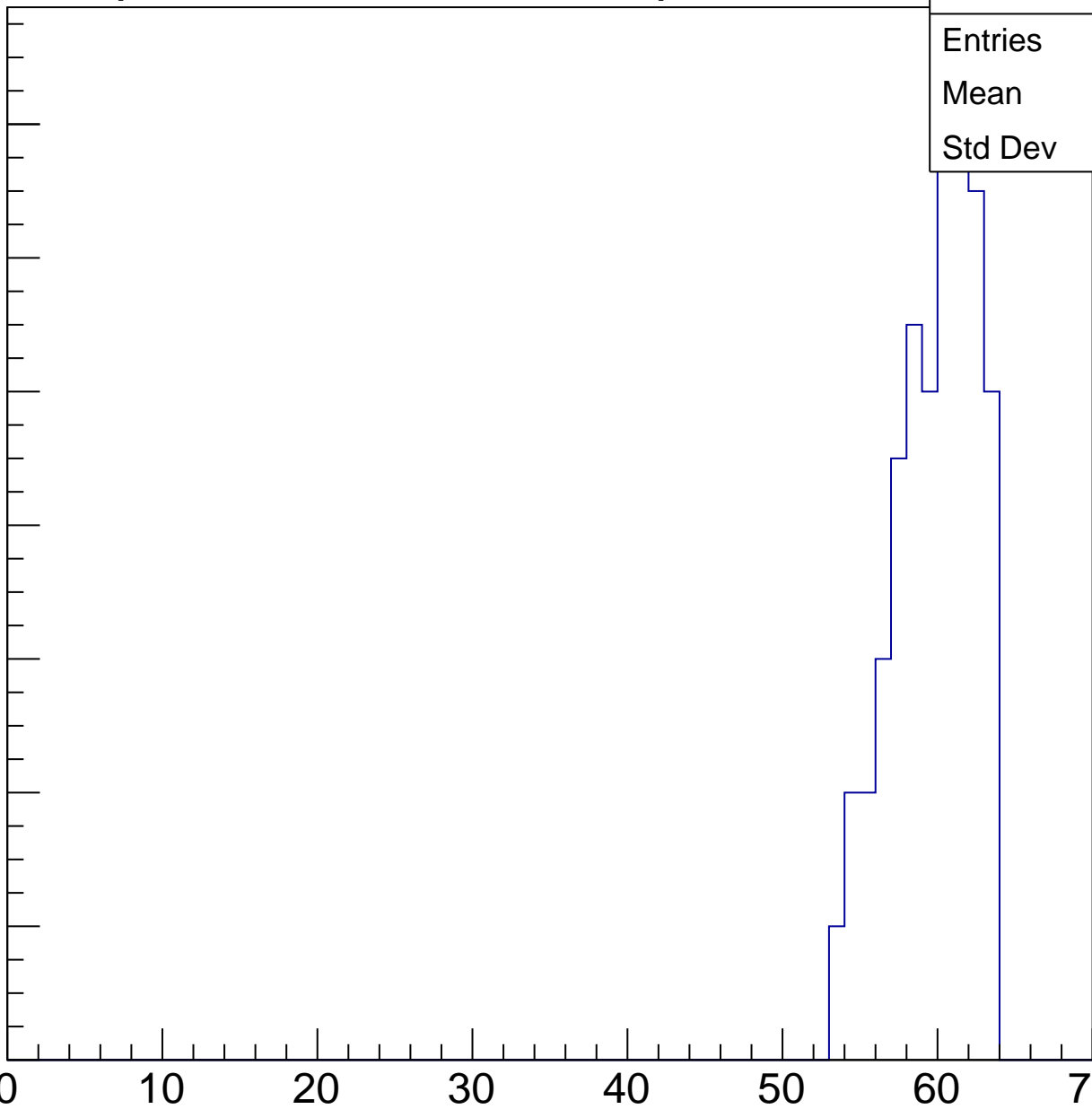
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	59.29
Std Dev	2.622

ampl

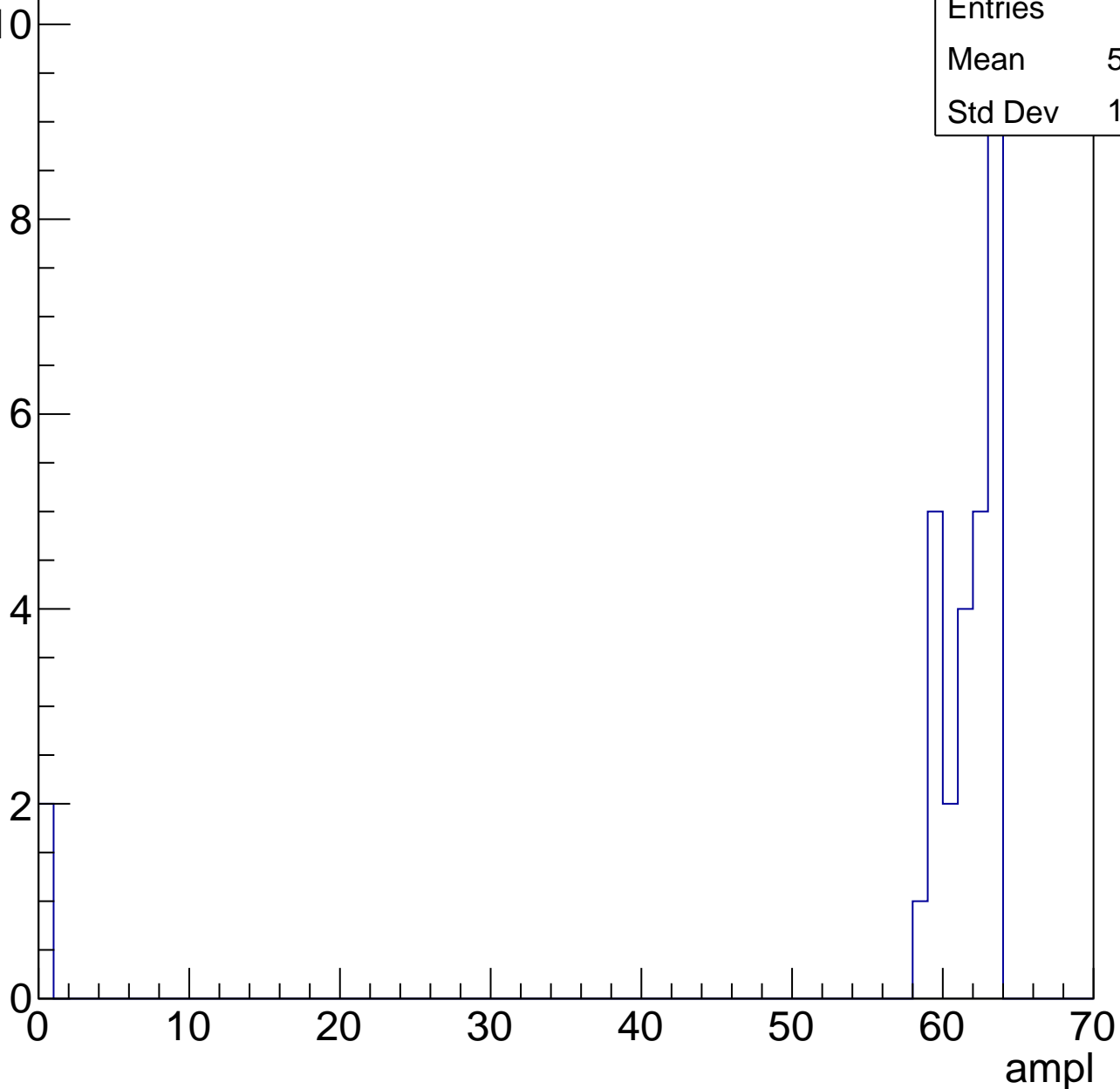


B0L101S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	29
Mean	57.14
Std Dev	15.63



B0L101S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch31, adc0

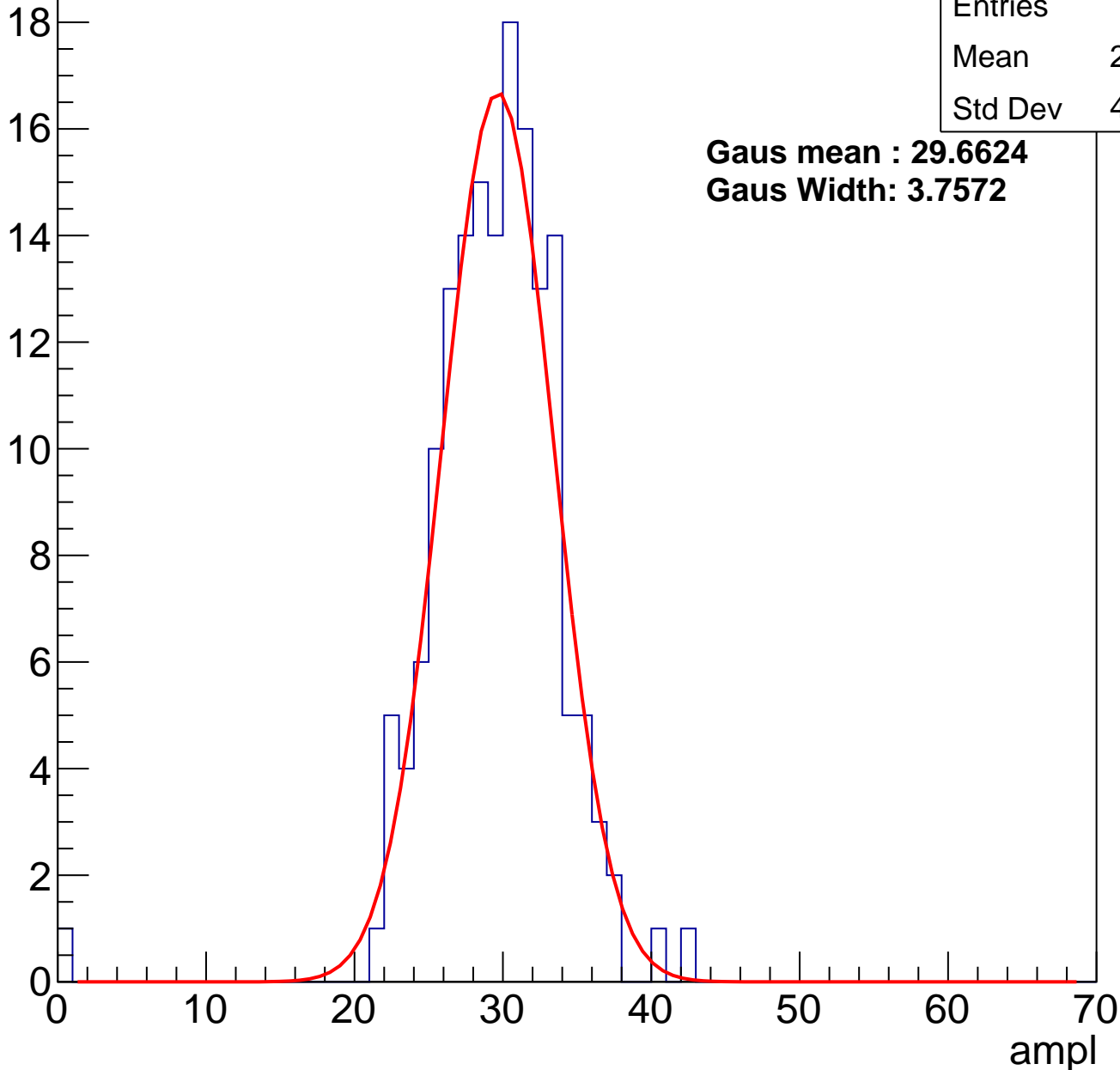
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	29.08
Std Dev	4.377

Gaus mean : 29.6624

Gaus Width: 3.7572

Entry



B0L101S, U15-ch31, adc1

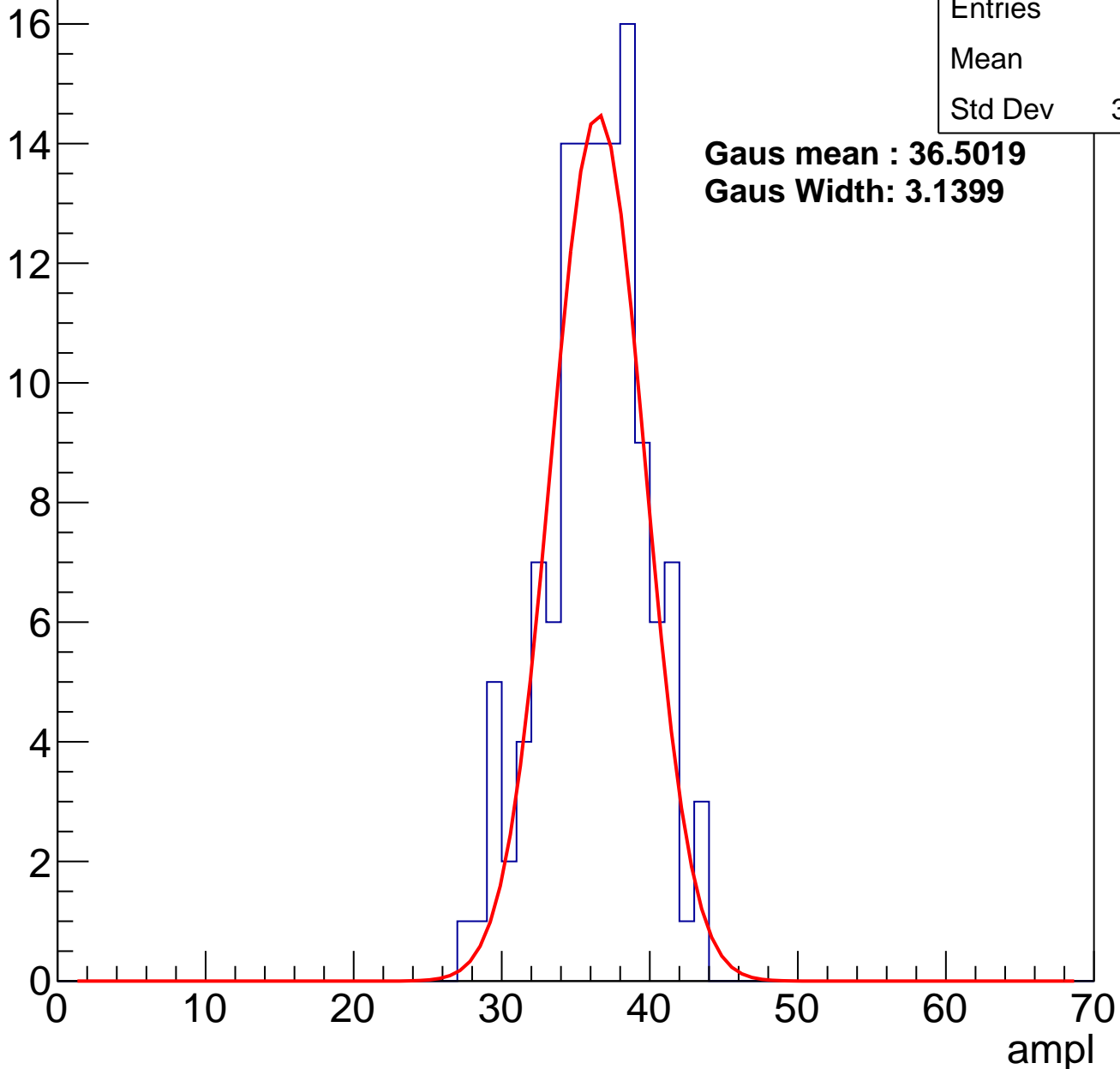
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	35.9
Std Dev	3.372

Gaus mean : 36.5019

Gaus Width: 3.1399

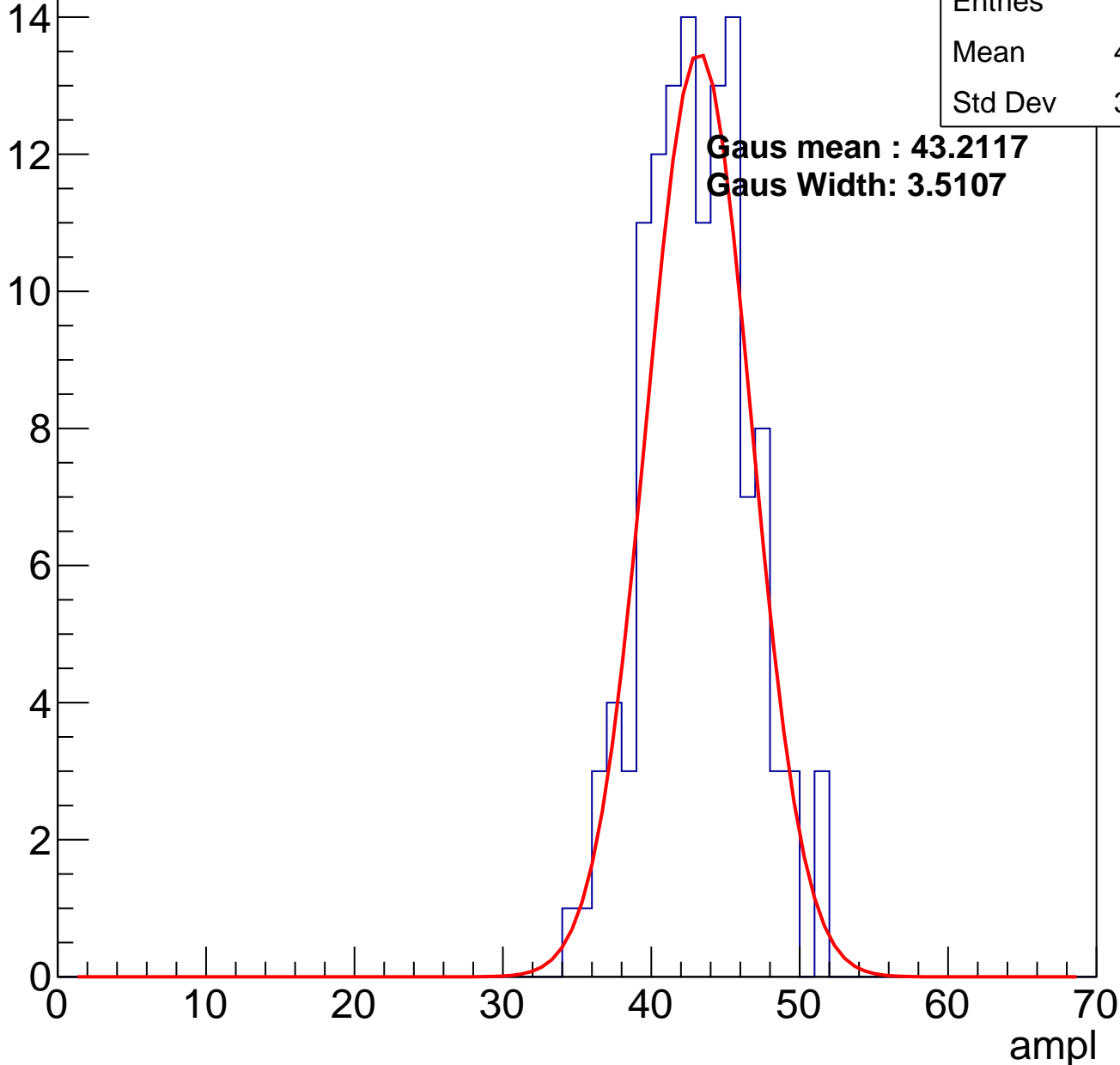
Entry



B0L101S, U15-ch31, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

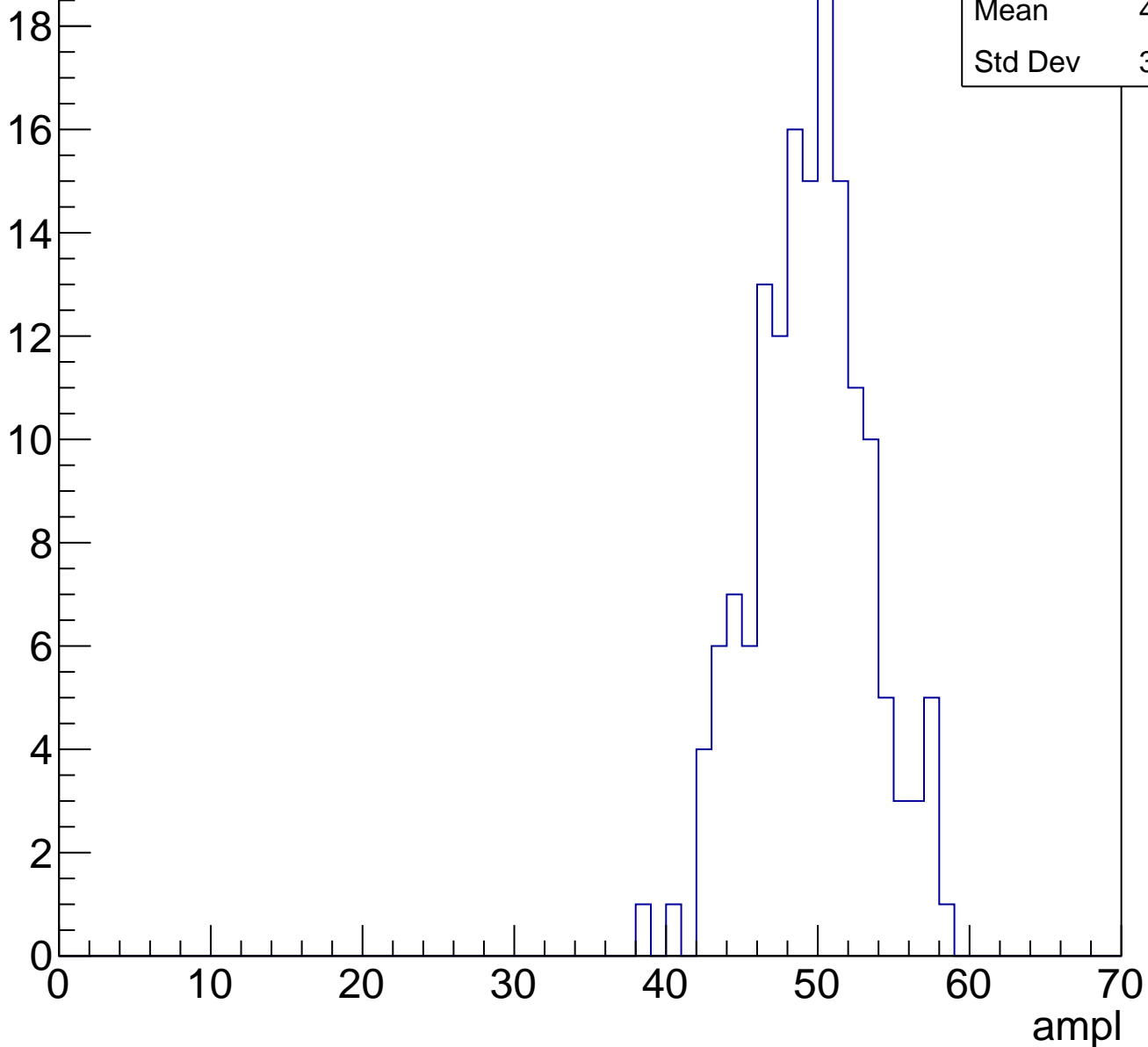


B0L101S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	49.08
Std Dev	3.778

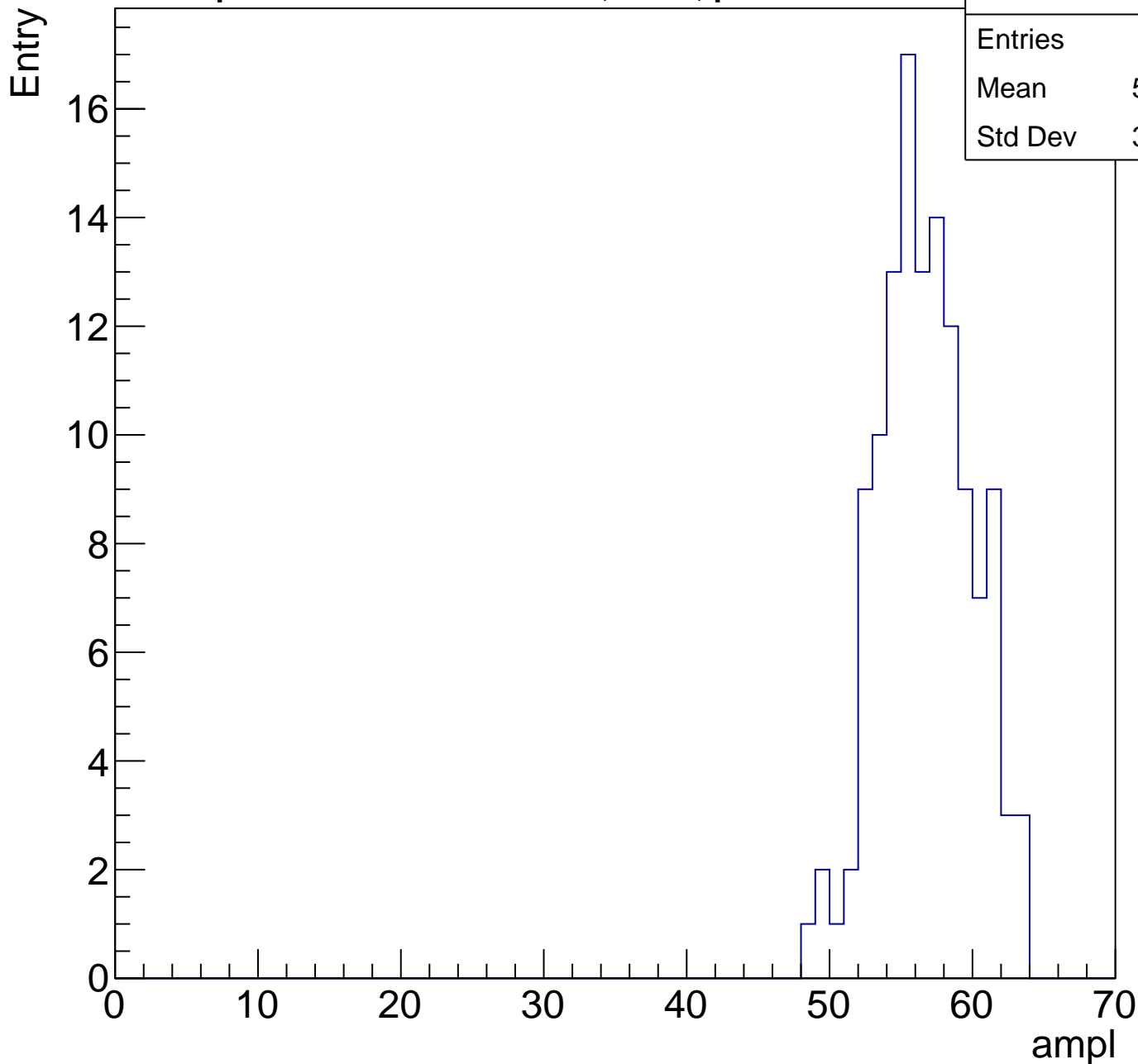
Entry



B0L101S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	56.24
Std Dev	3.198



B0L101S, U15-ch31, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	72
Mean	59.39
Std Dev	7.382

ampl

0

10

20

30

40

50

60

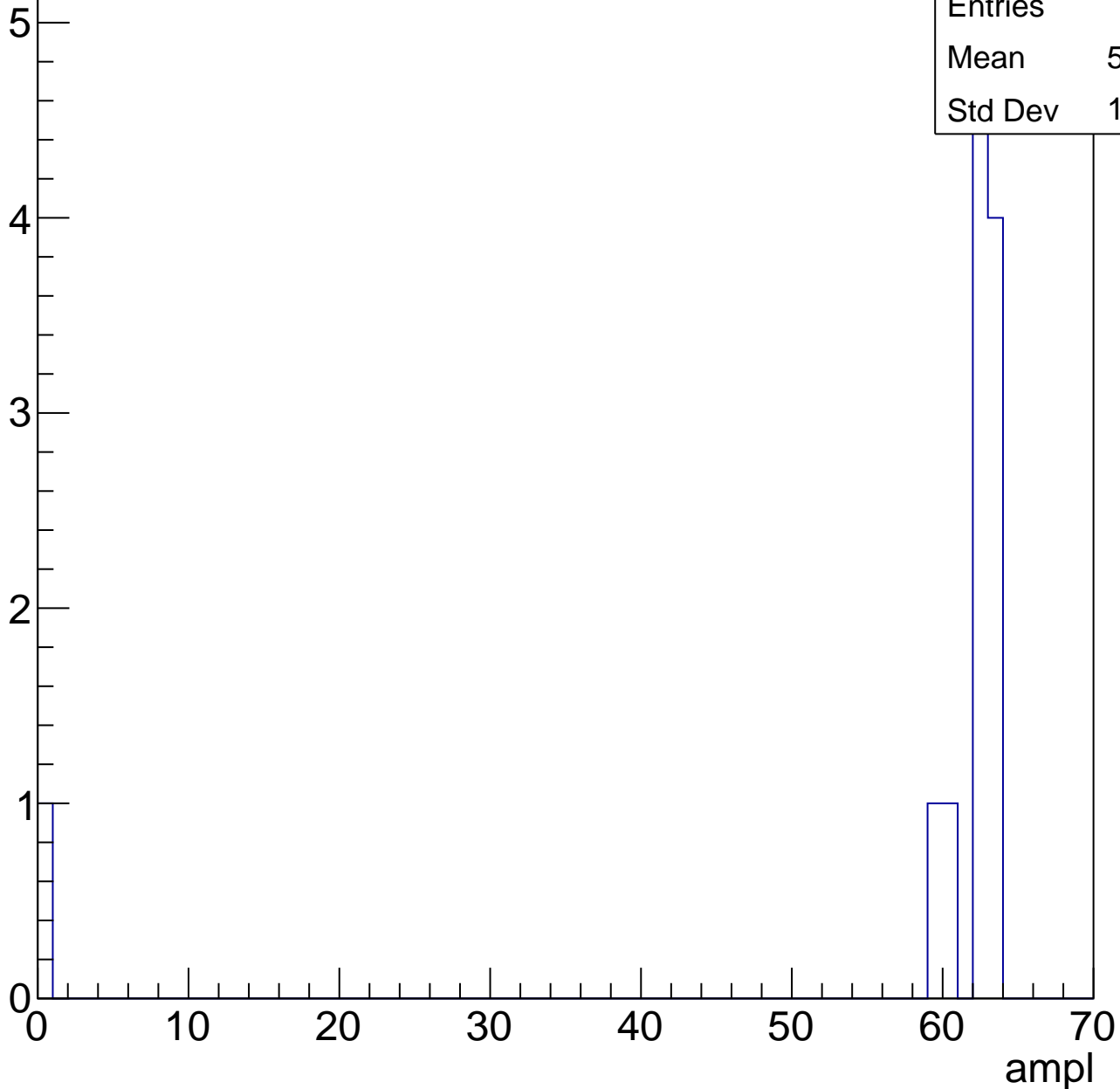
70

B0L101S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.75
Std Dev	17.15



B0L101S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L101S, U15-ch32, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	27.08
Std Dev	3.421

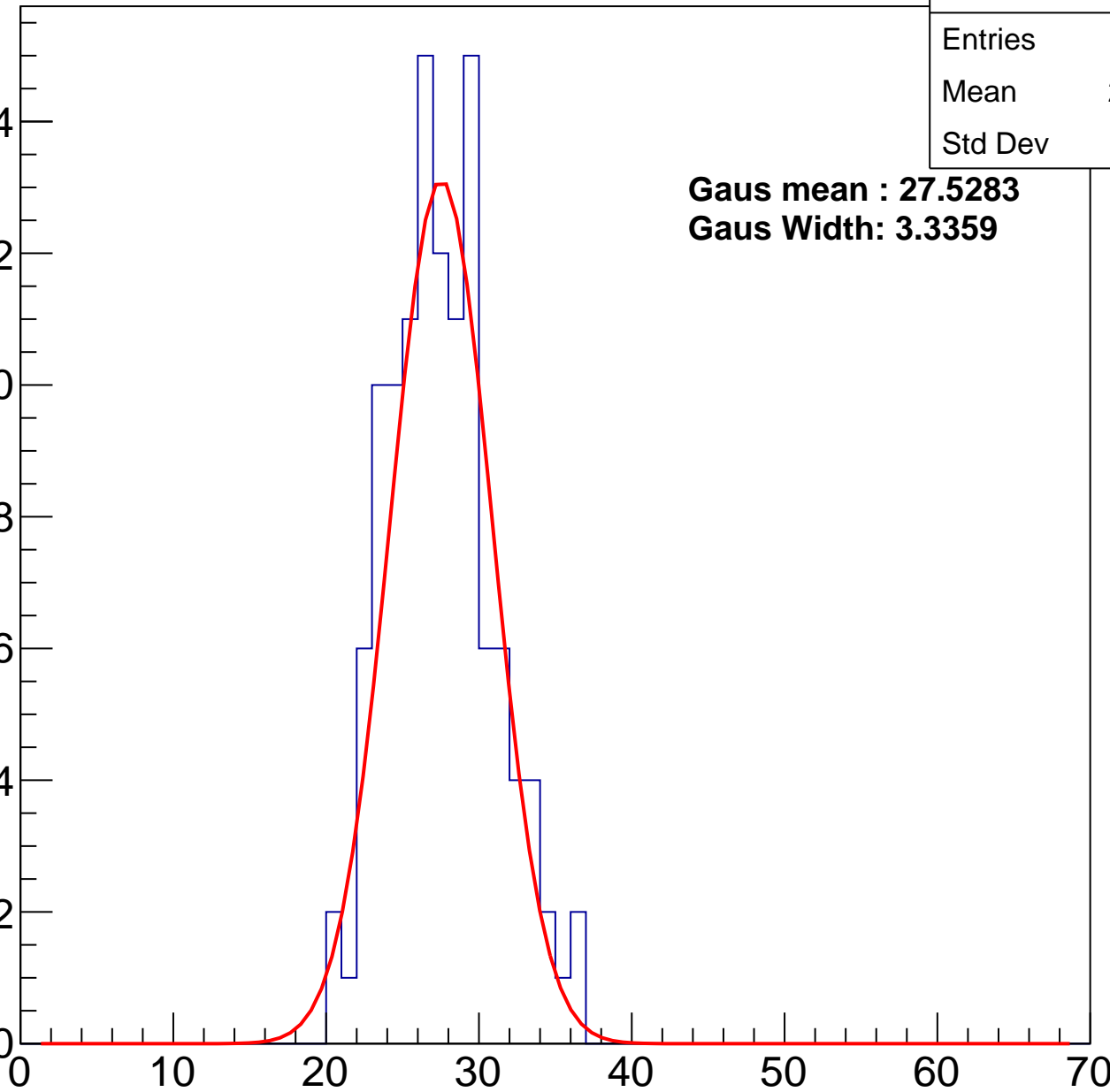
Gaus mean : 27.5283

Gaus Width: 3.3359

Entry

14
12
10
8
6
4
2
0

ampl



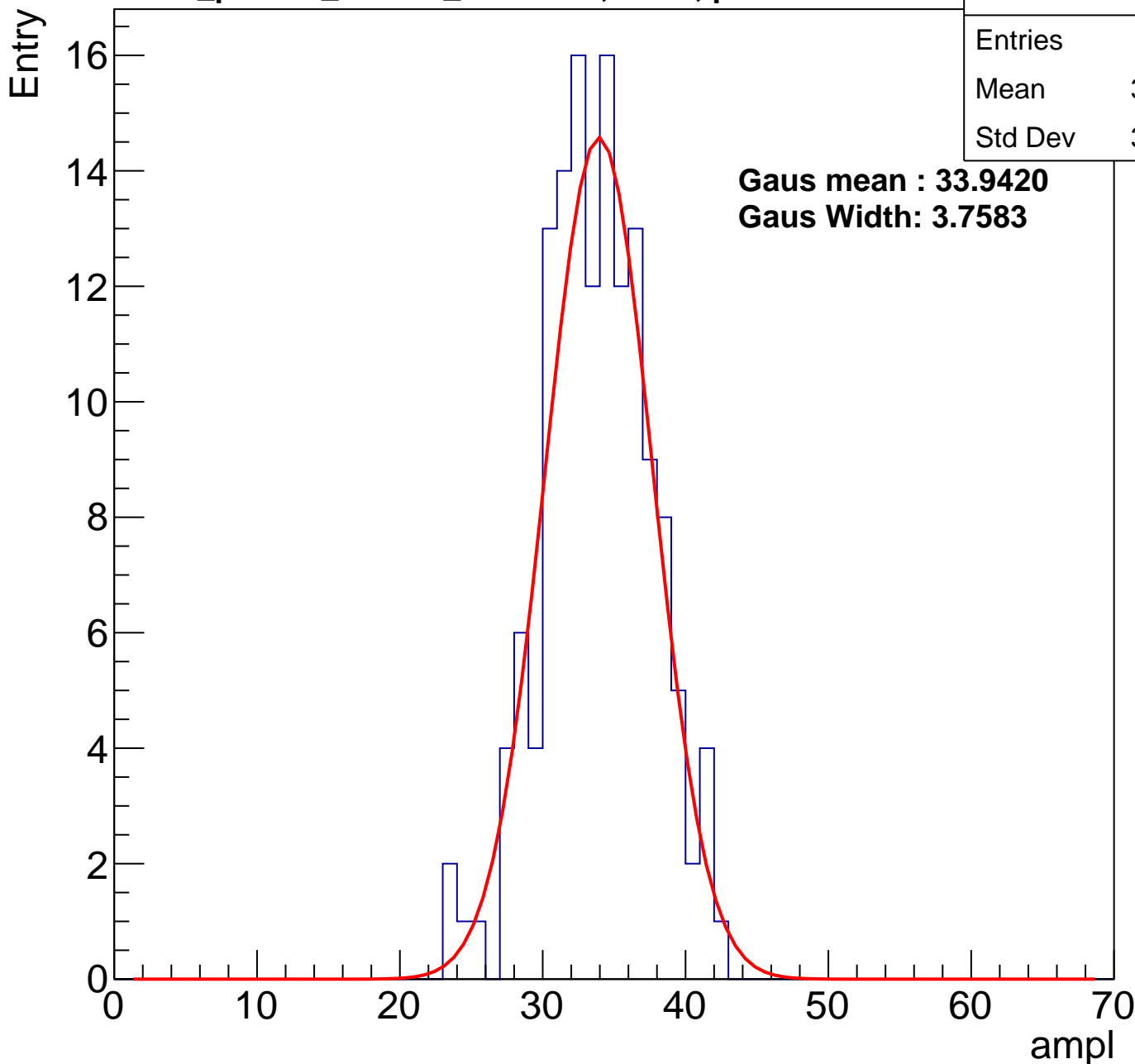
B0L101S, U15-ch32, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	33.35
Std Dev	3.753

Gaus mean : 33.9420

Gaus Width: 3.7583



B0L101S, U15-ch32, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	40.22
Std Dev	3.459

Gaus mean : 40.5932

Gaus Width: 3.3736

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

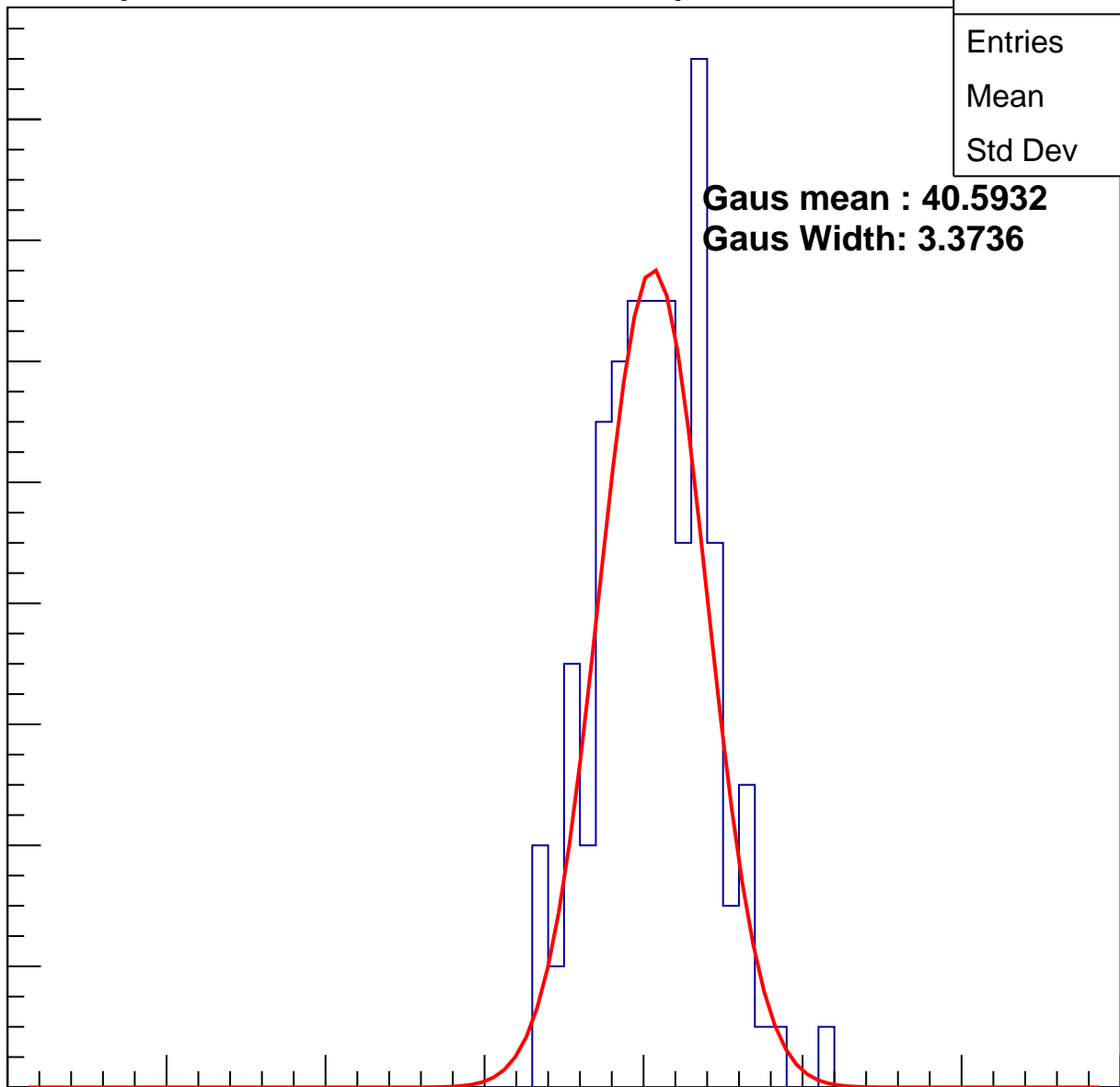
40

50

60

70

ampl



B0L101S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	47.01
Std Dev	3.68

Entry

16

14

12

10

8

6

4

2

0

0

10

20

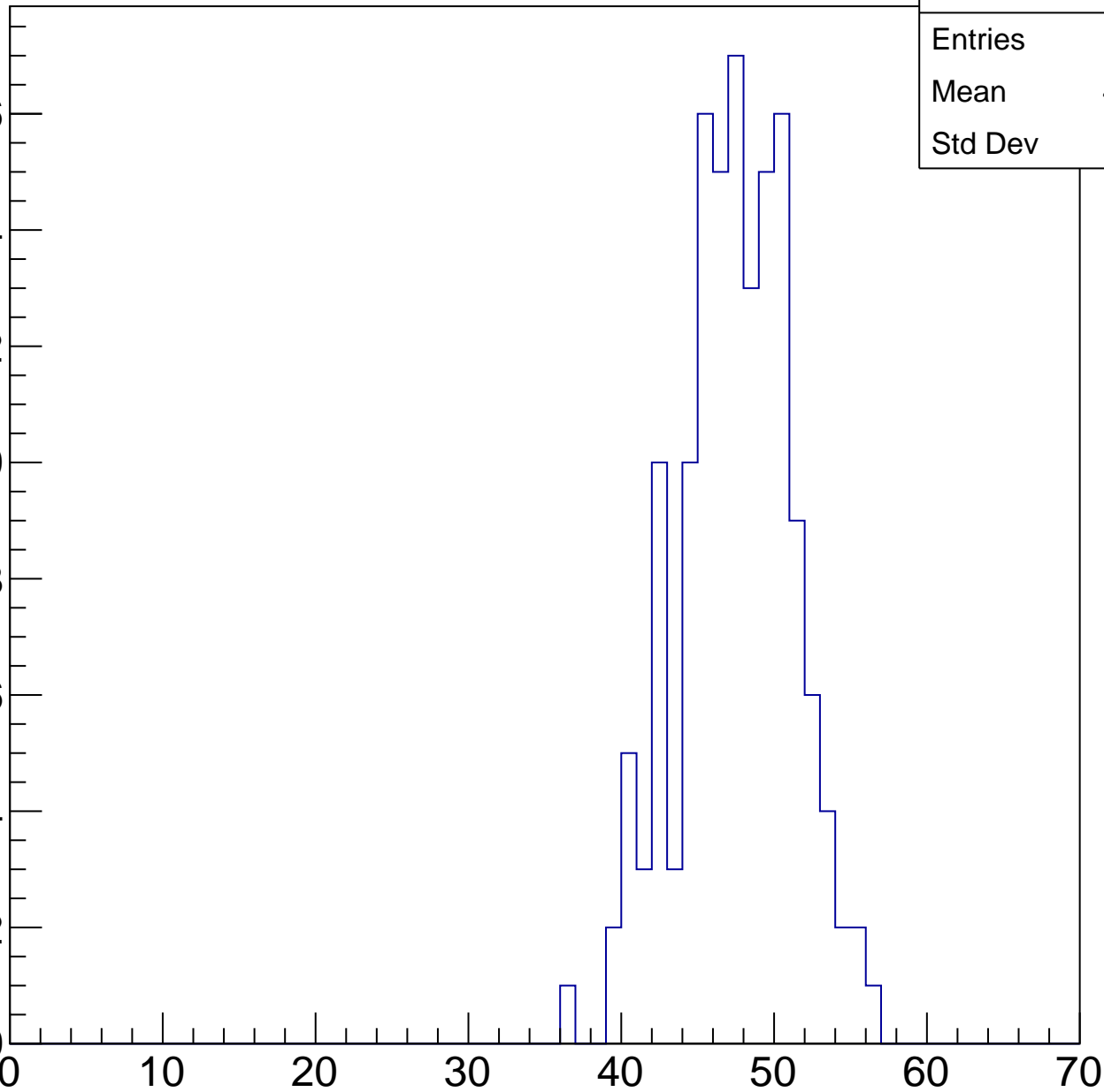
30

40

50

60

ampl



B0L101S, U15-ch32, adc4

calib_packv5_042523_0143.root, FC#1, port C1

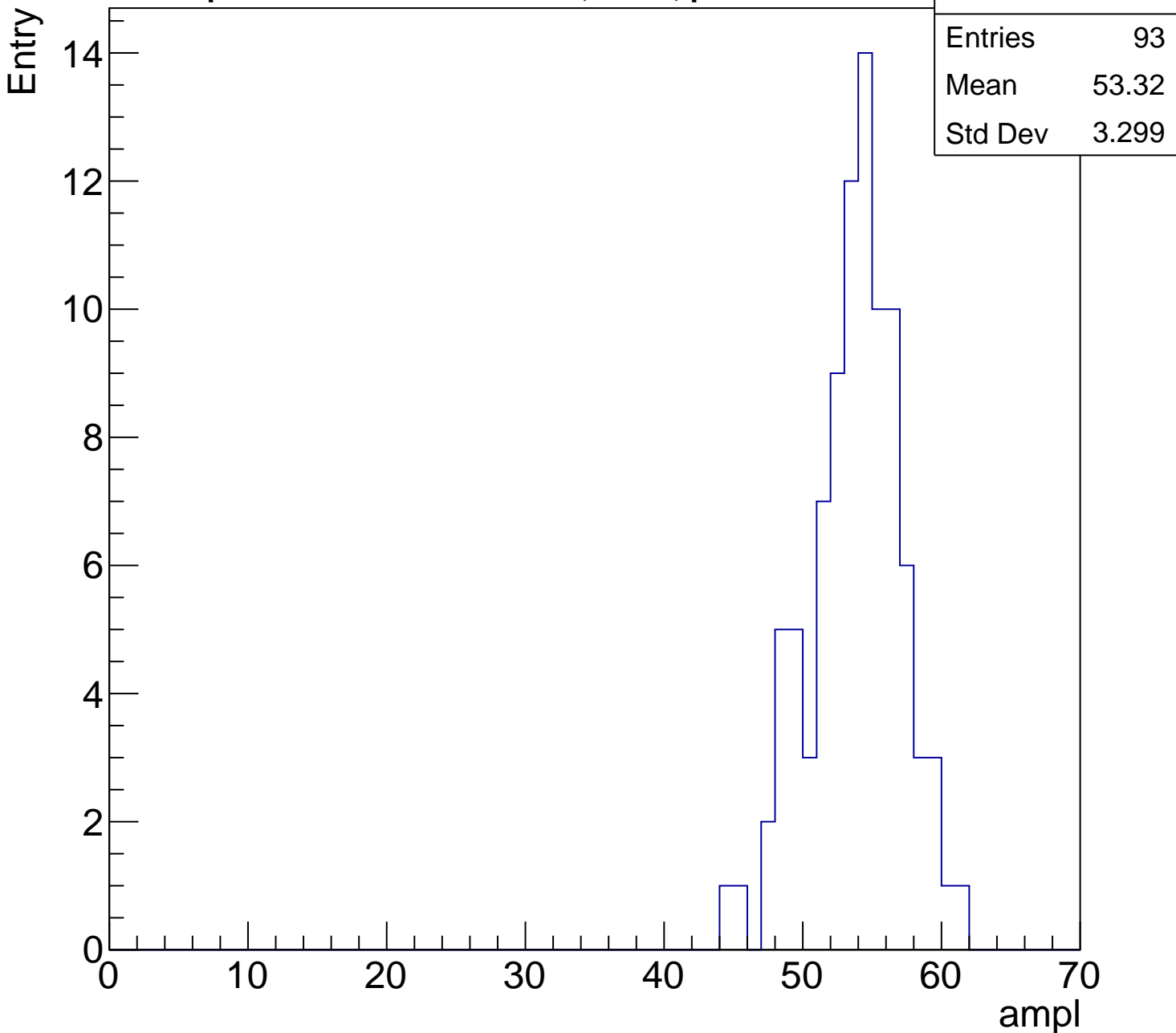
Entries	93
Mean	53.32
Std Dev	3.299

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch32, adc5

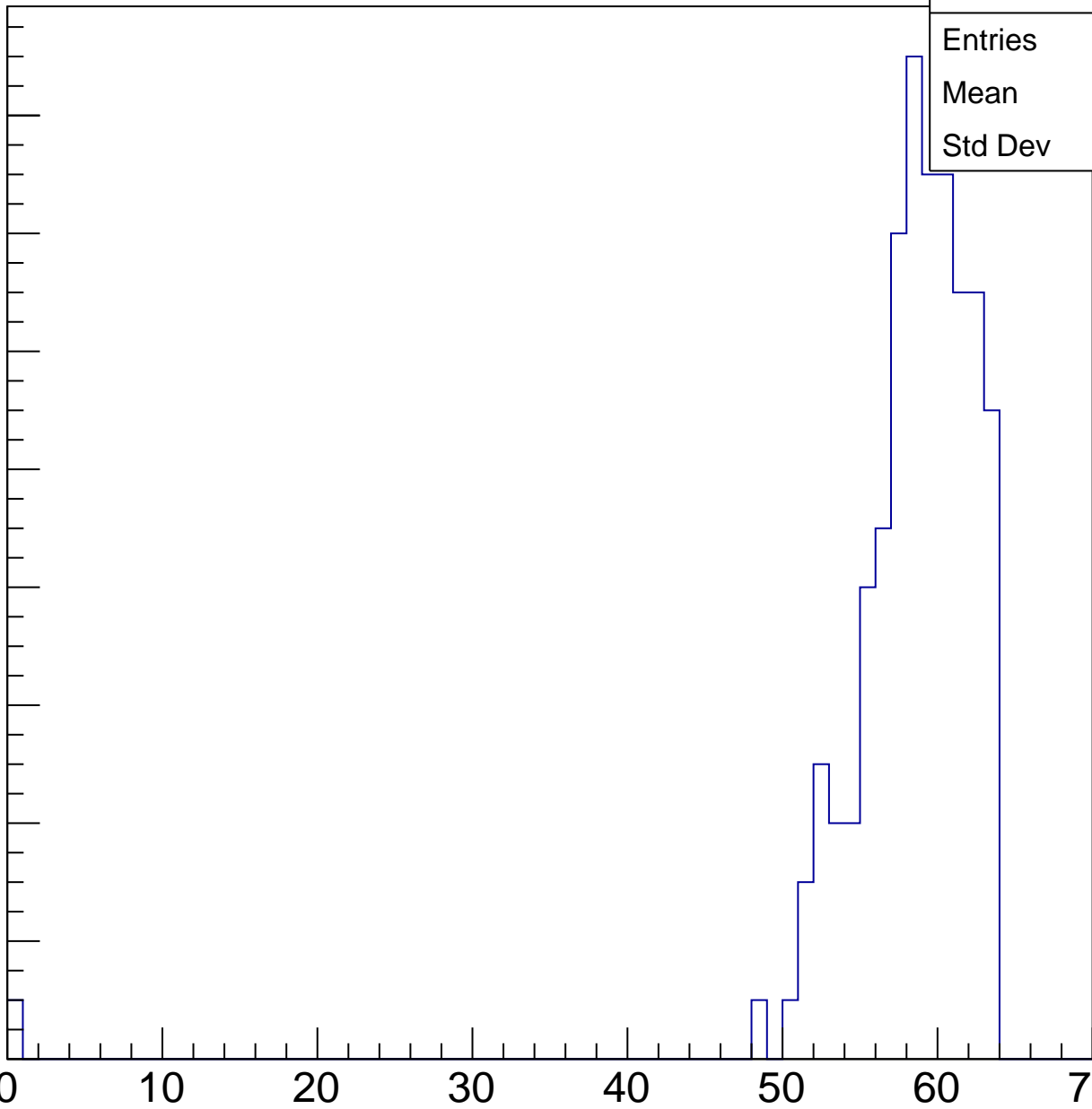
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	57.79
Std Dev	5.991

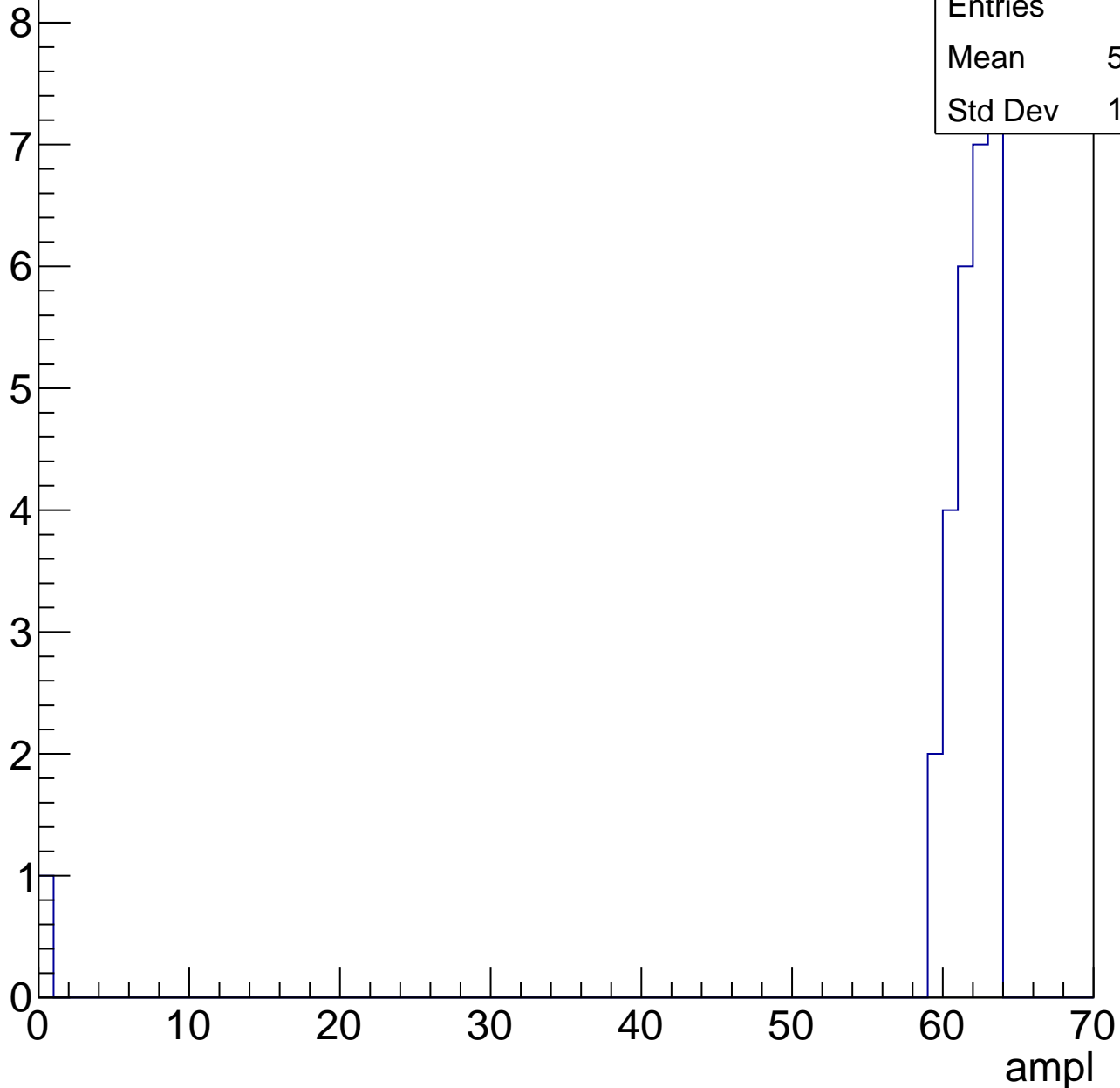
ampl



B0L101S, U15-ch32, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	39.5
Std Dev	23.5

ampl

B0L101S, U15-ch33, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	28.92
Std Dev	3.486

Gaus mean : 29.3212

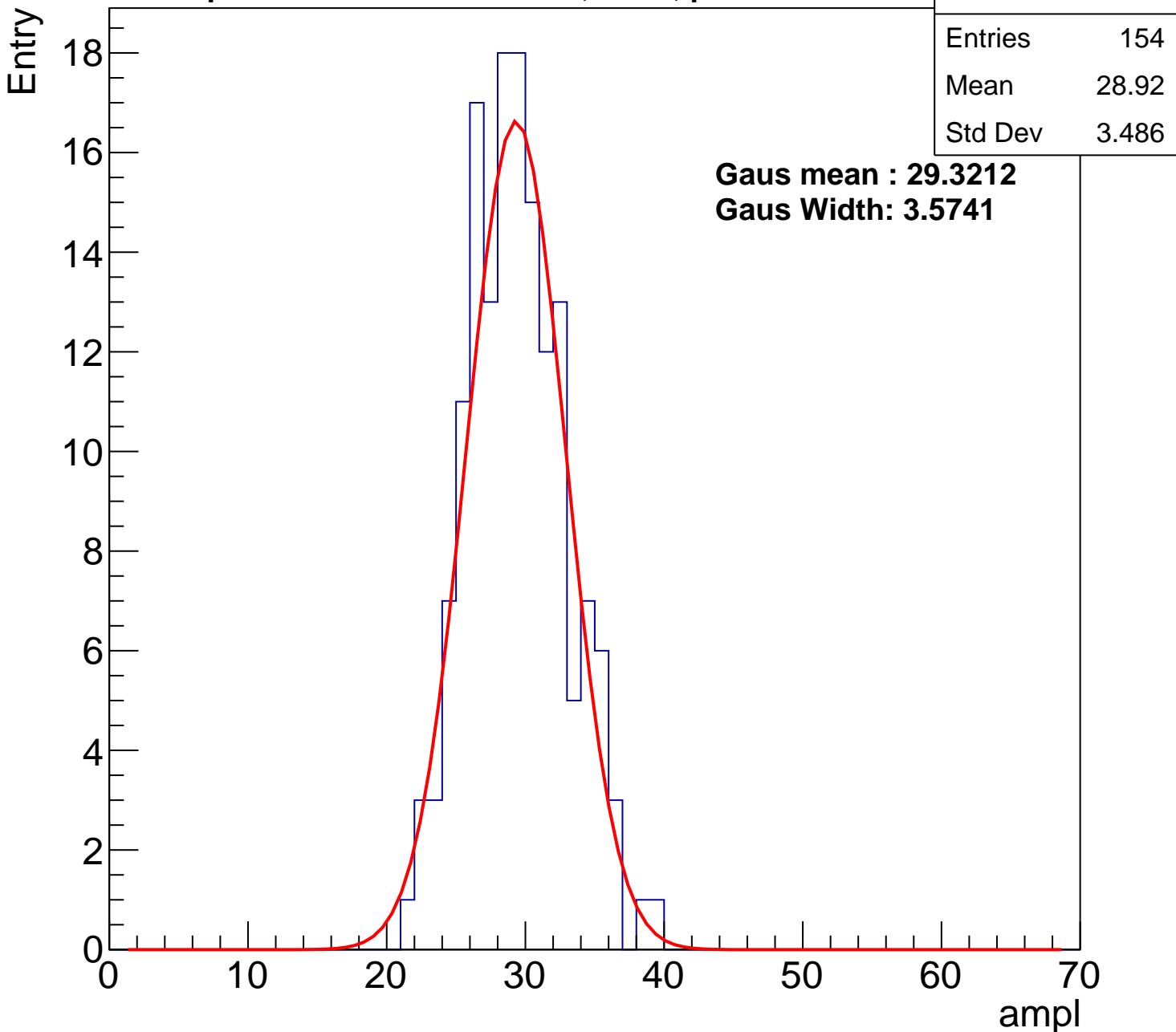
Gaus Width: 3.5741

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

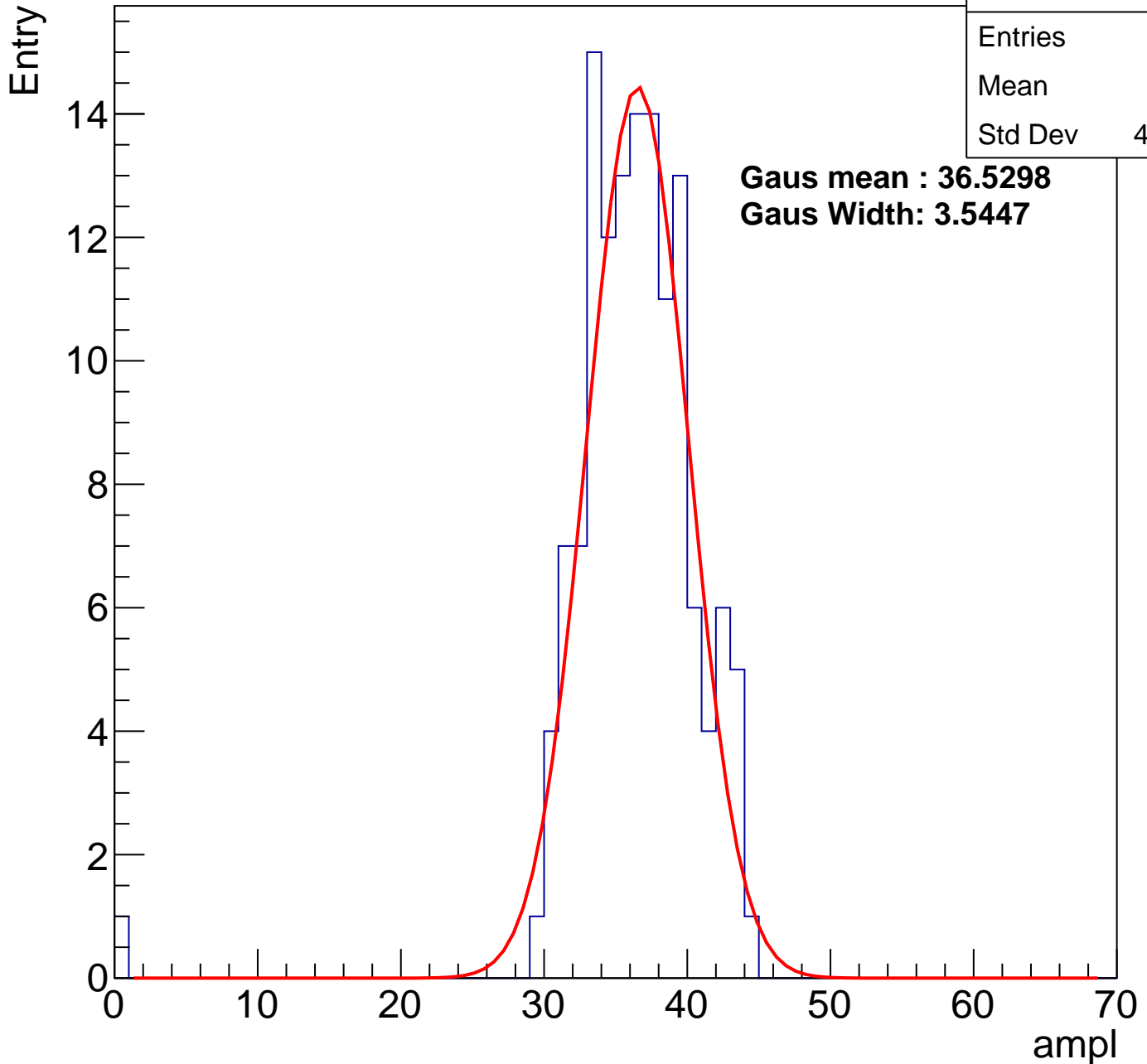


B0L101S, U15-ch33, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	35.9
Std Dev	4.625

Gaus mean : 36.5298
Gaus Width: 3.5447



B0L101S, U15-ch33, adc2

calib_packv5_042523_0143.root, FC#1, port C1

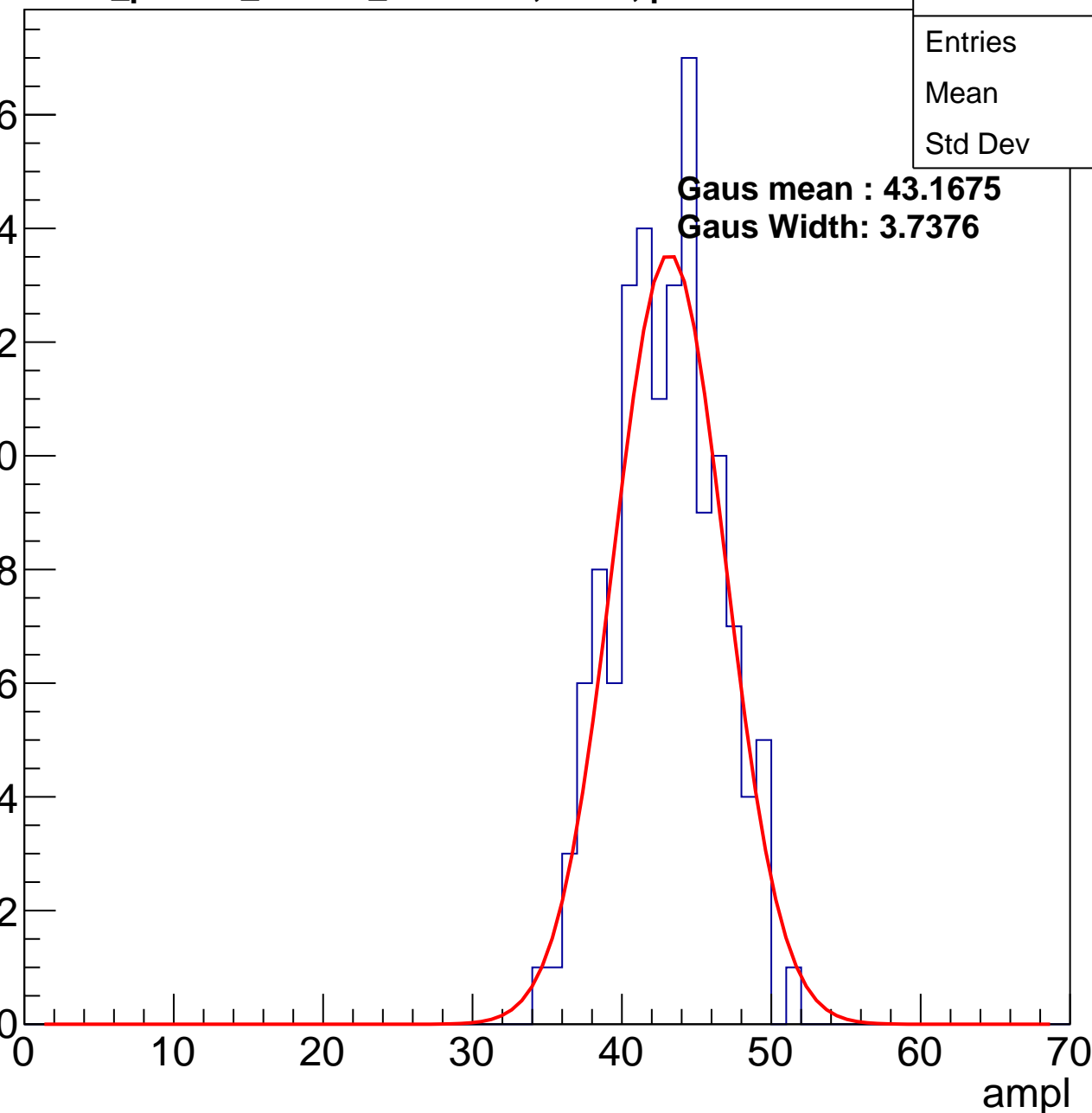
Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	42.5
Std Dev	3.471

Gaus mean : 43.1675

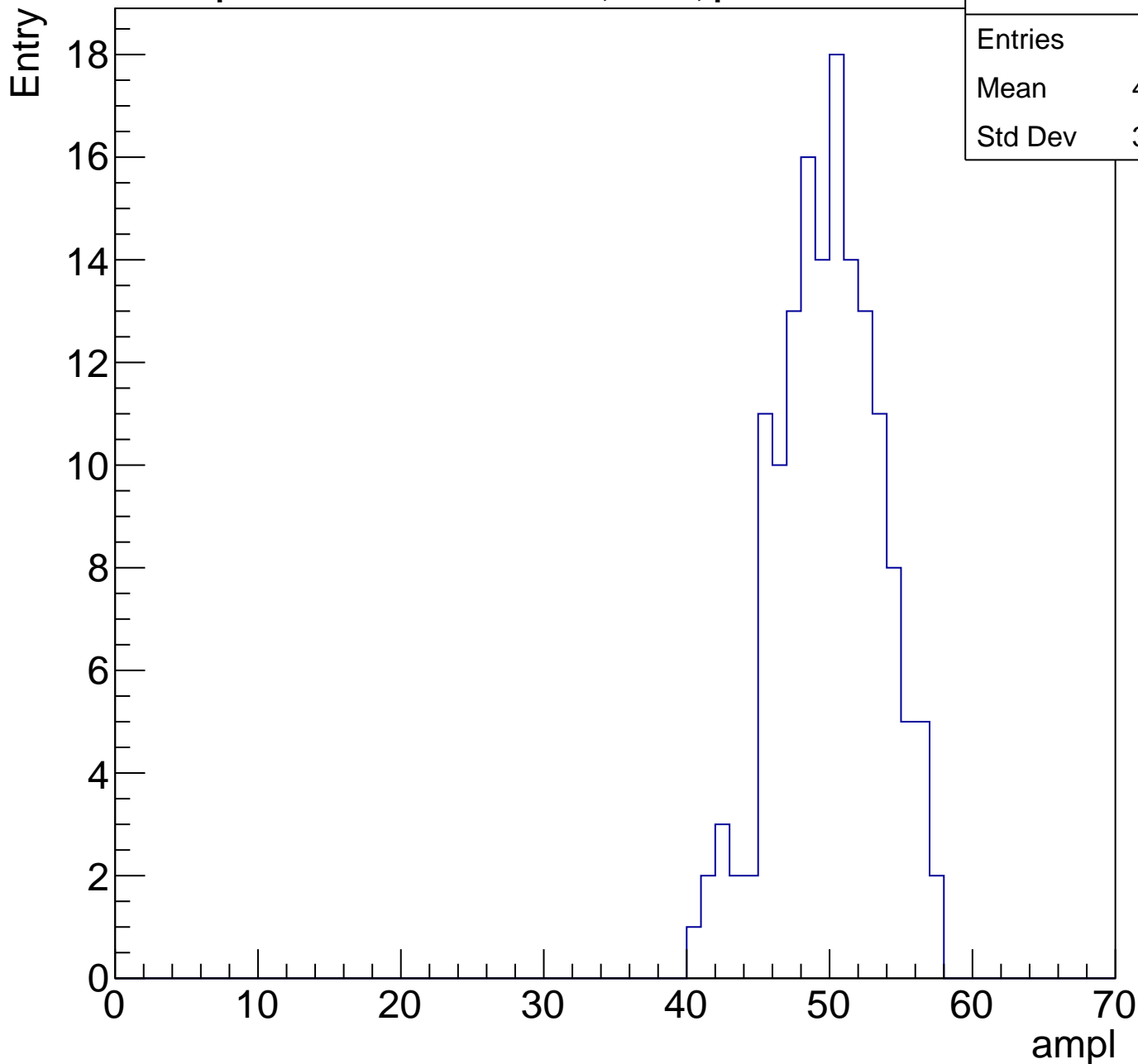
Gaus Width: 3.7376



B0L101S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	49.44
Std Dev	3.562

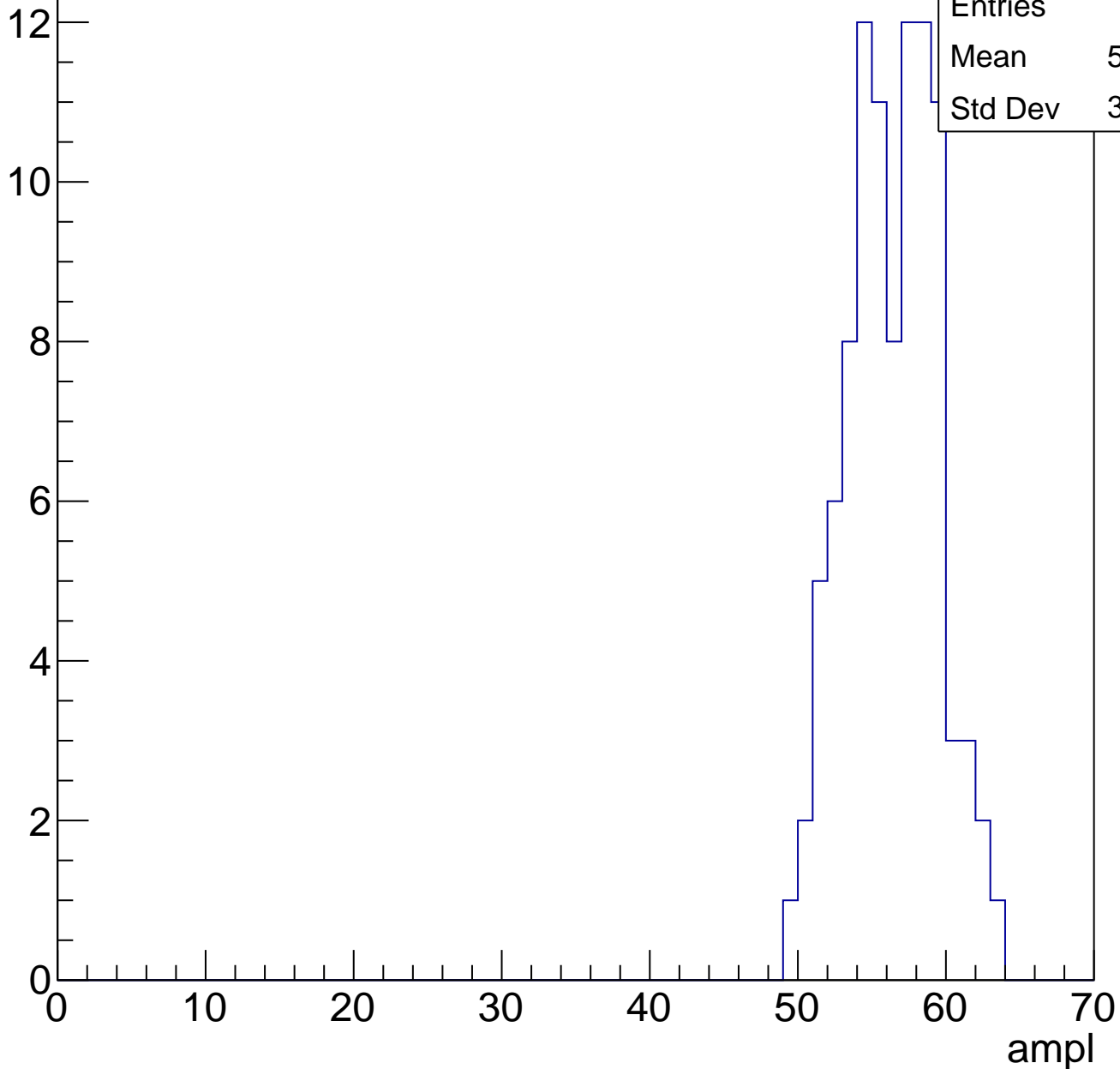


B0L101S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	97
Mean	55.88
Std Dev	3.003

Entry



B0L101S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#1, port C1

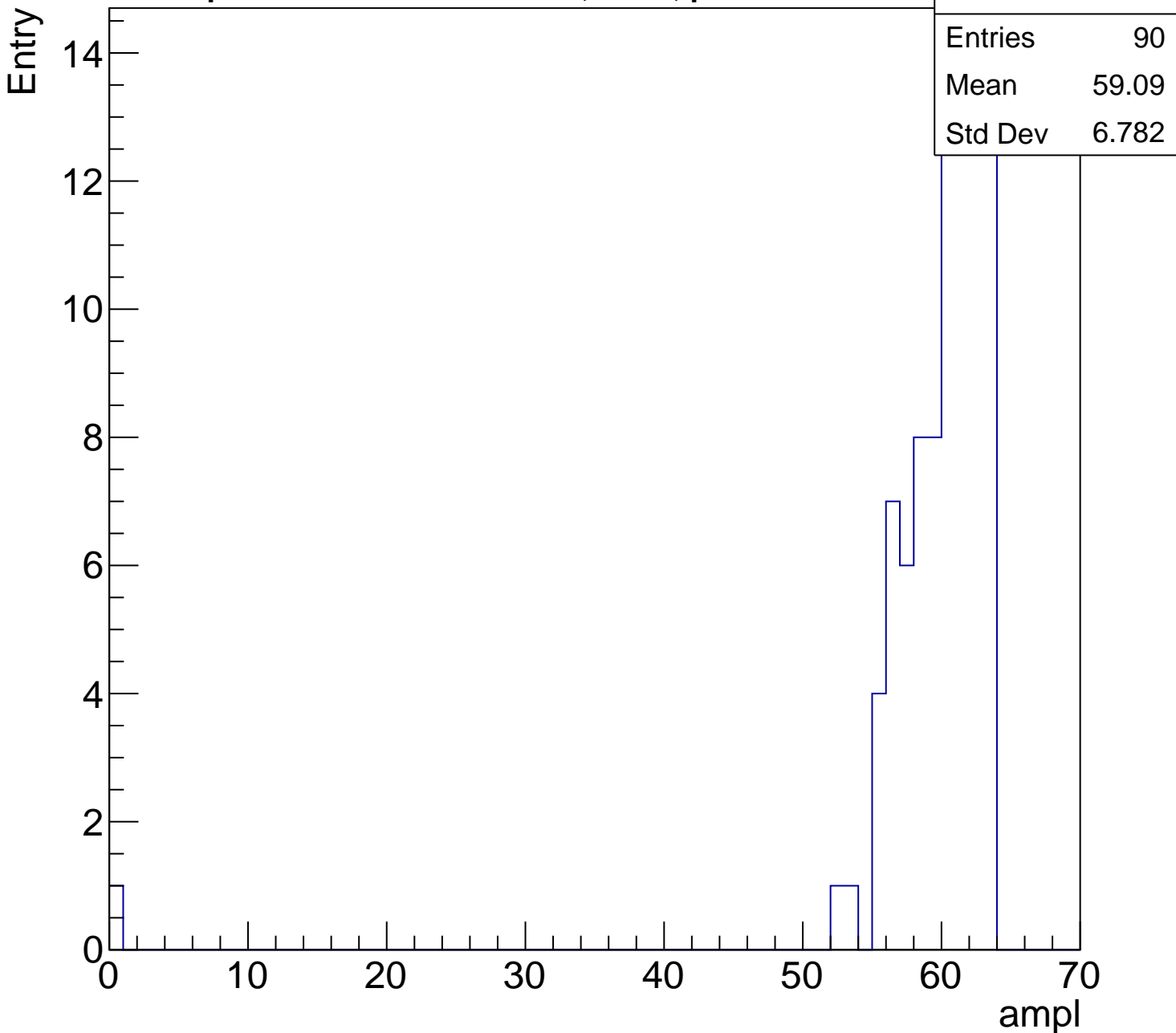
Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	59.09
Std Dev	6.782

ampl

0 10 20 30 40 50 60 70

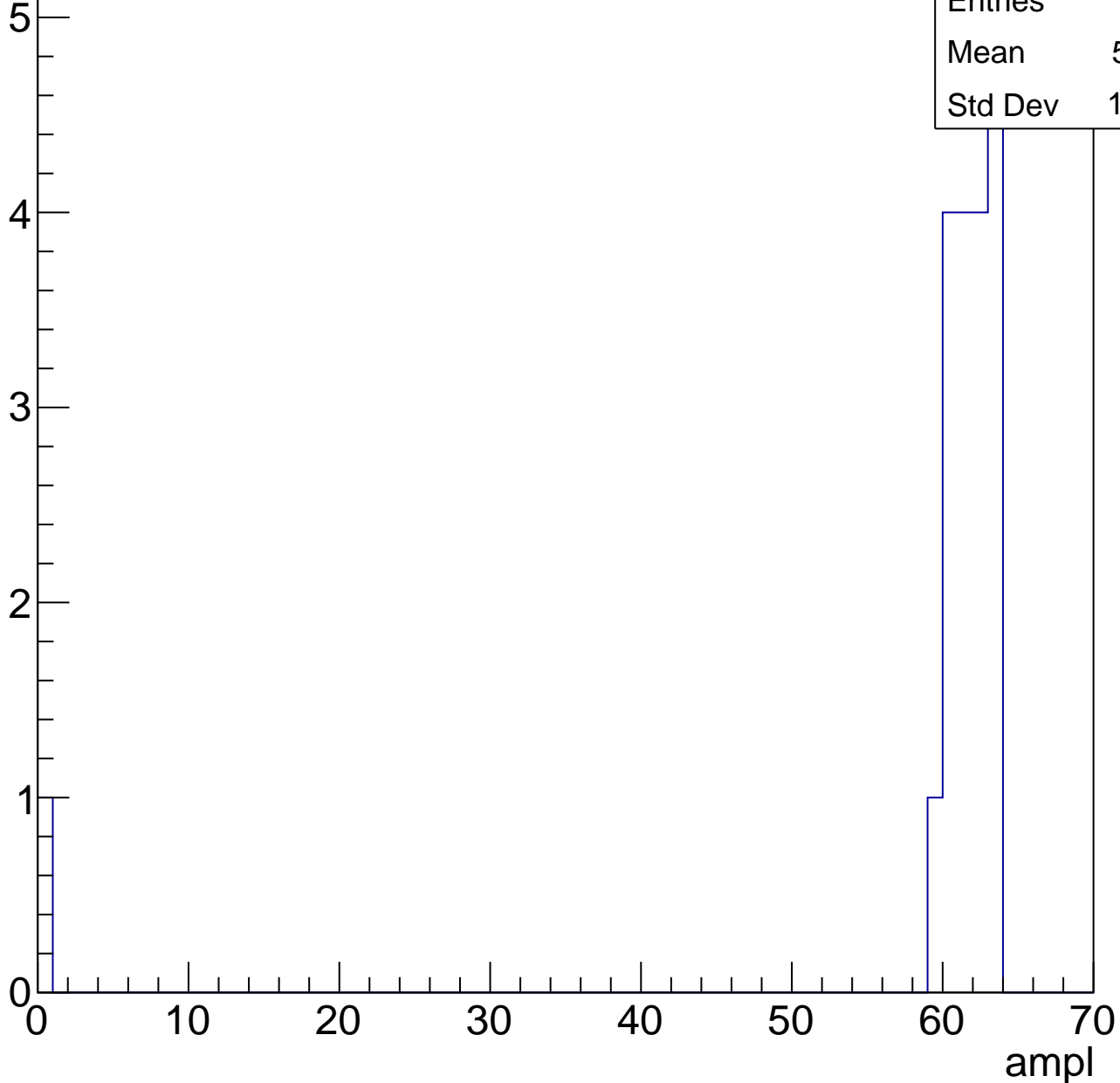


B0L101S, U15-ch33, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	19
Mean	58.21
Std Dev	13.77



B0L101S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch34, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	162
Mean	29.81
Std Dev	3.761

Gaus mean : 30.3398

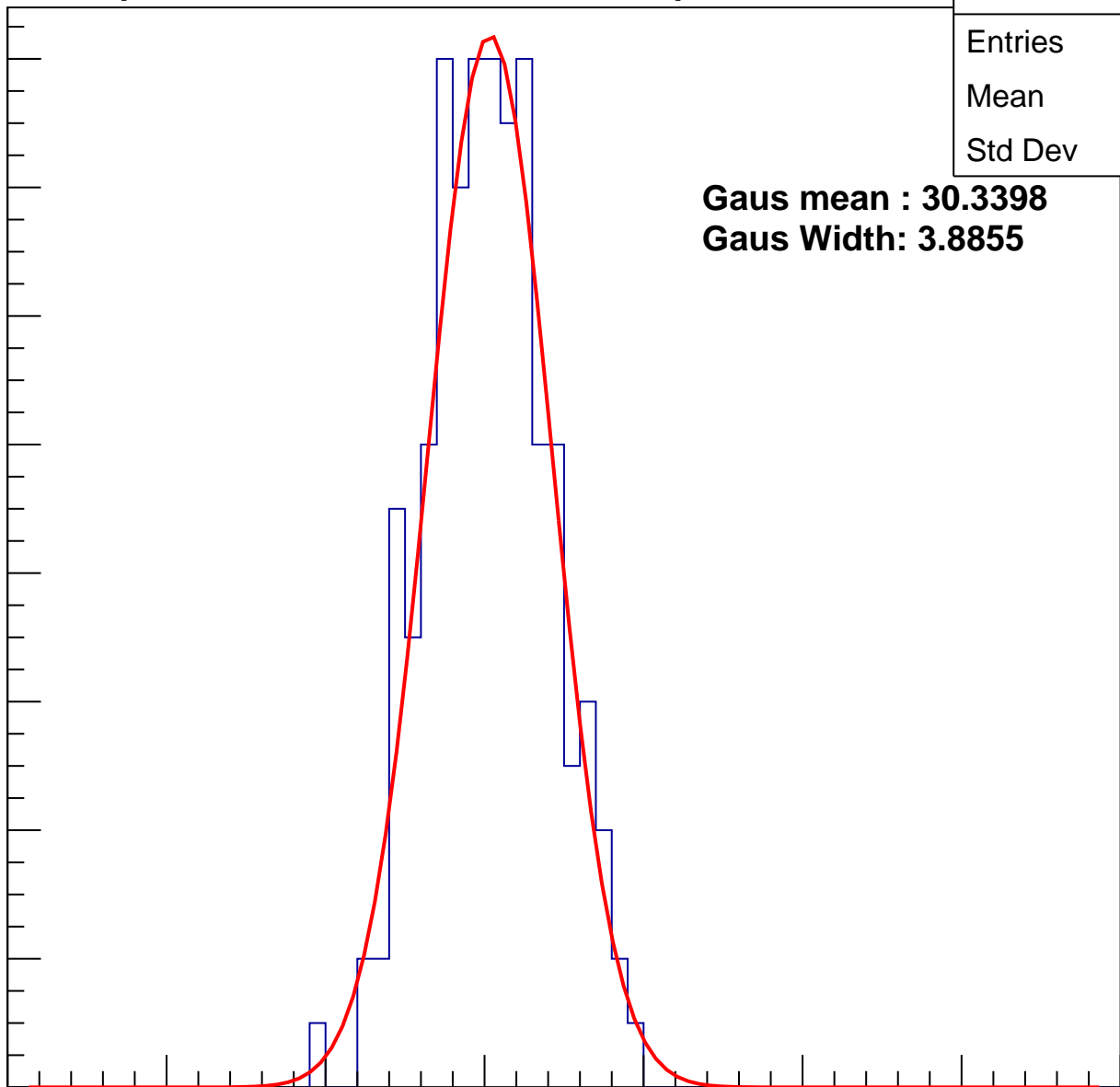
Gaus Width: 3.8855

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch34, adc1

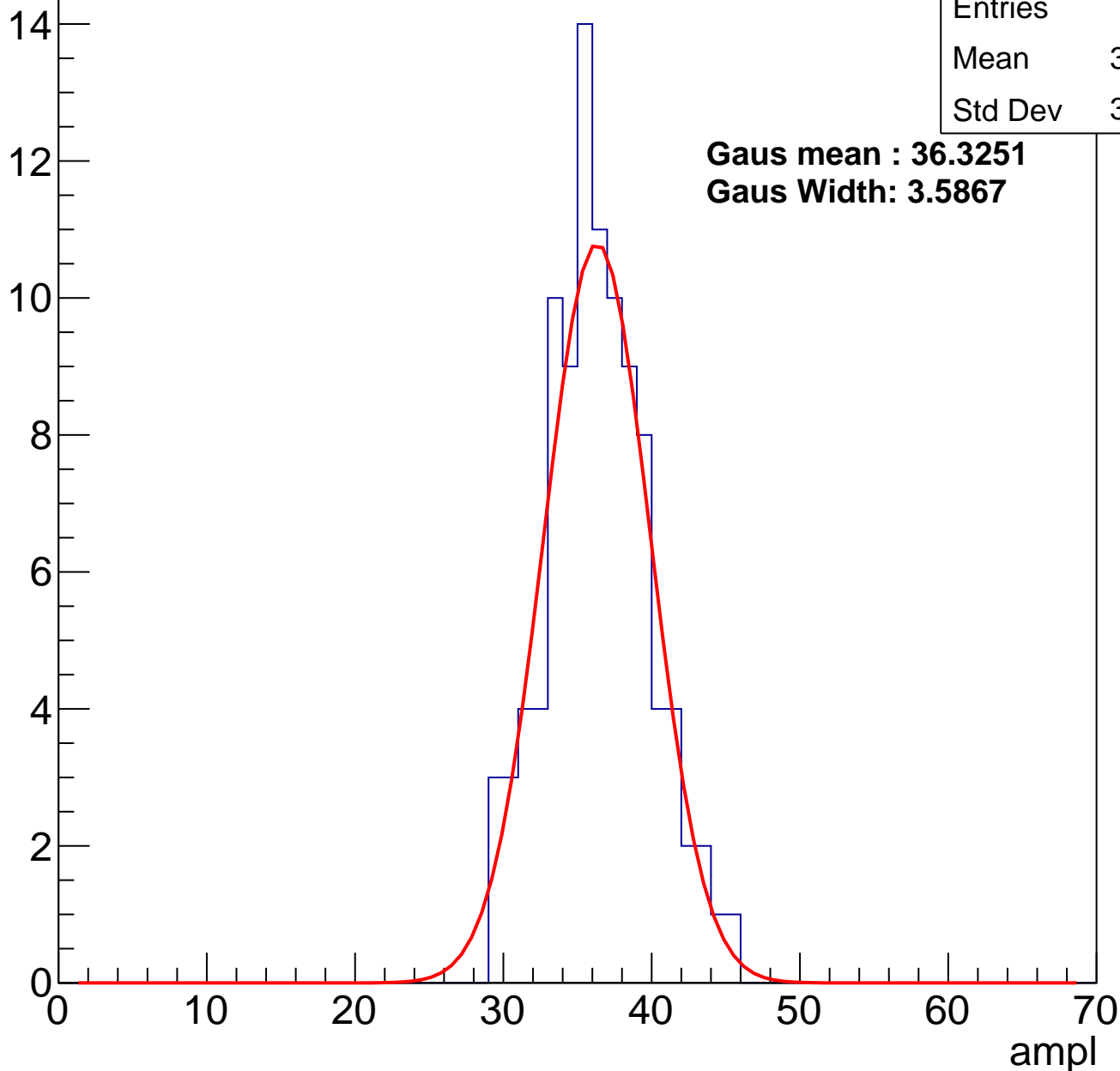
calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	35.94
Std Dev	3.417

Gaus mean : 36.3251

Gaus Width: 3.5867

Entry



B0L101S, U15-ch34, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	42.43
Std Dev	3.753

Gaus mean : 42.6669

Gaus Width: 3.5177

Entry

14

12

10

8

6

4

2

0

0

10

20

30

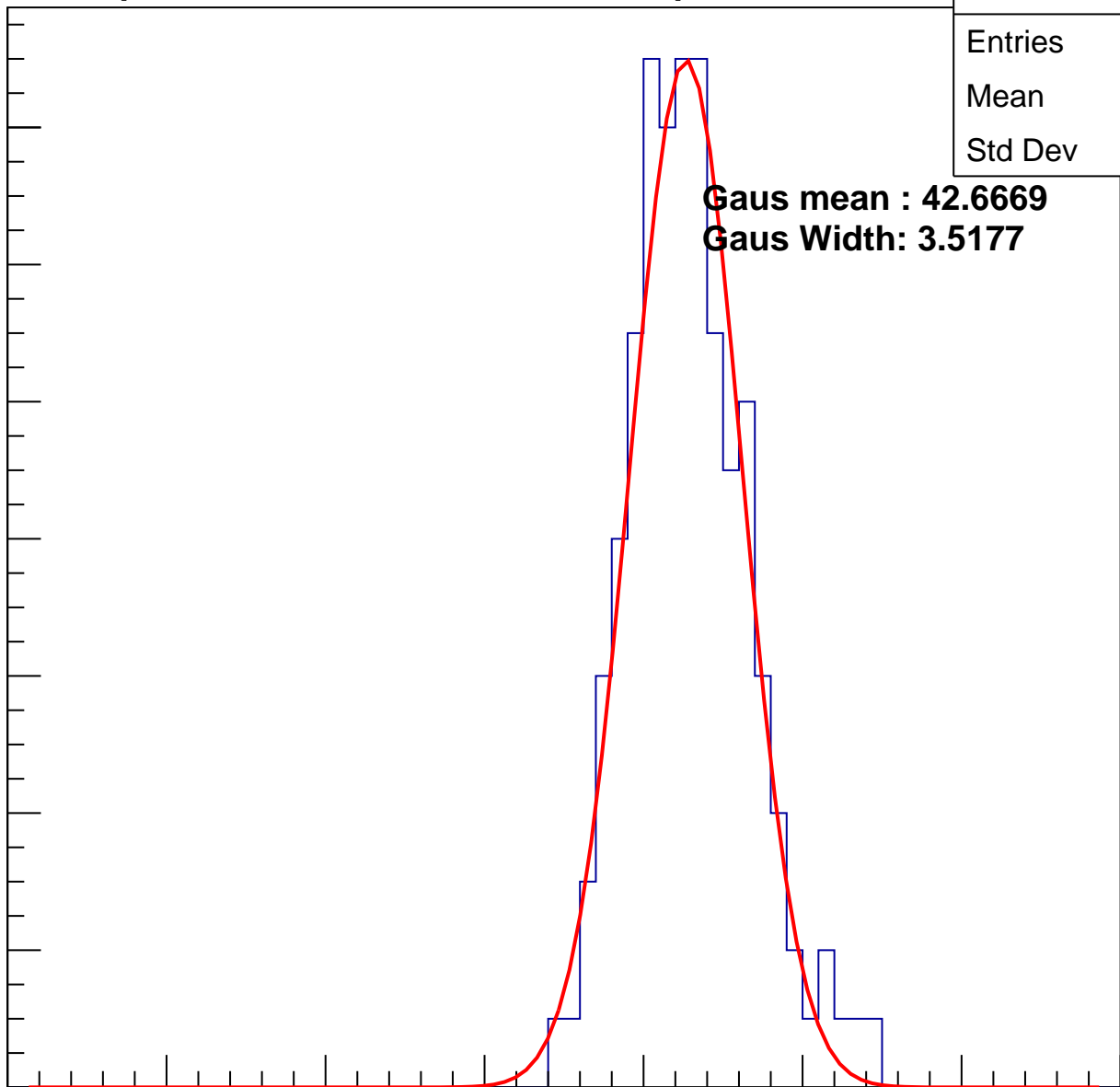
40

50

60

70

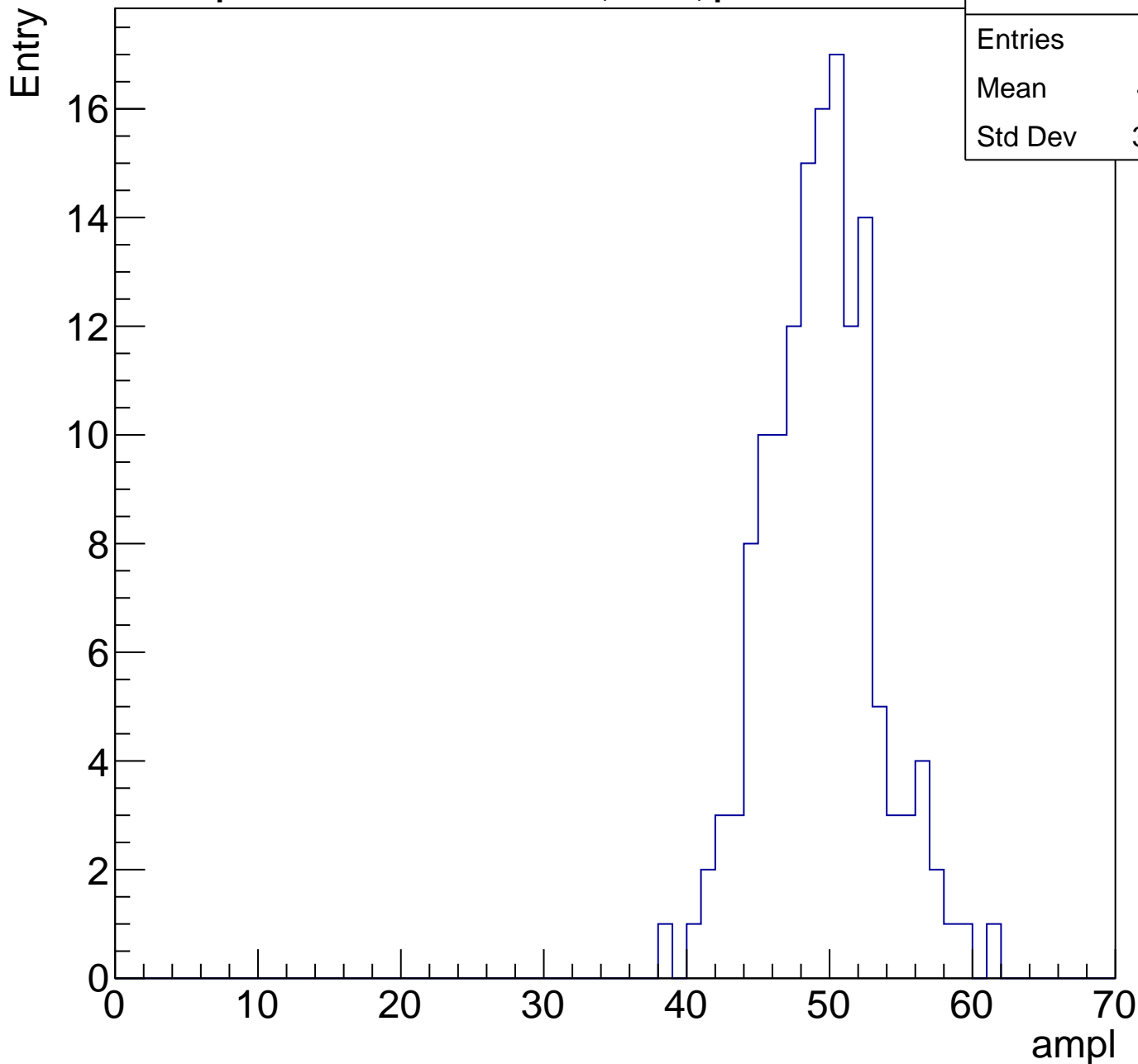
ampl



B0L101S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	48.91
Std Dev	3.909



B0L101S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#1, port C1

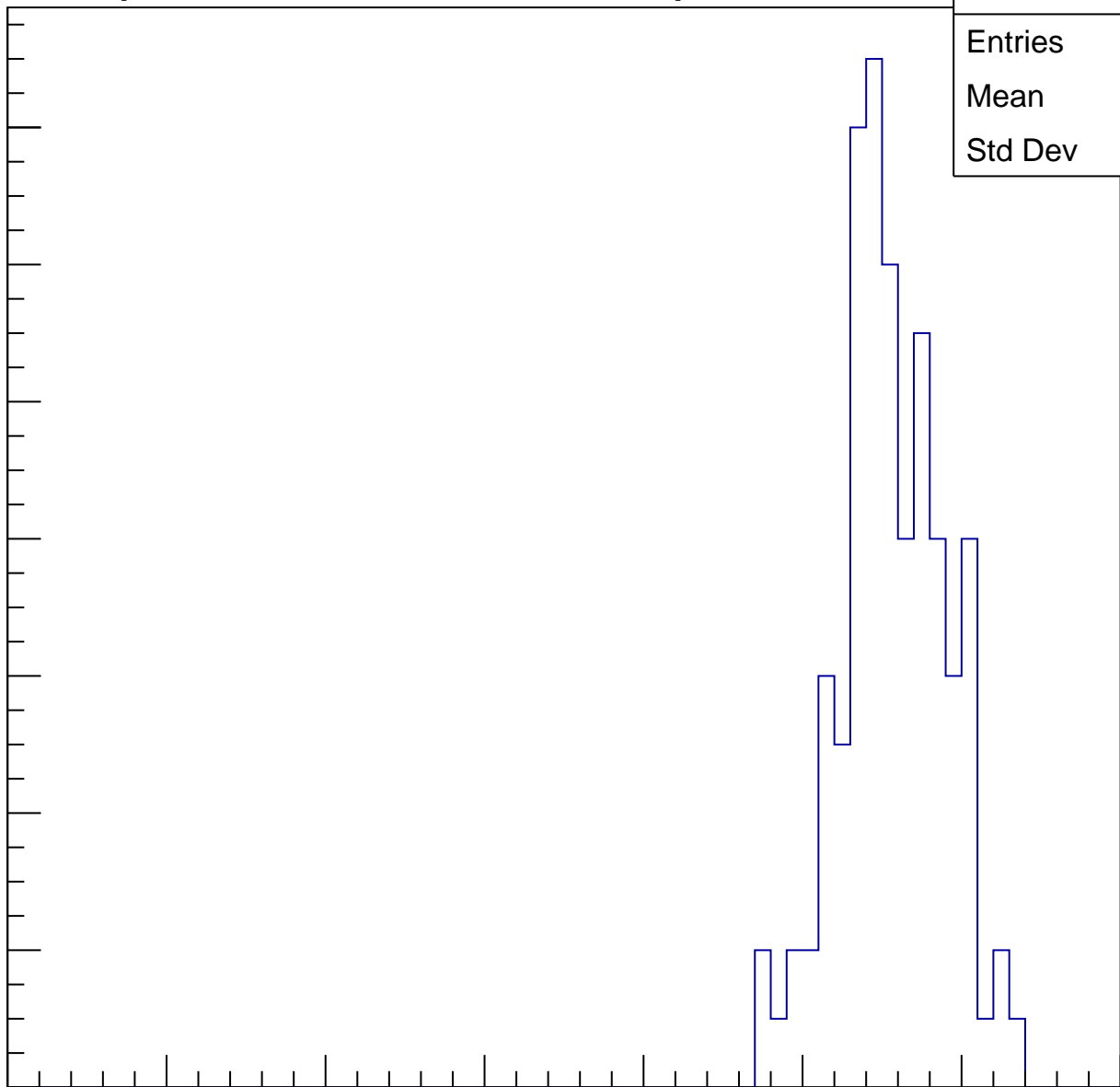
Entries	104
Mean	55.18
Std Dev	3.29

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch34, adc5

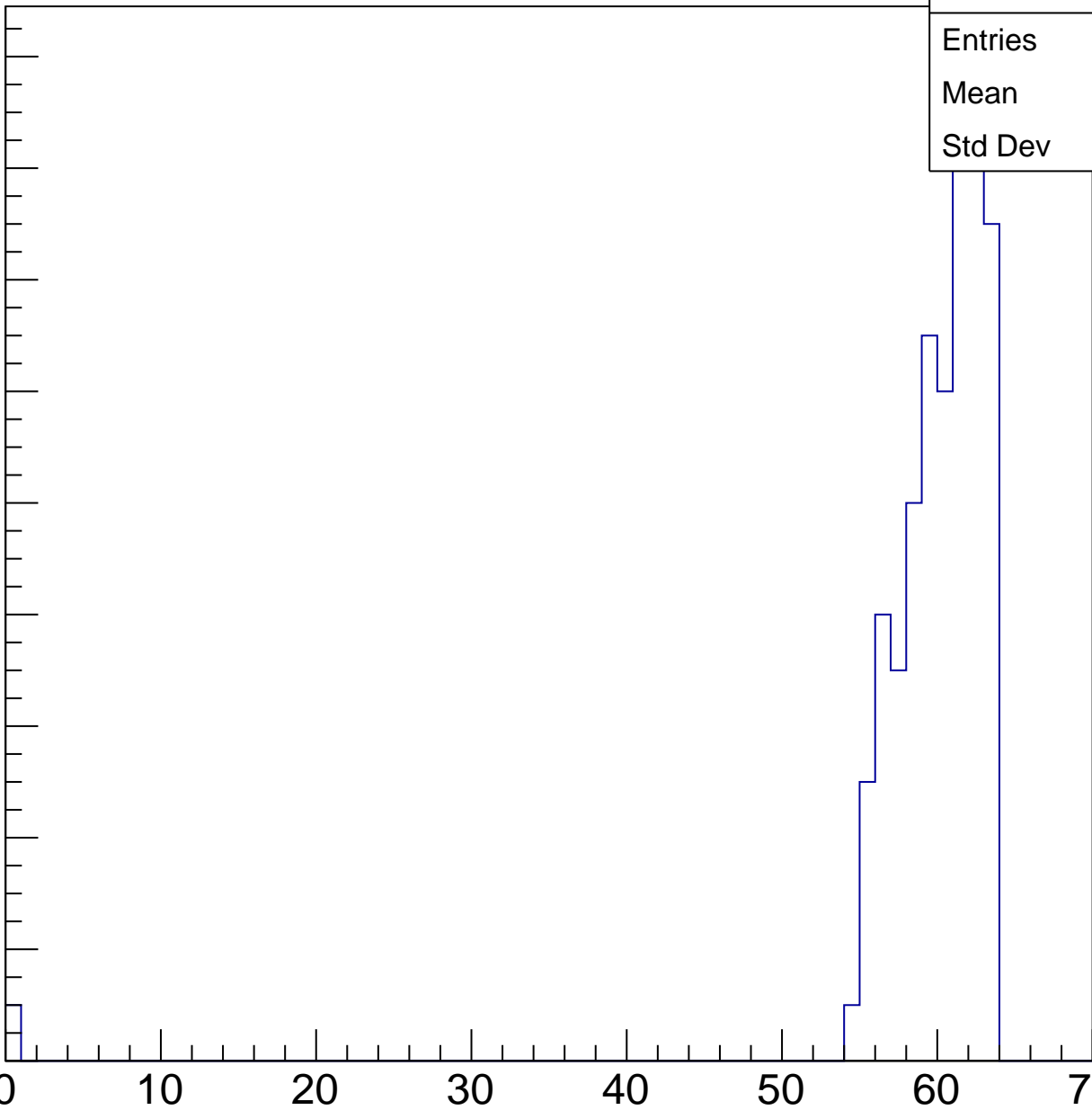
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	106
Mean	59.23
Std Dev	6.264

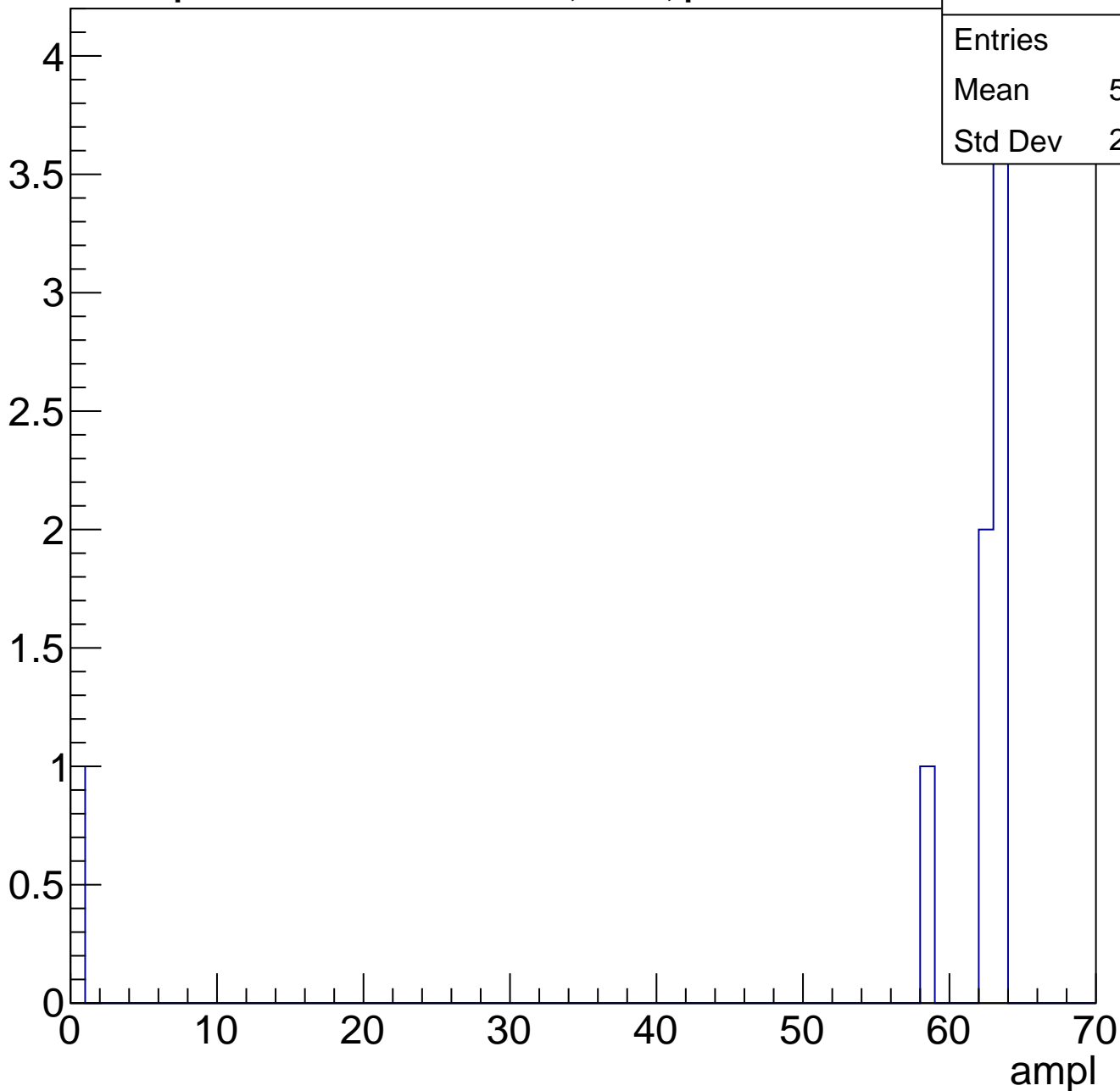
ampl



B0L101S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch35, adc0

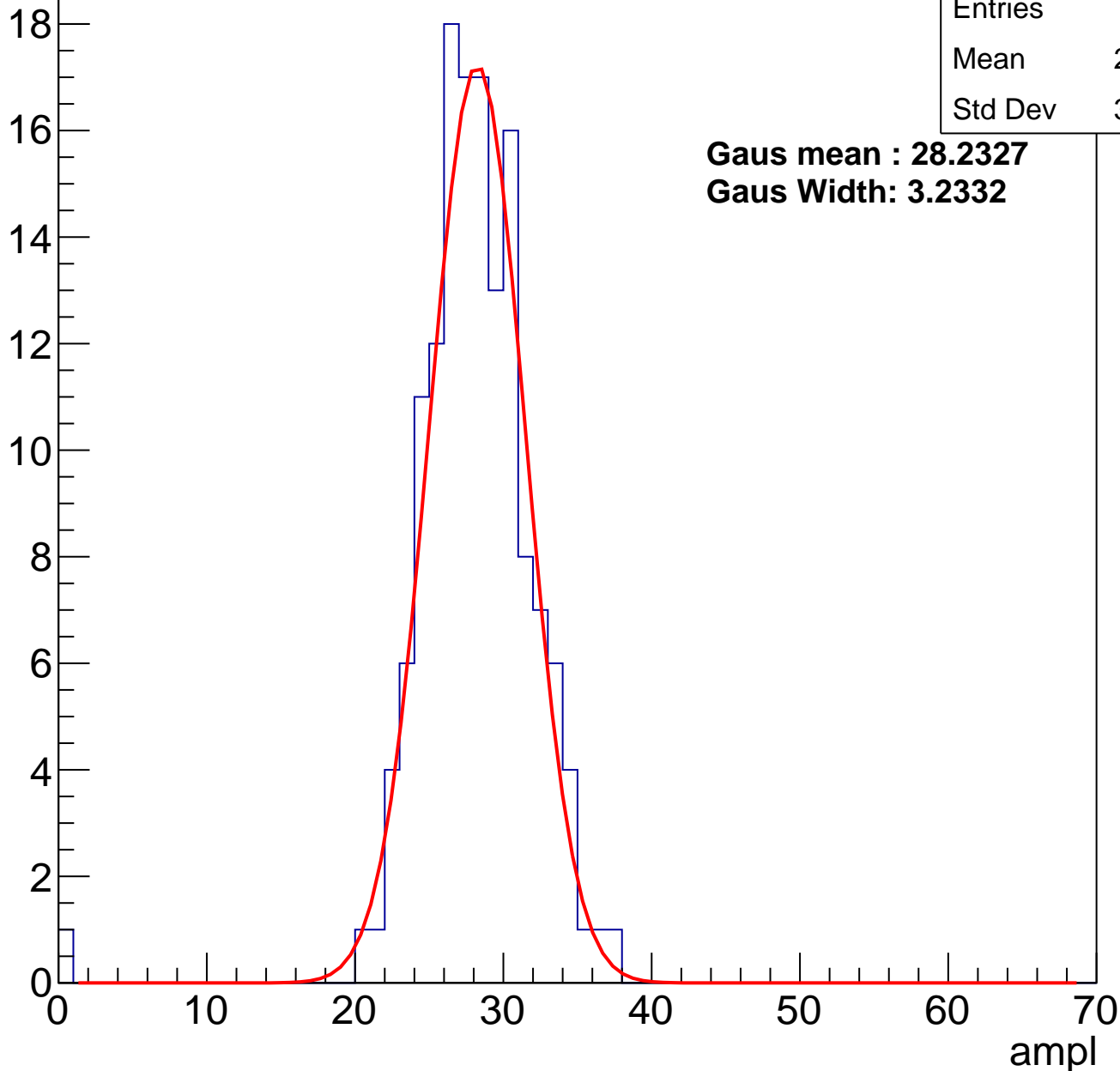
calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	27.62
Std Dev	3.976

Gaus mean : 28.2327

Gaus Width: 3.2332

Entry



B0L101S, U15-ch35, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	34.29
Std Dev	3.431

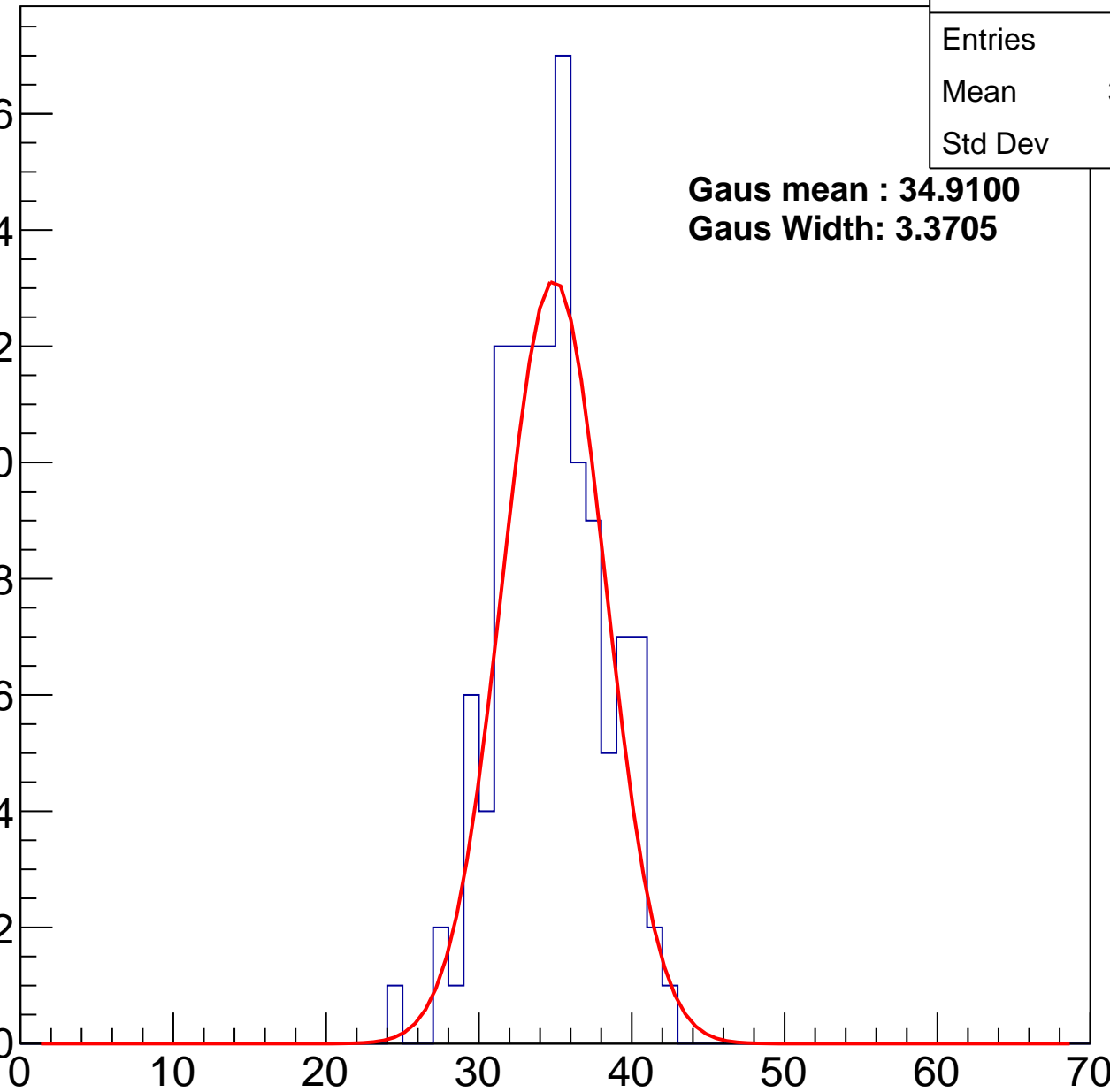
Gaus mean : 34.9100

Gaus Width: 3.3705

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch35, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

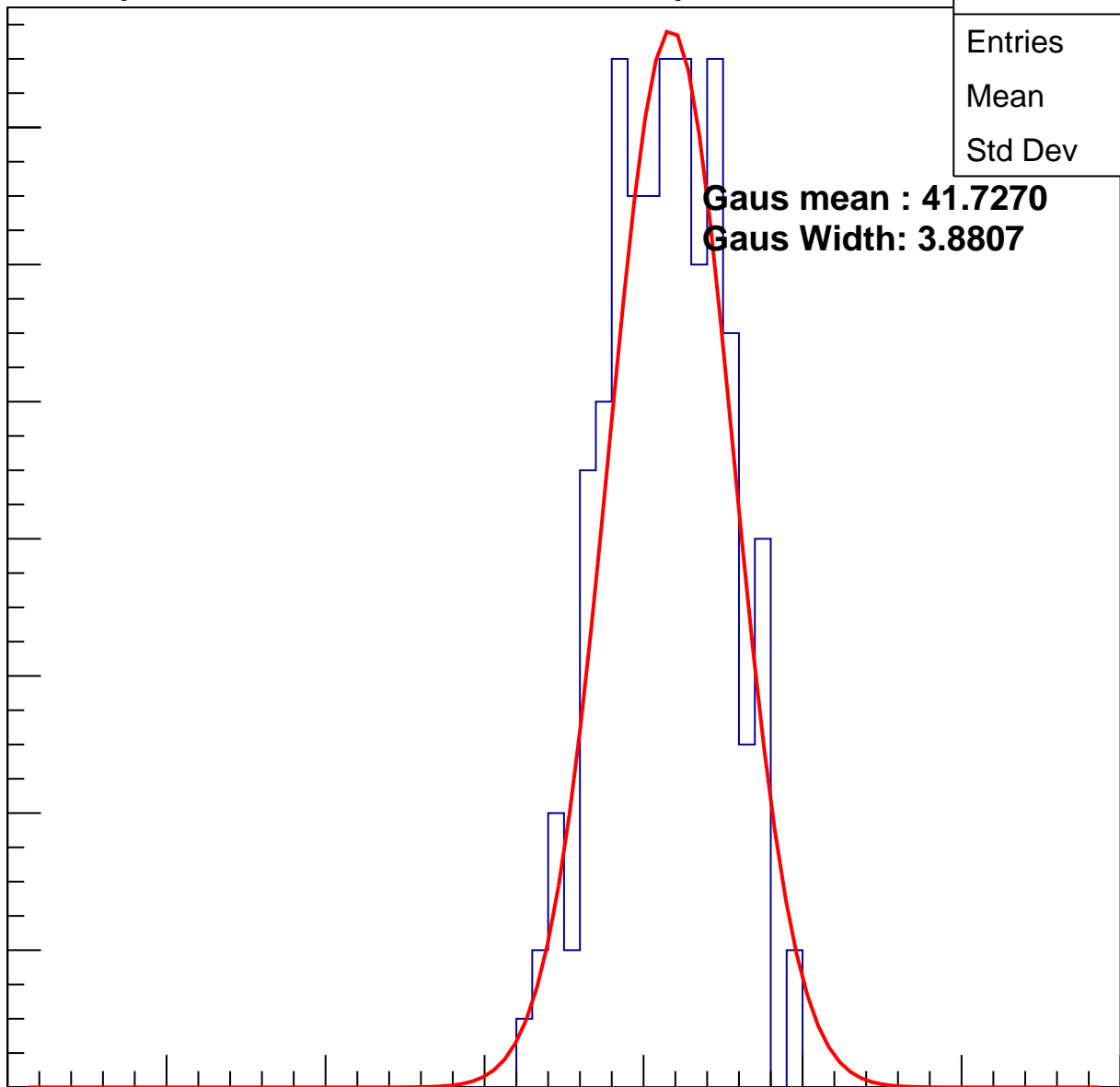
70

ampl

Entries	152
Mean	40.89
Std Dev	3.605

Gaus mean : 41.7270

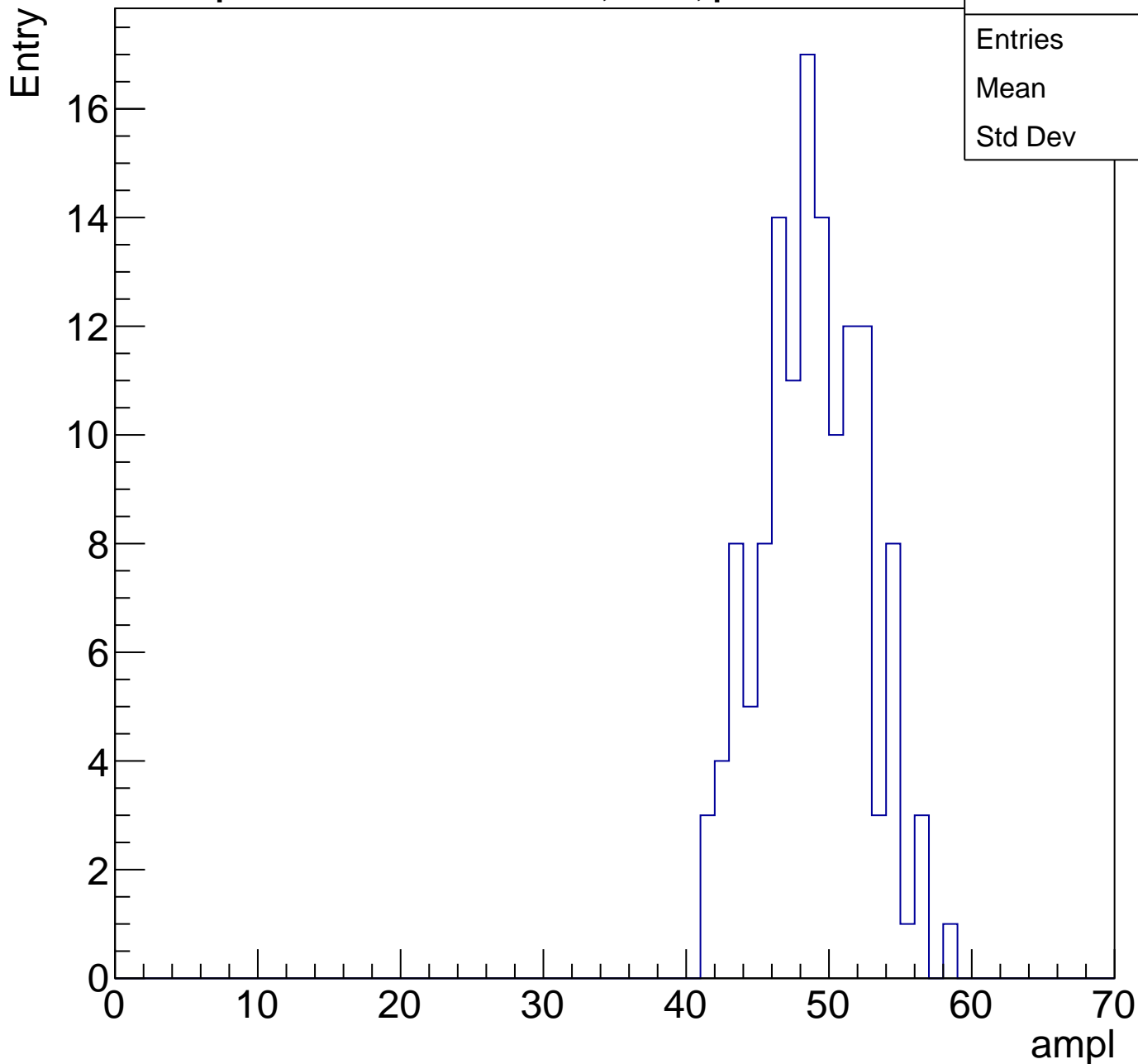
Gaus Width: 3.8807



B0L101S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	48.4
Std Dev	3.61

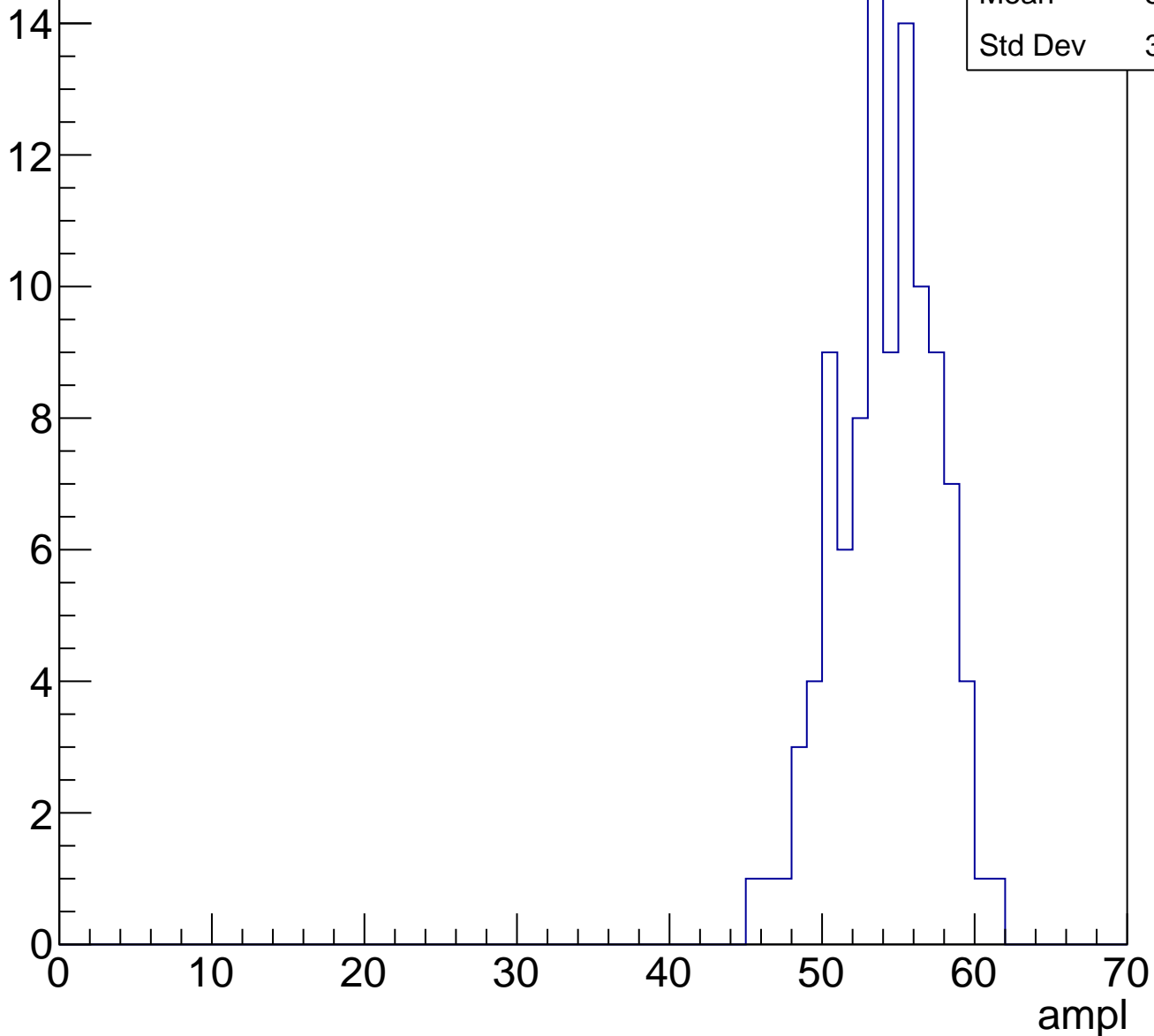


B0L101S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	53.76
Std Dev	3.228

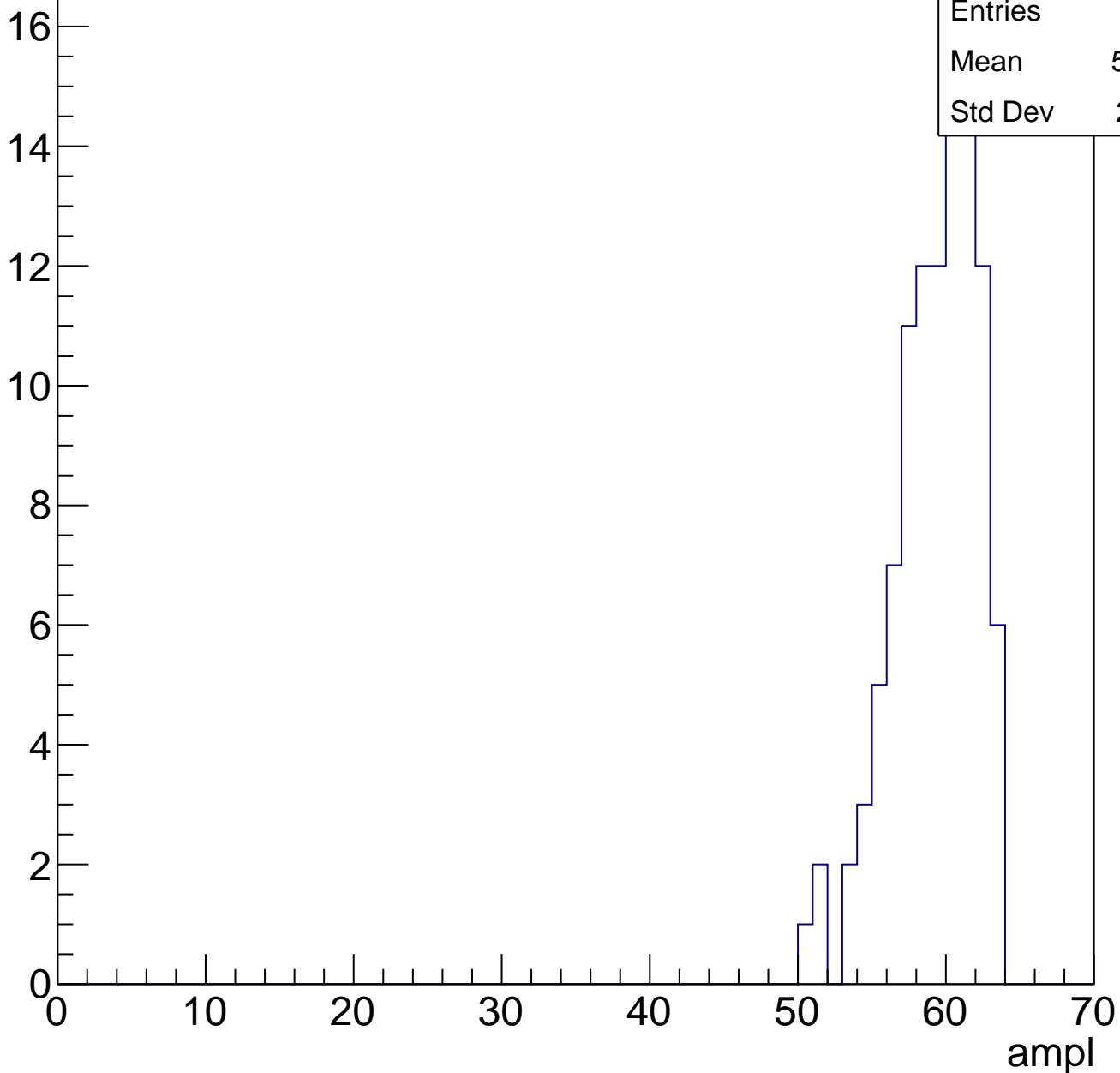
Entry



B0L101S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

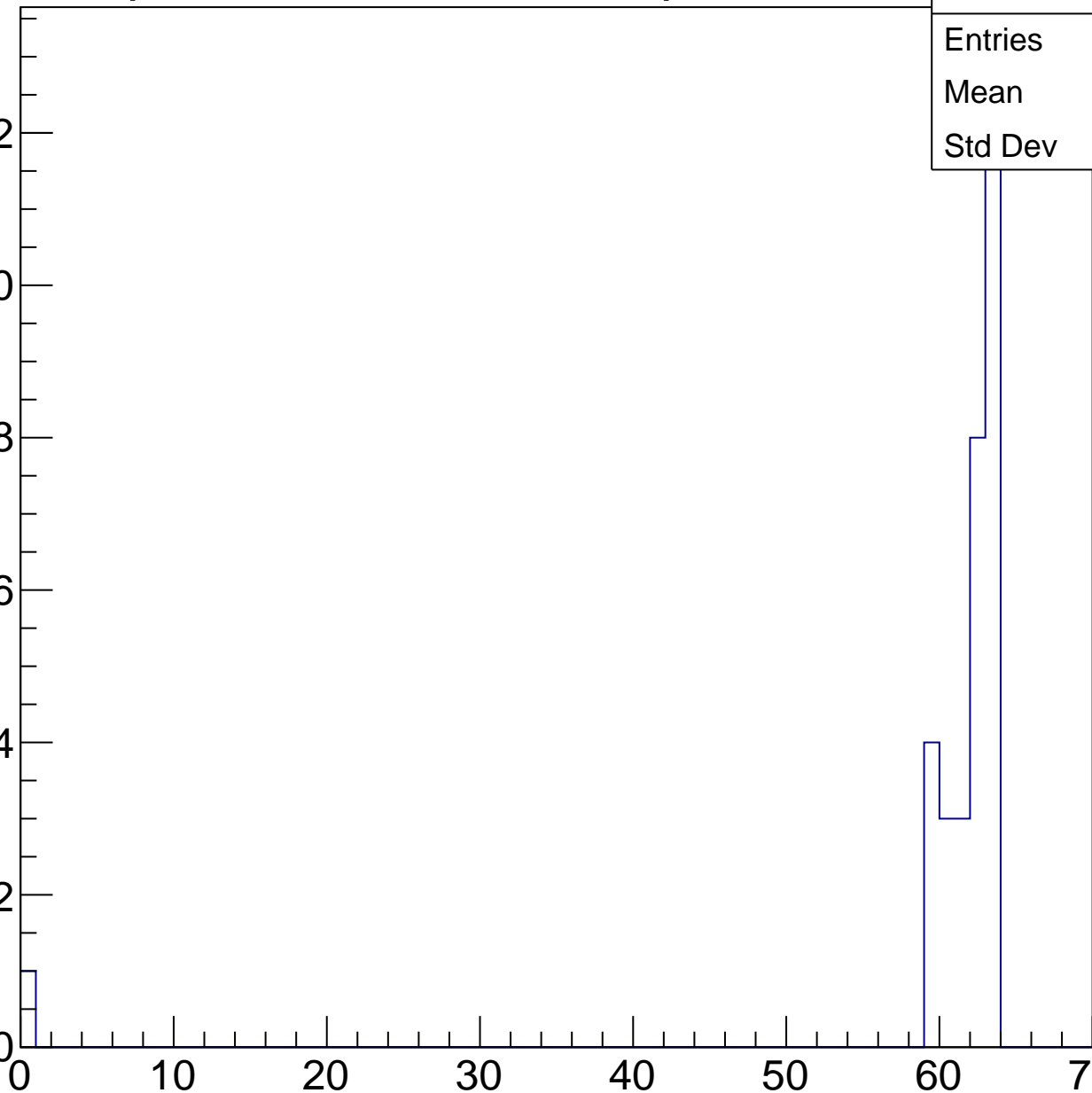
4

2

0

Entries	32
Mean	59.81
Std Dev	10.83

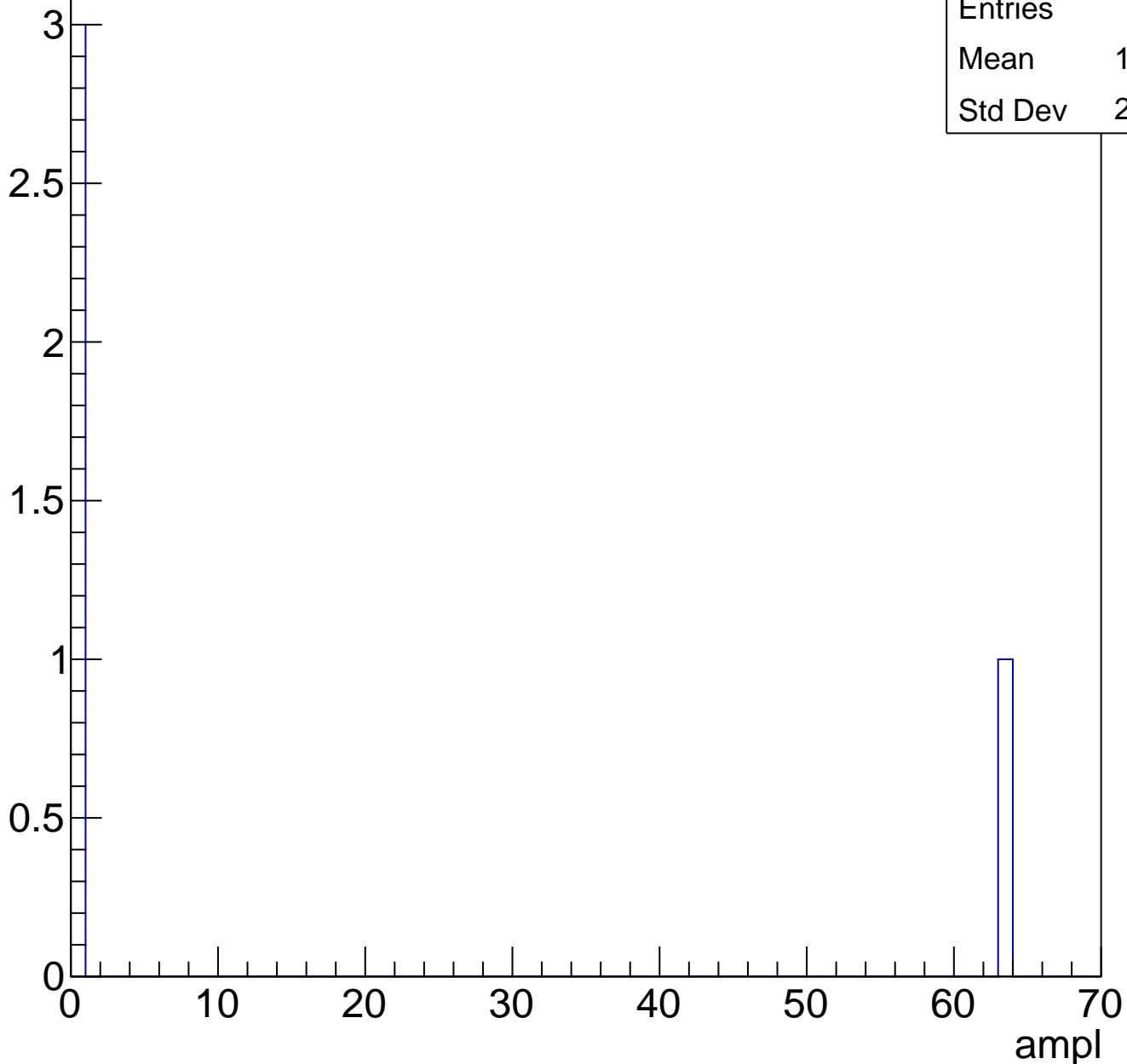
ampl



B0L101S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	15.75
Std Dev	27.28

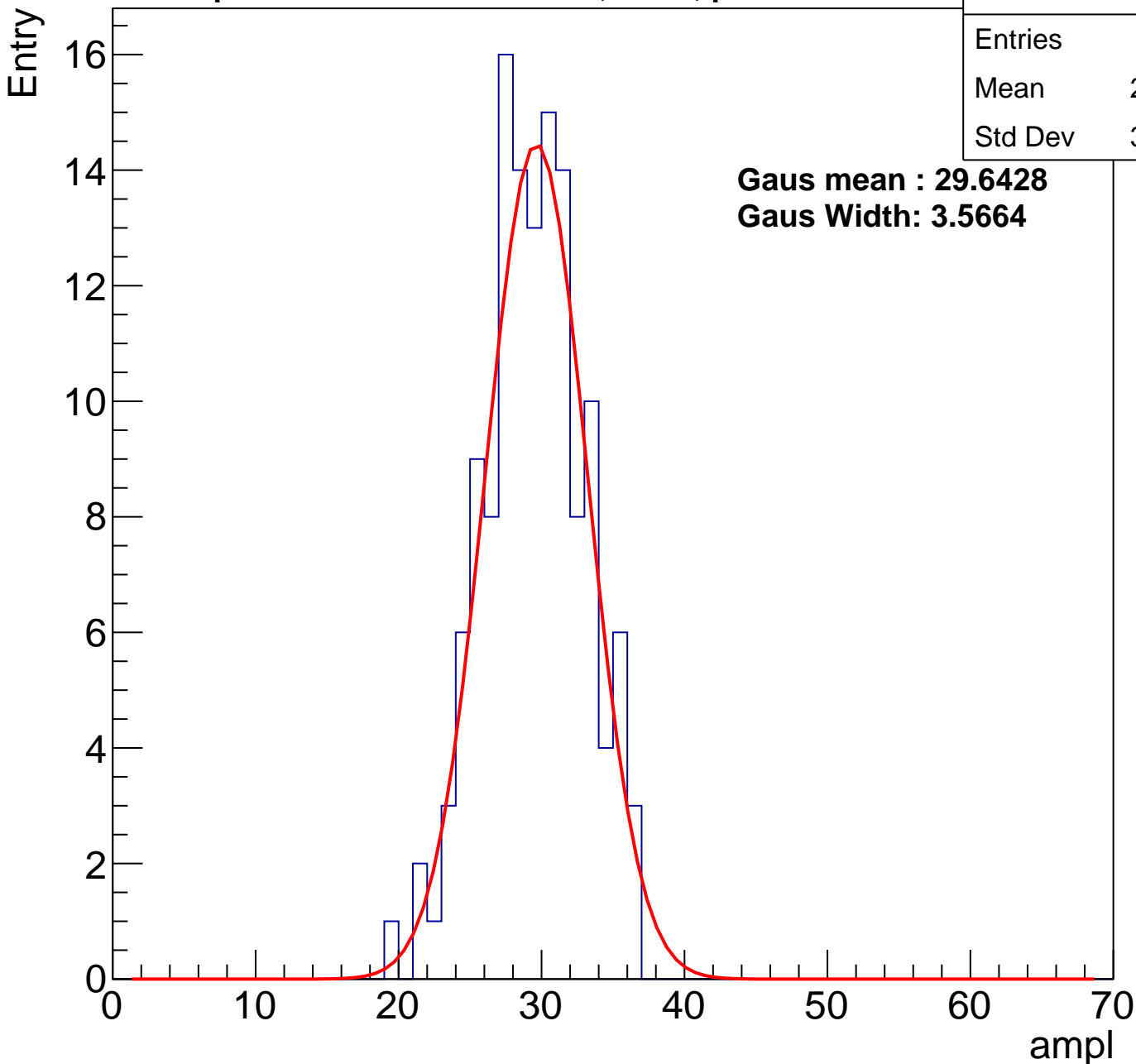
B0L101S, U15-ch36, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	28.98
Std Dev	3.463

Gaus mean : 29.6428

Gaus Width: 3.5664



B0L101S, U15-ch36, adc1

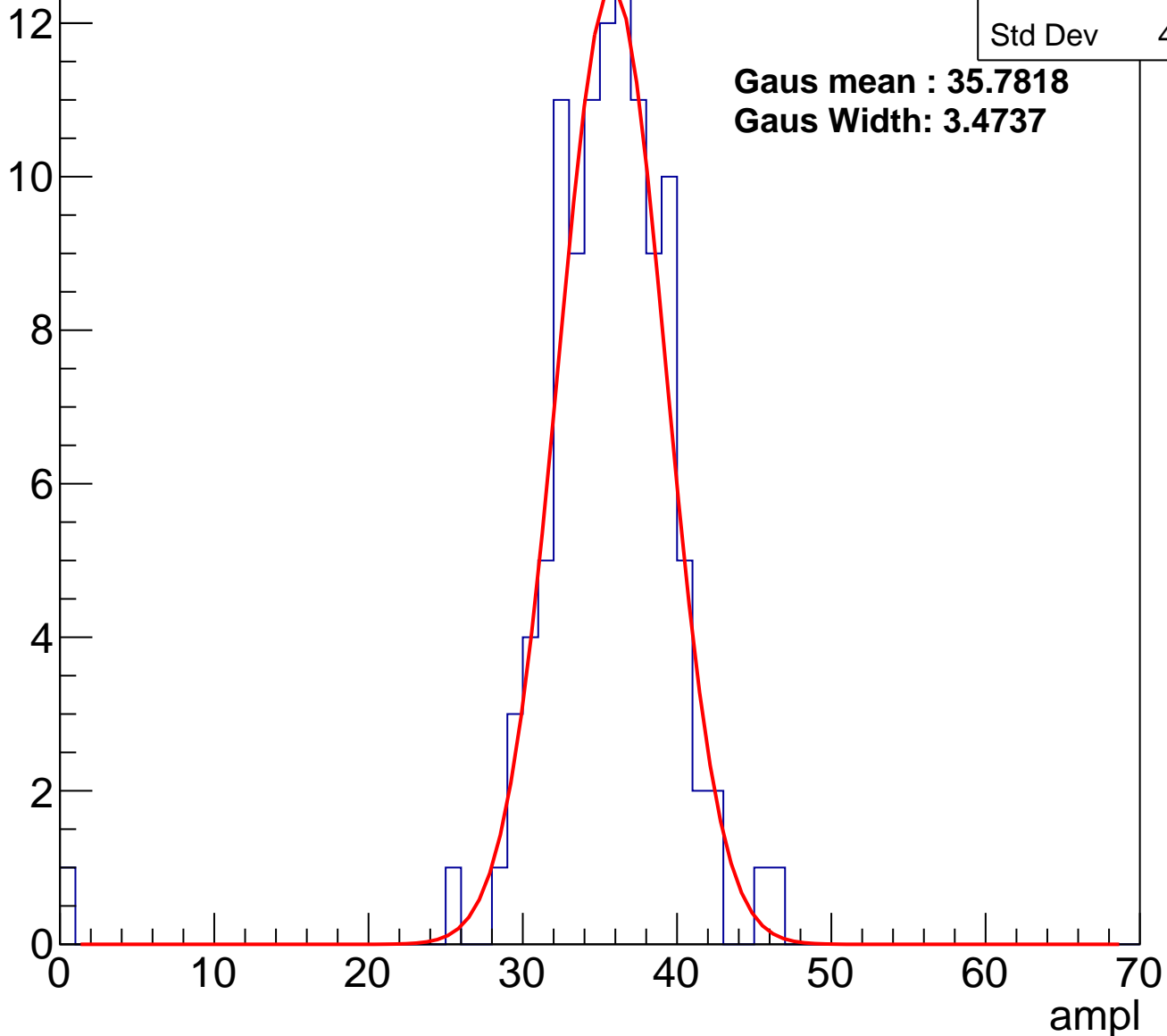
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	35.02
Std Dev	4.838

Gaus mean : 35.7818

Gaus Width: 3.4737

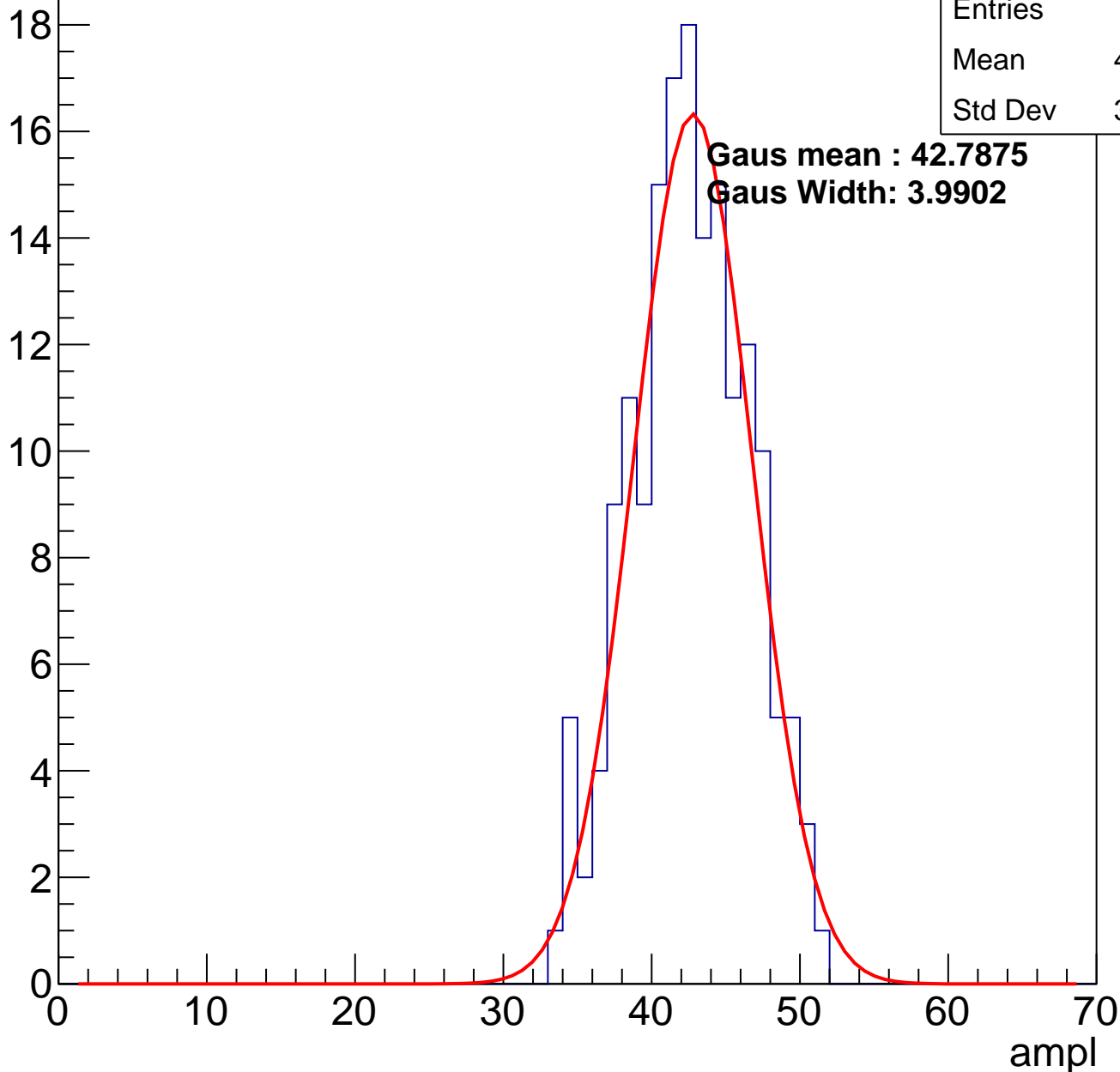
Entry



B0L101S, U15-ch36, adc2

calib_packv5_042523_0143.root, FC#1, port C1

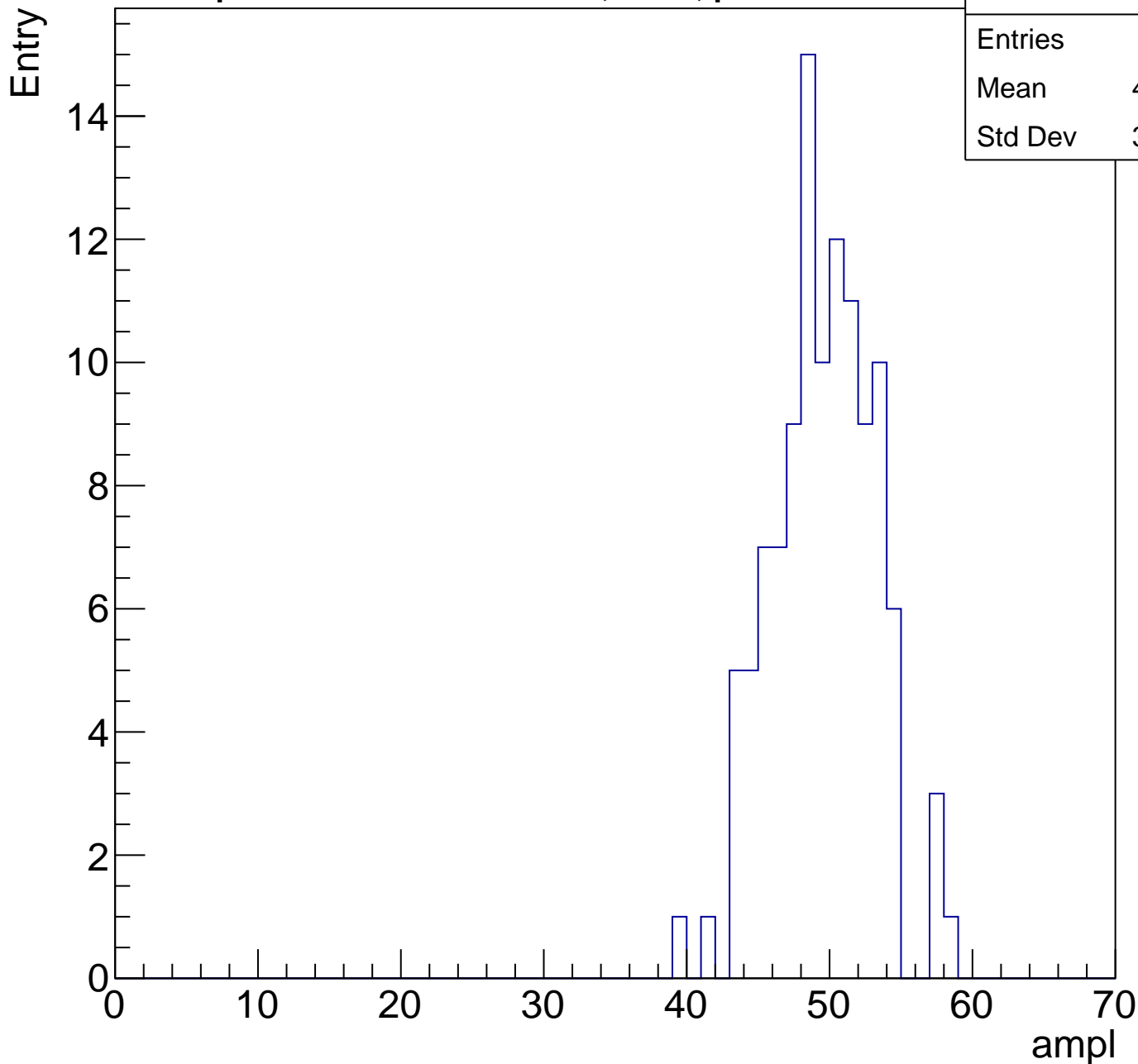
Entry



B0L101S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#1, port C1

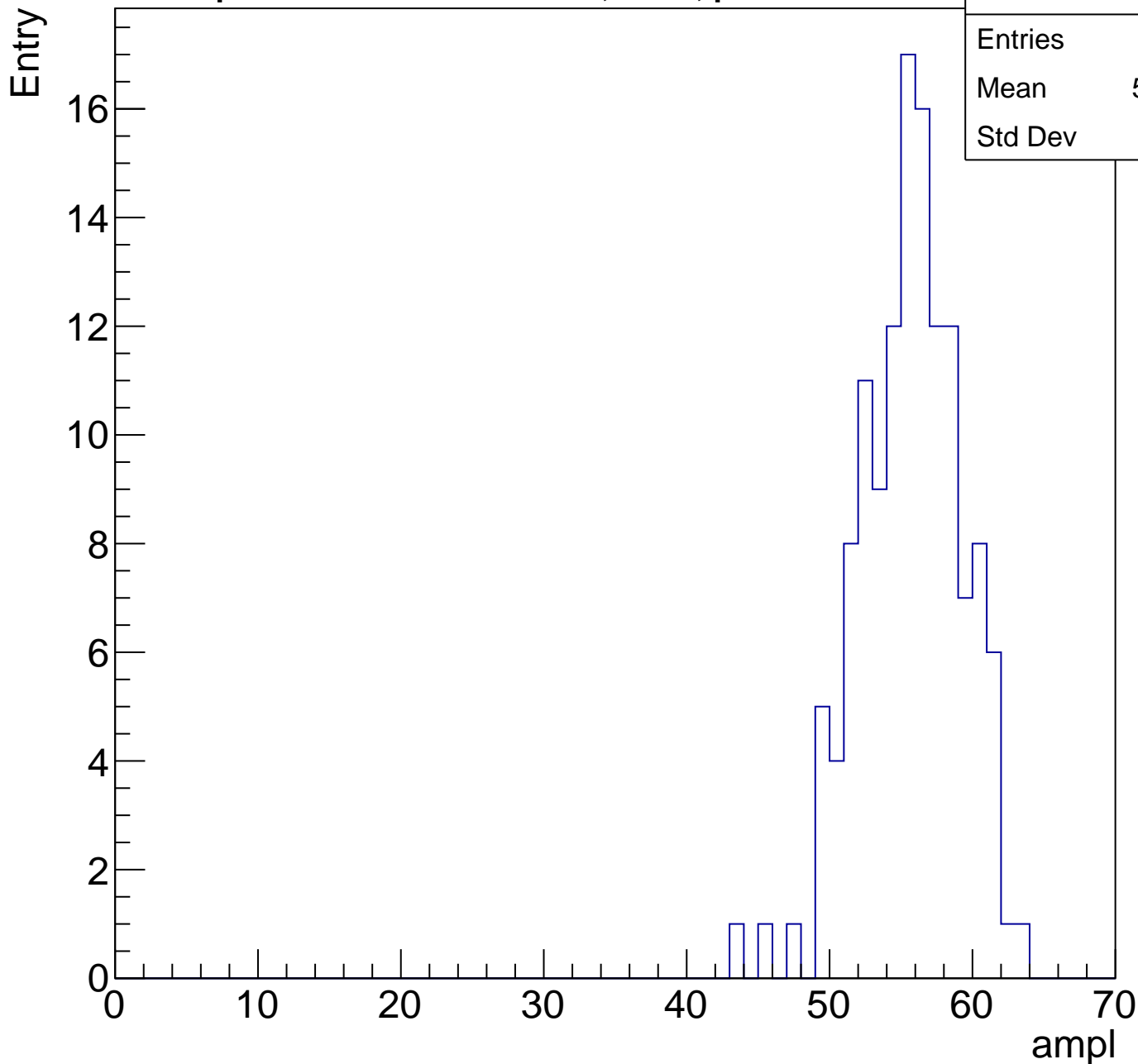
Entries	112
Mean	49.08
Std Dev	3.554



B0L101S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	55.15
Std Dev	3.56



B0L101S, U15-ch36, adc5

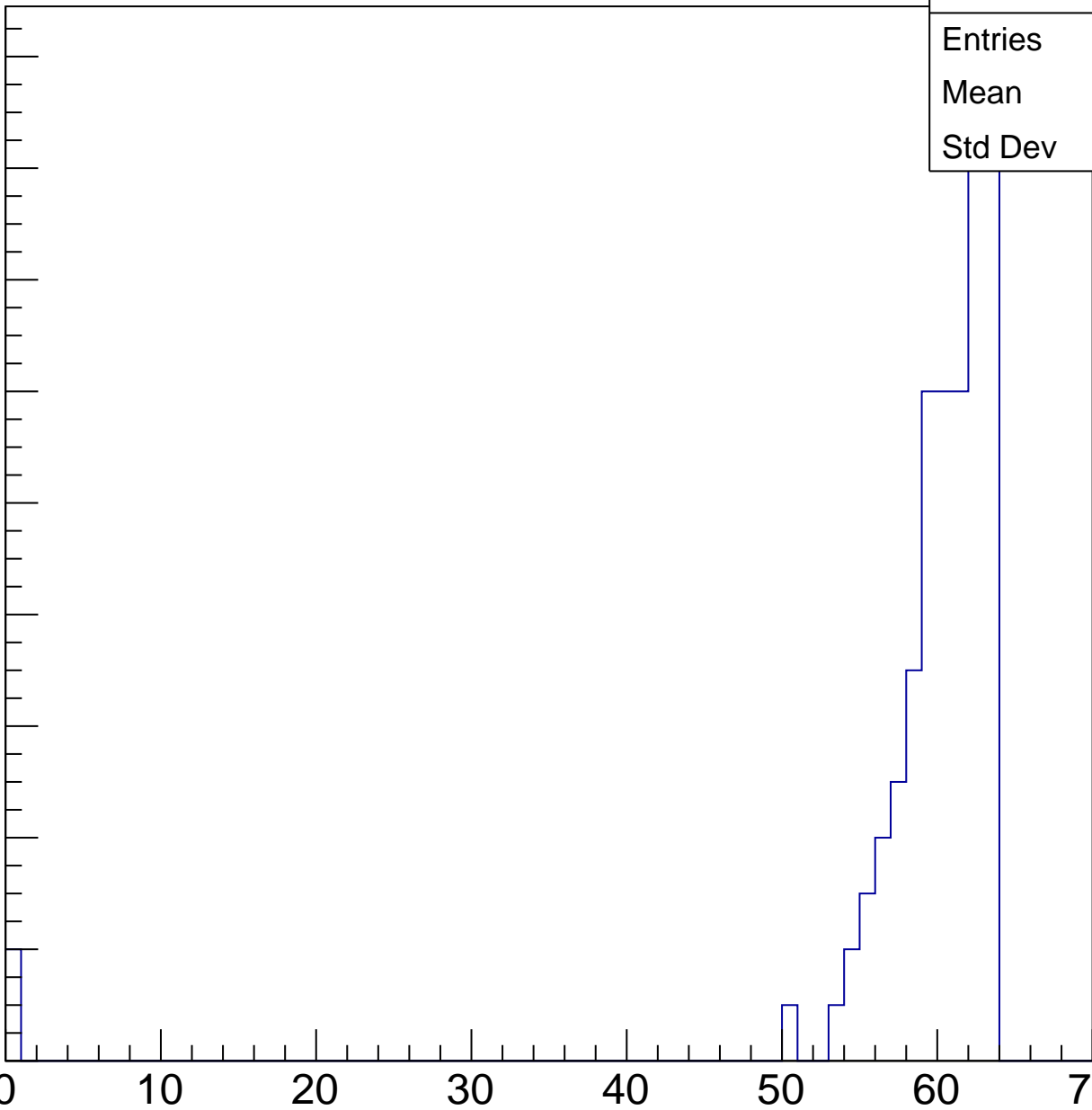
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	95
Mean	58.71
Std Dev	9.017

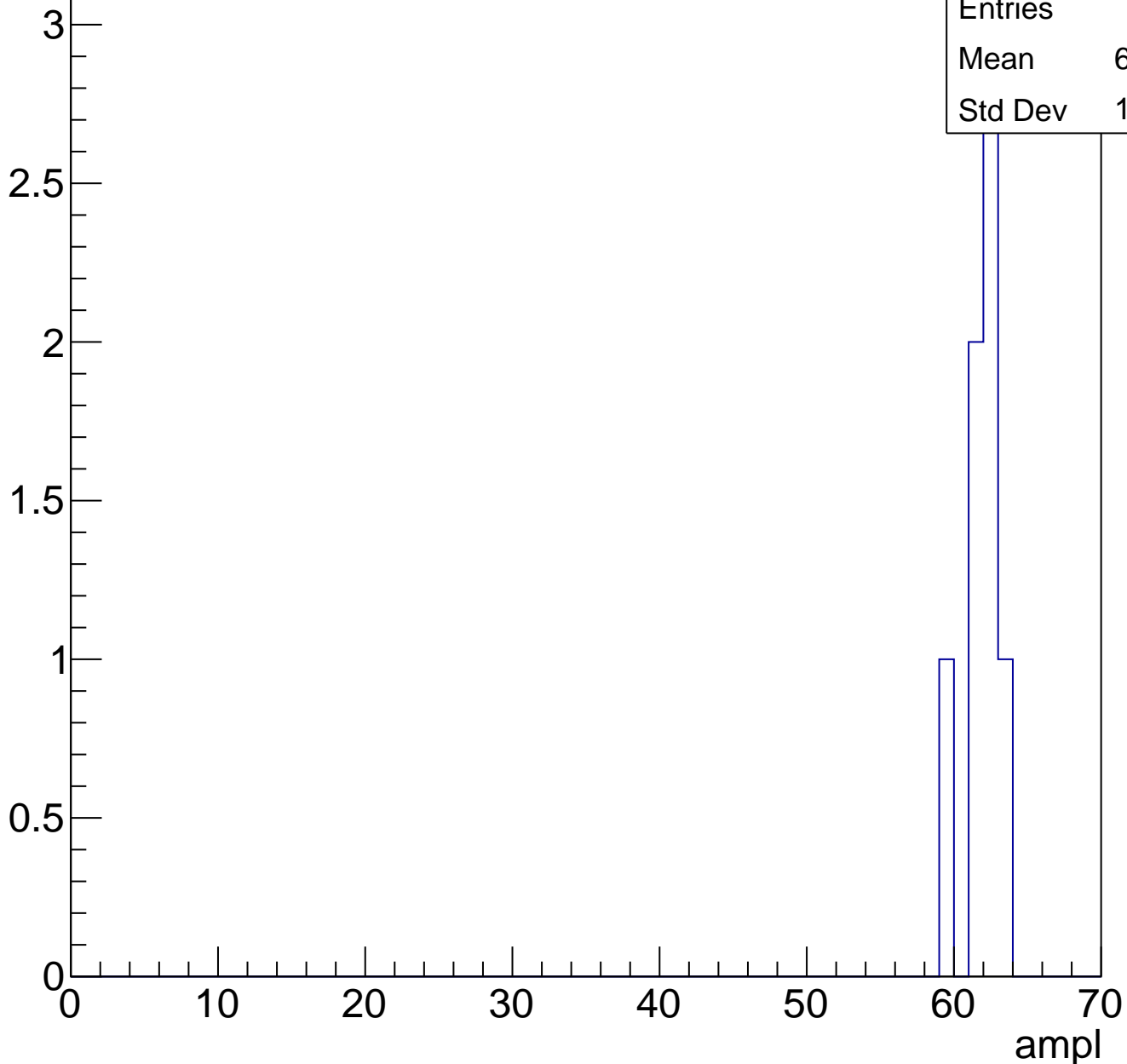
ampl



B0L101S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch37, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.19
Std Dev	4.159

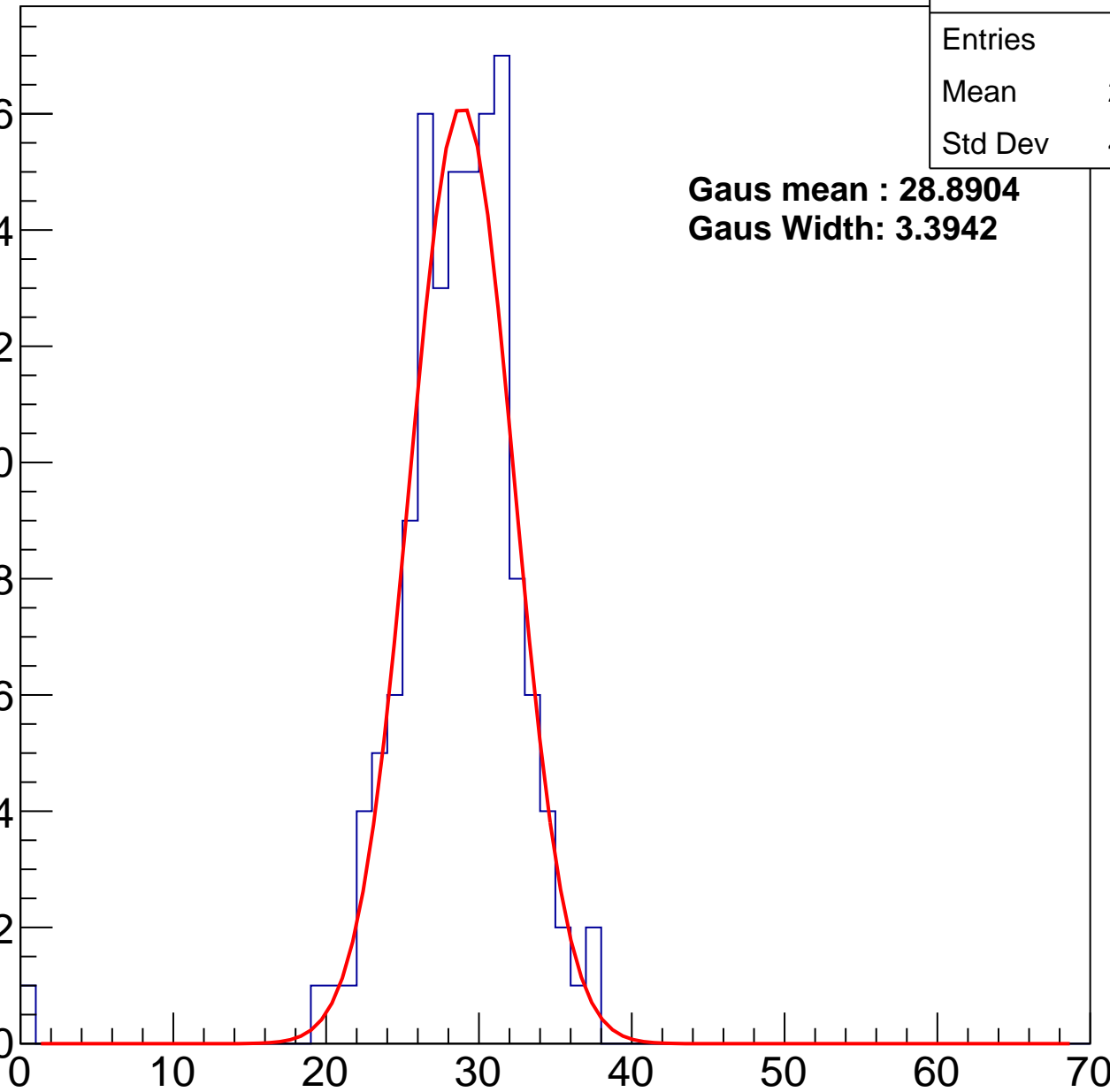
Gaus mean : 28.8904

Gaus Width: 3.3942

Entry

16
14
12
10
8
6
4
2
0

ampl

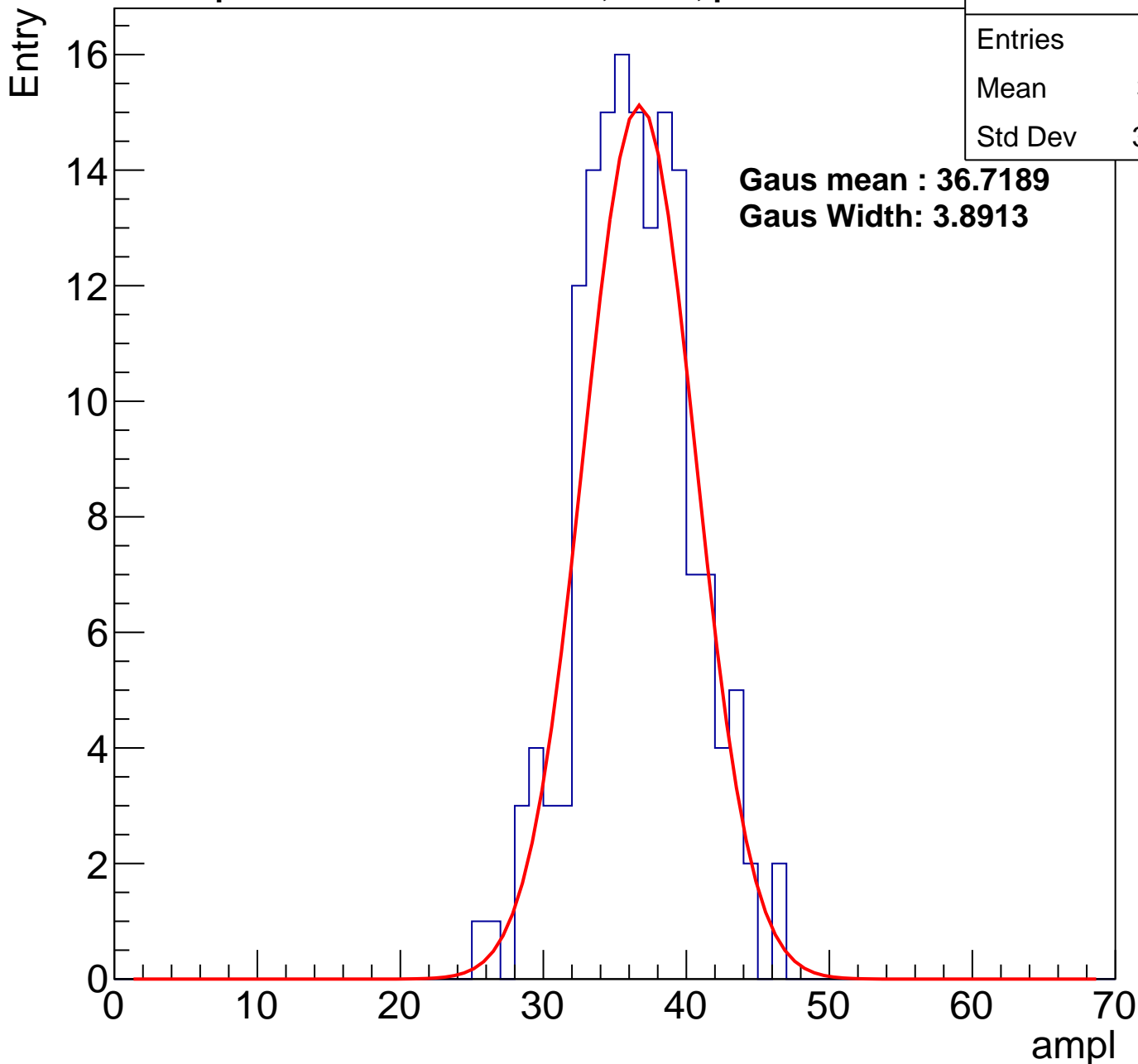


B0L101S, U15-ch37, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	36.01
Std Dev	3.913

Gaus mean : 36.7189
Gaus Width: 3.8913



B0L101S, U15-ch37, adc2

calib_packv5_042523_0143.root, FC#1, port C1

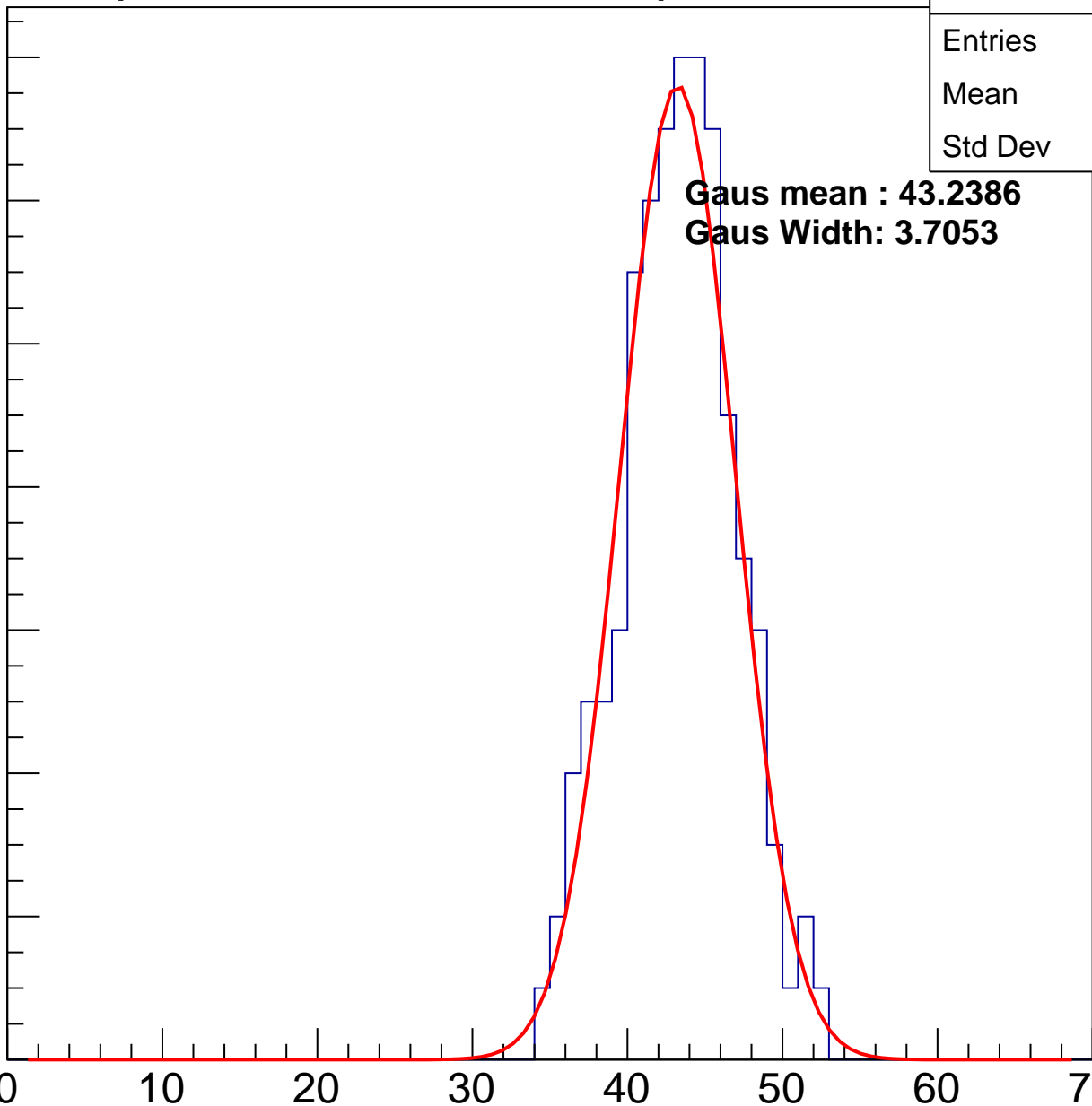
Entry

14
12
10
8
6
4
2
0

Entries	129
Mean	42.79
Std Dev	3.668

Gaus mean : 43.2386
Gaus Width: 3.7053

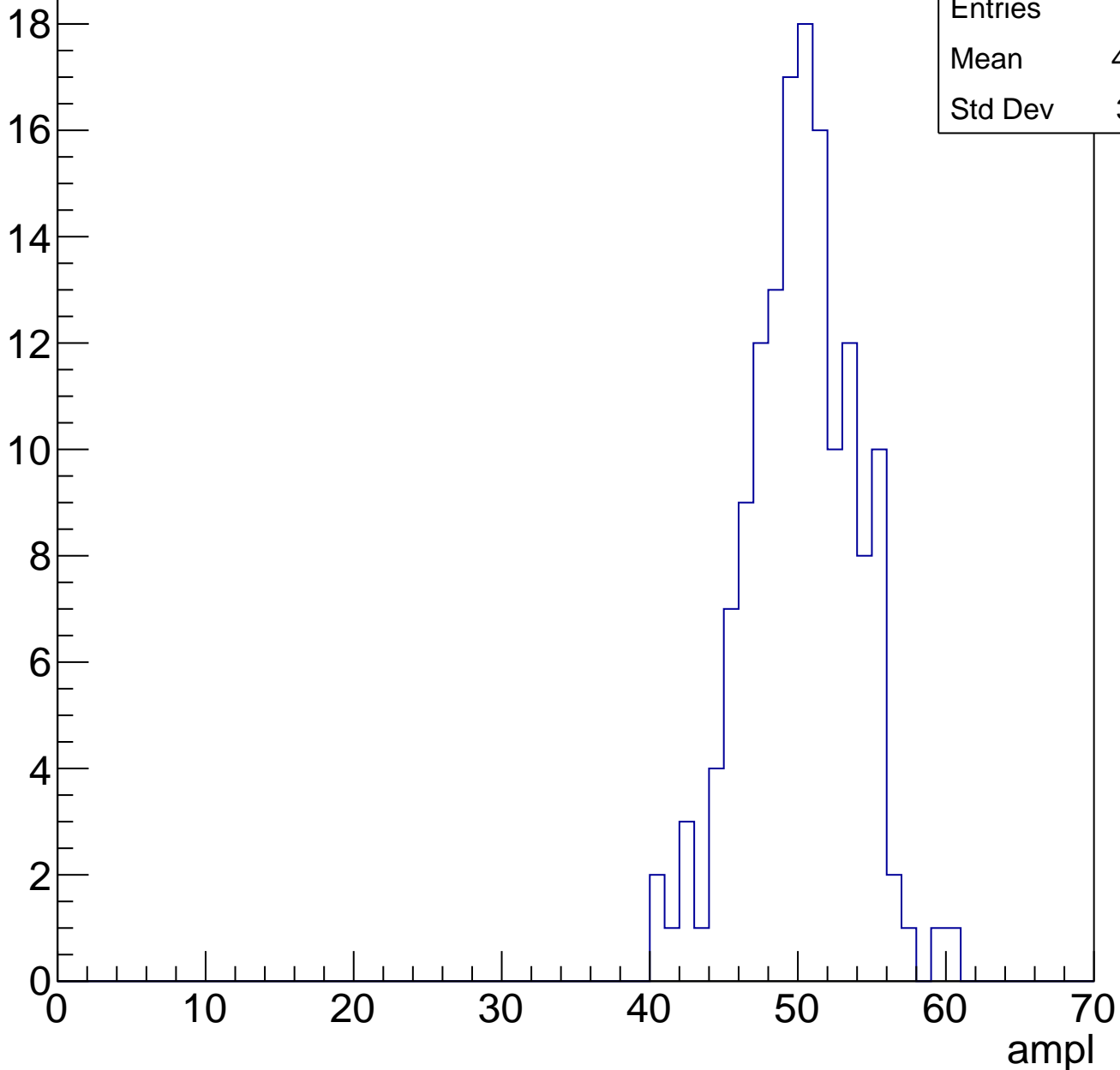
0 10 20 30 40 50 60 70
ampl



B0L101S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#1, port C1

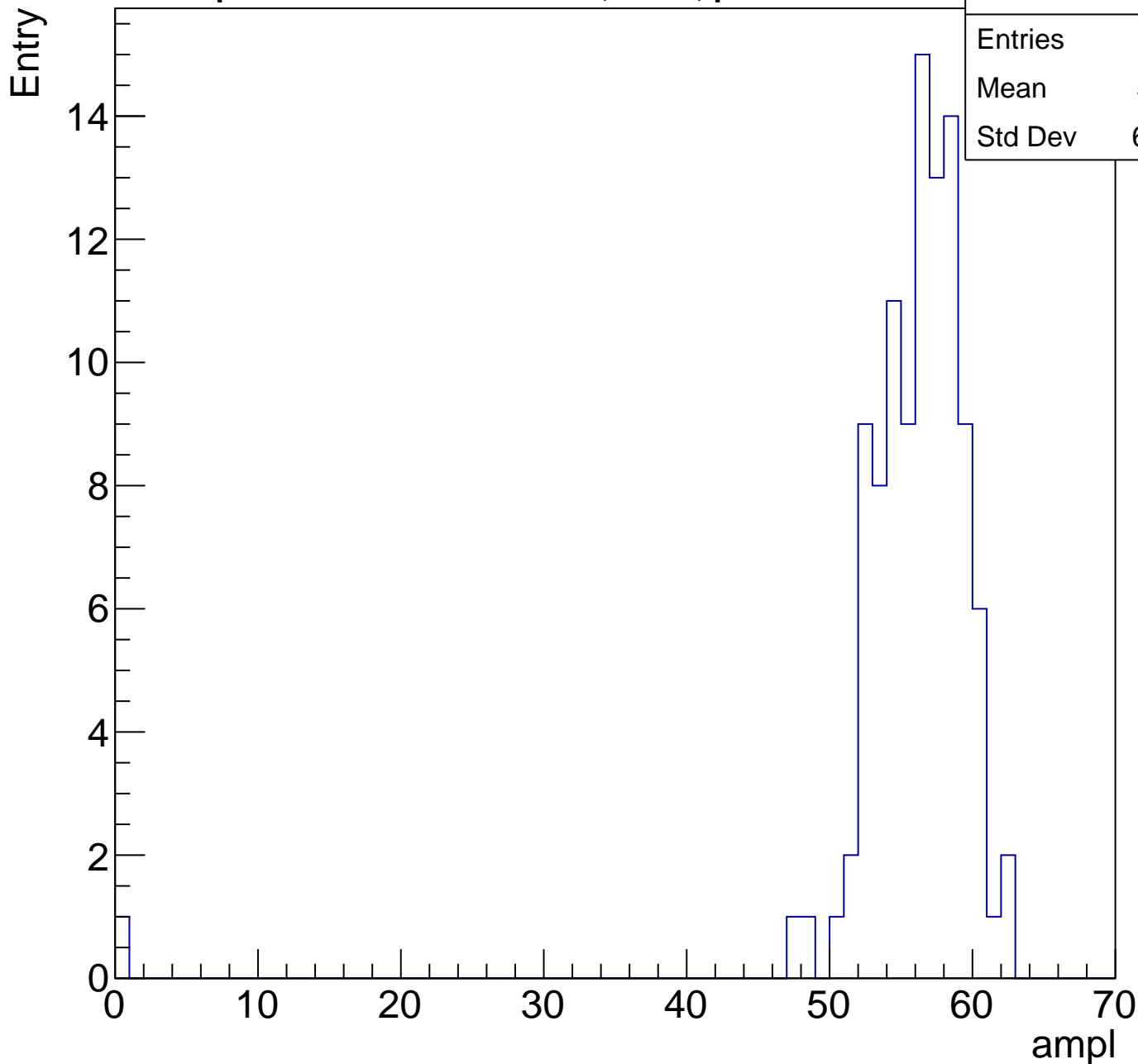
Entry



B0L101S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.31
Std Dev	6.177



B0L101S, U15-ch37, adc5

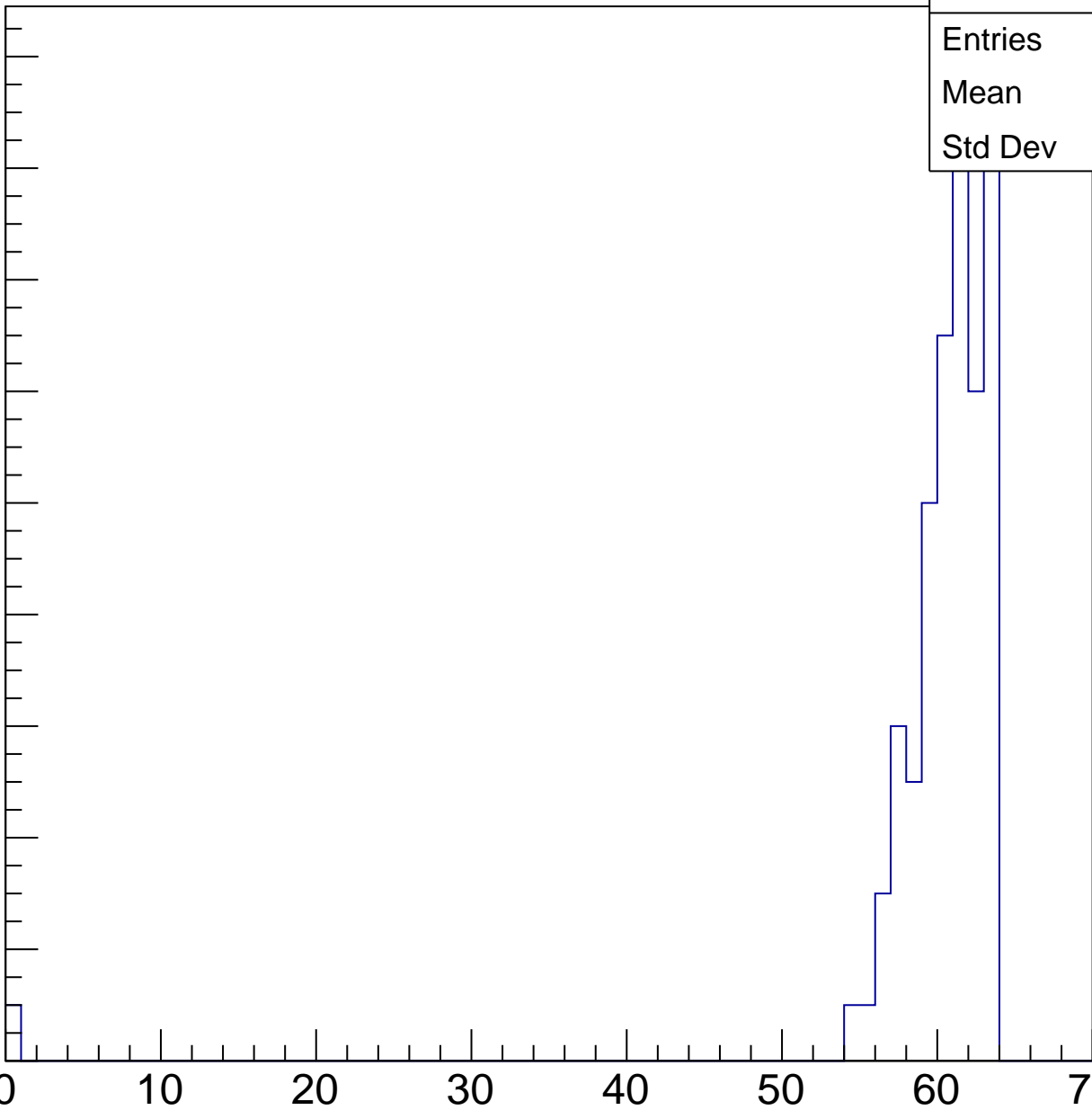
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.64
Std Dev	6.813

ampl

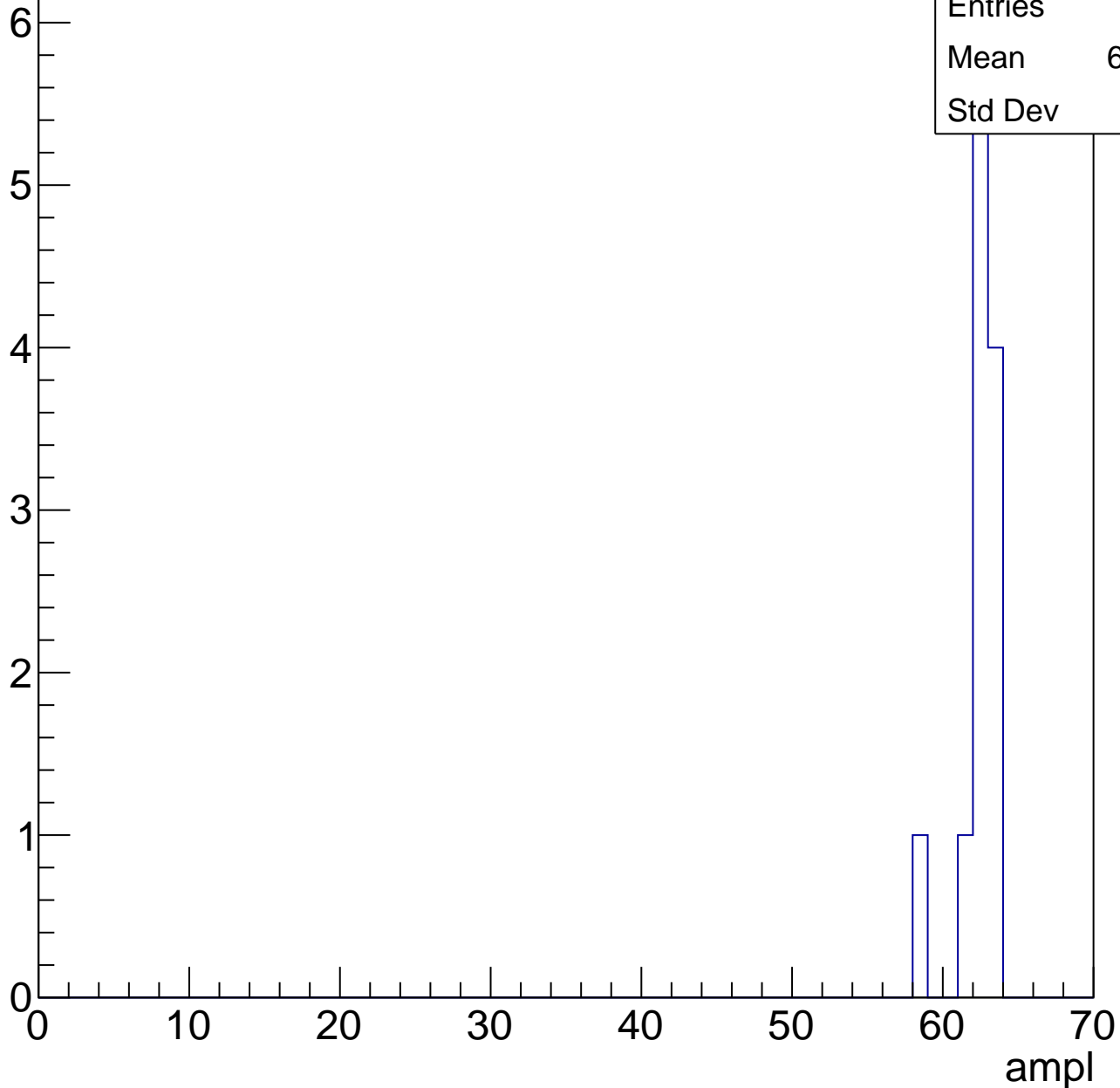


B0L101S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.92
Std Dev	1.32



B0L101S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch38, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	29.58
Std Dev	5.071

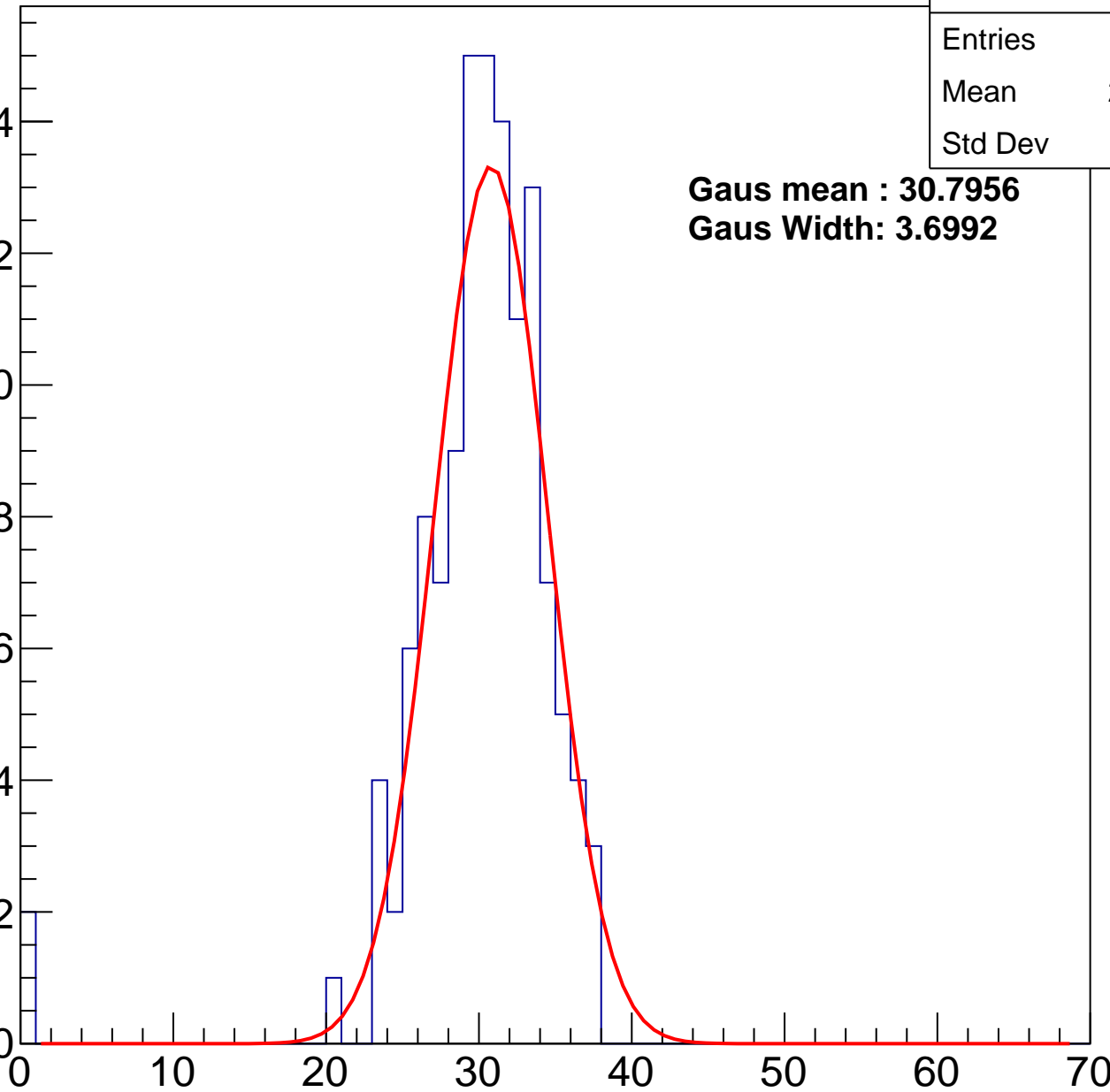
Gaus mean : 30.7956

Gaus Width: 3.6992

Entry

14
12
10
8
6
4
2
0

ampl

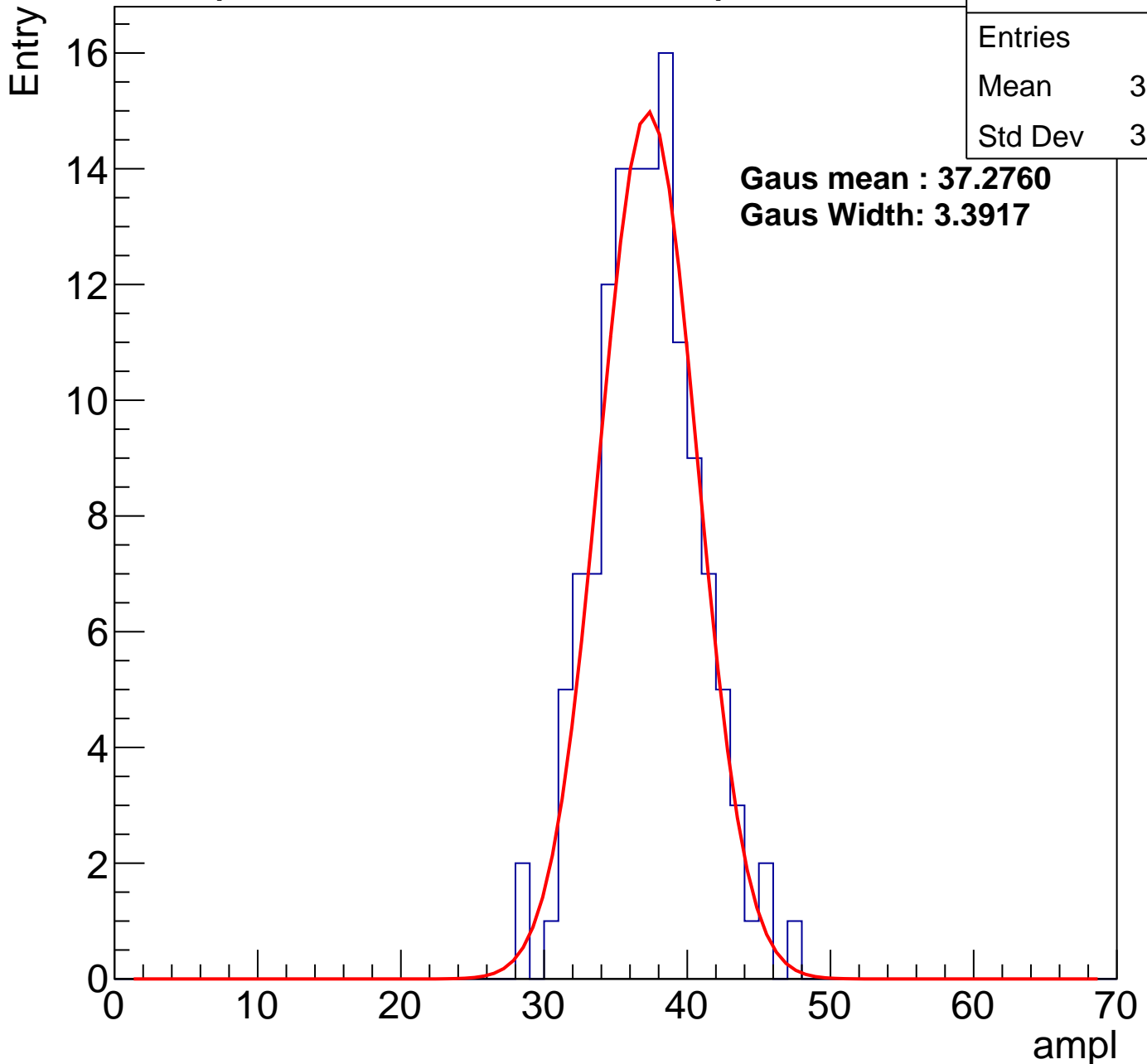


B0L101S, U15-ch38, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.79
Std Dev	3.509

Gaus mean : 37.2760
Gaus Width: 3.3917



B0L101S, U15-ch38, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

160

Mean

43.63

Std Dev

3.485

Gaus mean : 44.0748

Gaus Width: 3.4623

0

10

20

30

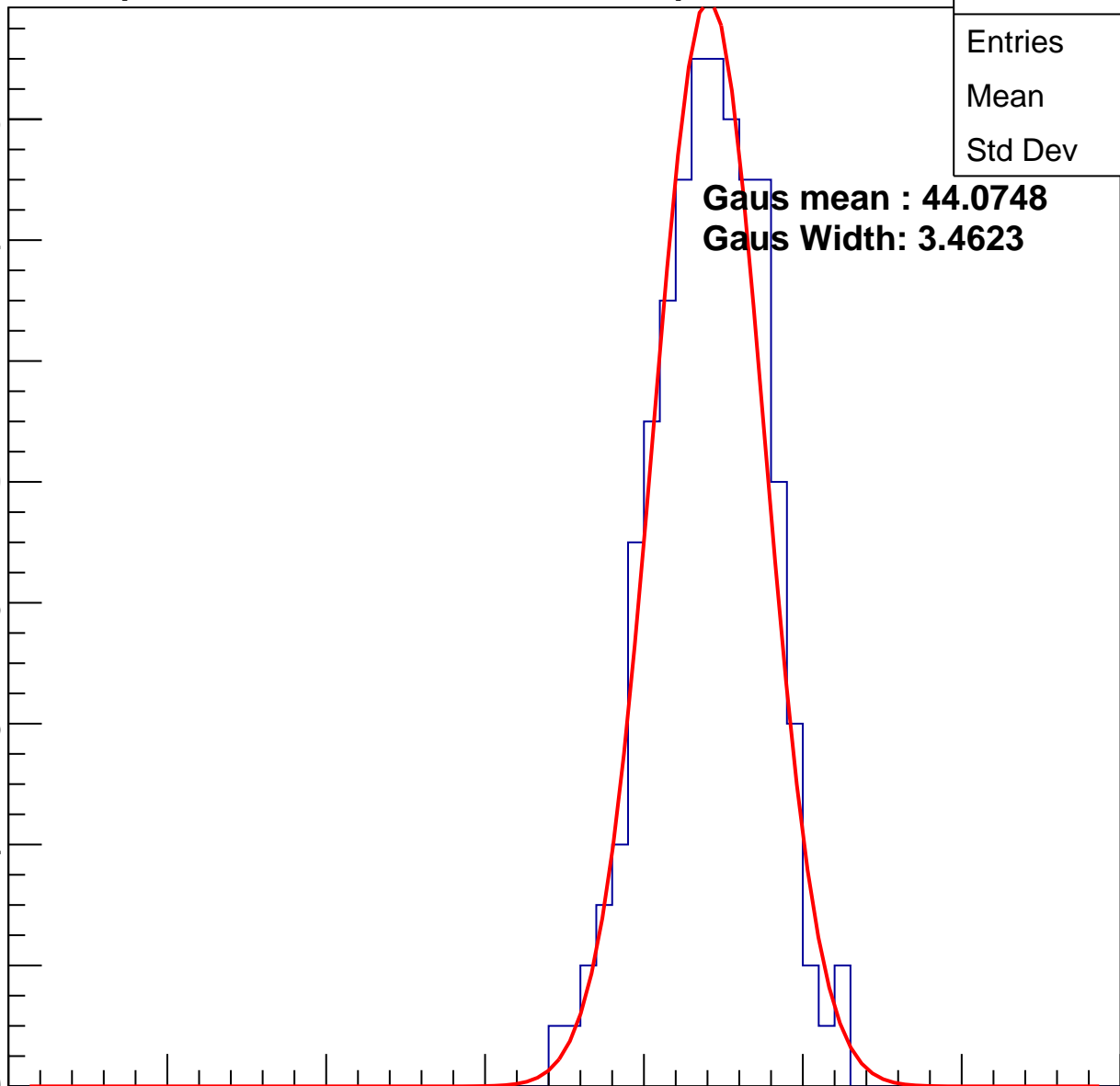
40

50

60

70

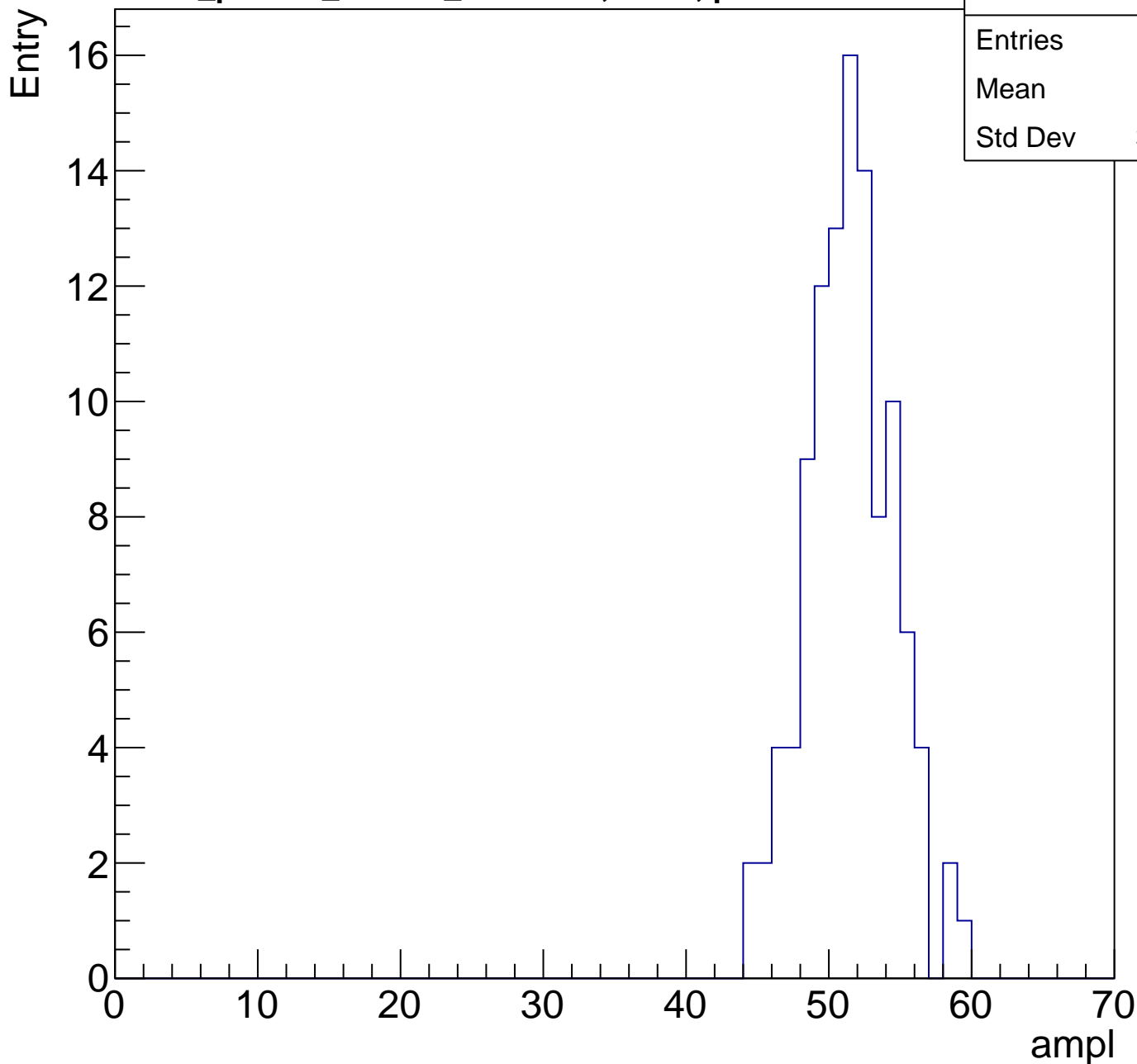
ampl



B0L101S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#1, port C1

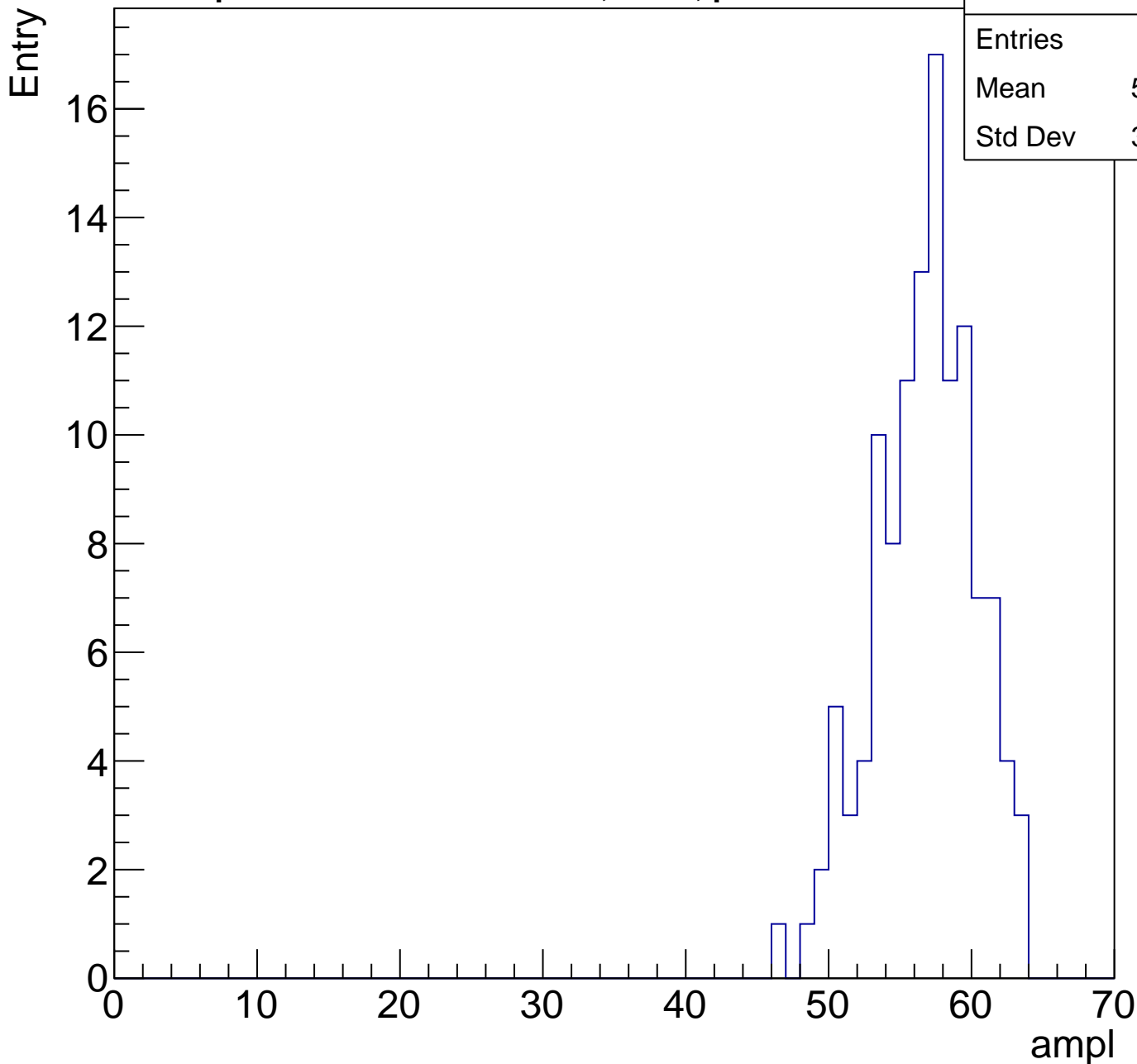
Entries	107
Mean	51
Std Dev	3.011



B0L101S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	56.28
Std Dev	3.493



B0L101S, U15-ch38, adc5

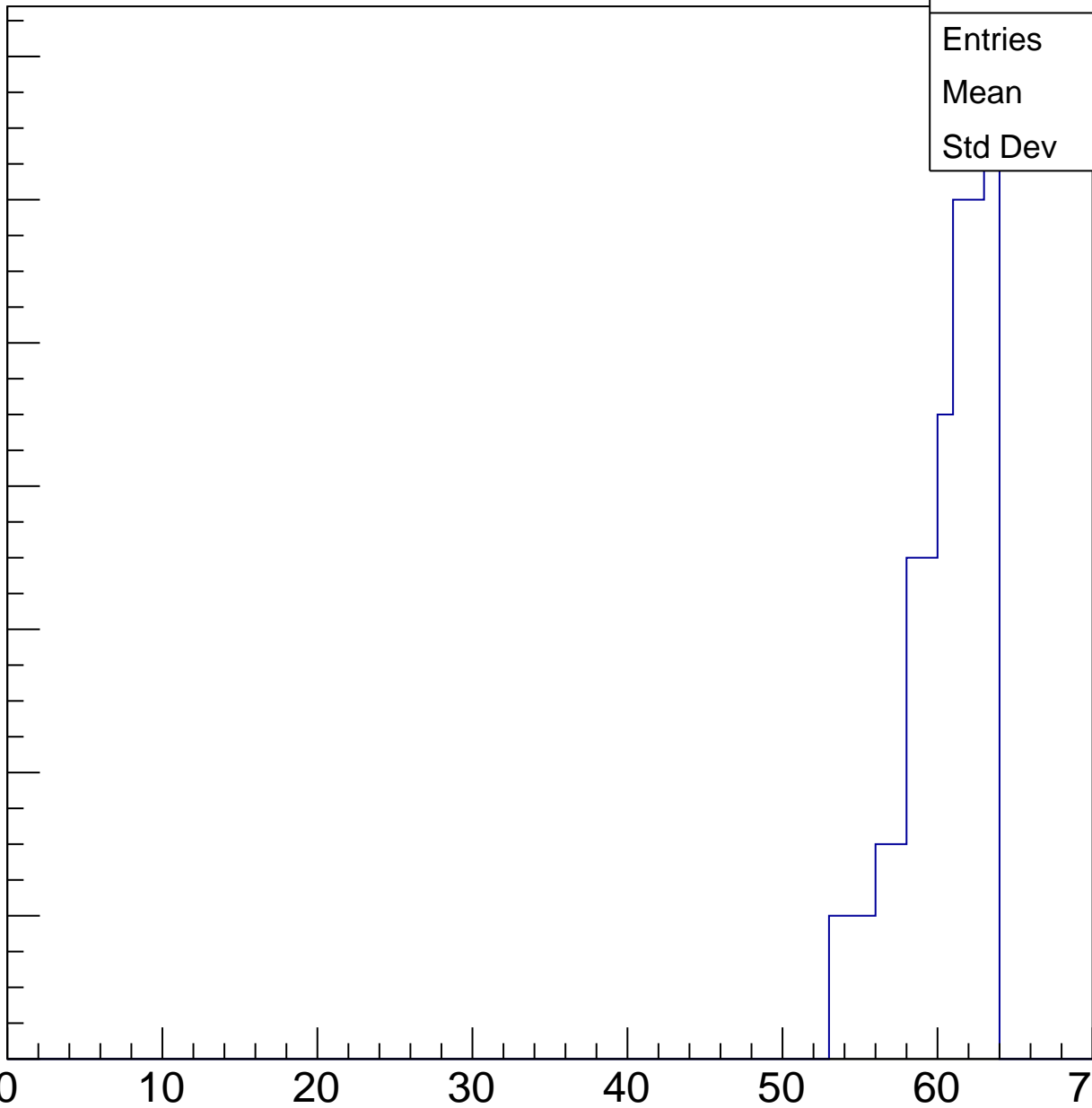
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	60
Std Dev	2.659

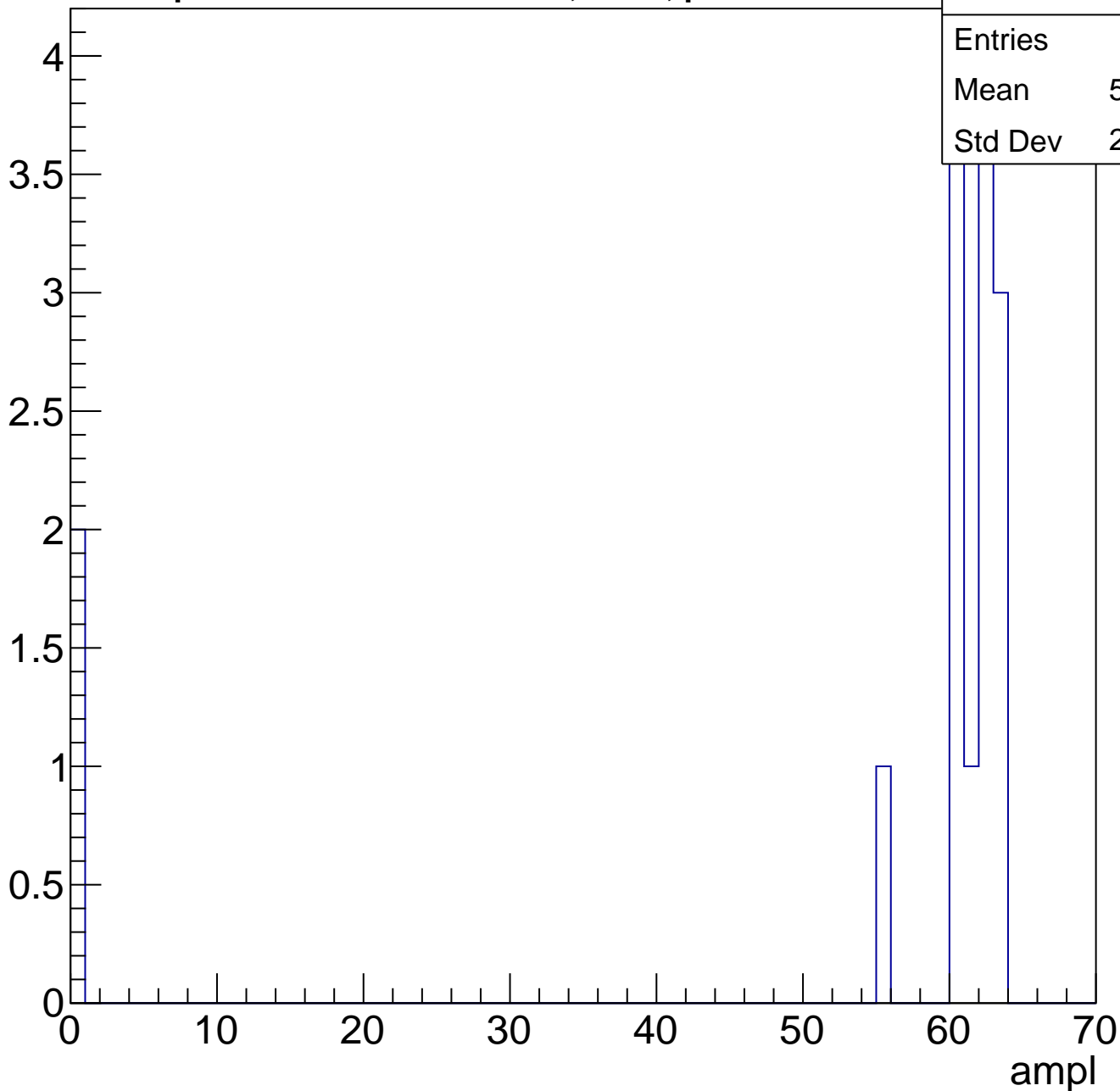
ampl



B0L101S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	15
Mean	52.87
Std Dev	20.83

B0L101S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch39, adc0

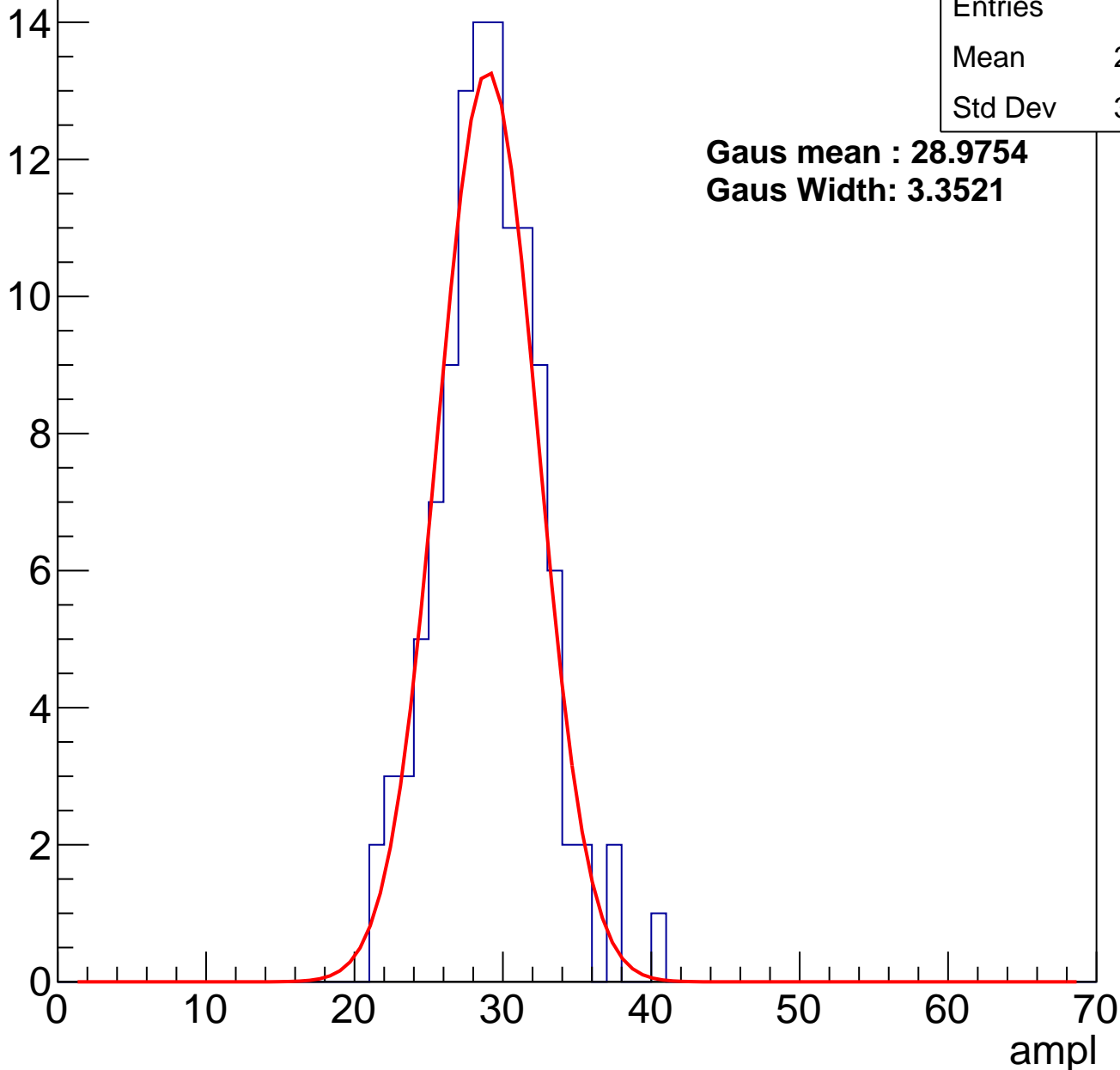
calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	28.63
Std Dev	3.447

Gaus mean : 28.9754

Gaus Width: 3.3521

Entry



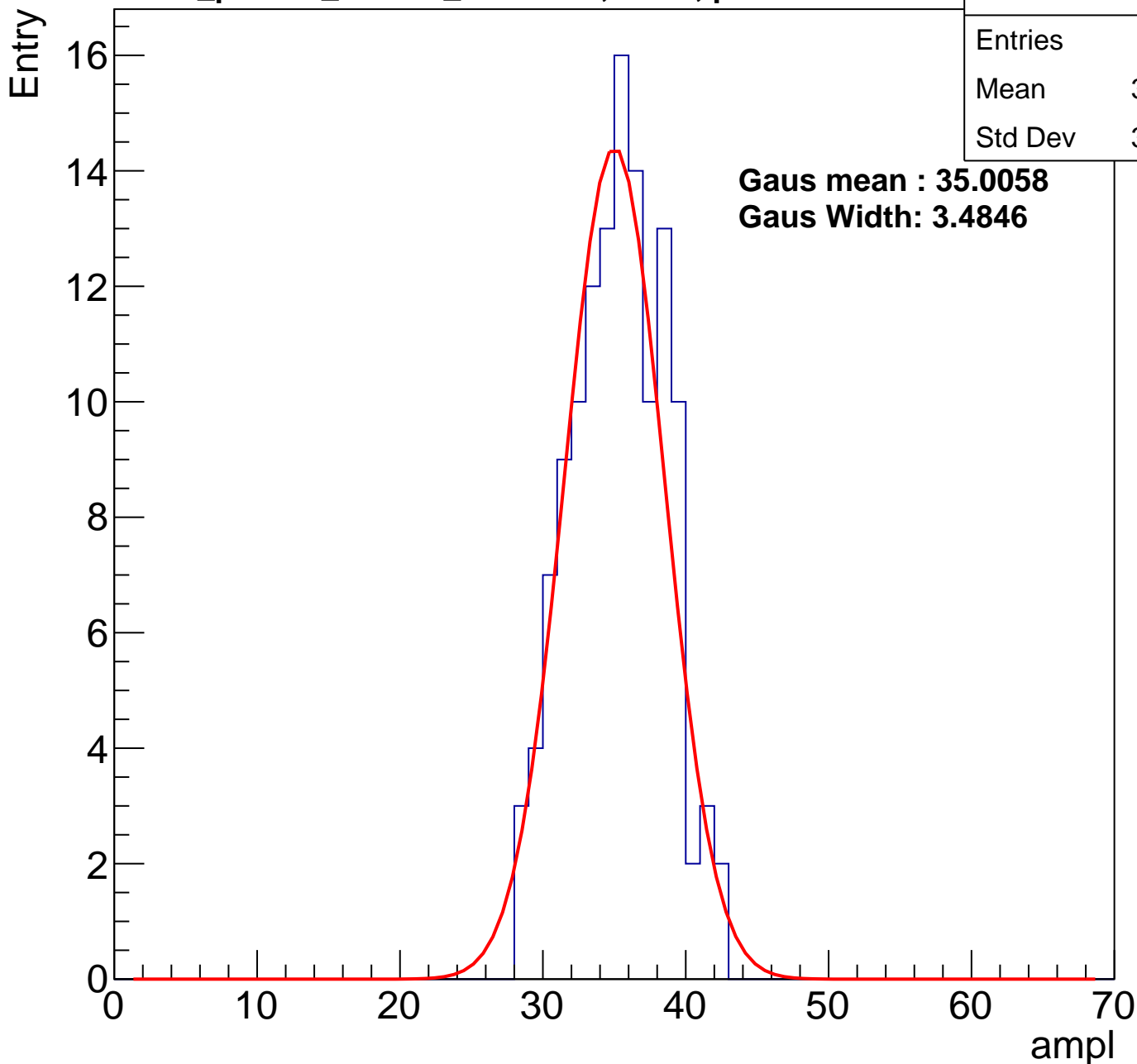
B0L101S, U15-ch39, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	34.78
Std Dev	3.238

Gaus mean : 35.0058

Gaus Width: 3.4846



B0L101S, U15-ch39, adc2

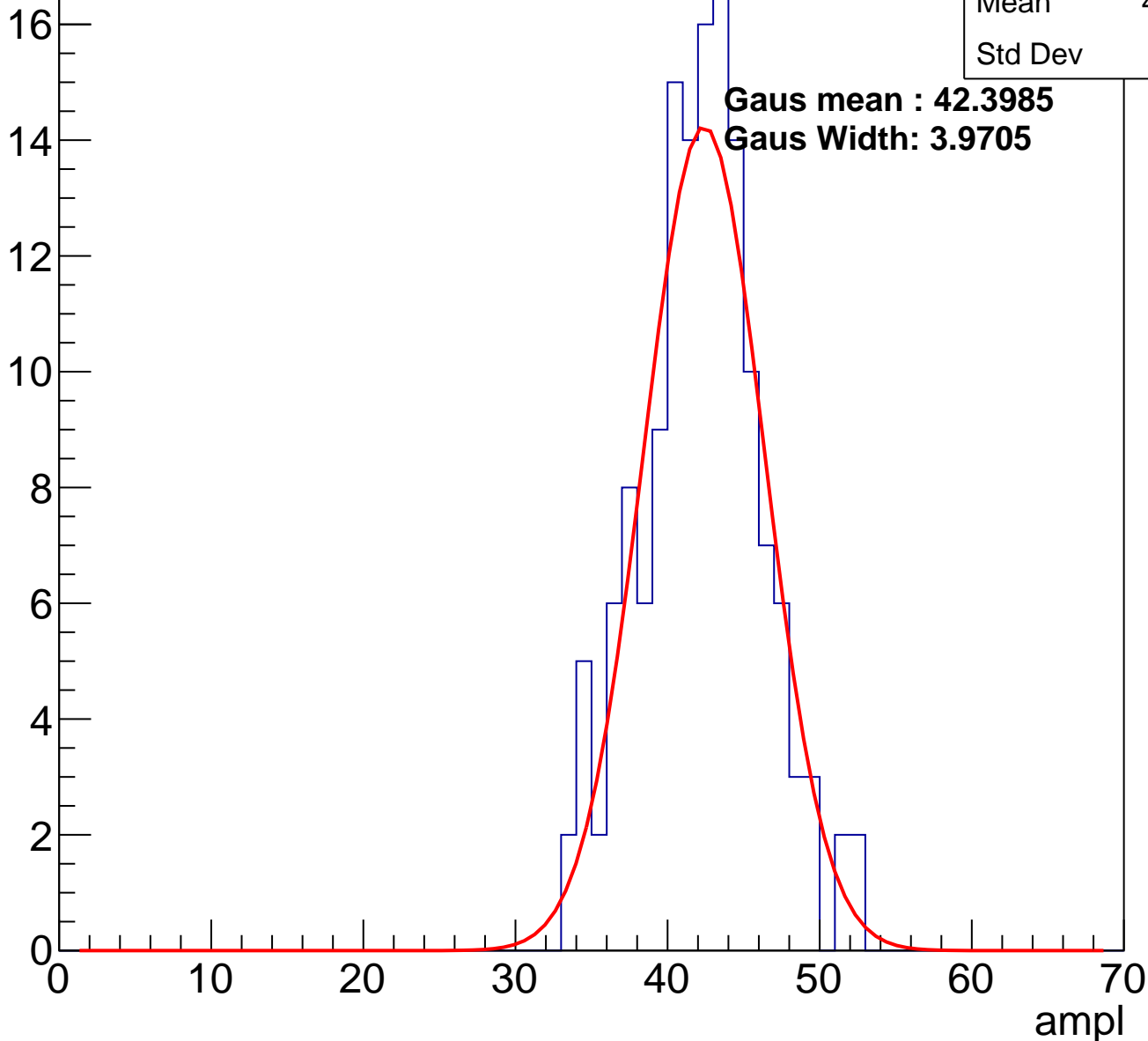
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	147
Mean	41.78
Std Dev	3.94

Gaus mean : 42.3985

Gaus Width: 3.9705



B0L101S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#1, port C1

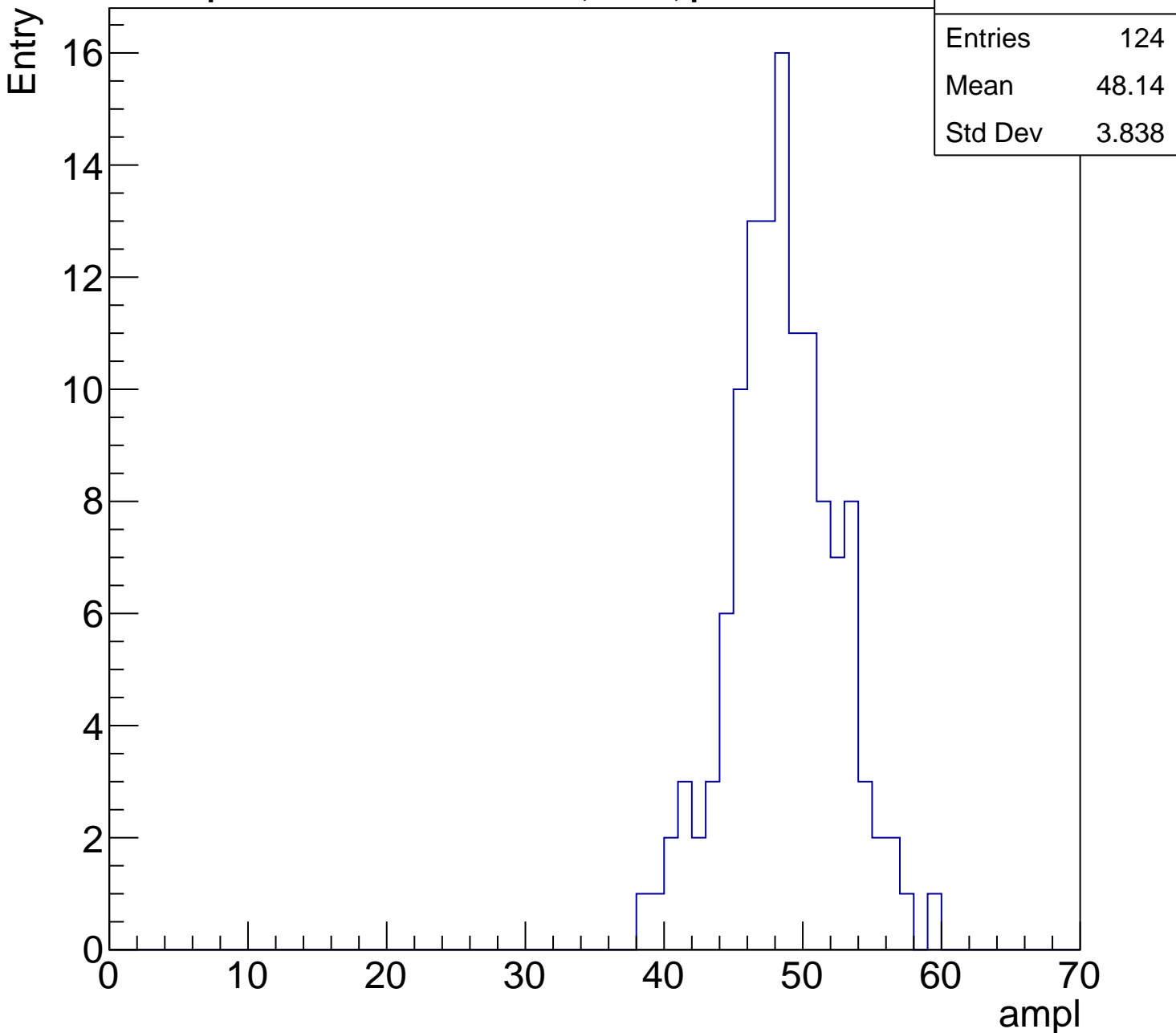
Entries	124
Mean	48.14
Std Dev	3.838

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

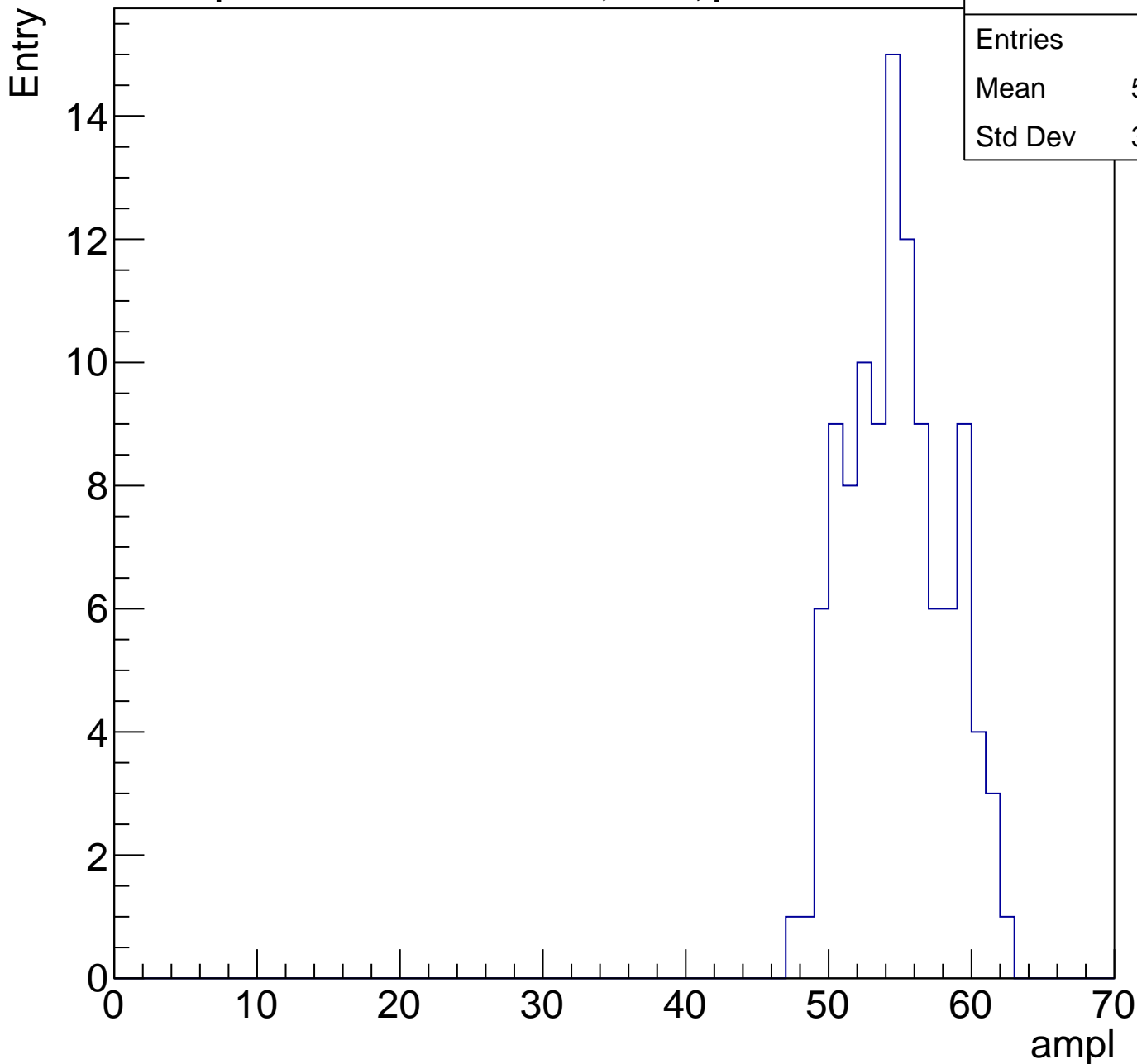
ampl



B0L101S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	54.35
Std Dev	3.406



B0L101S, U15-ch39, adc5

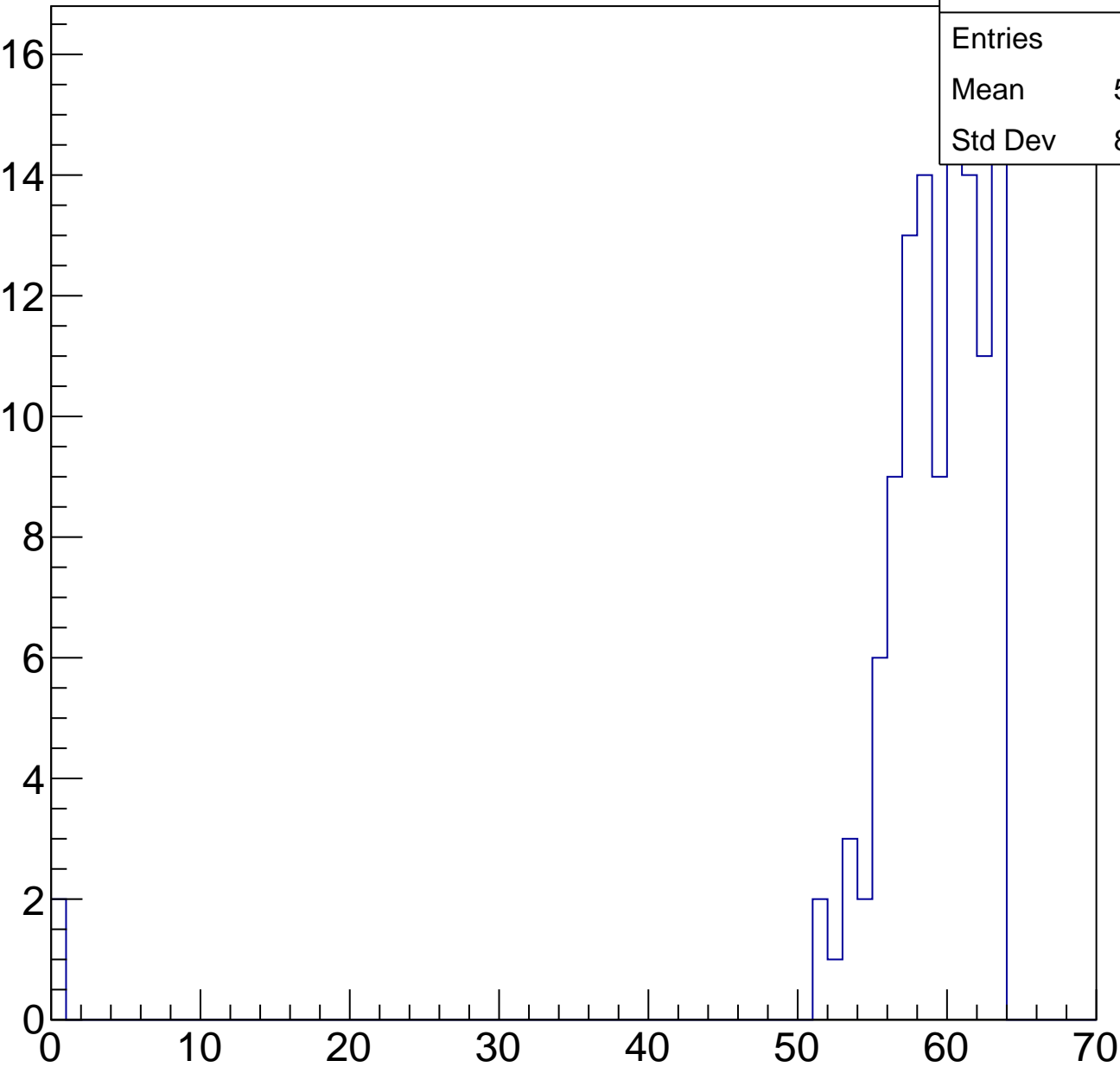
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	57.99
Std Dev	8.155

ampl

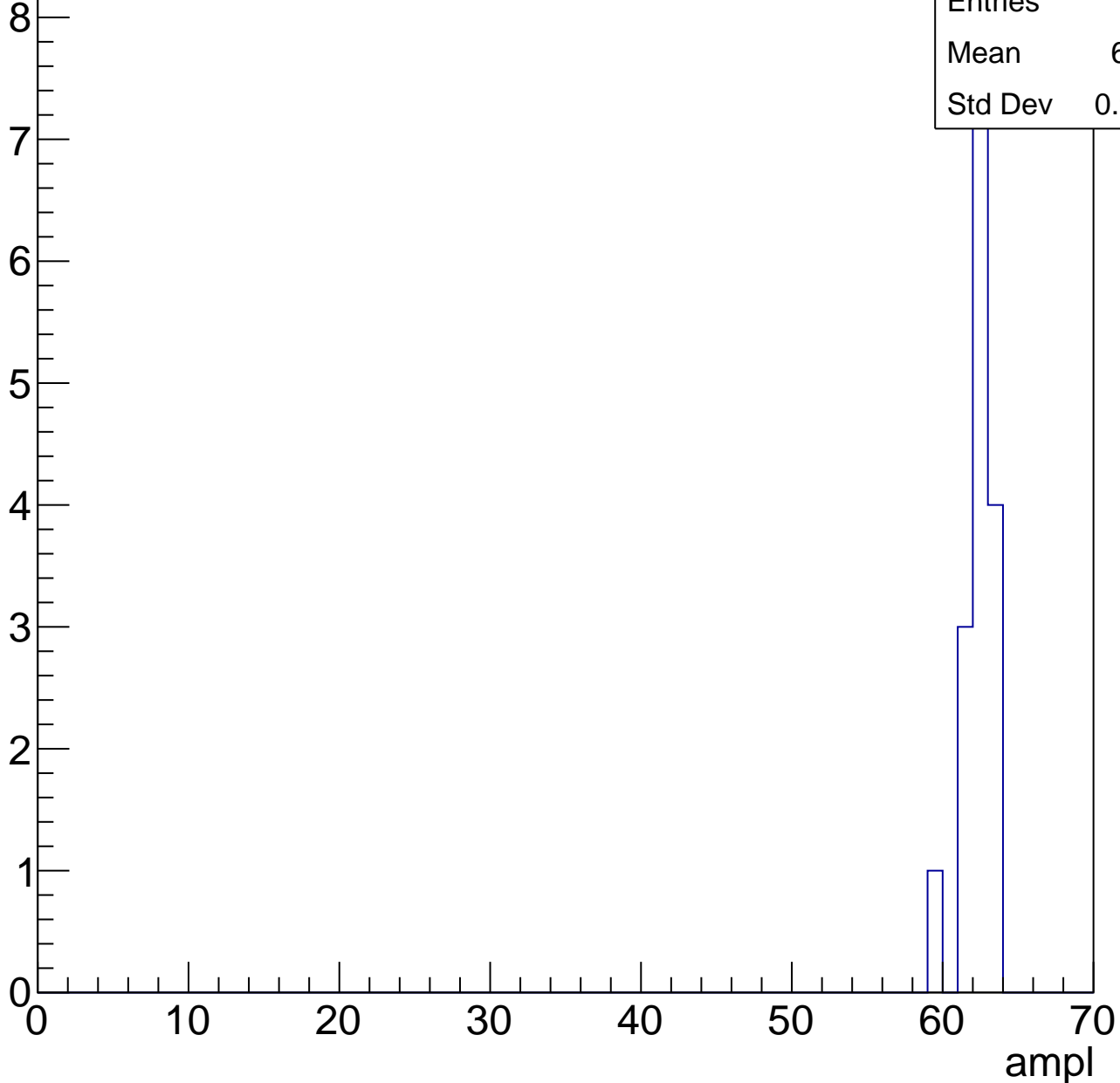


B0L101S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	61.88
Std Dev	0.9922



B0L101S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

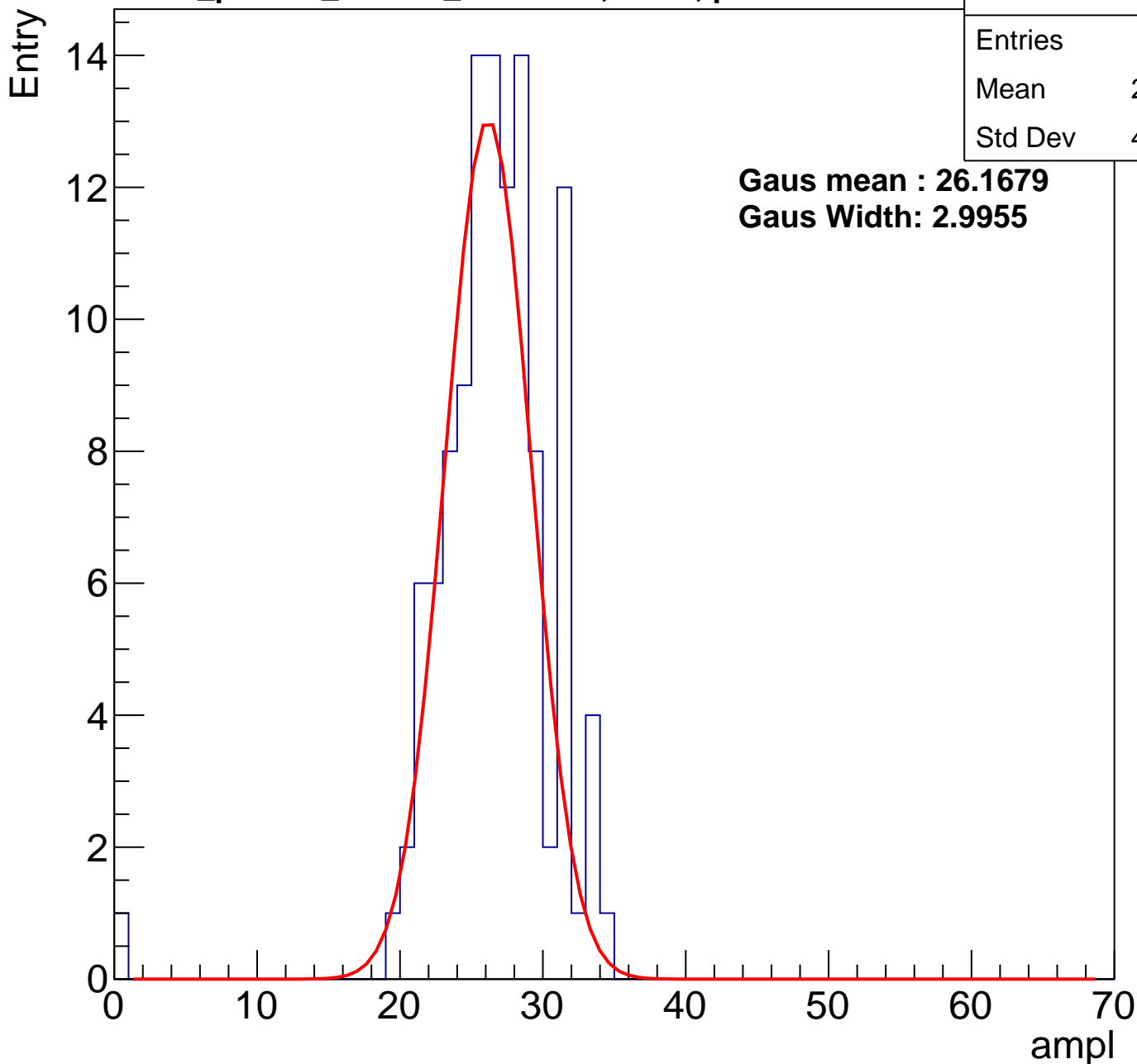
B0L101S, U15-ch40, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	26.17
Std Dev	4.097

Gaus mean : 26.1679

Gaus Width: 2.9955



B0L101S, U15-ch40, adc1

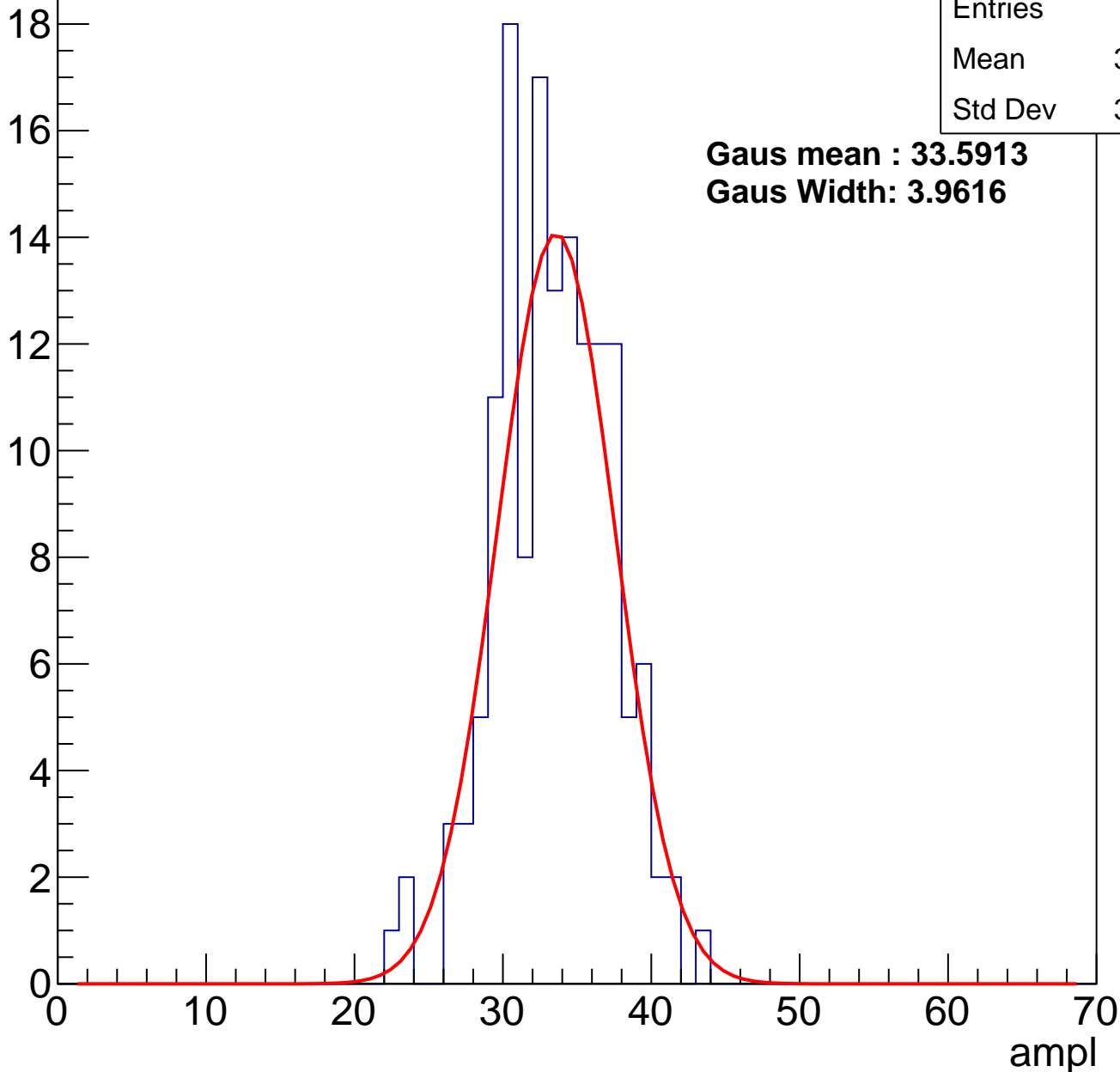
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	32.98
Std Dev	3.795

Gaus mean : 33.5913

Gaus Width: 3.9616

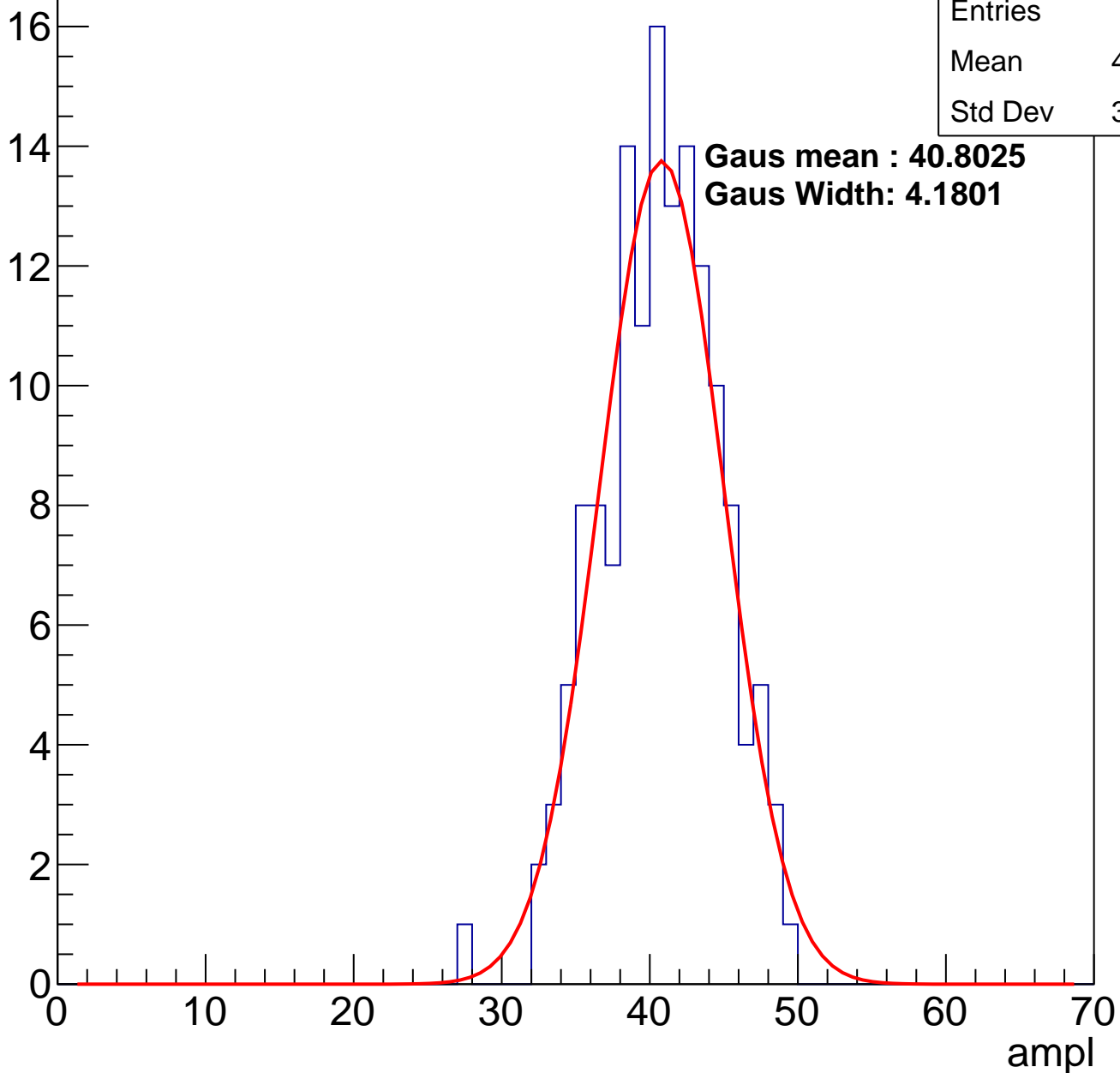
Entry



B0L101S, U15-ch40, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

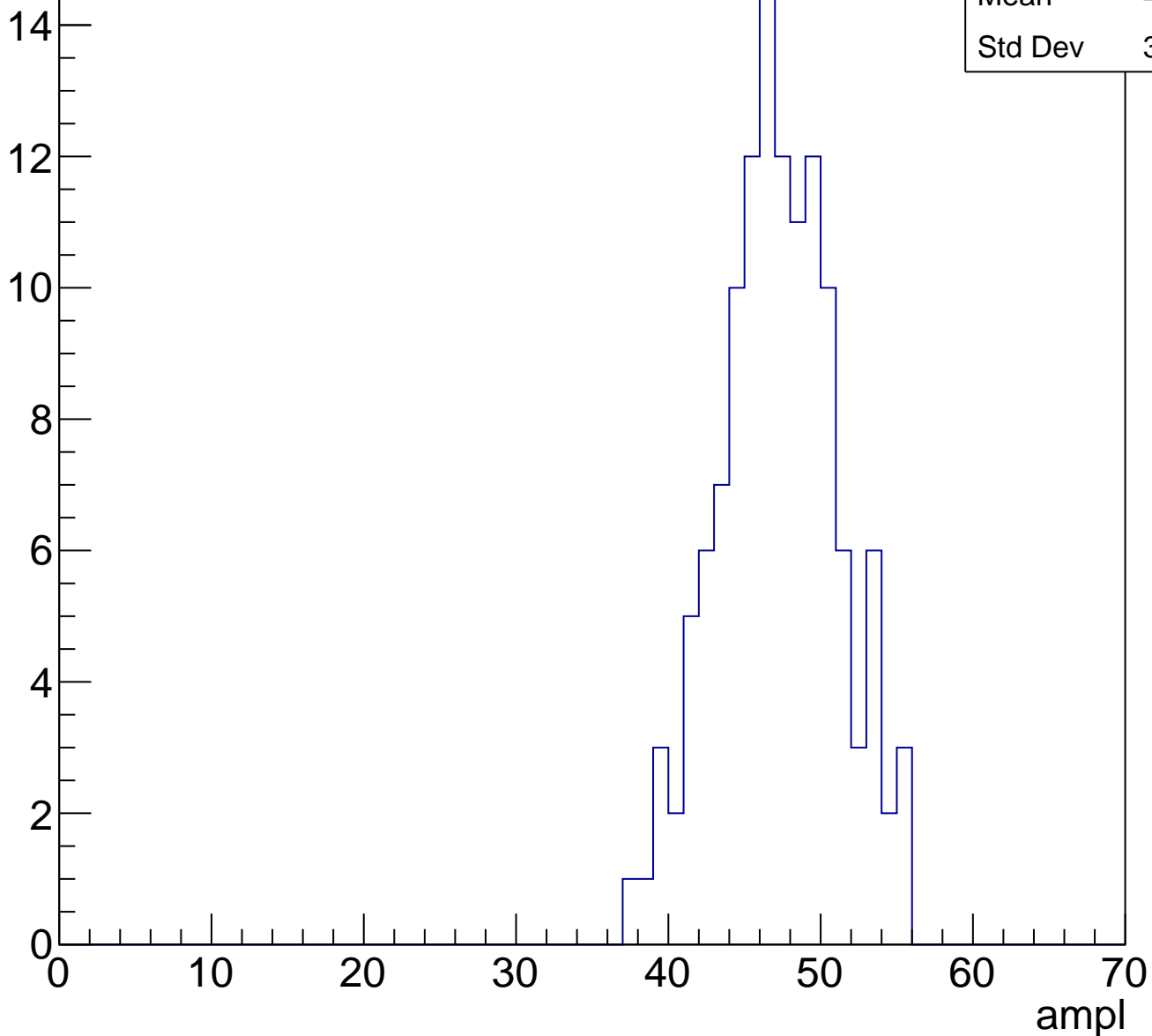


B0L101S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	46.72
Std Dev	3.849

Entry



B0L101S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	53.04
Std Dev	3.23

Entry

14

12

10

8

6

4

2

0

0

10

20

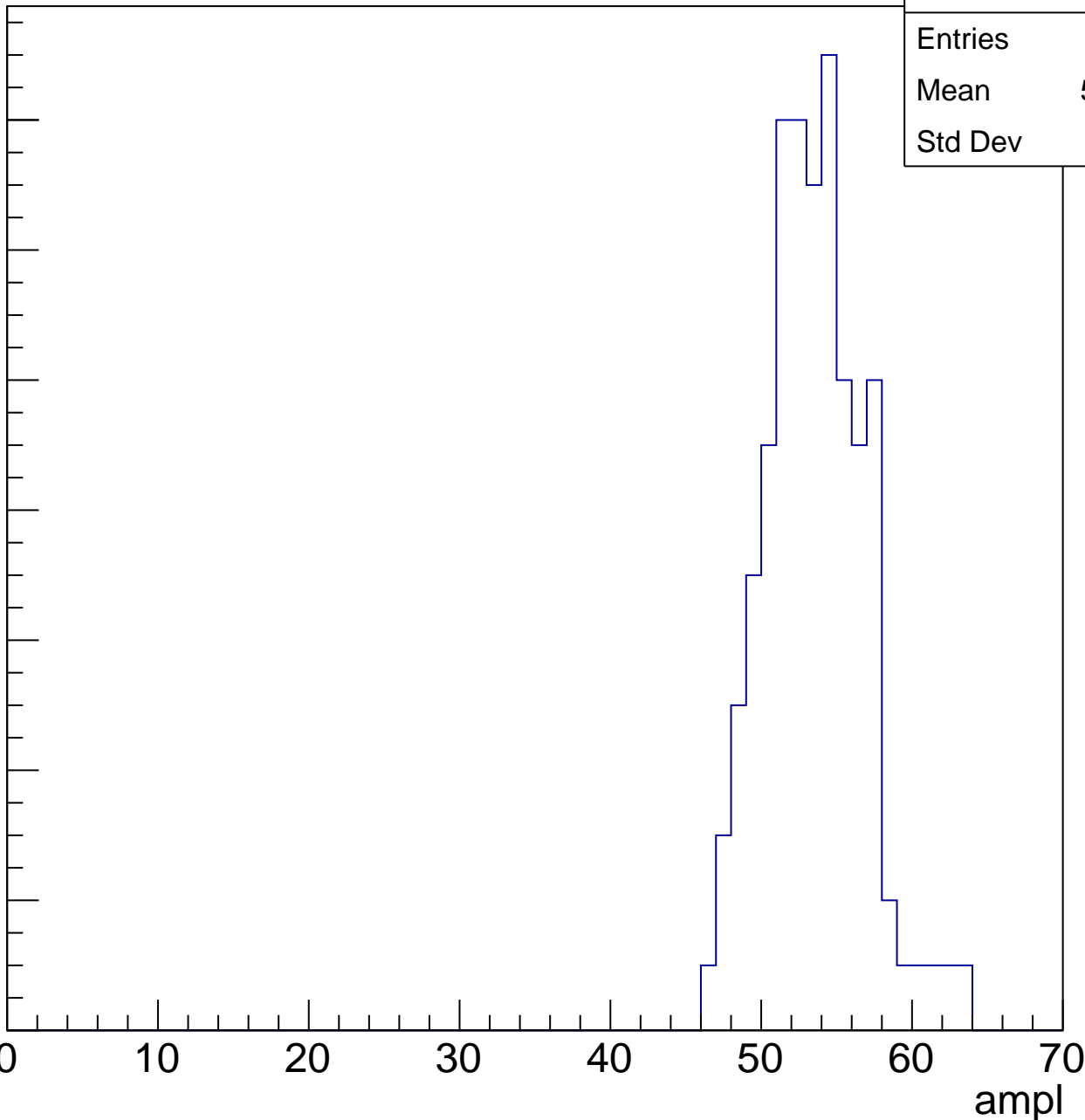
30

40

50

60

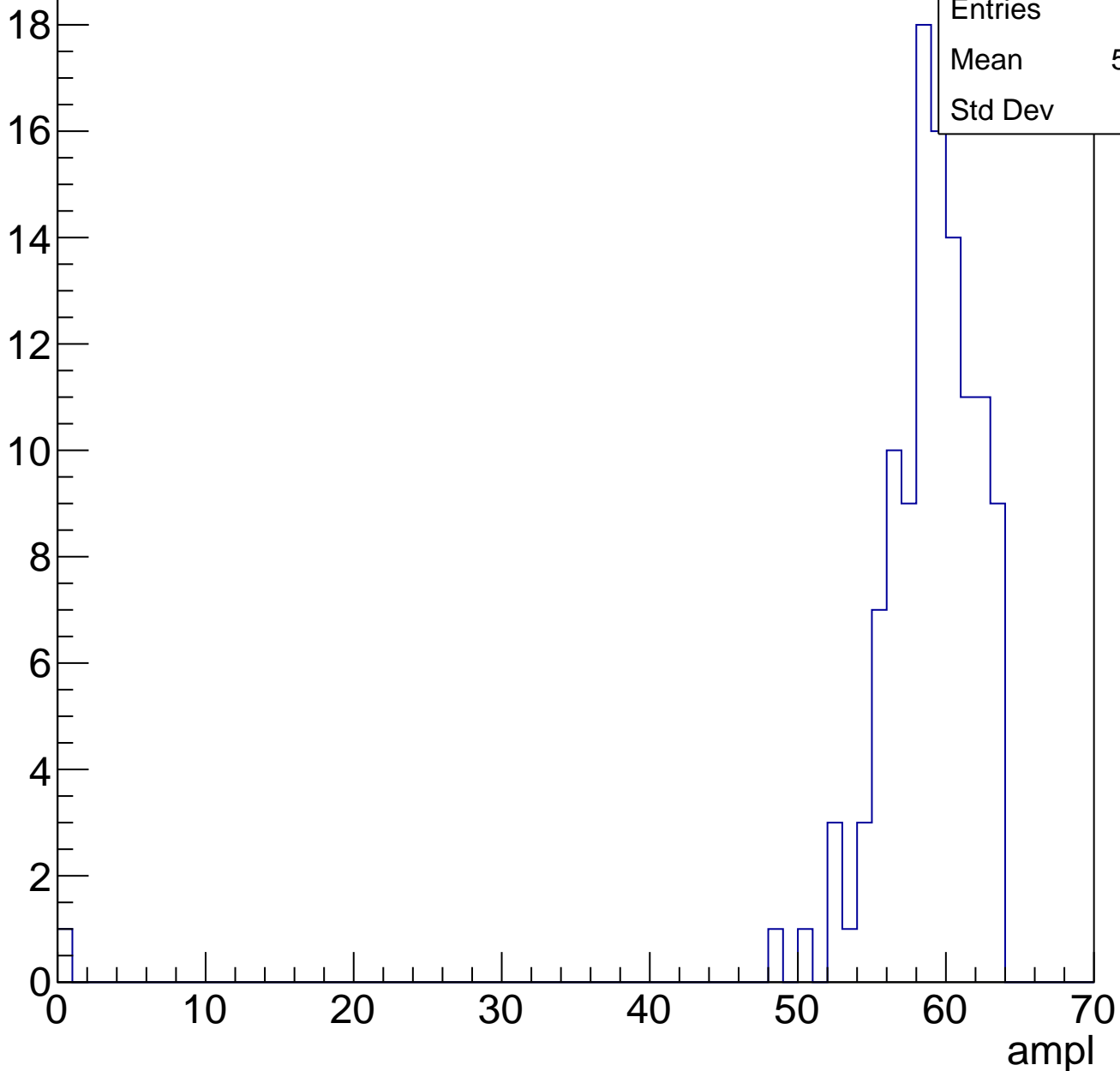
ampl



B0L101S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

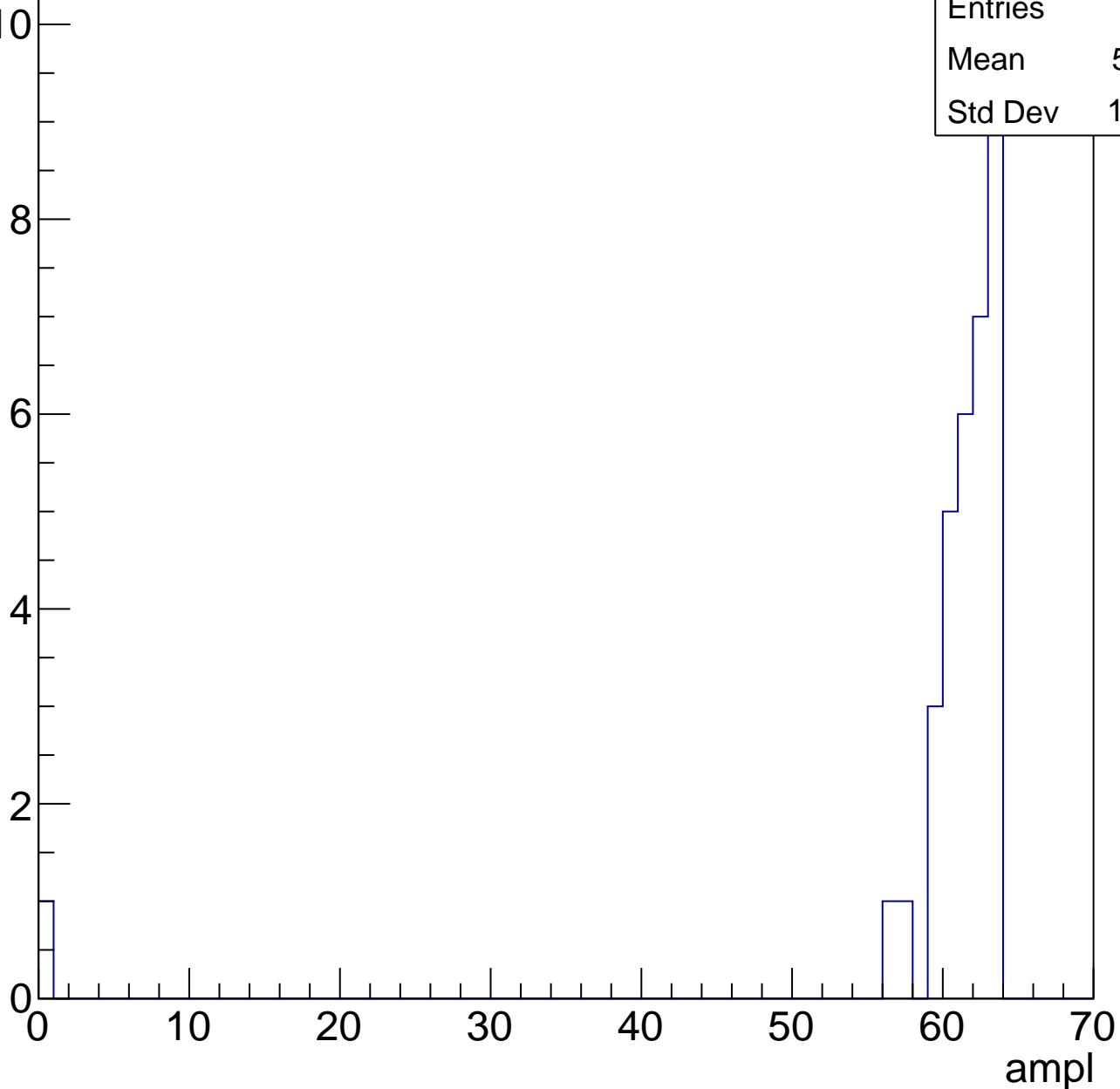


B0L101S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	34
Mean	59.41
Std Dev	10.49



B0L101S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L101S, U15-ch41, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	29.8
Std Dev	4.084

Gaus mean : 30.3018

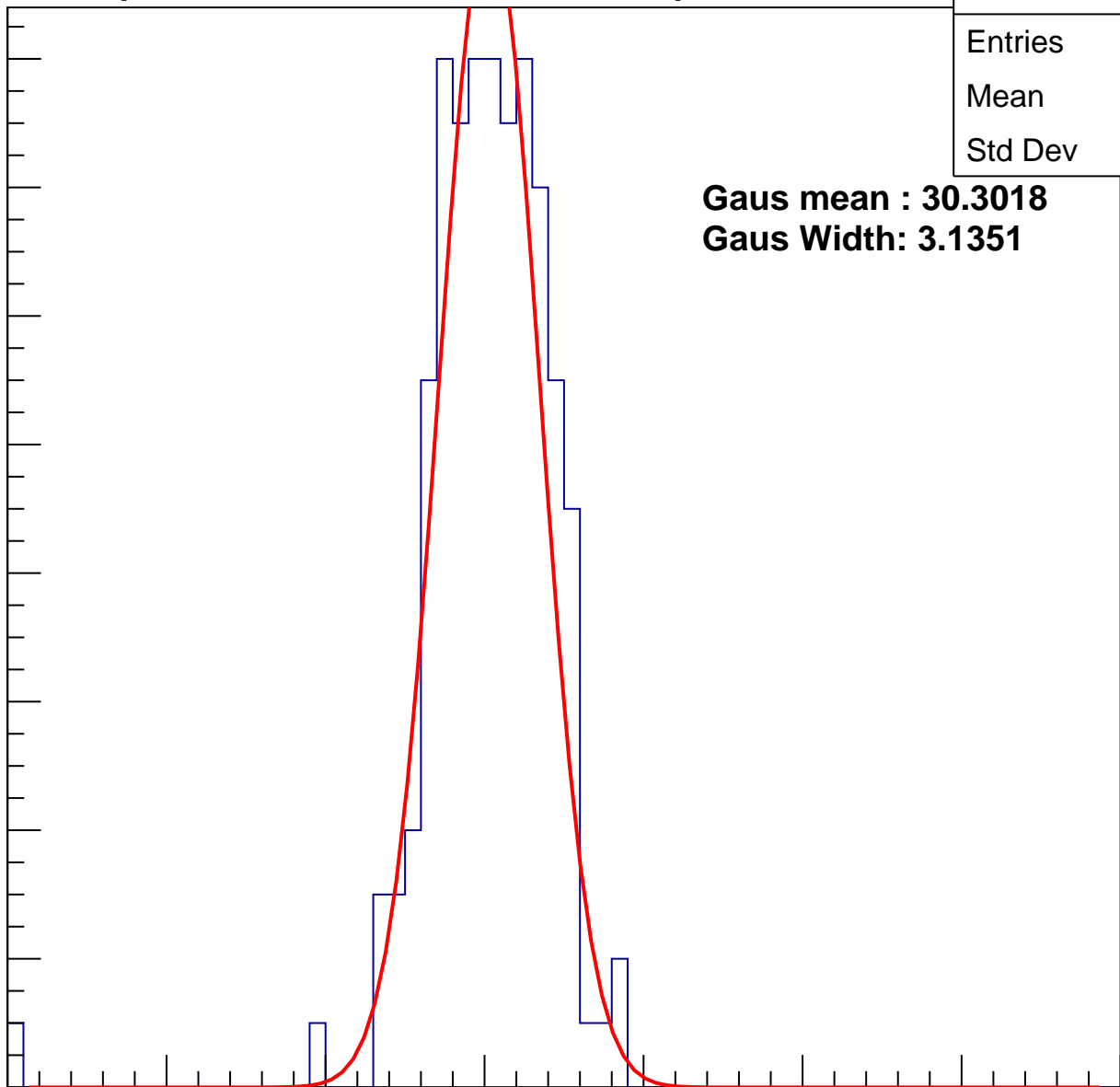
Gaus Width: 3.1351

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch41, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	118
Mean	37.31
Std Dev	3.063

Gaus mean : 37.8107

Gaus Width: 3.2013

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

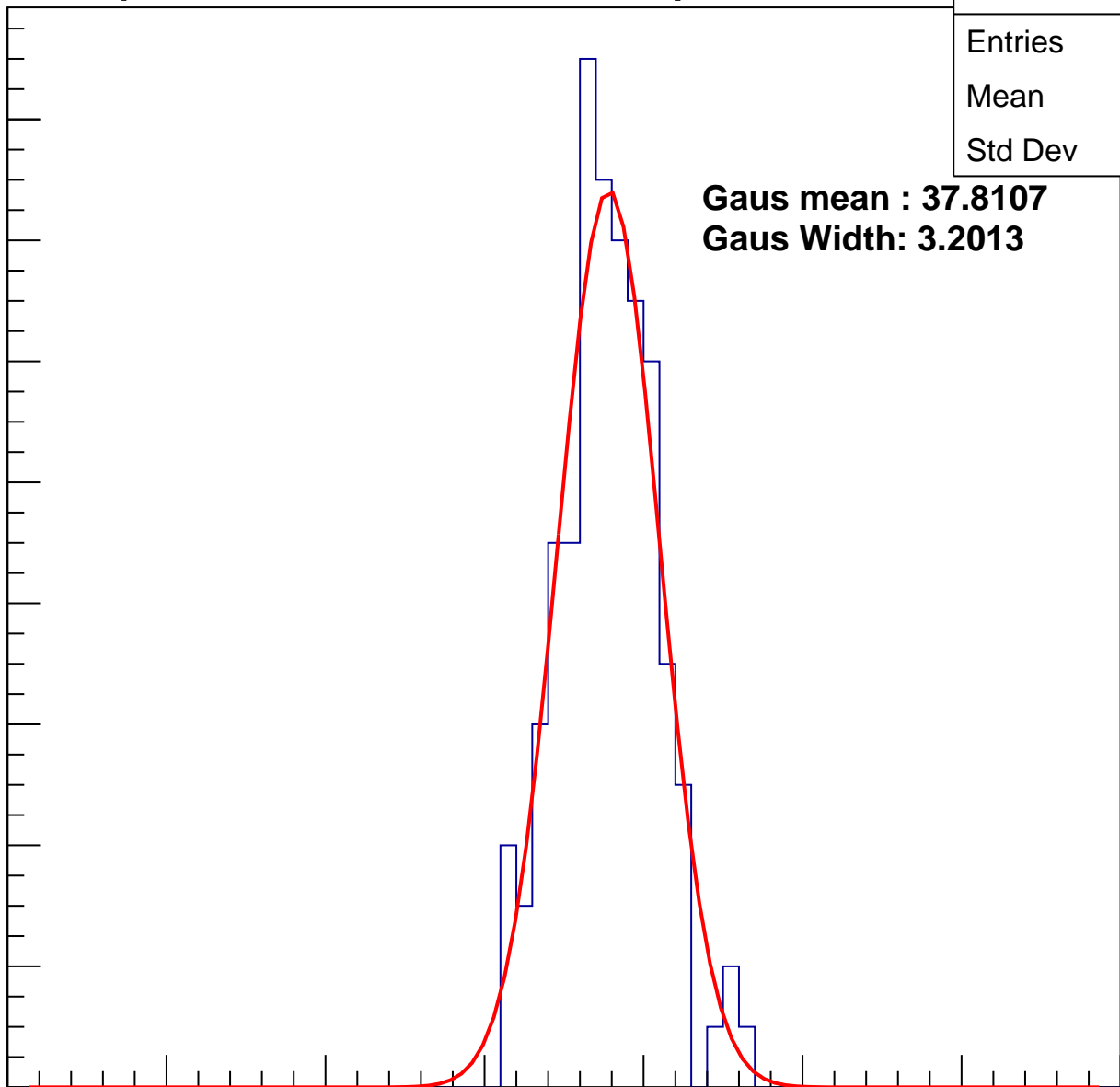
40

50

60

70

ampl



B0L101S, U15-ch41, adc2

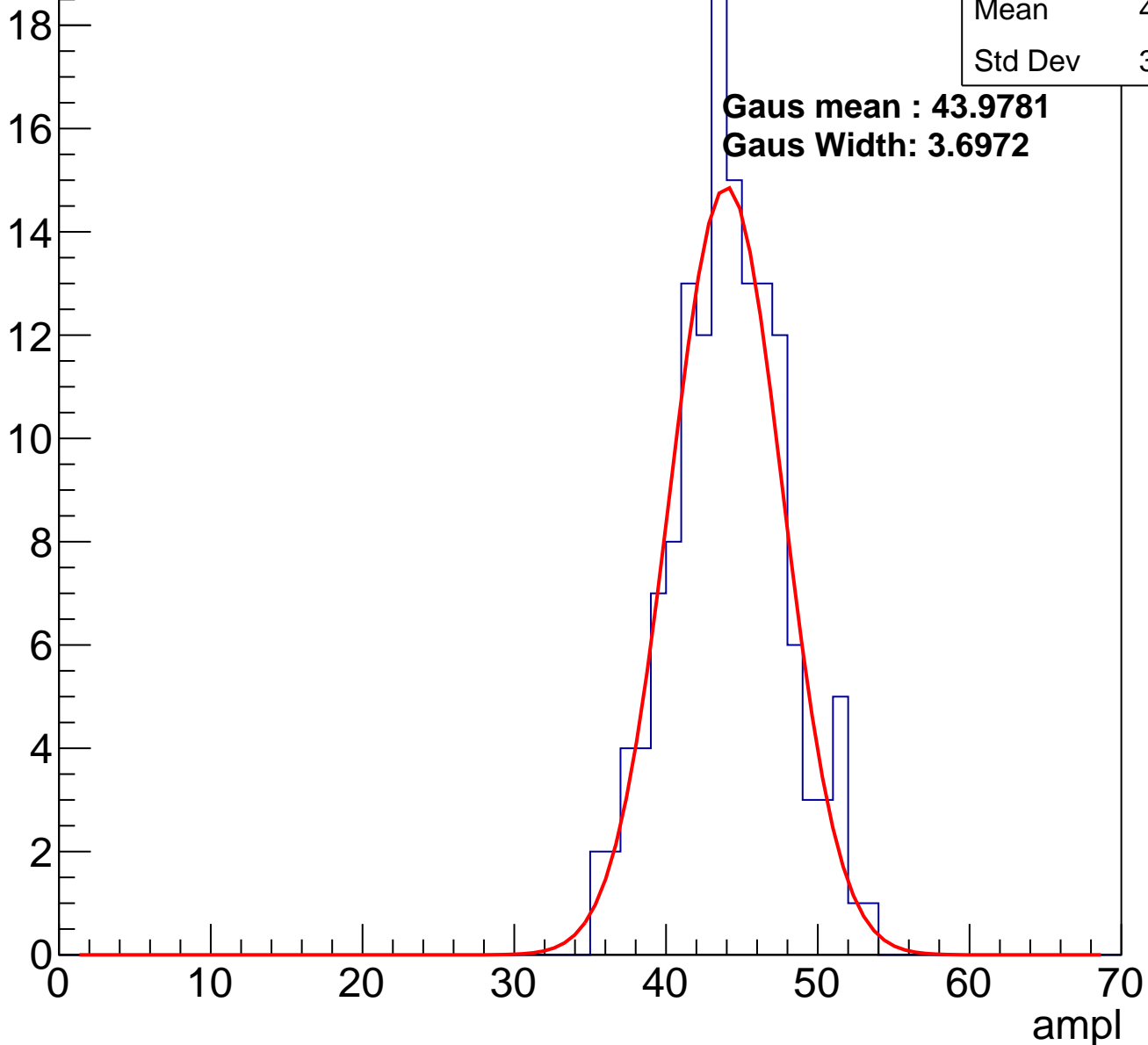
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	43.64
Std Dev	3.684

Gaus mean : 43.9781

Gaus Width: 3.6972

Entry

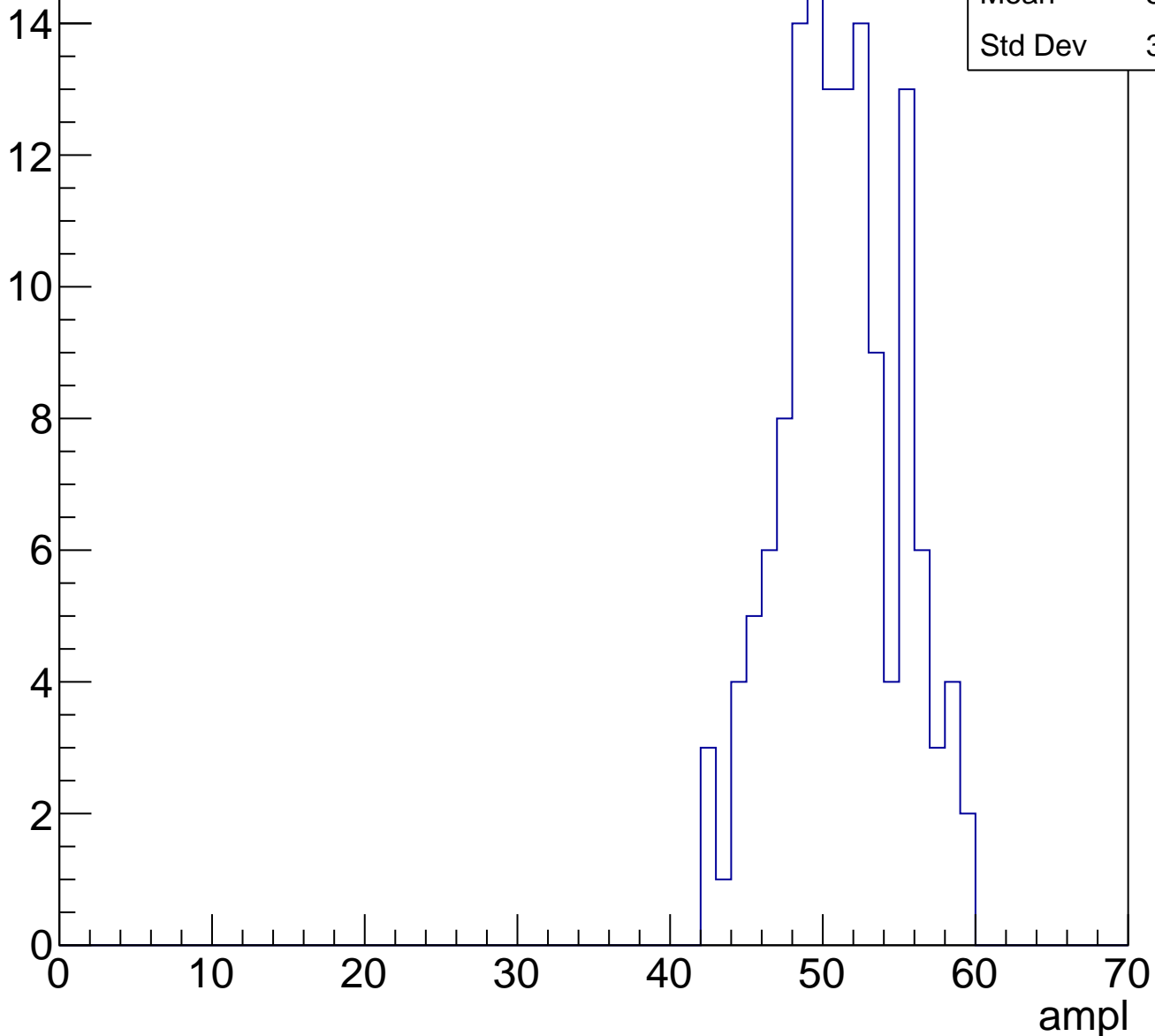


B0L101S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	50.62
Std Dev	3.836

Entry



B0L101S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#1, port C1

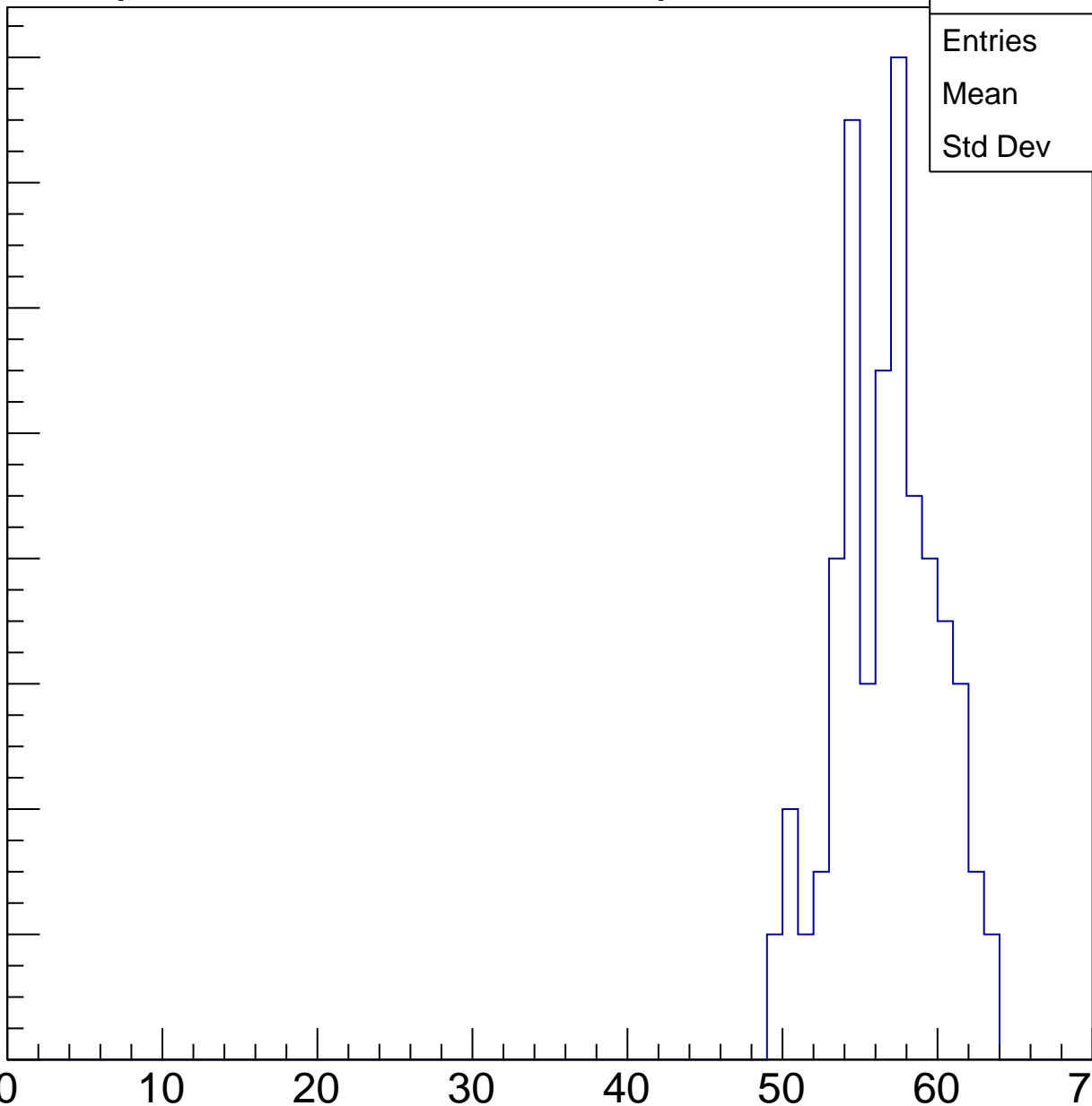
Entries	102
Mean	56.27
Std Dev	3.227

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch41, adc5

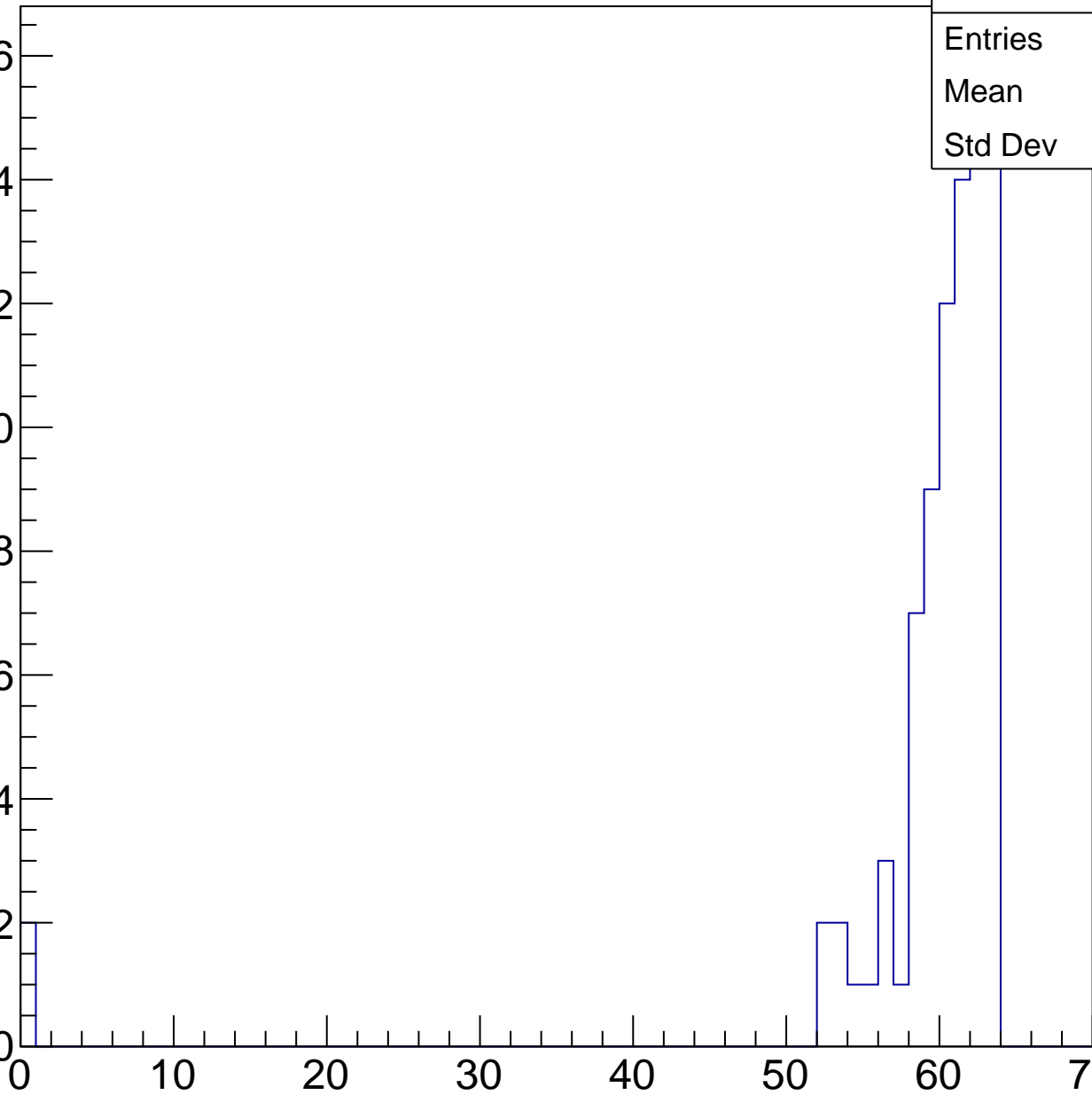
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	85
Mean	58.73
Std Dev	9.491

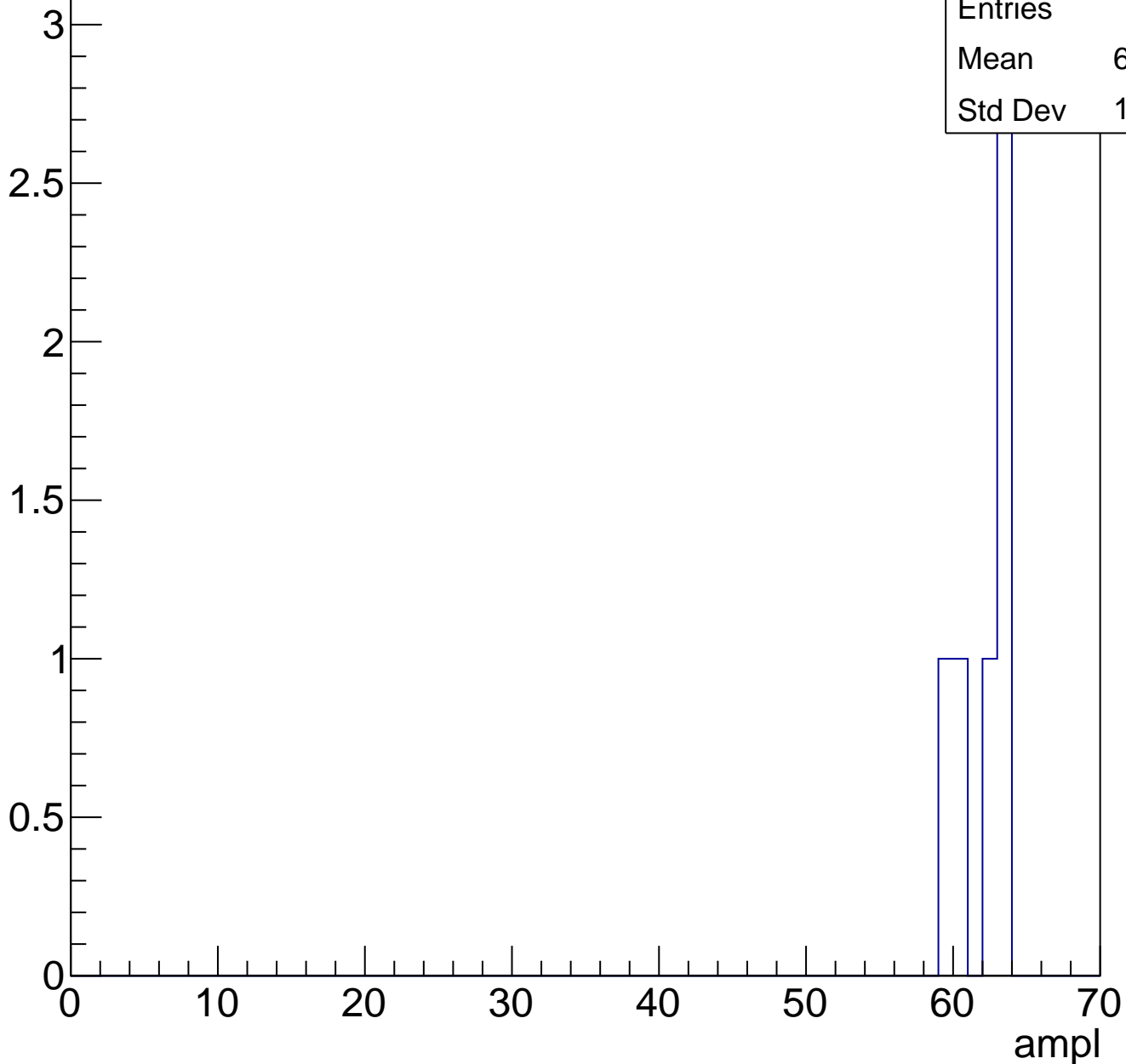
ampl



B0L101S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch42, adc0

calib_packv5_042523_0143.root, FC#1, port C1

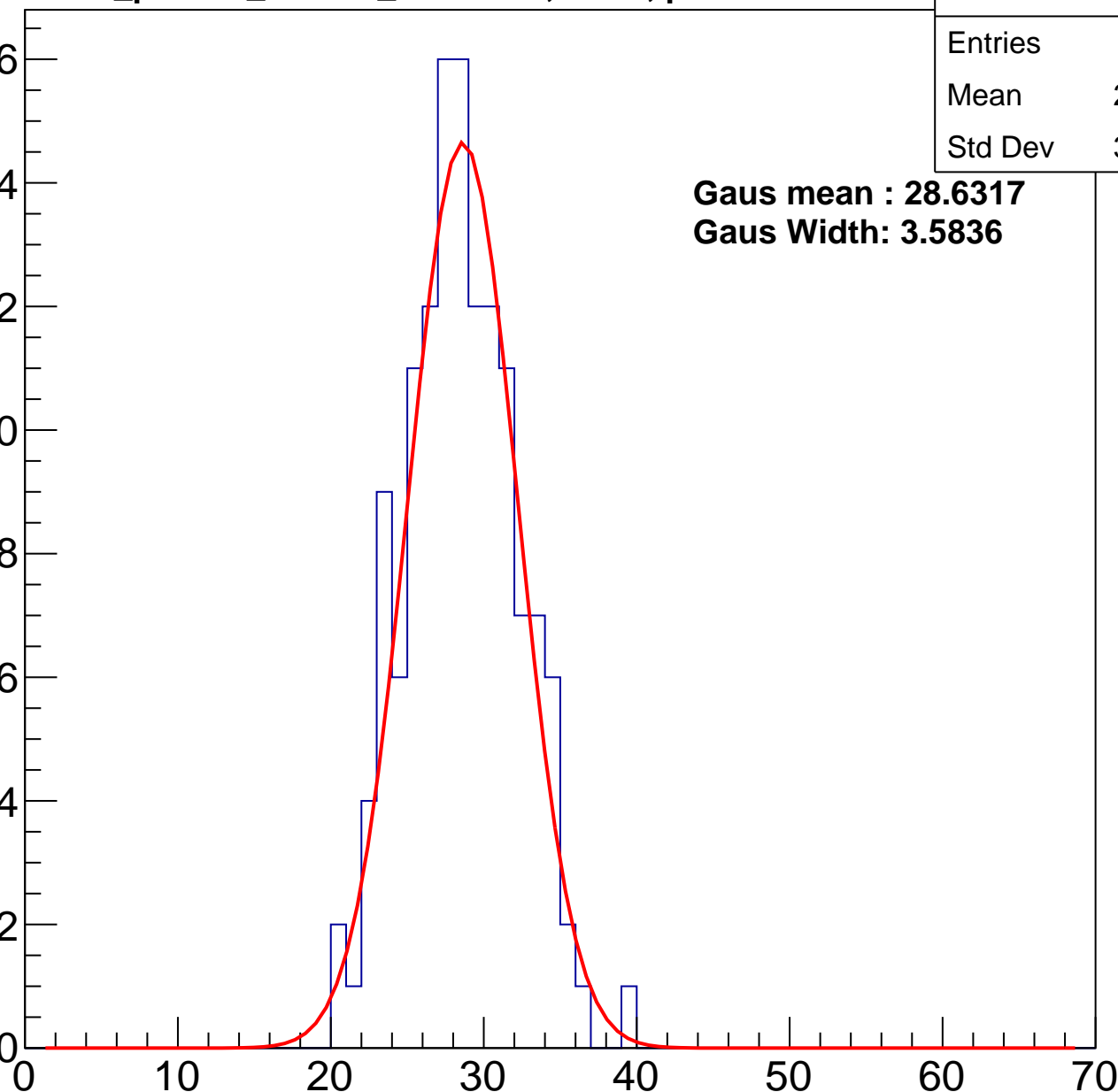
Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	28.09
Std Dev	3.586

Gaus mean : 28.6317

Gaus Width: 3.5836



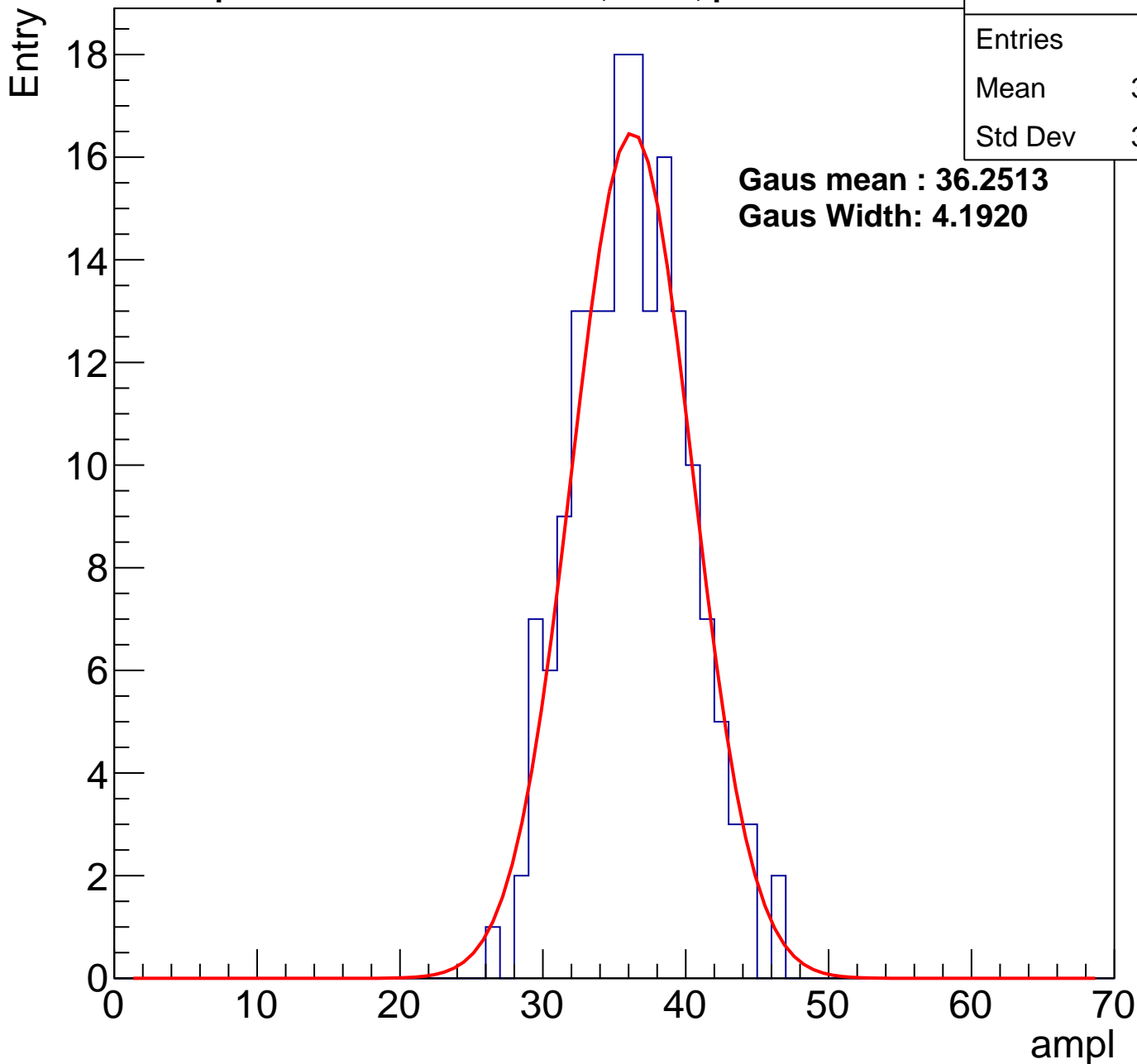
ampl

B0L101S, U15-ch42, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	35.78
Std Dev	3.923

Gaus mean : 36.2513
Gaus Width: 4.1920



B0L101S, U15-ch42, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	42.79
Std Dev	3.502

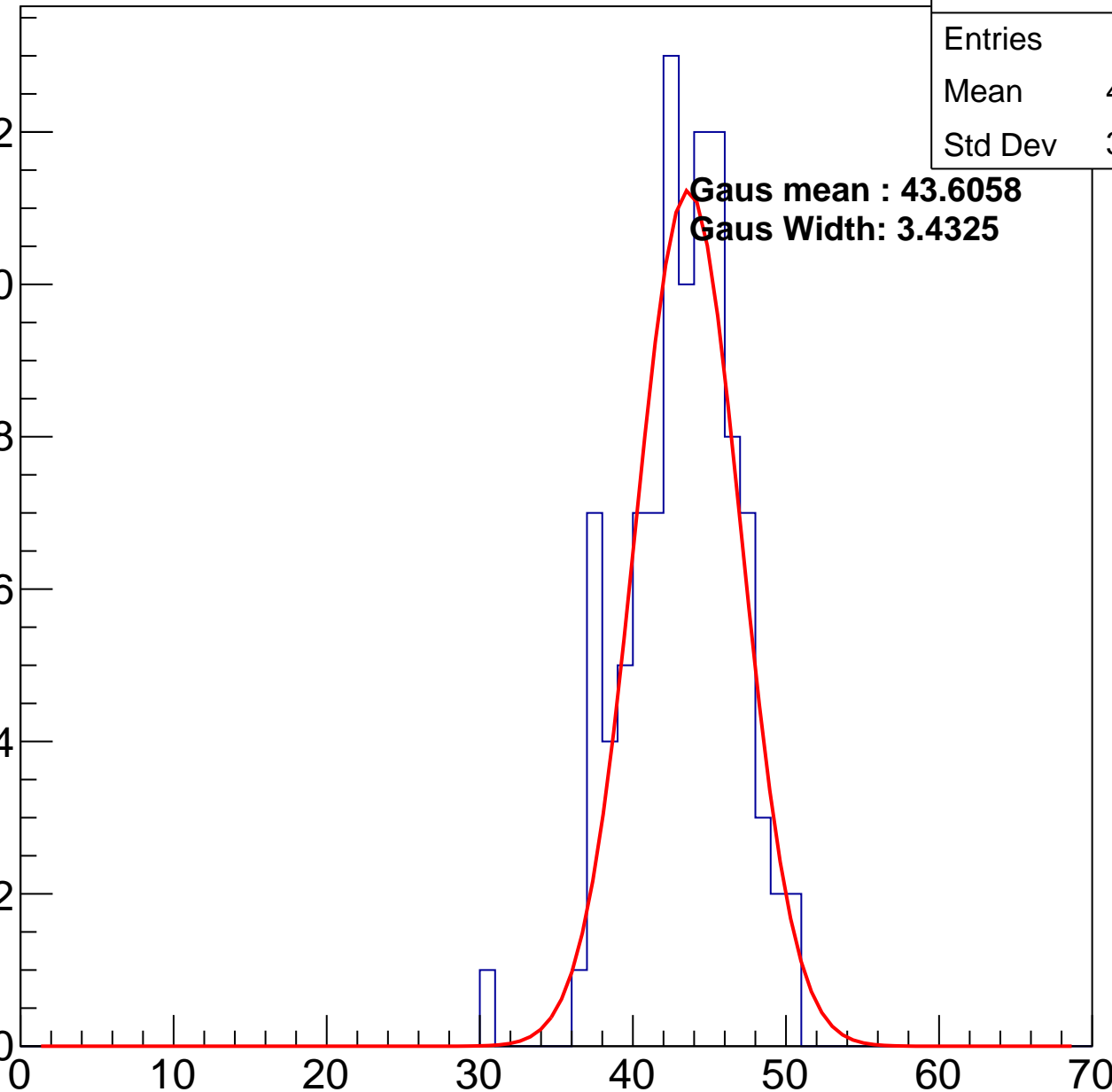
Gaus mean : 43.6058

Gaus Width: 3.4325

Entry

12
10
8
6
4
2
0

ampl



B0L101S, U15-ch42, adc3

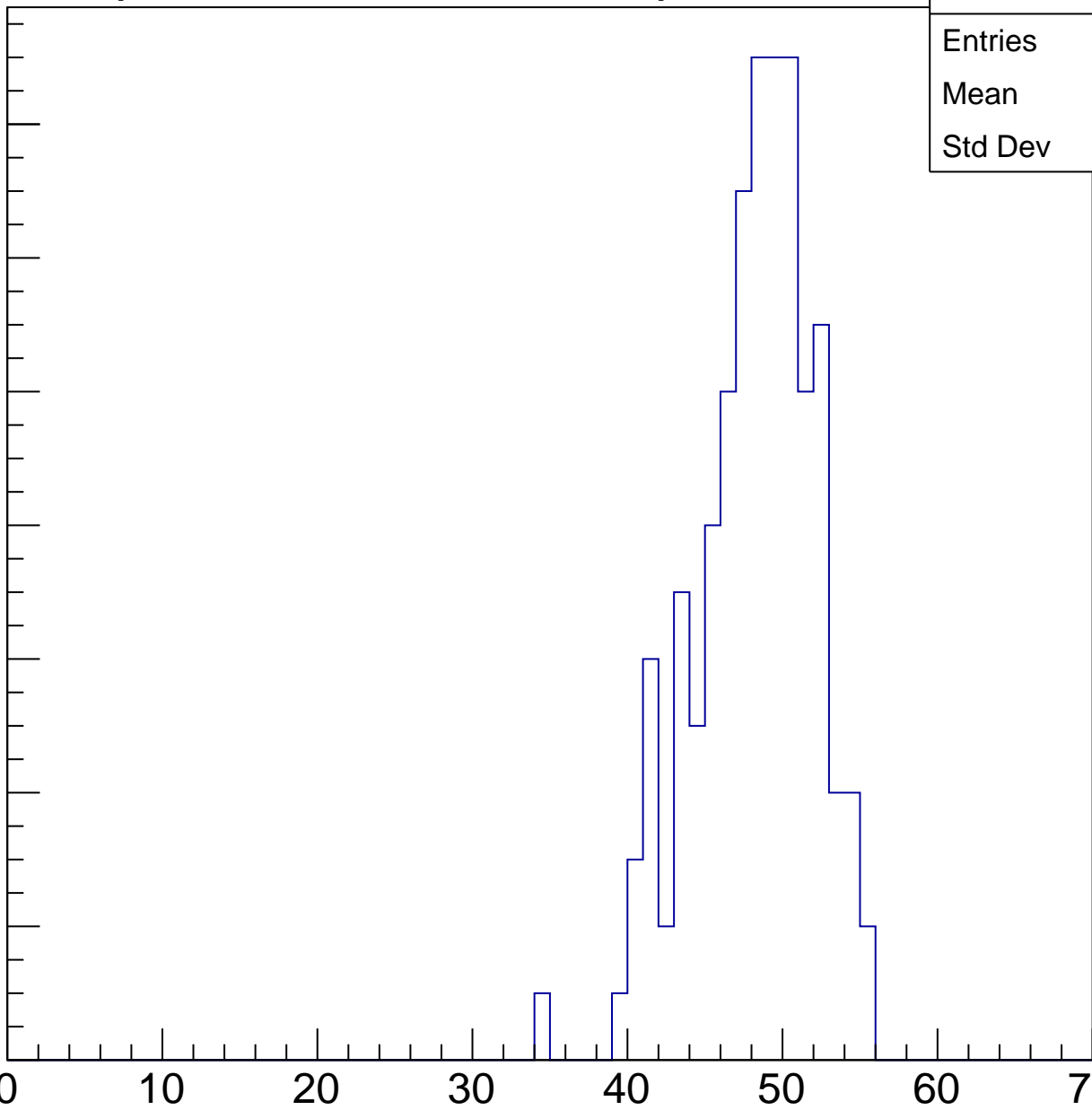
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	132
Mean	47.73
Std Dev	3.782

ampl



B0L101S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#1, port C1

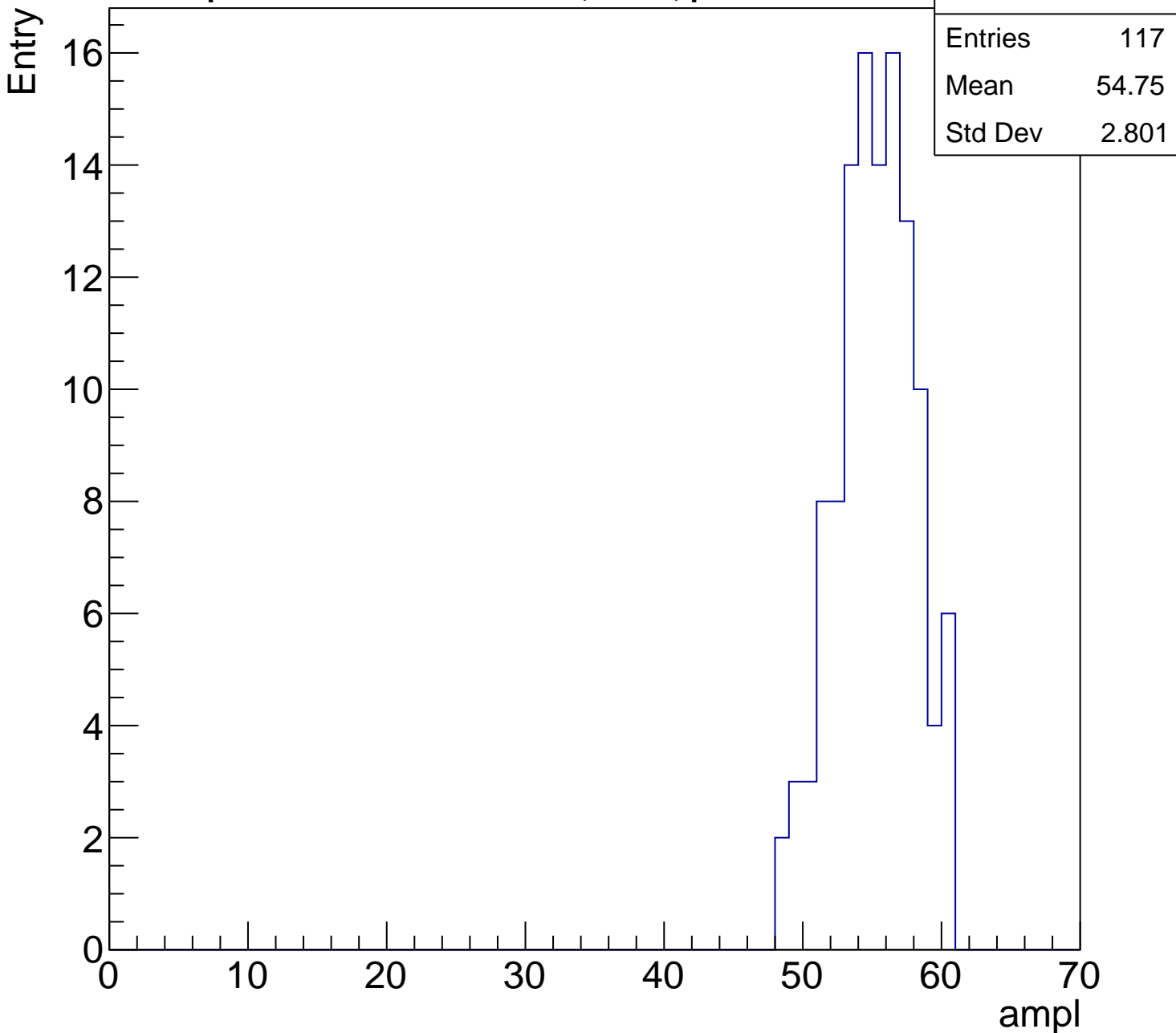
Entries	117
Mean	54.75
Std Dev	2.801

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch42, adc5

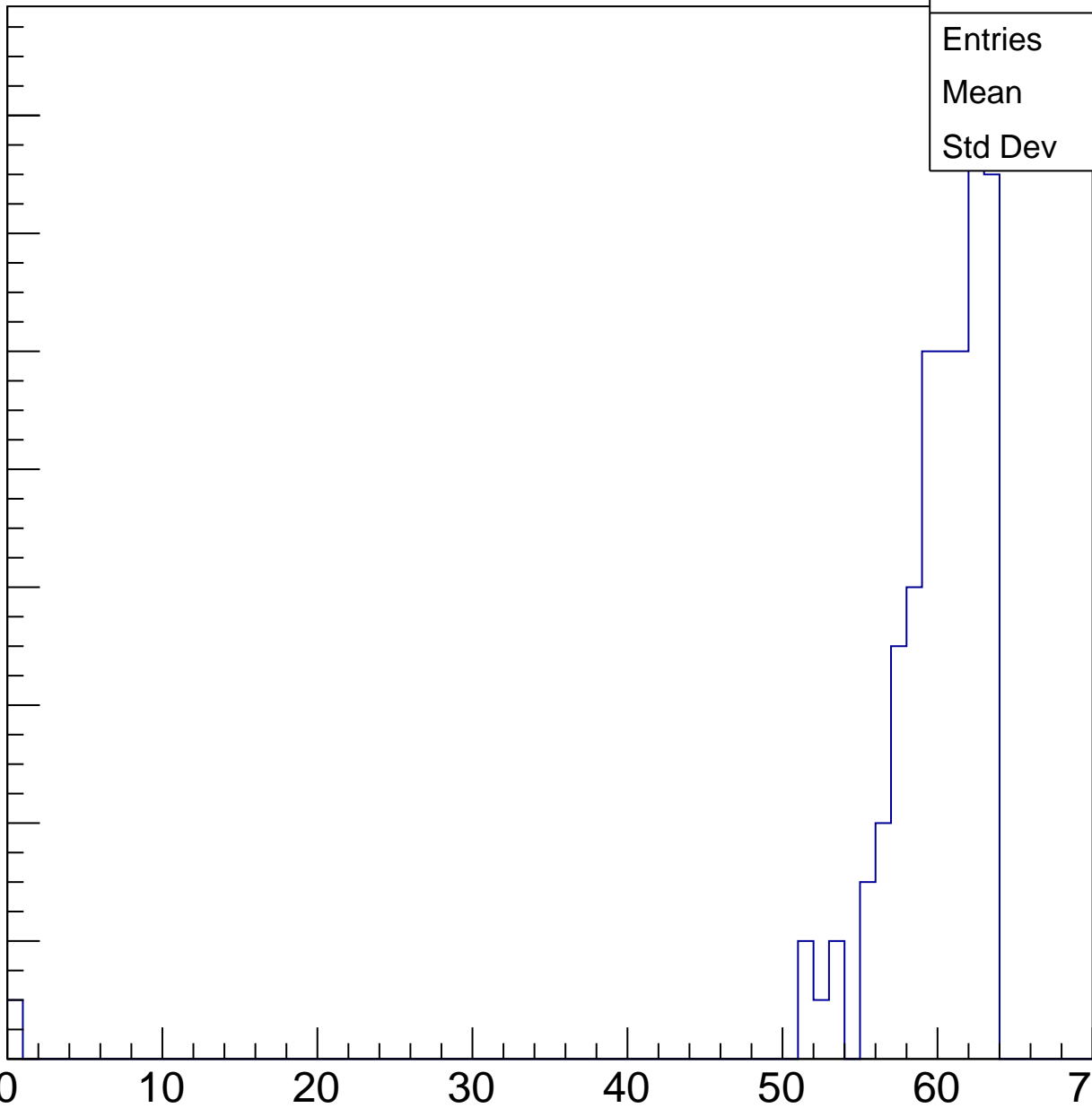
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.07
Std Dev	6.695

ampl

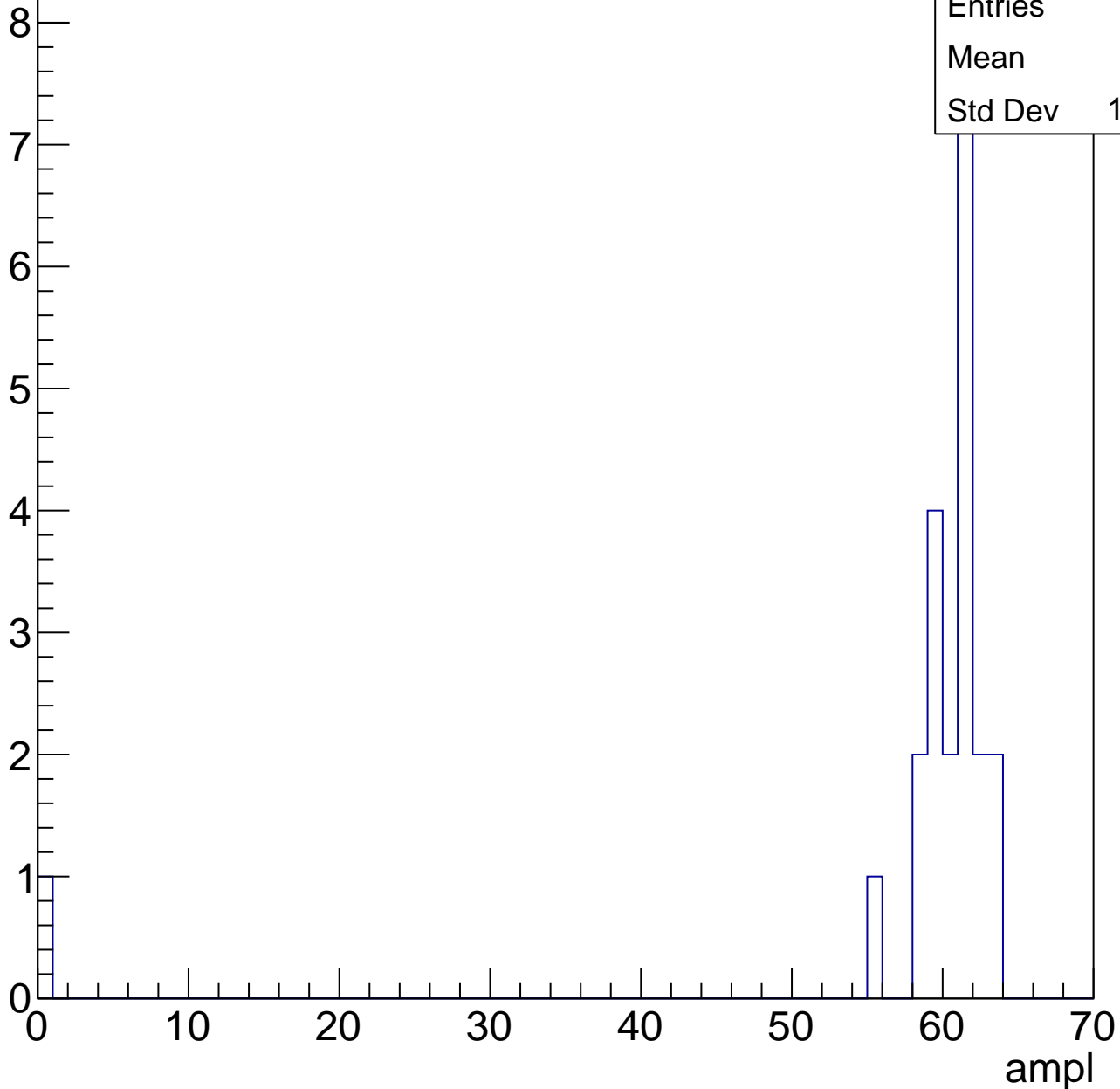


B0L101S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

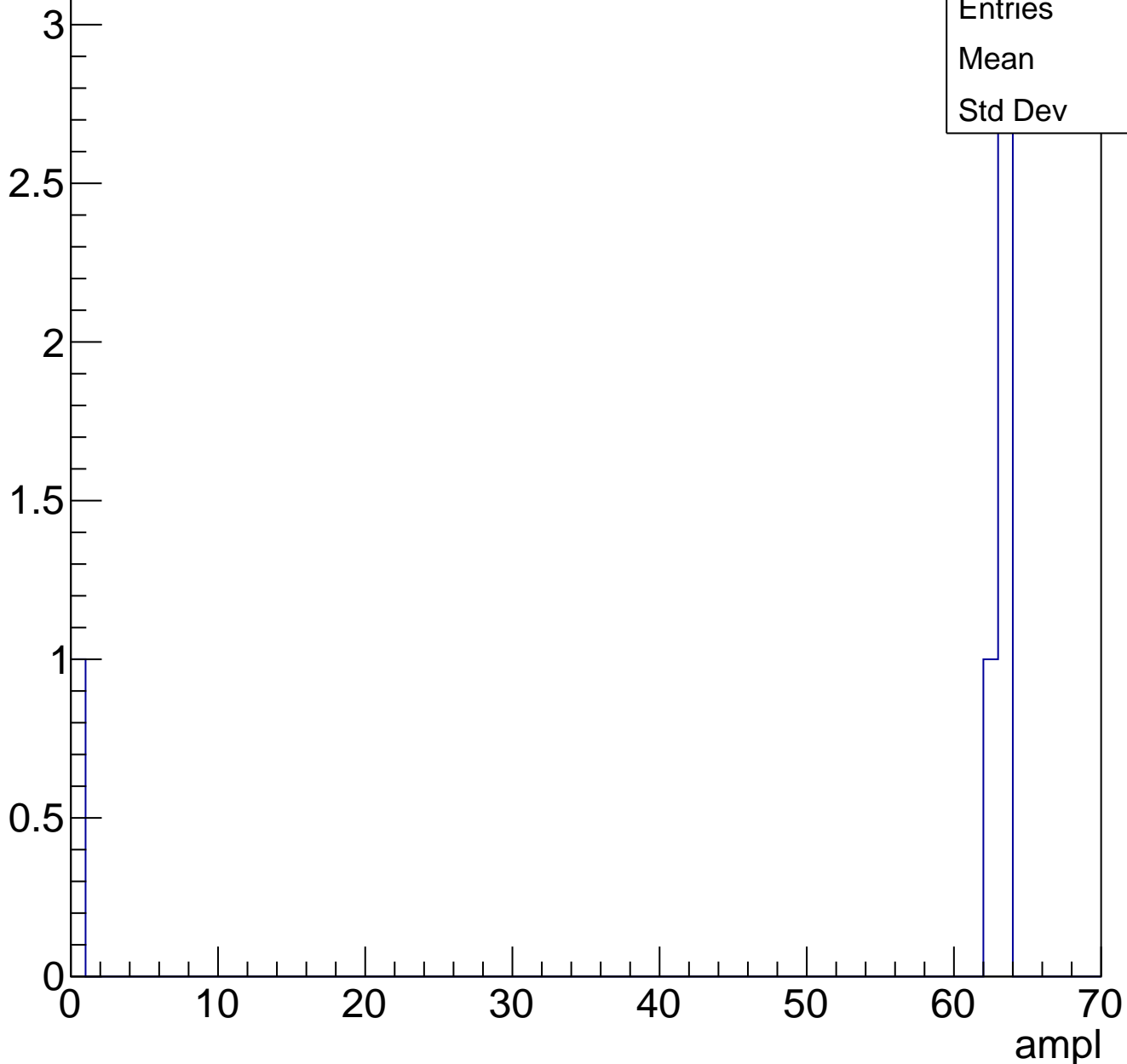
Entries	22
Mean	57.5
Std Dev	12.67



B0L101S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch43, adc0

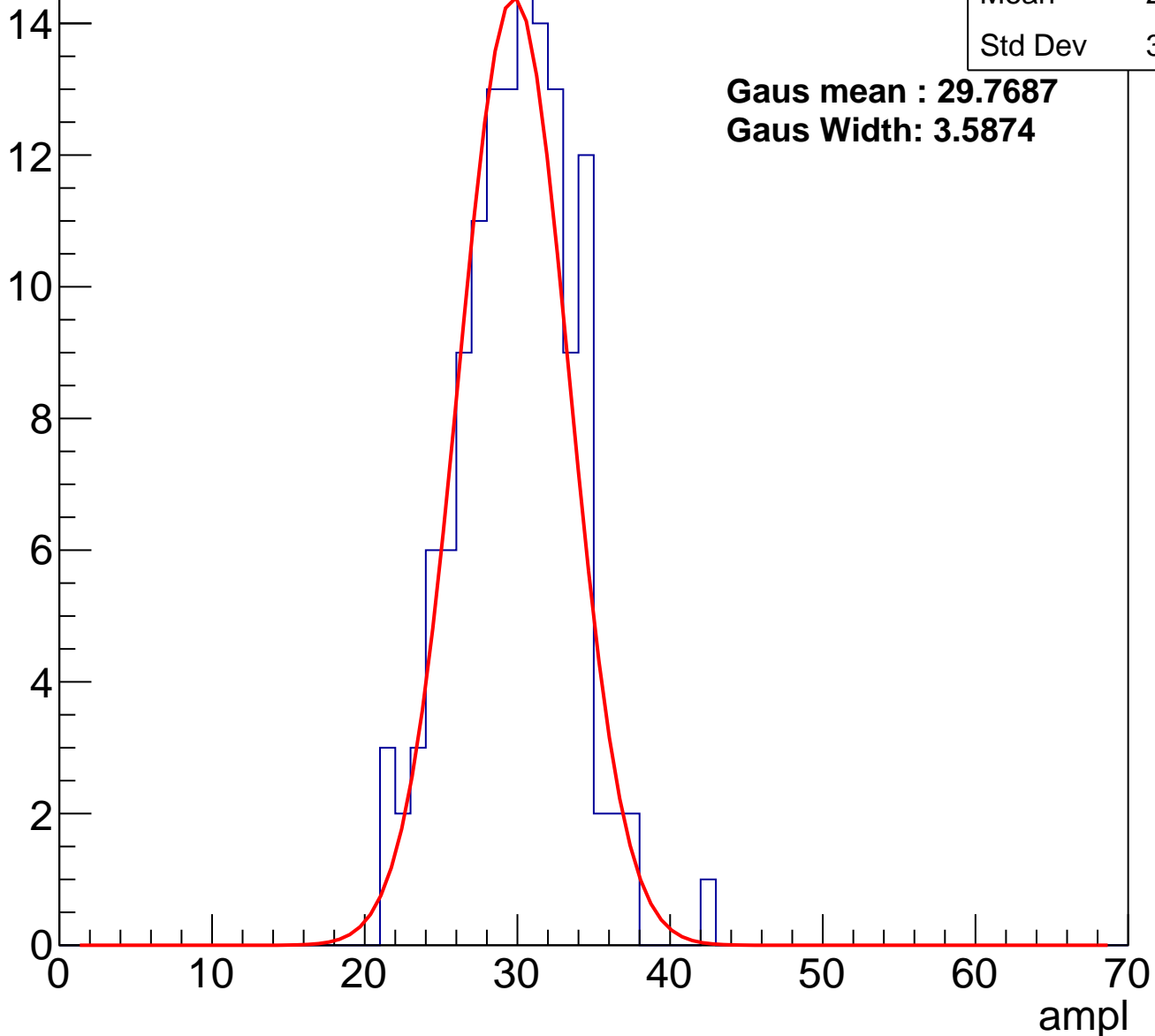
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	29.45
Std Dev	3.692

Gaus mean : 29.7687

Gaus Width: 3.5874

Entry



B0L101S, U15-ch43, adc1

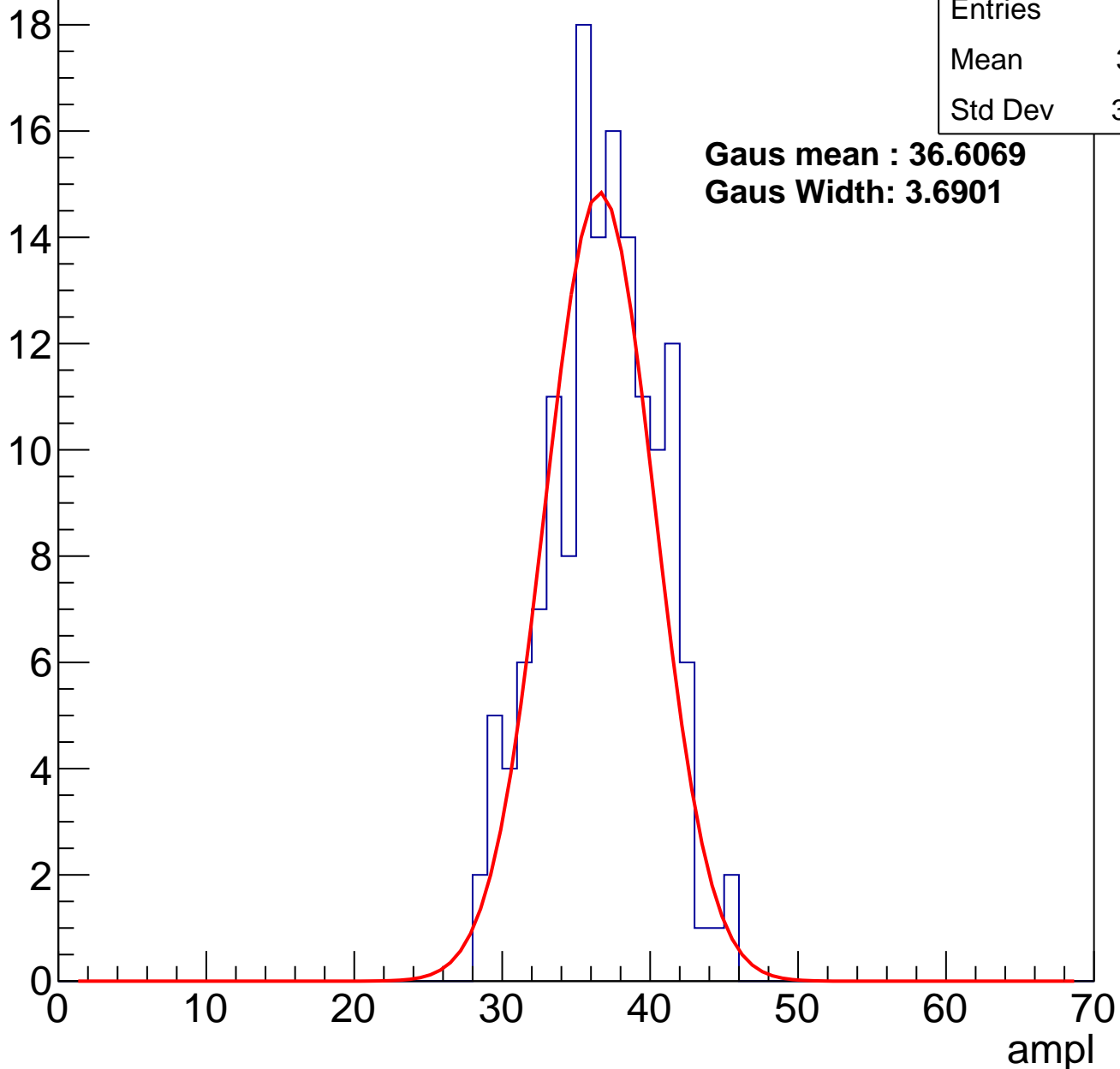
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	36.31
Std Dev	3.709

Gaus mean : 36.6069

Gaus Width: 3.6901

Entry



B0L101S, U15-ch43, adc2

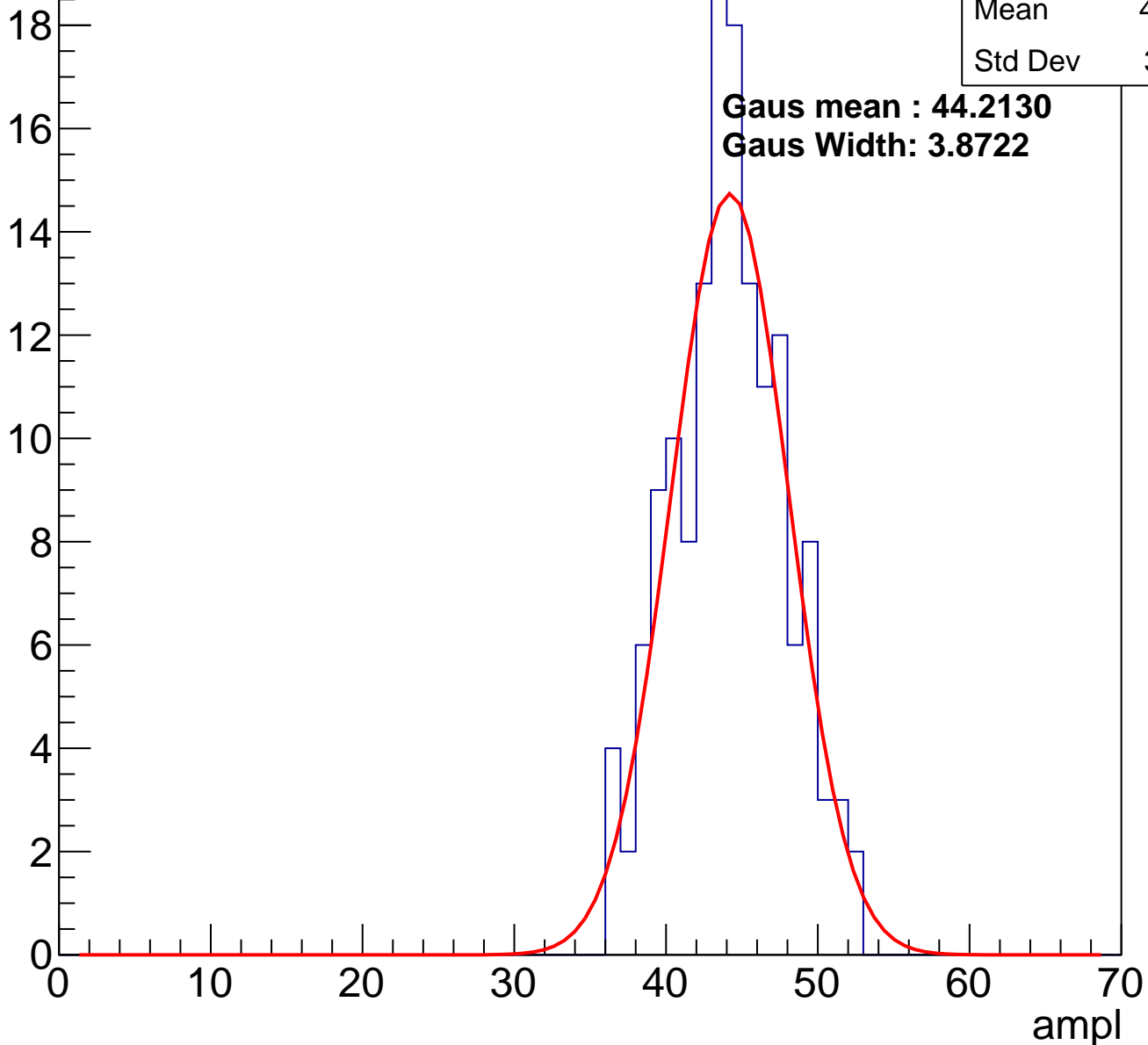
calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	43.69
Std Dev	3.621

Gaus mean : 44.2130

Gaus Width: 3.8722

Entry



B0L101S, U15-ch43, adc3

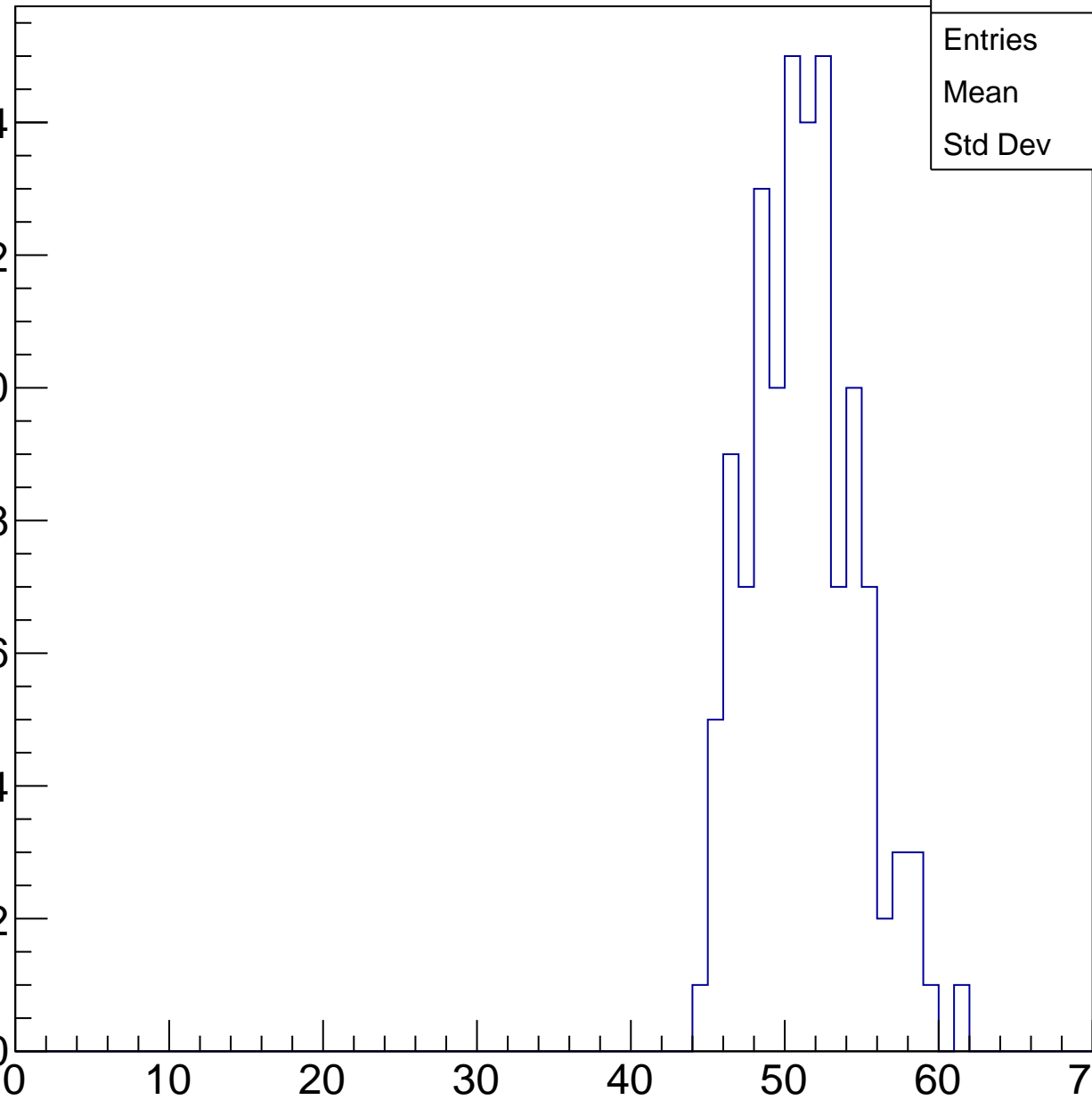
calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	50.76
Std Dev	3.427

Entry

14
12
10
8
6
4
2
0

ampl

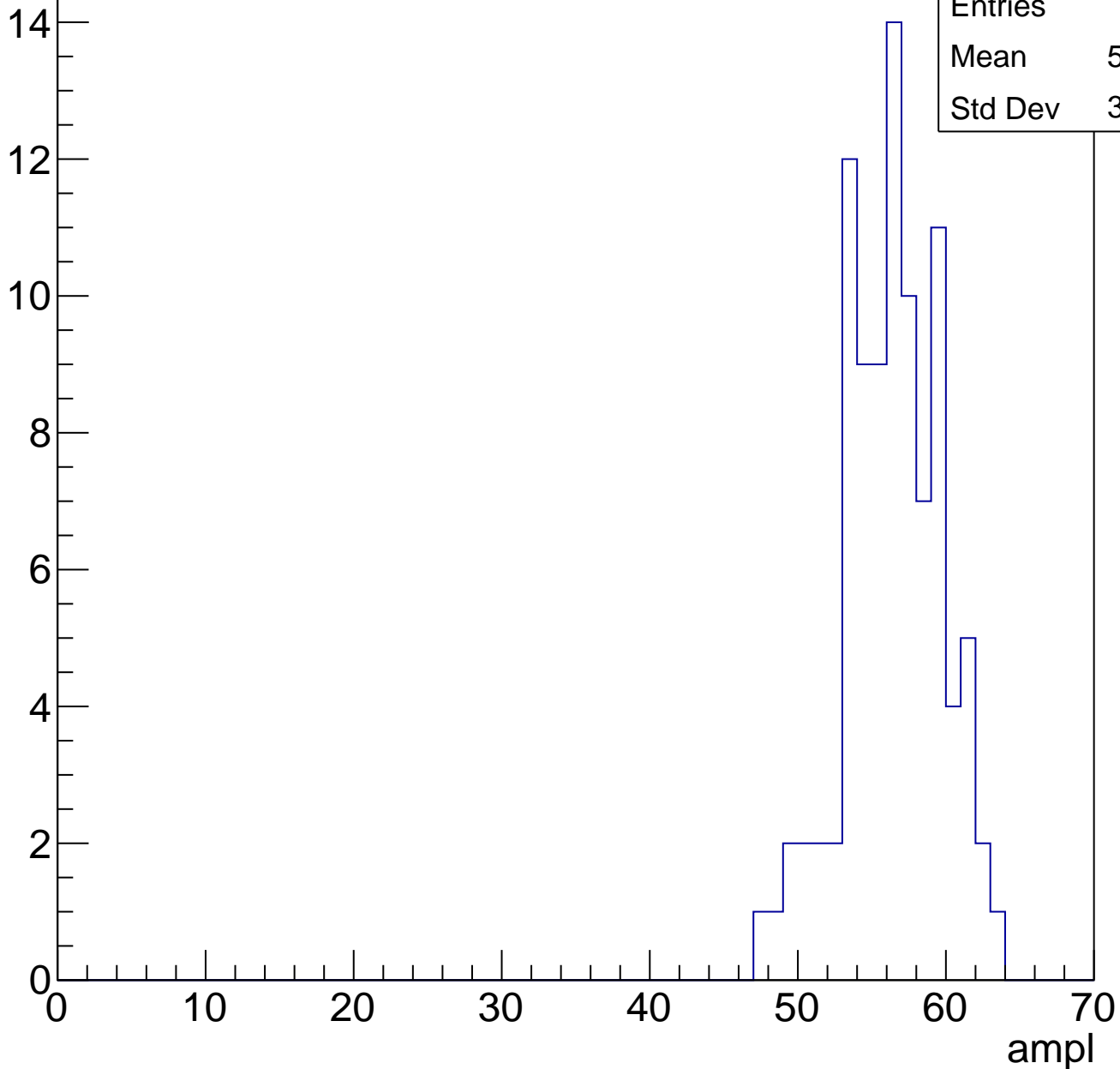


B0L101S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	55.93
Std Dev	3.256

Entry



B0L101S, U15-ch43, adc5

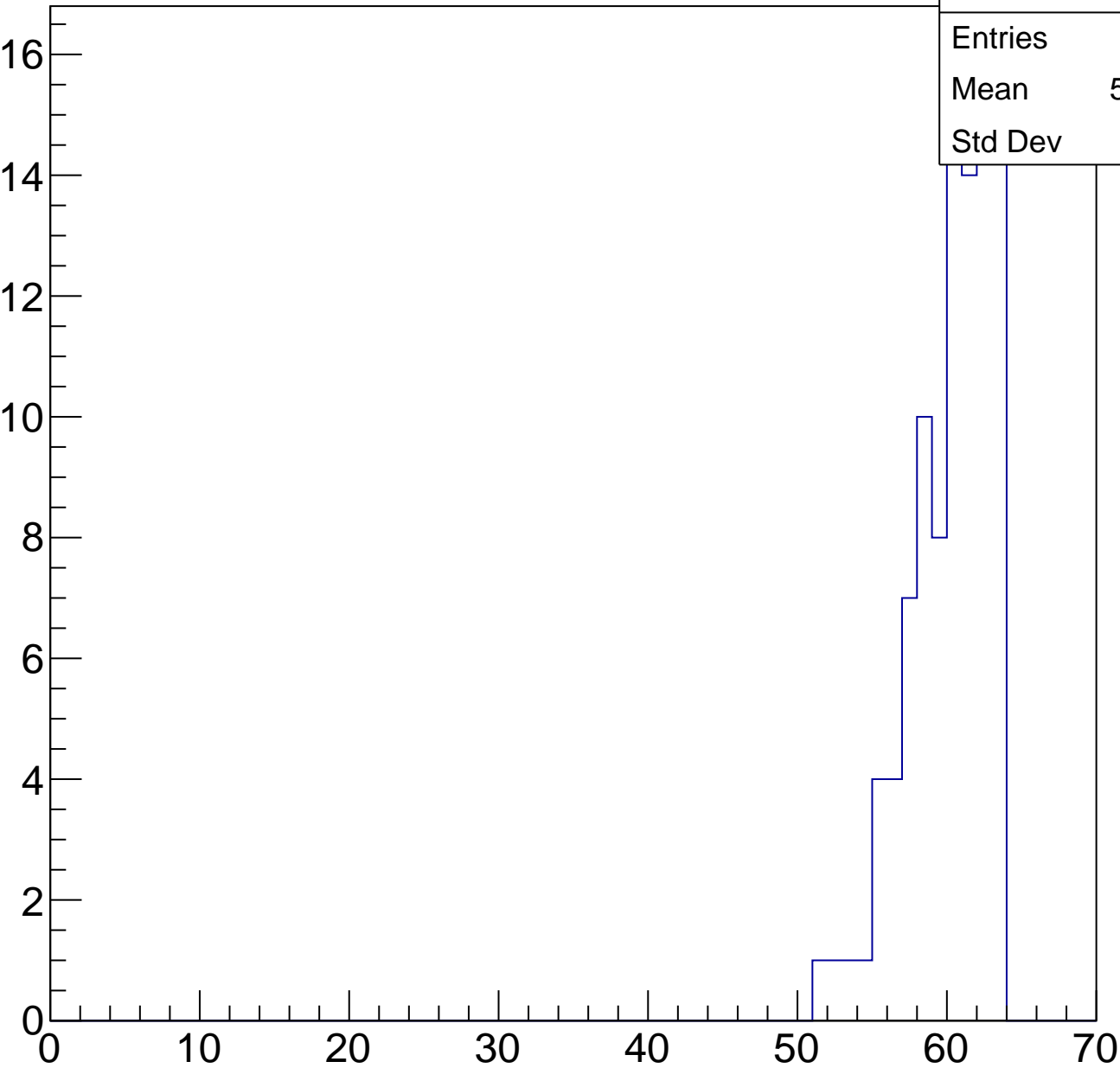
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	59.76
Std Dev	2.72

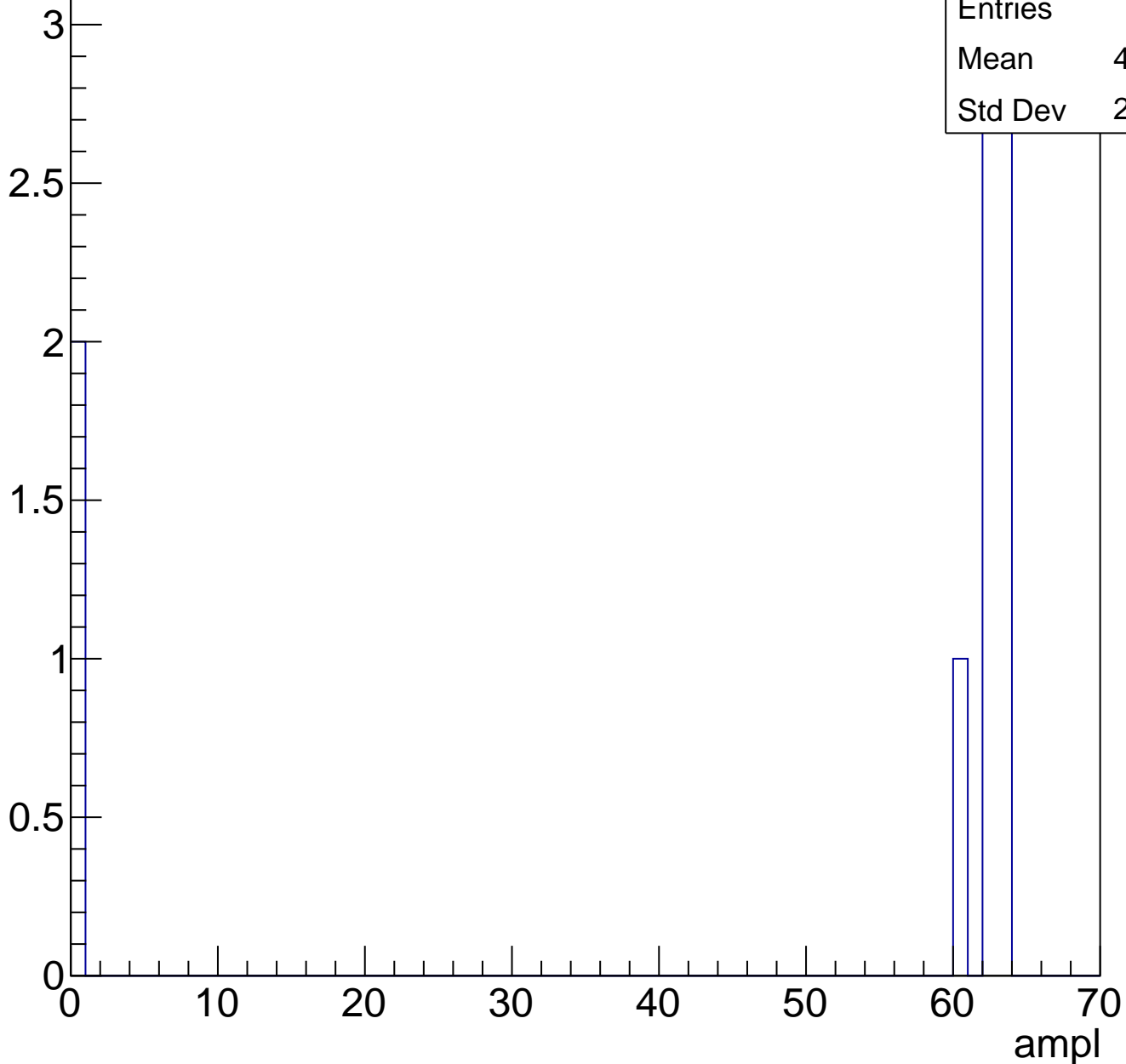
ampl



B0L101S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch44, adc0

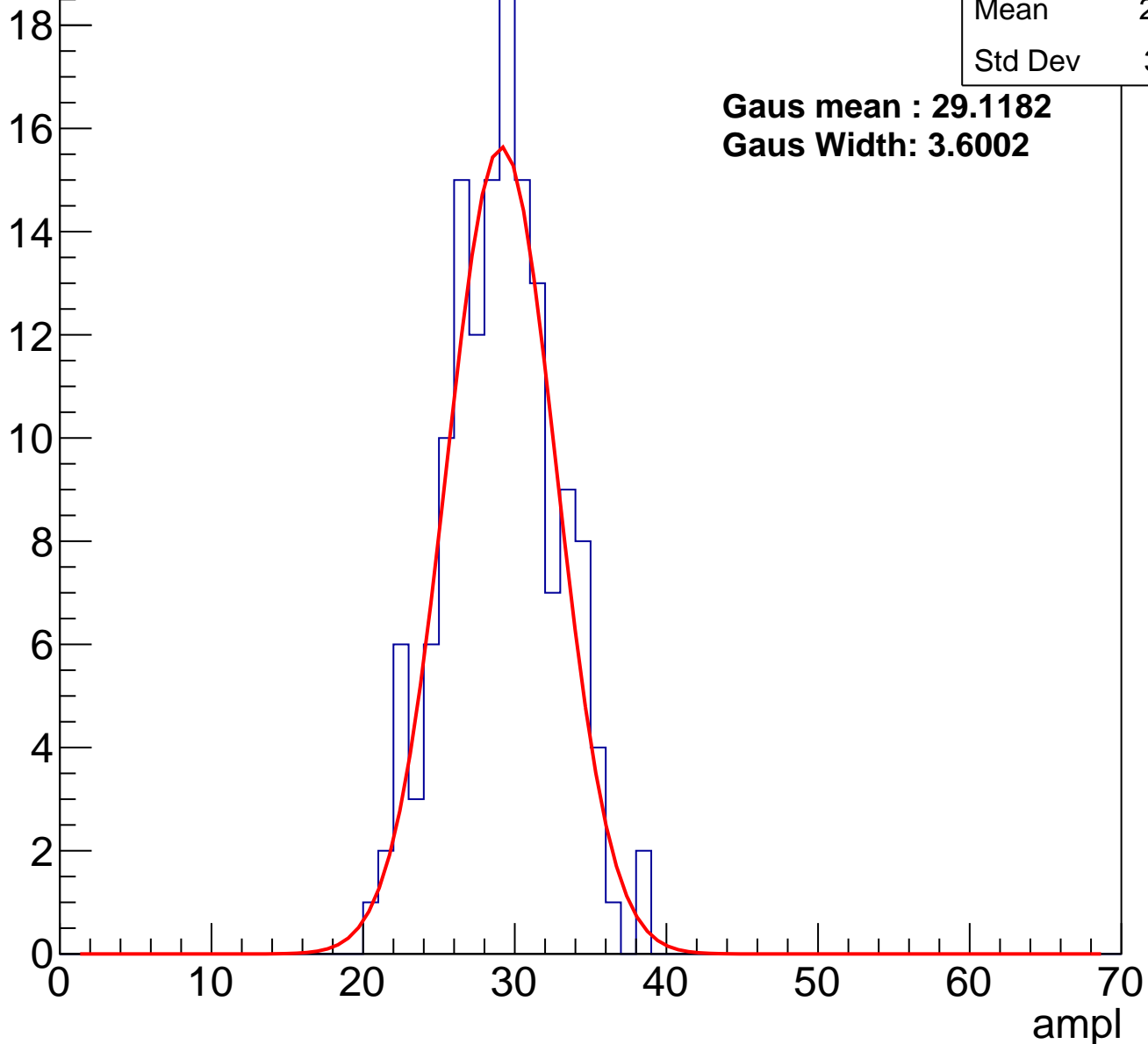
calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	28.65
Std Dev	3.611

Gaus mean : 29.1182

Gaus Width: 3.6002

Entry



B0L101S, U15-ch44, adc1

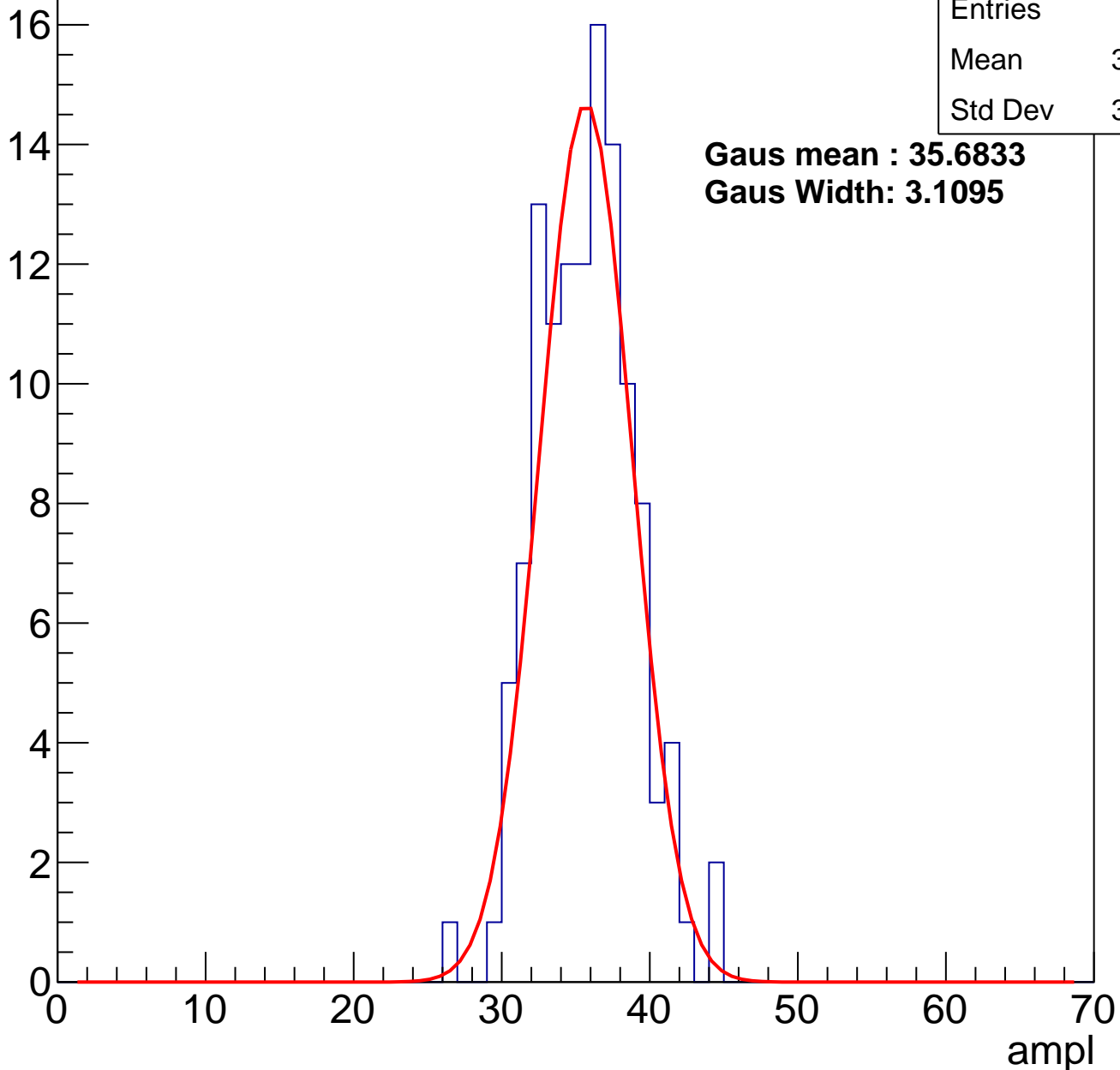
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	35.24
Std Dev	3.215

Gaus mean : 35.6833

Gaus Width: 3.1095

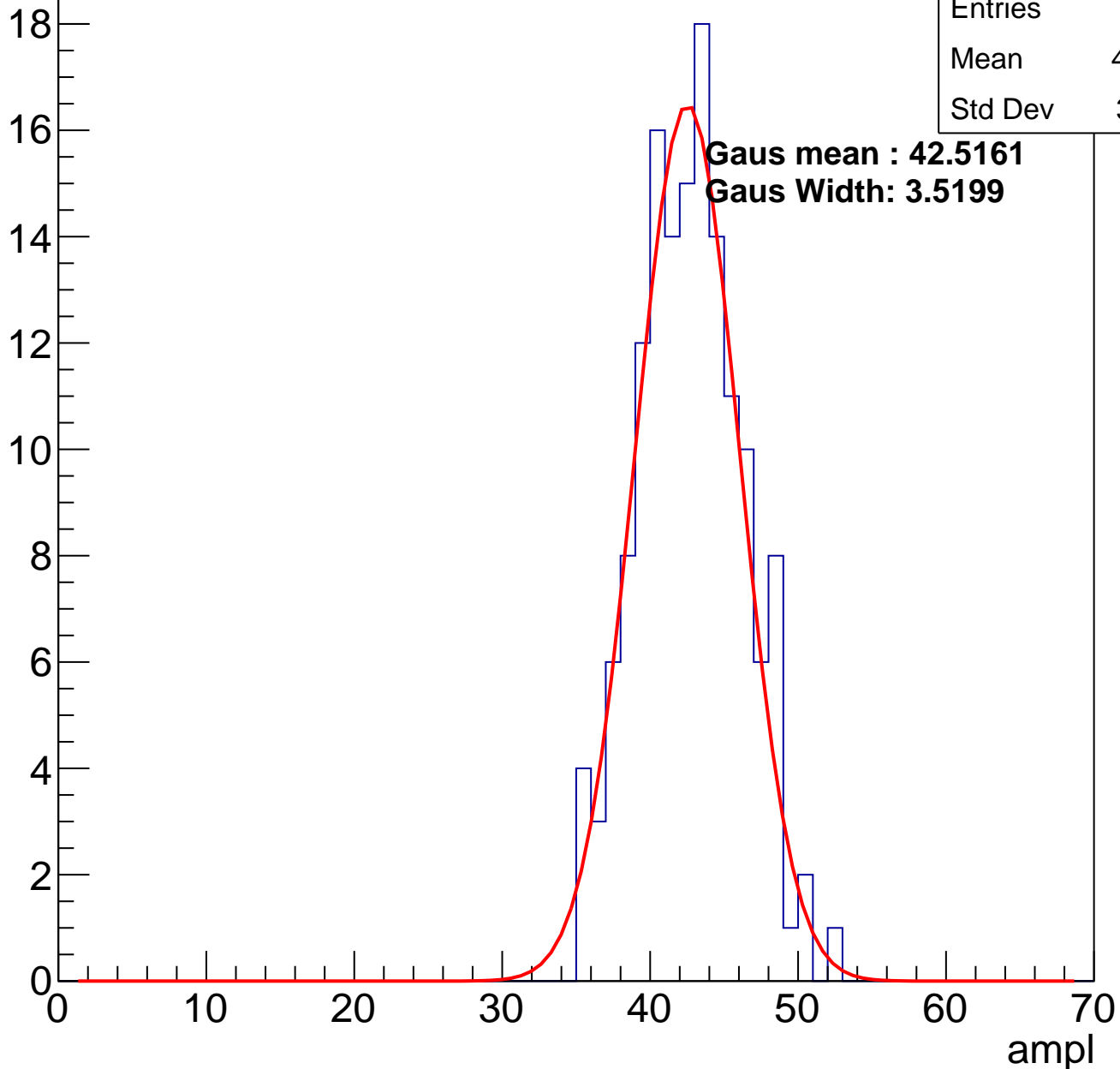
Entry



B0L101S, U15-ch44, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	149
Mean	42.27
Std Dev	3.481

Gaus mean : 42.5161

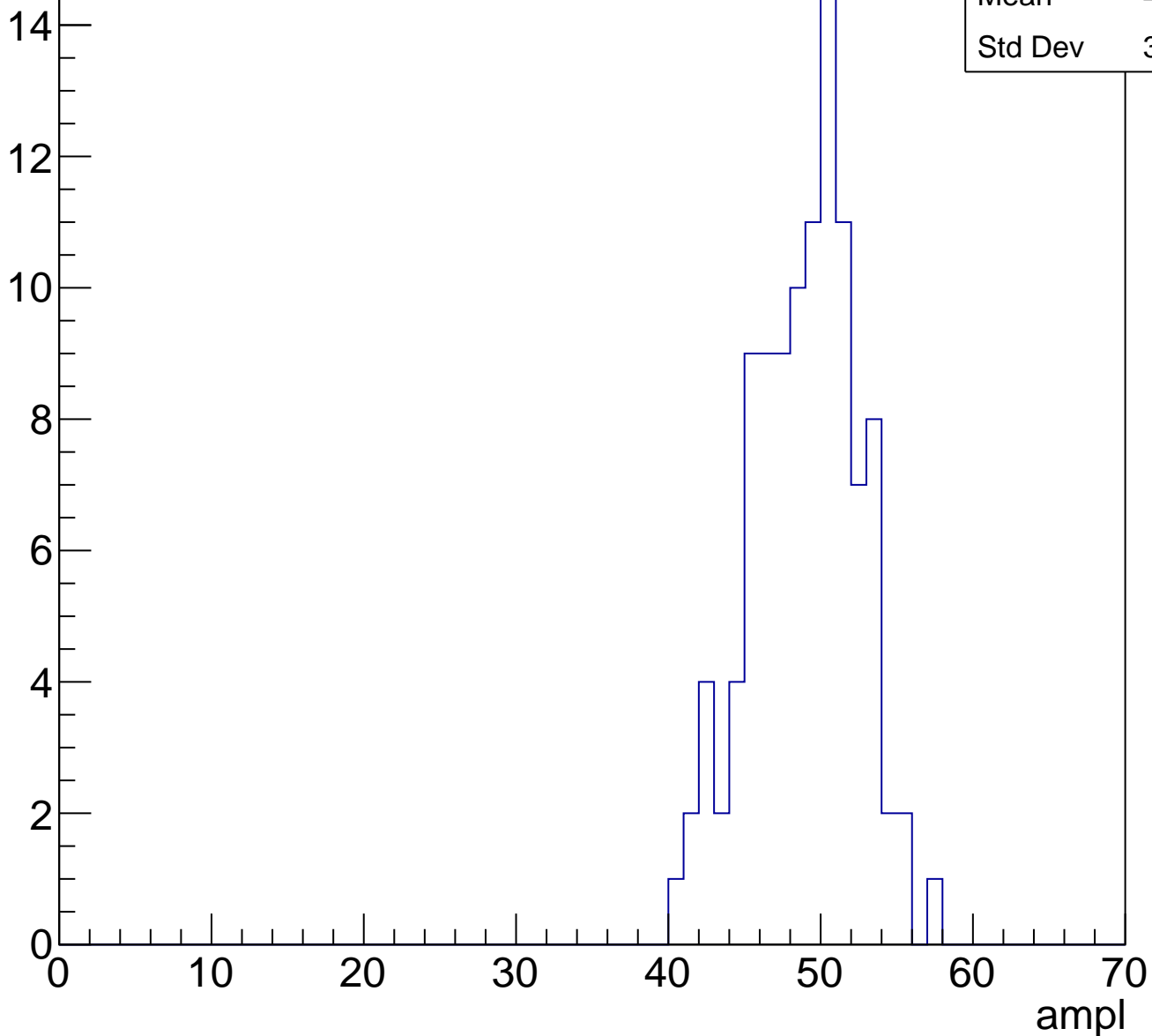
Gaus Width: 3.5199

B0L101S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	48.48
Std Dev	3.424

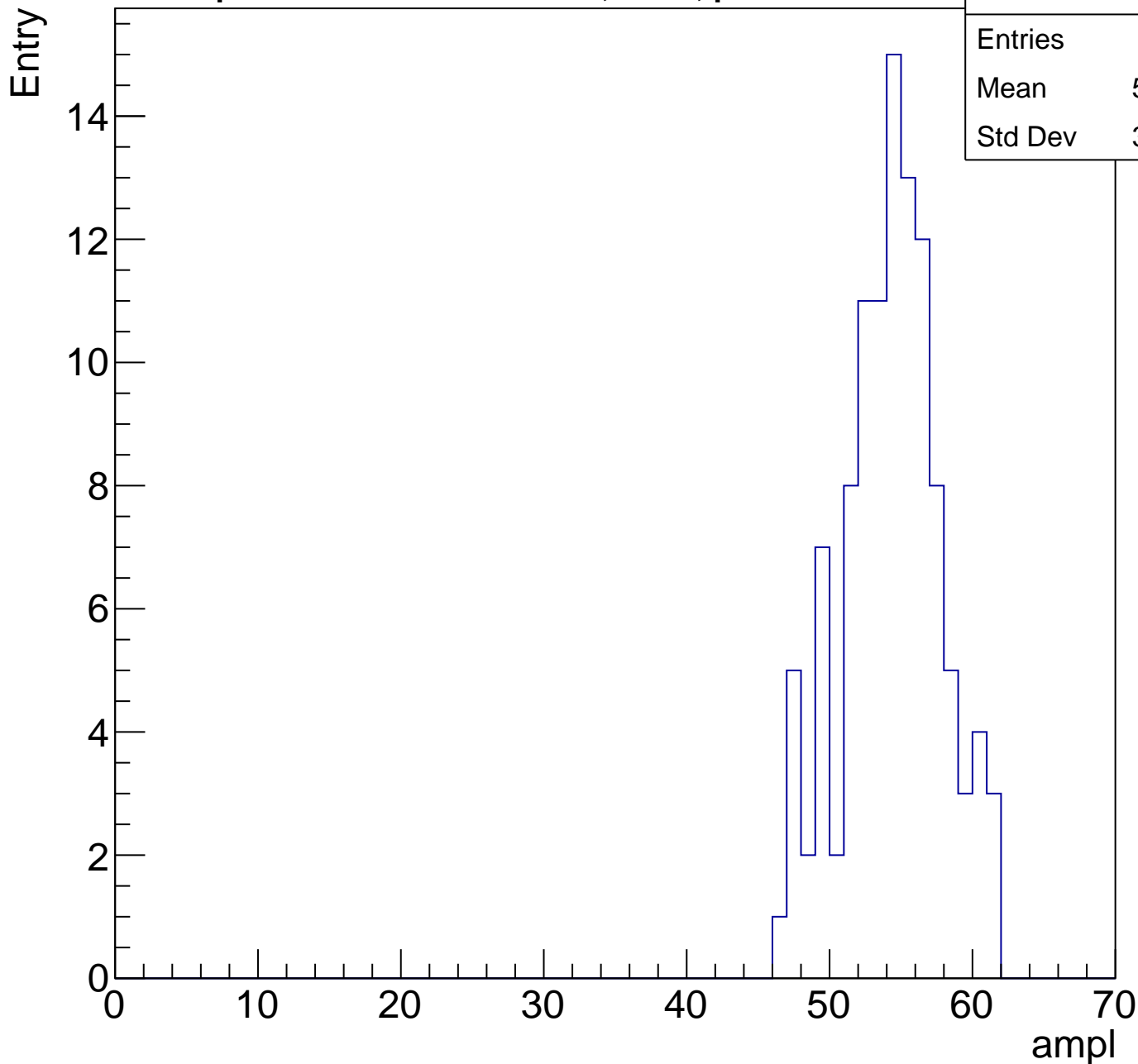
Entry



B0L101S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	53.87
Std Dev	3.427



B0L101S, U15-ch44, adc5

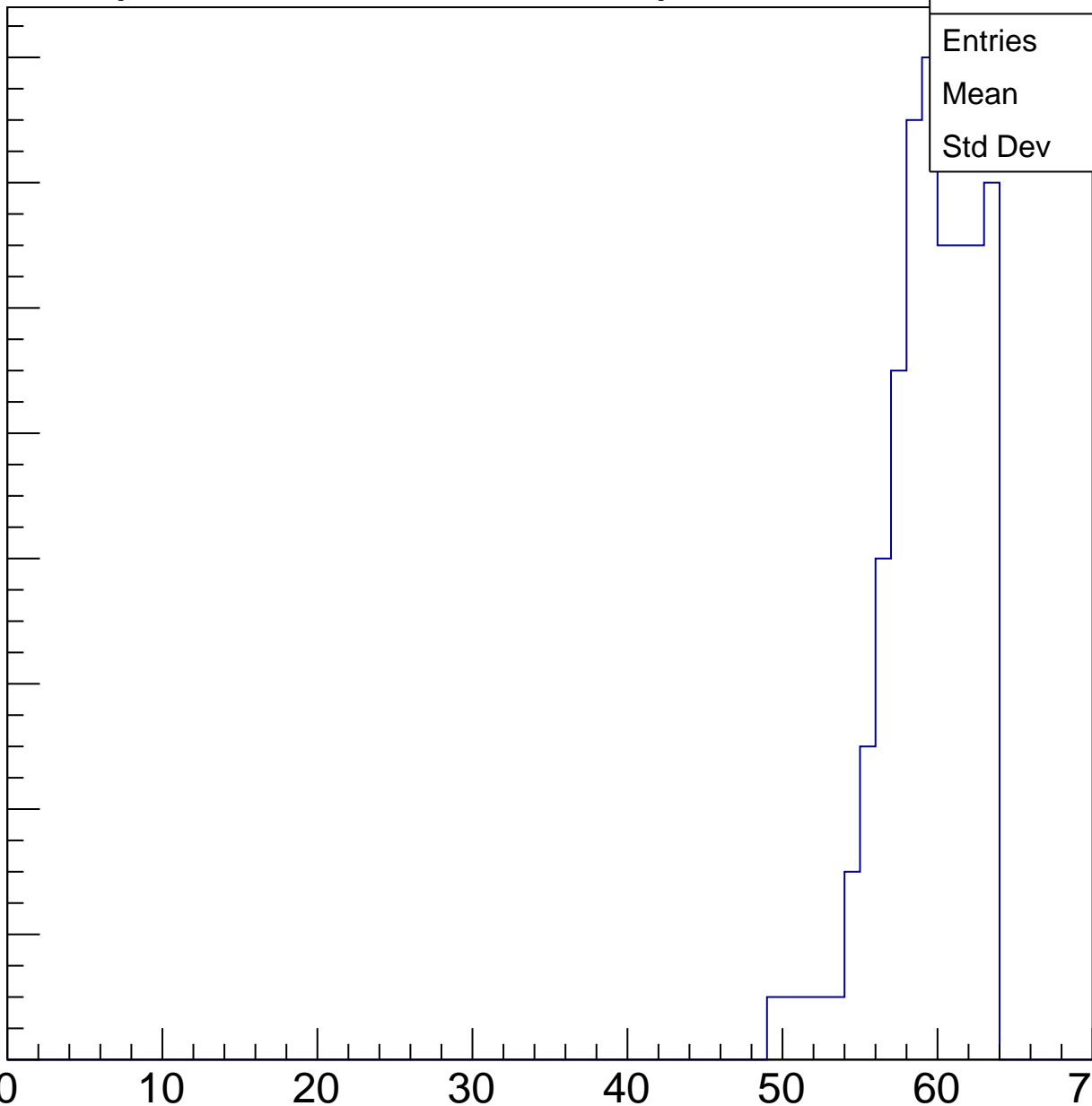
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	58.98
Std Dev	2.968

ampl

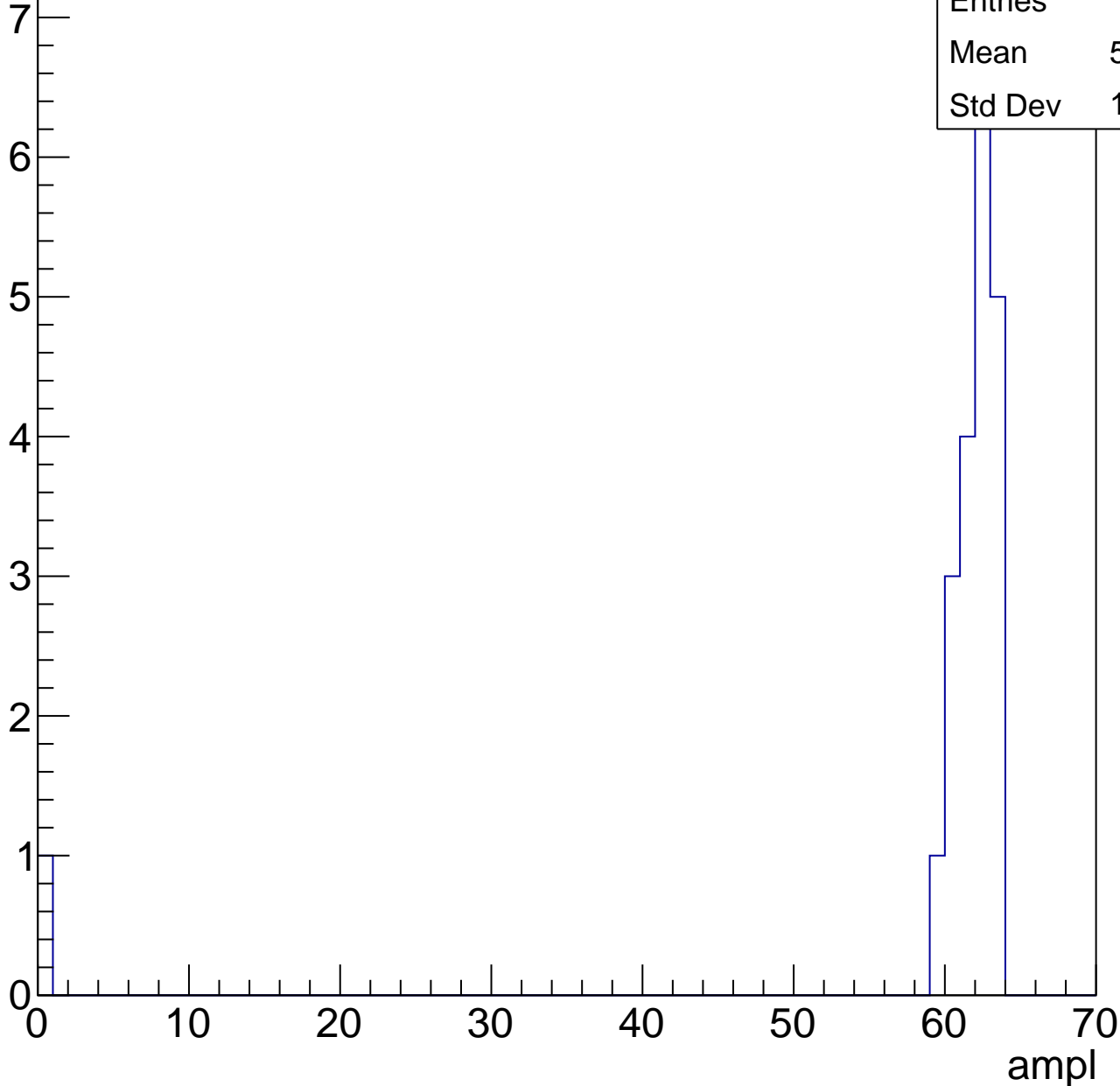


B0L101S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	58.67
Std Dev	13.17



B0L101S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch45, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	28.35
Std Dev	4.186

Gaus mean : 29.1998

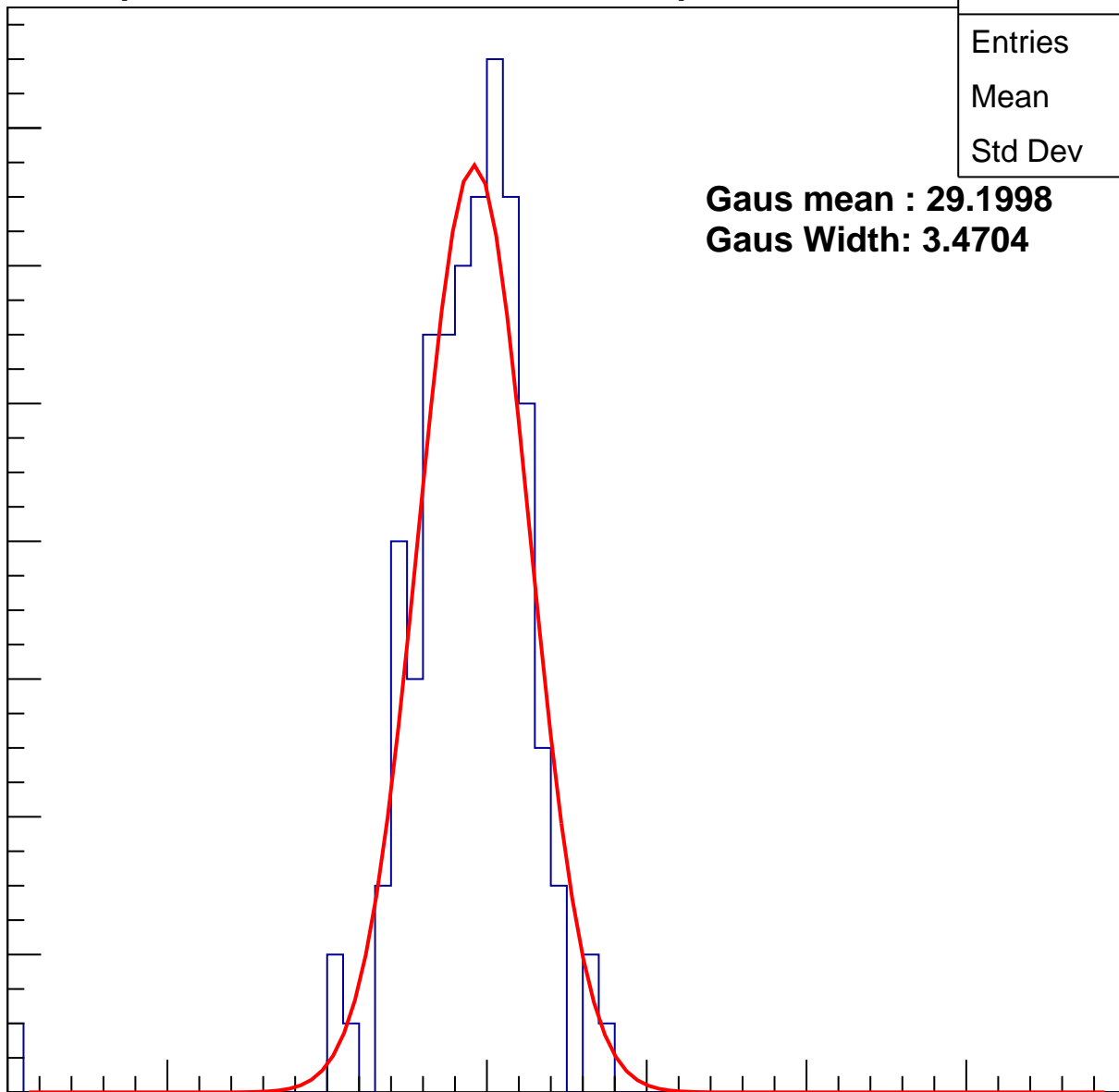
Gaus Width: 3.4704

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch45, adc1

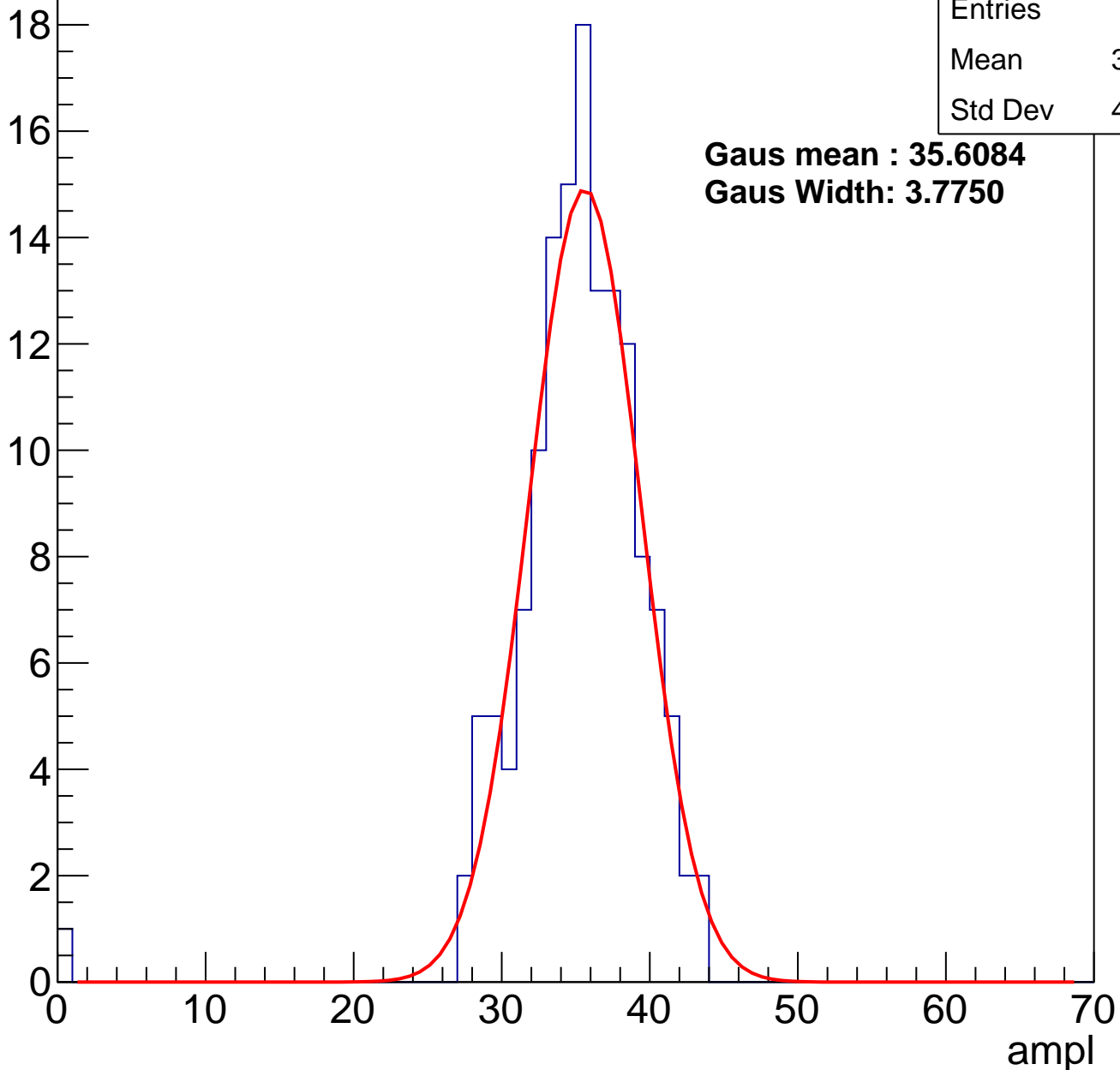
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	34.76
Std Dev	4.599

Gaus mean : 35.6084

Gaus Width: 3.7750

Entry



B0L101S, U15-ch45, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

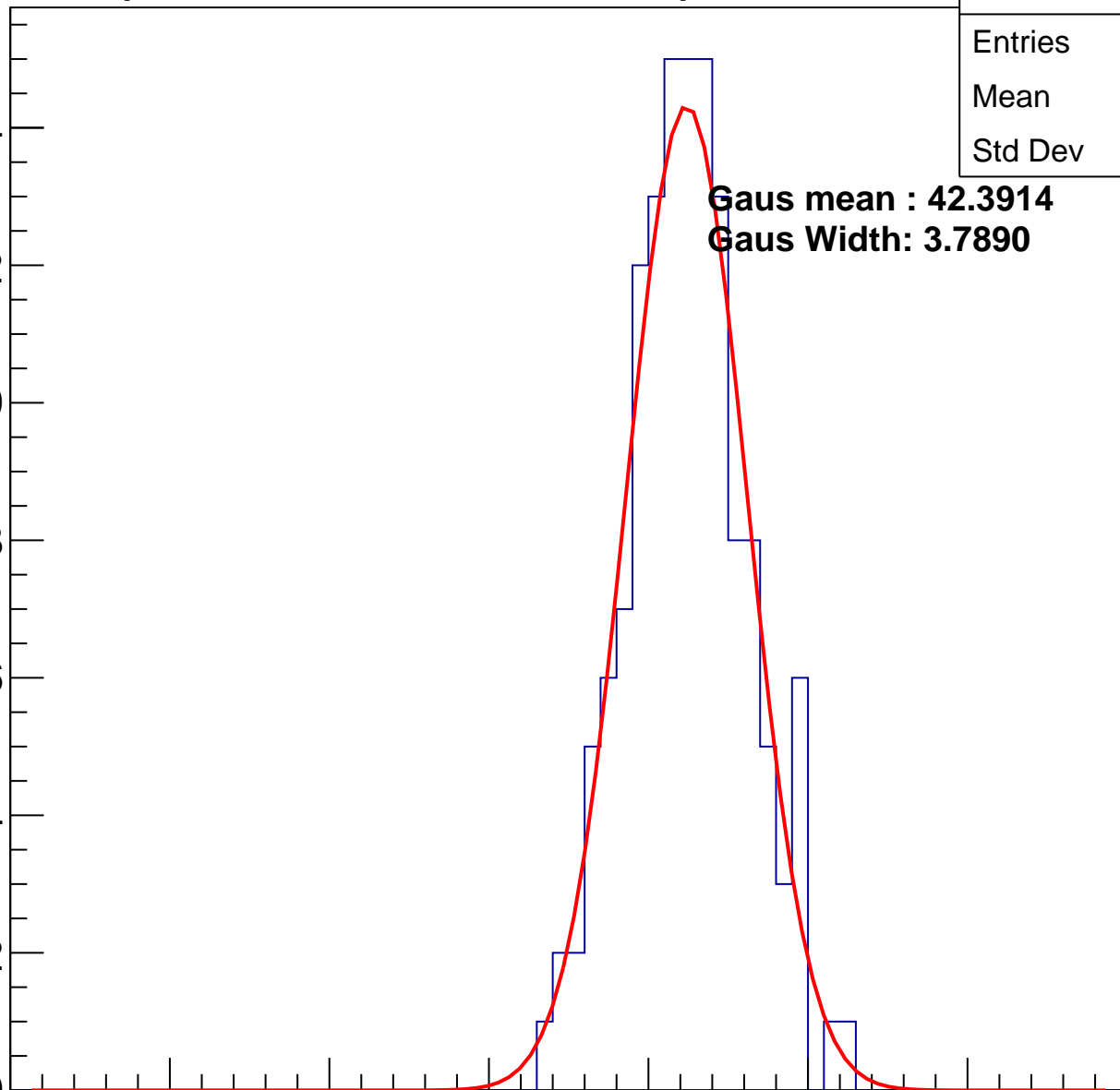
70

ampl

Entries	138
Mean	41.98
Std Dev	3.71

Gaus mean : 42.3914

Gaus Width: 3.7890

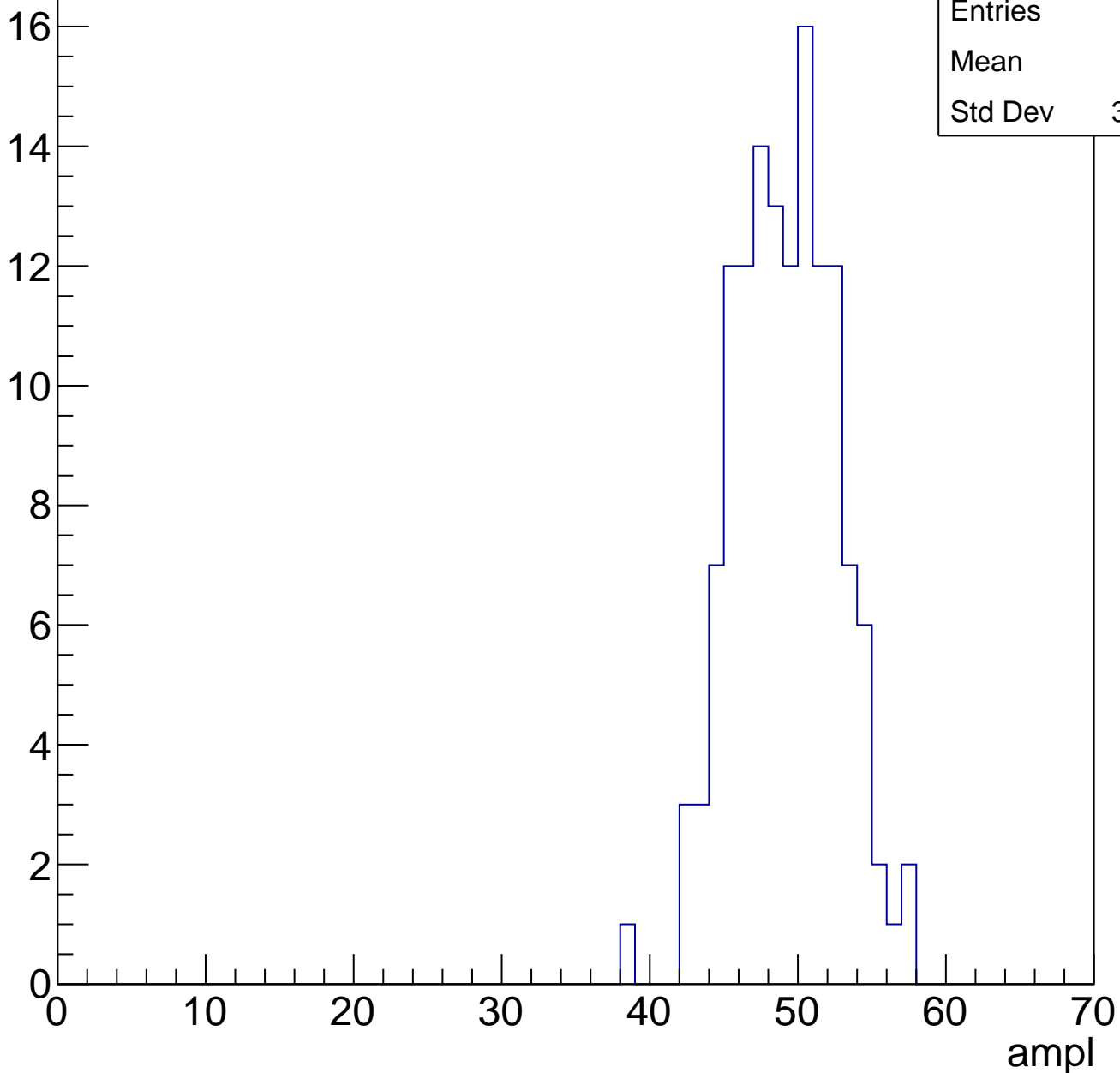


B0L101S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	48.7
Std Dev	3.432

Entry



B0L101S, U15-ch45, adc4

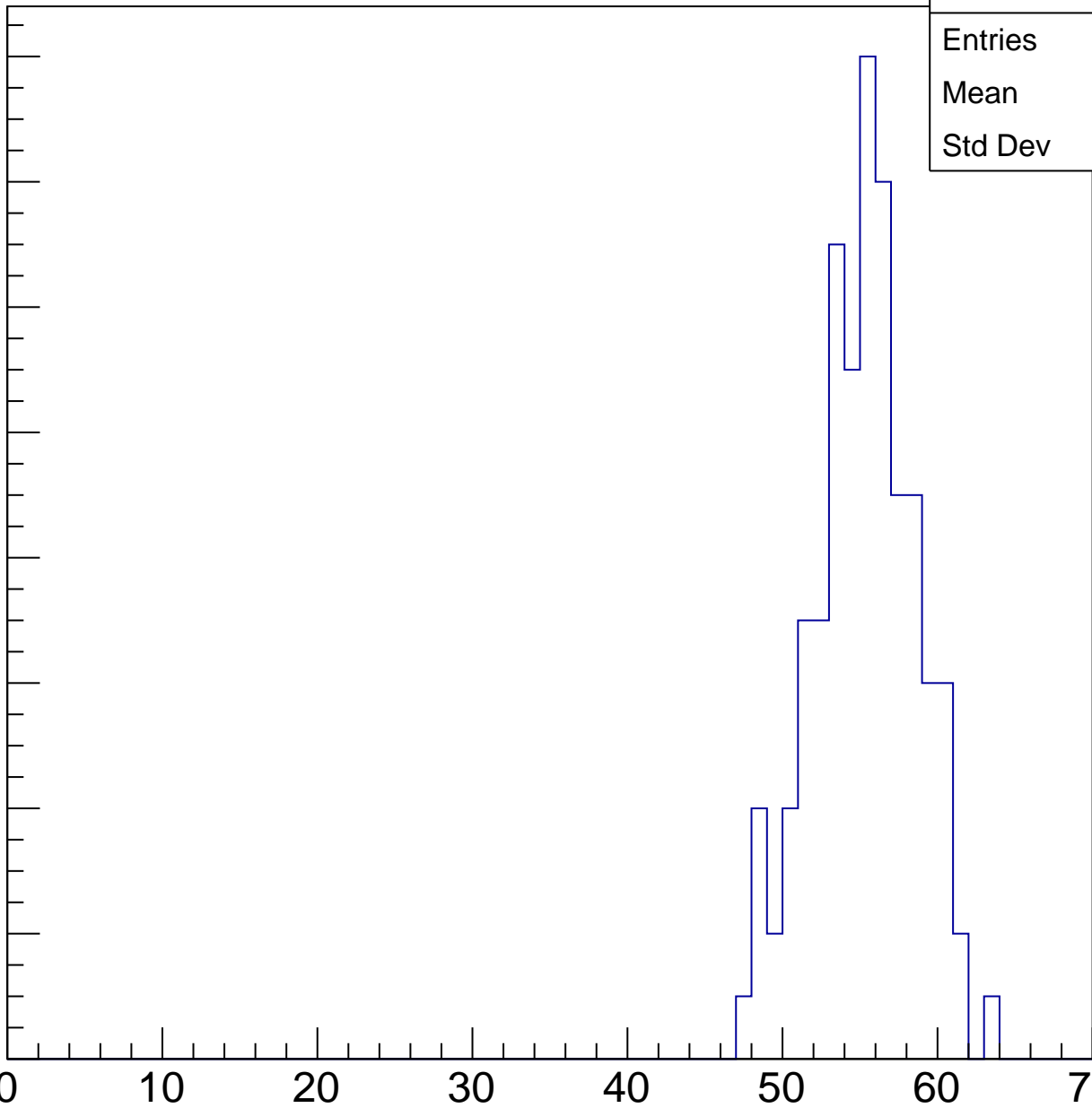
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	54.81
Std Dev	3.245

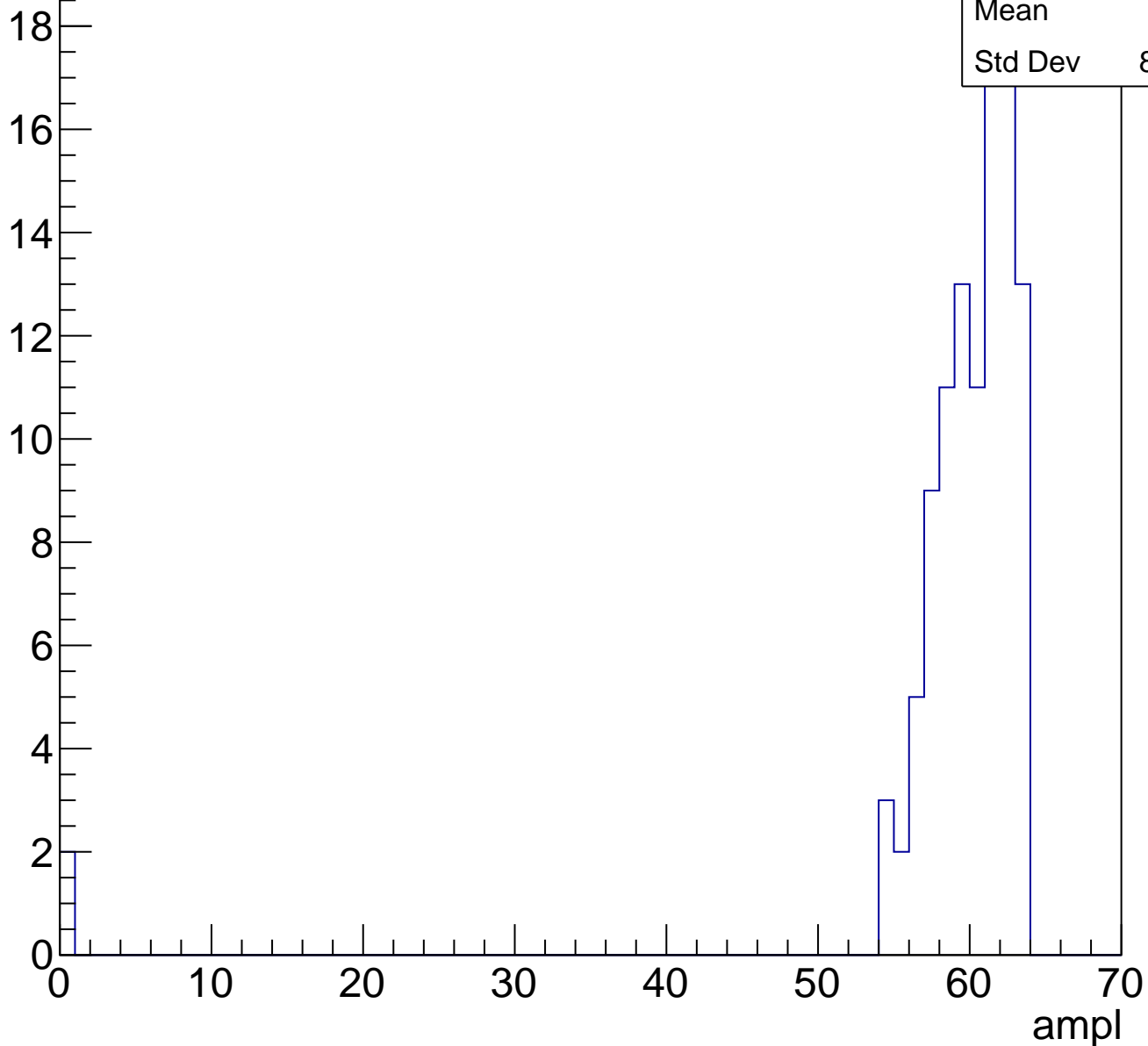
ampl



B0L101S, U15-ch45, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

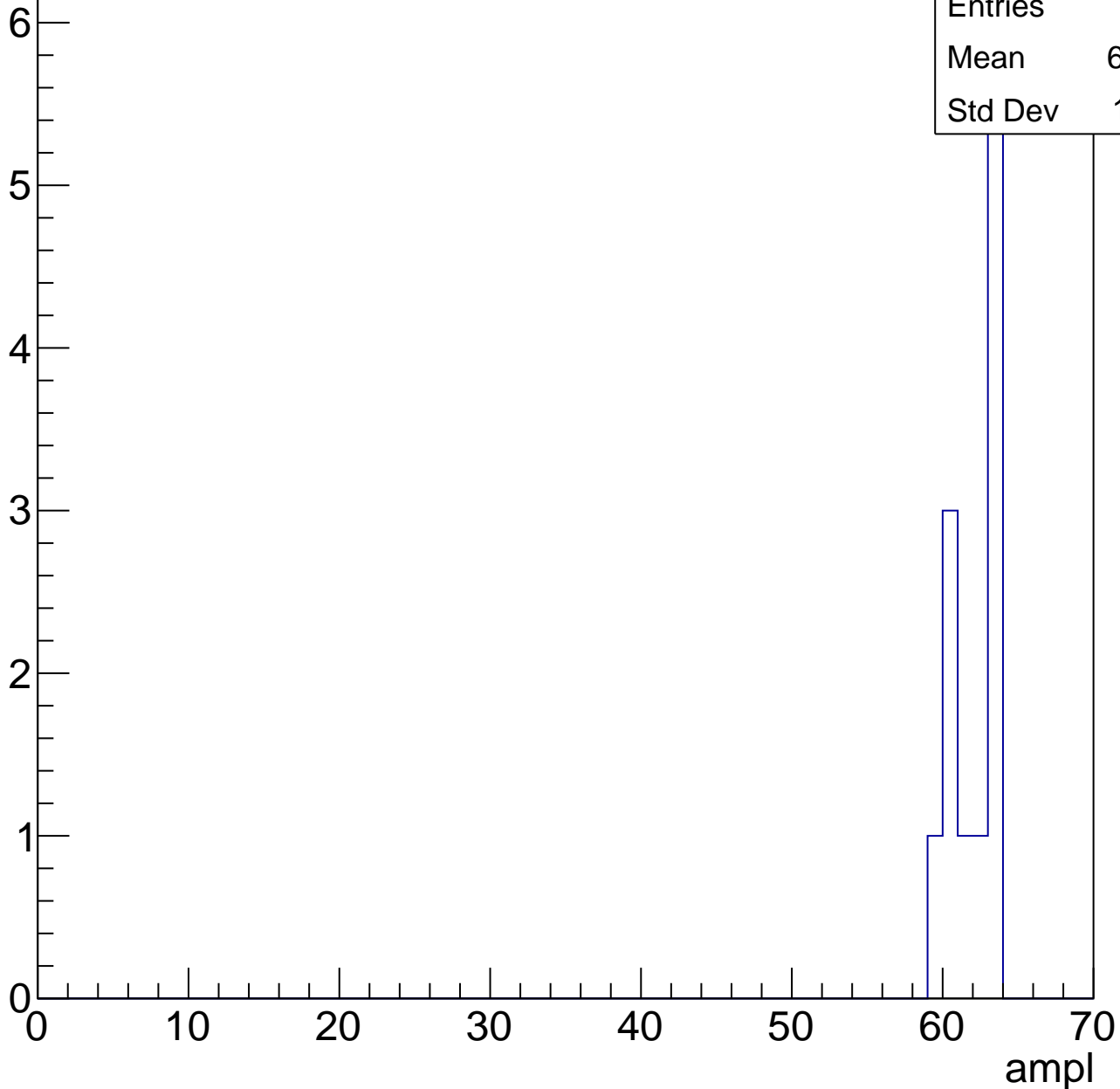


B0L101S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	61.67
Std Dev	1.491



B0L101S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch46, adc0

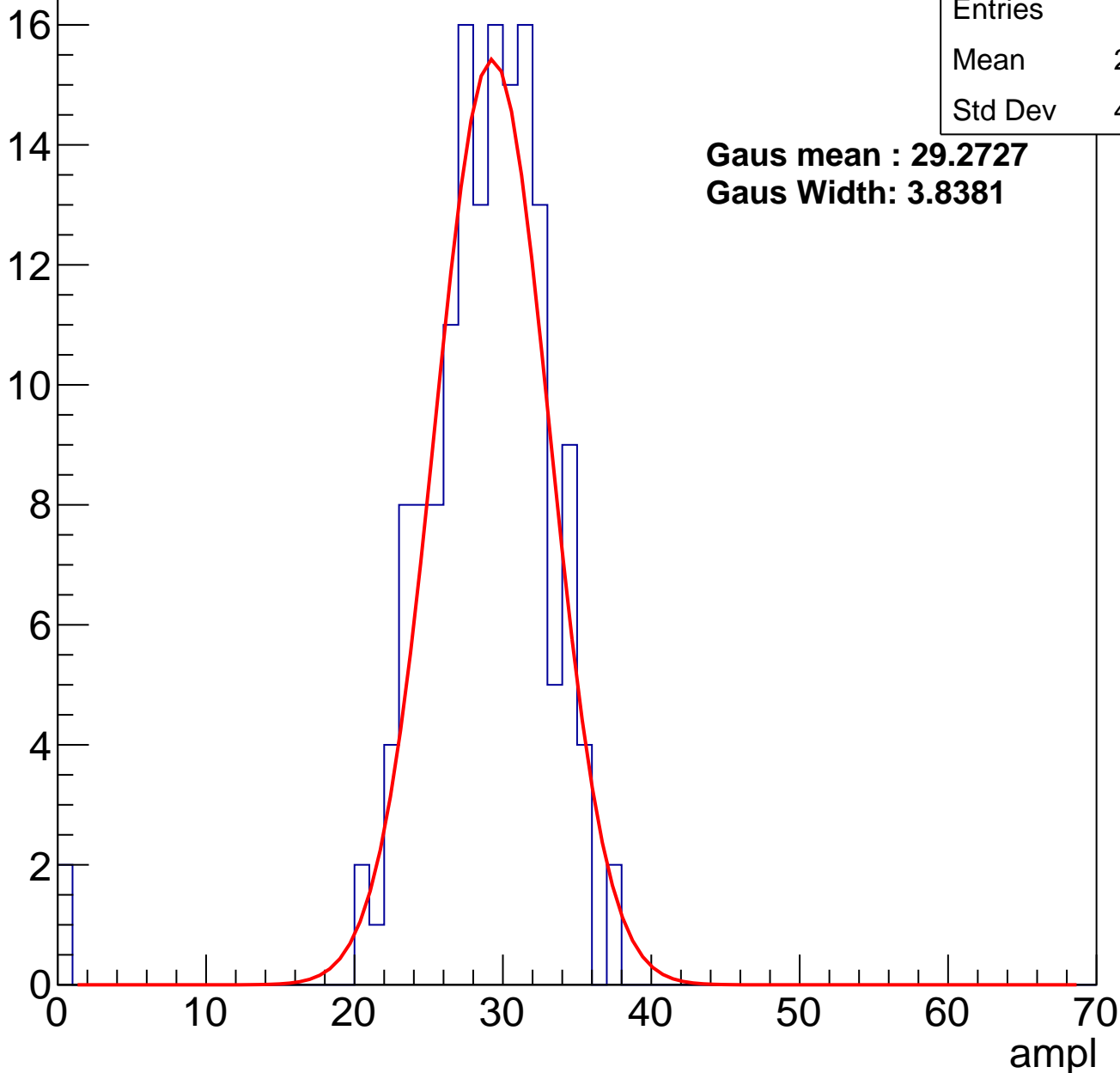
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.22
Std Dev	4.835

Gaus mean : 29.2727

Gaus Width: 3.8381

Entry



B0L101S, U15-ch46, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	35.59
Std Dev	3.748

Gaus mean : 35.9289

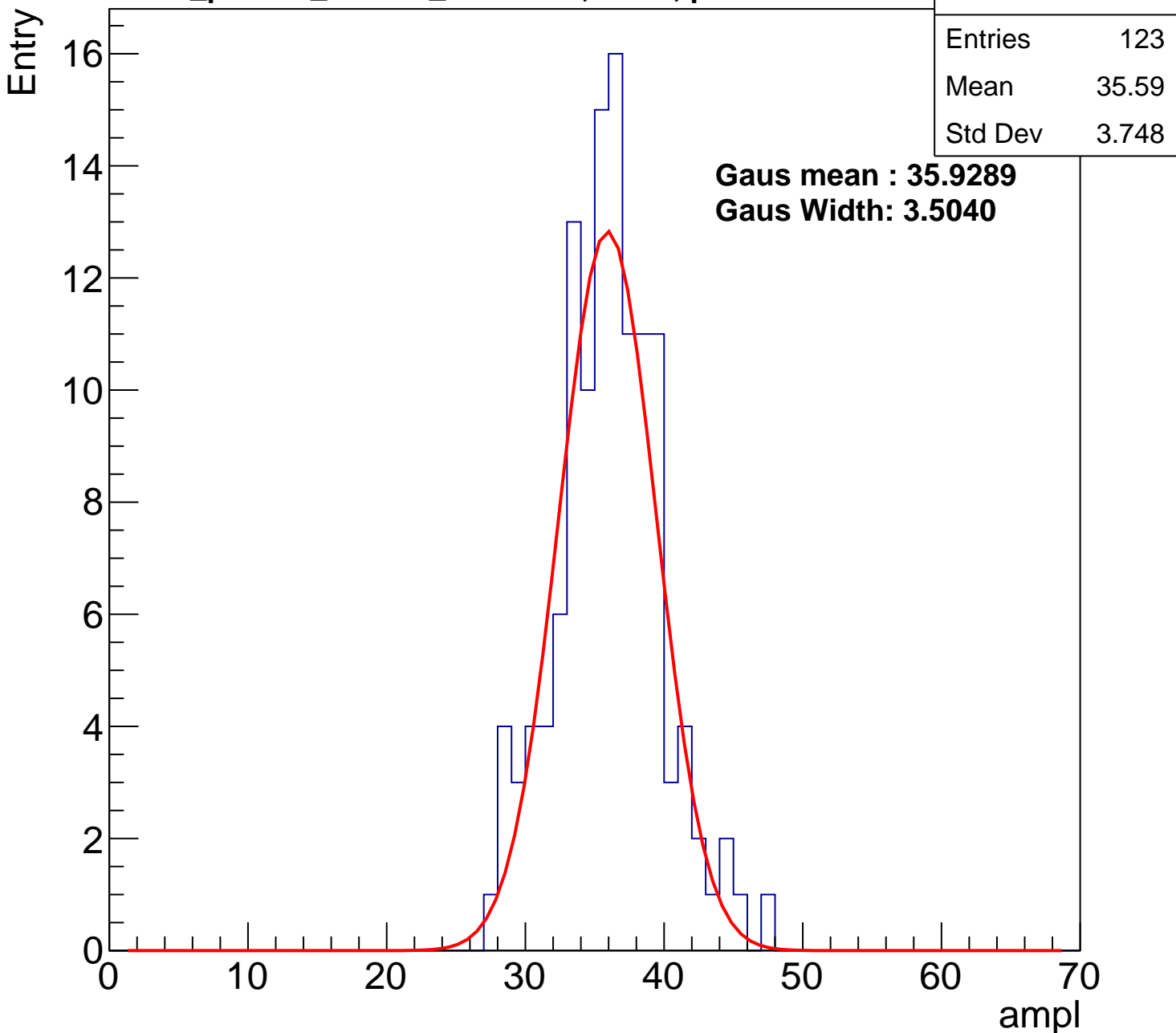
Gaus Width: 3.5040

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch46, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

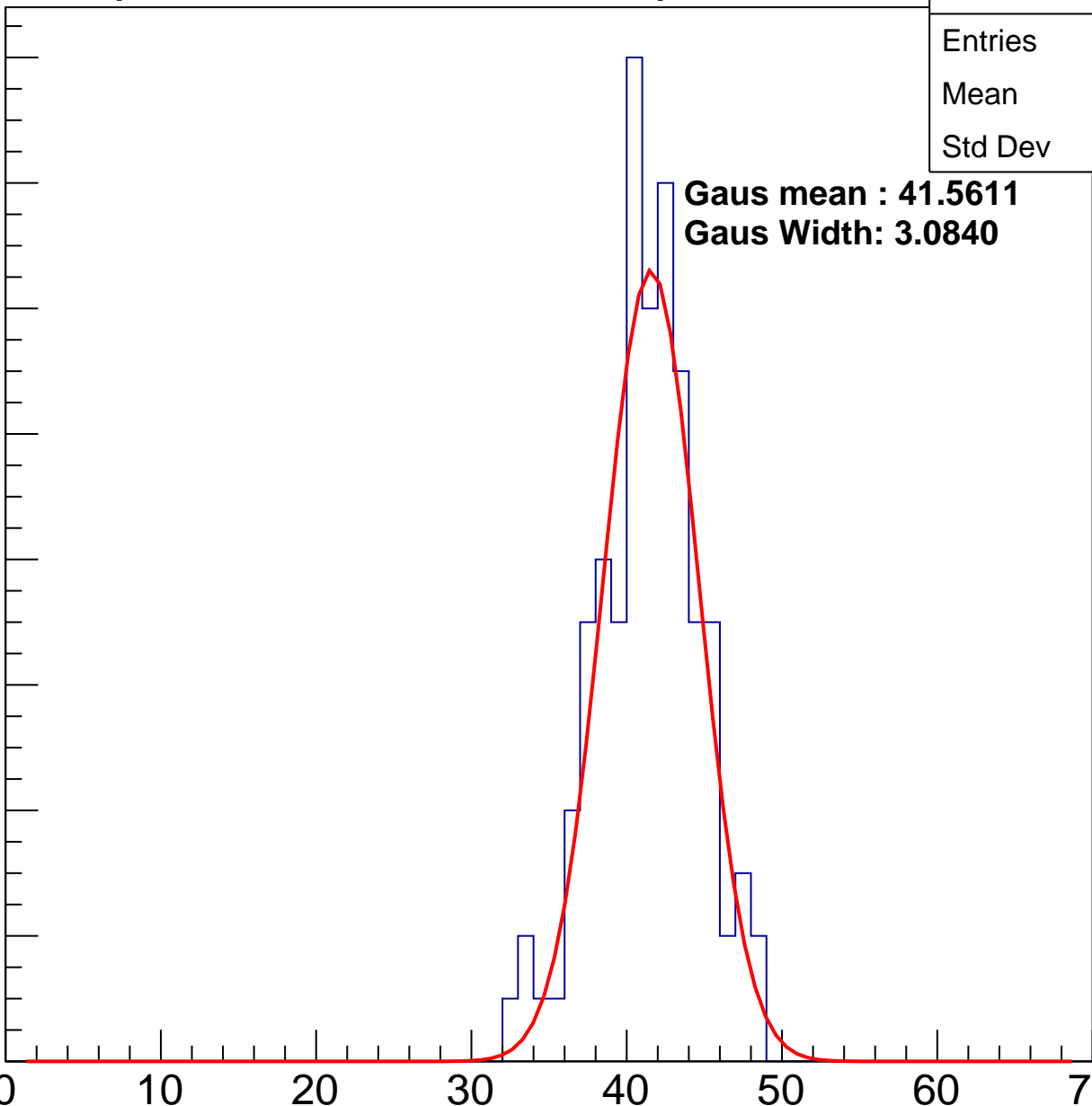
16
14
12
10
8
6
4
2
0

Entries	105
Mean	40.88
Std Dev	3.25

Gaus mean : 41.5611

Gaus Width: 3.0840

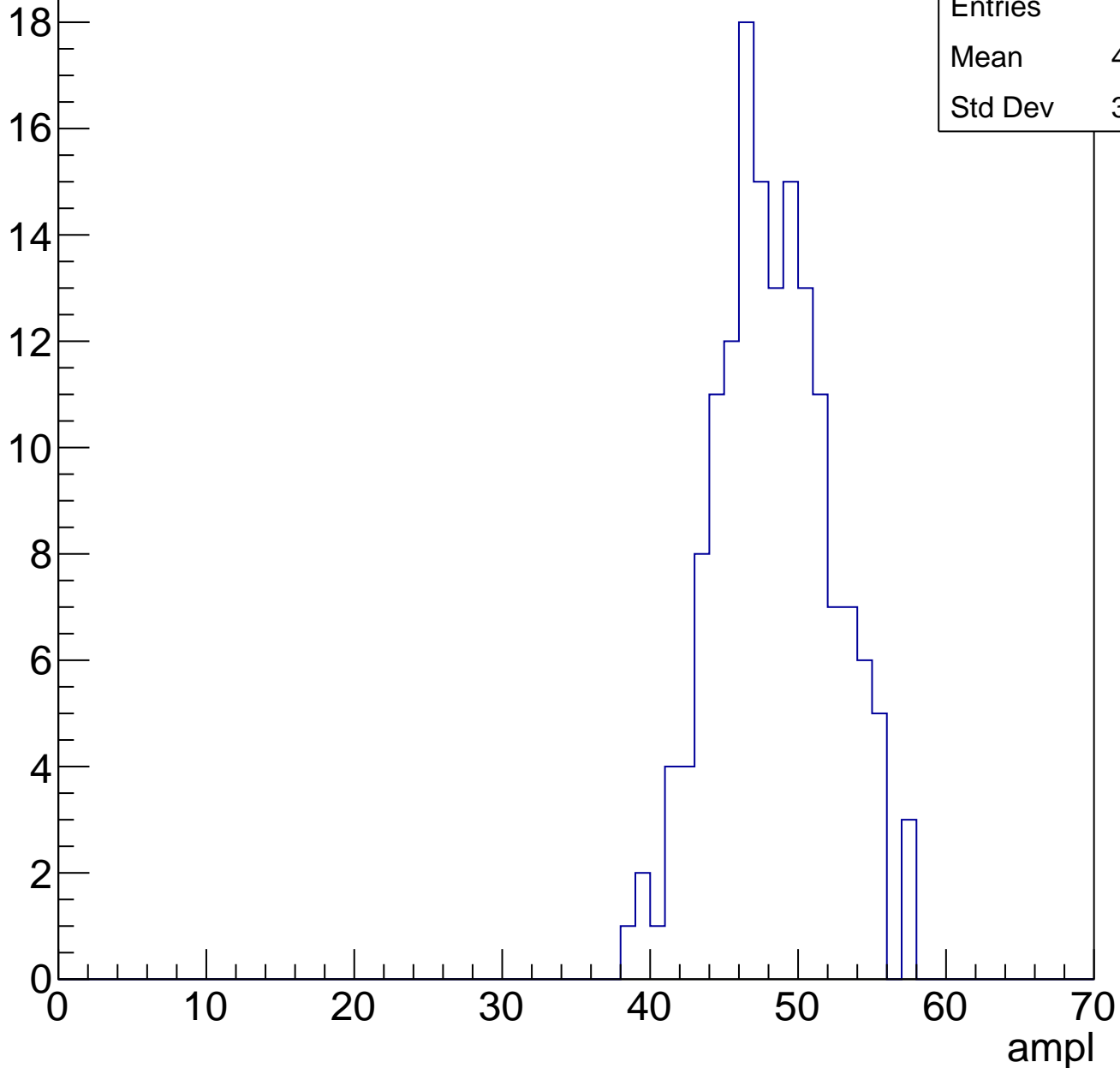
0 10 20 30 40 50 60 70
ampl



B0L101S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	54.56
Std Dev	3.539

Entry

12

10

8

6

4

2

0

0

10

20

30

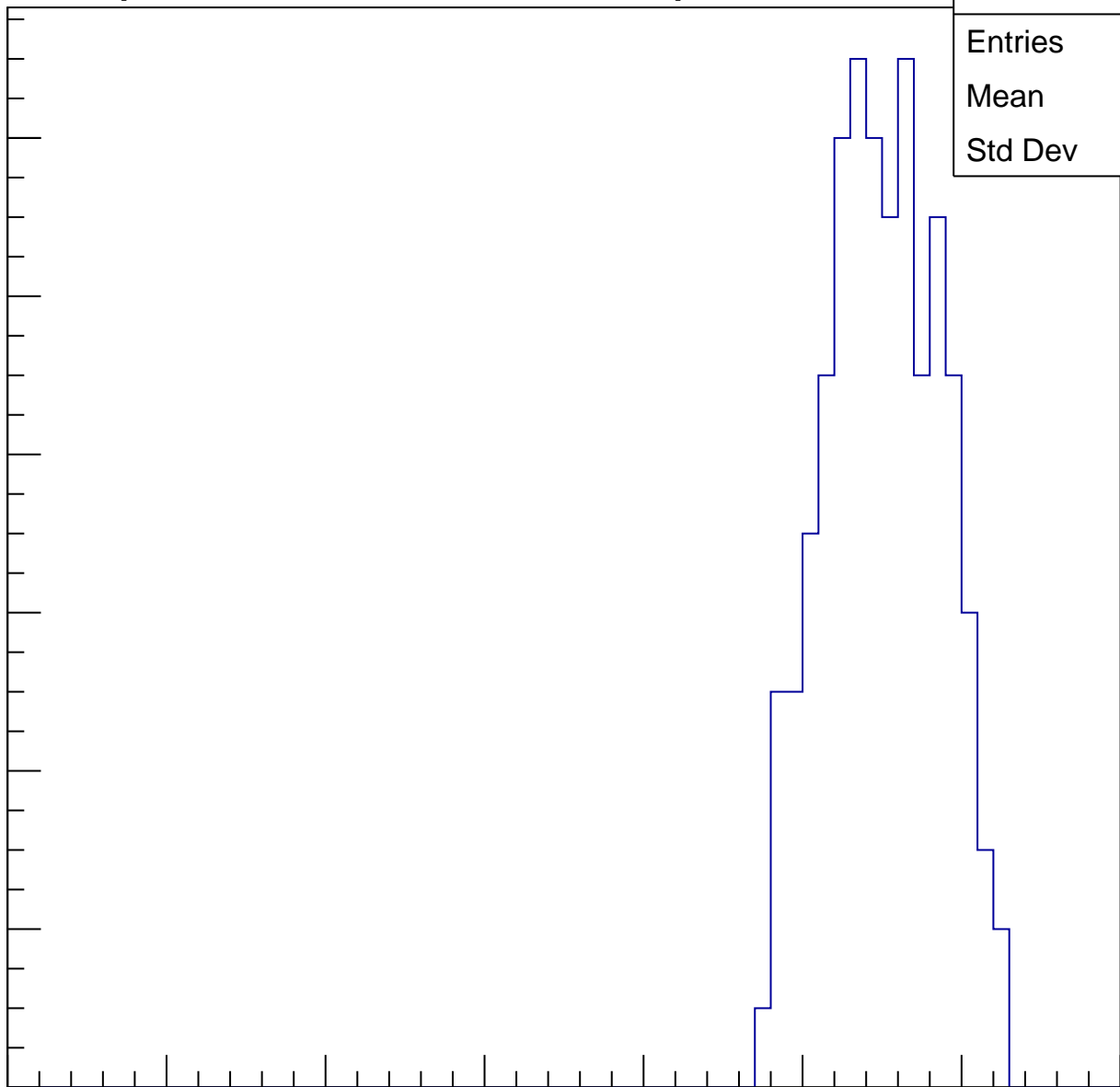
40

50

60

70

ampl



B0L101S, U15-ch46, adc5

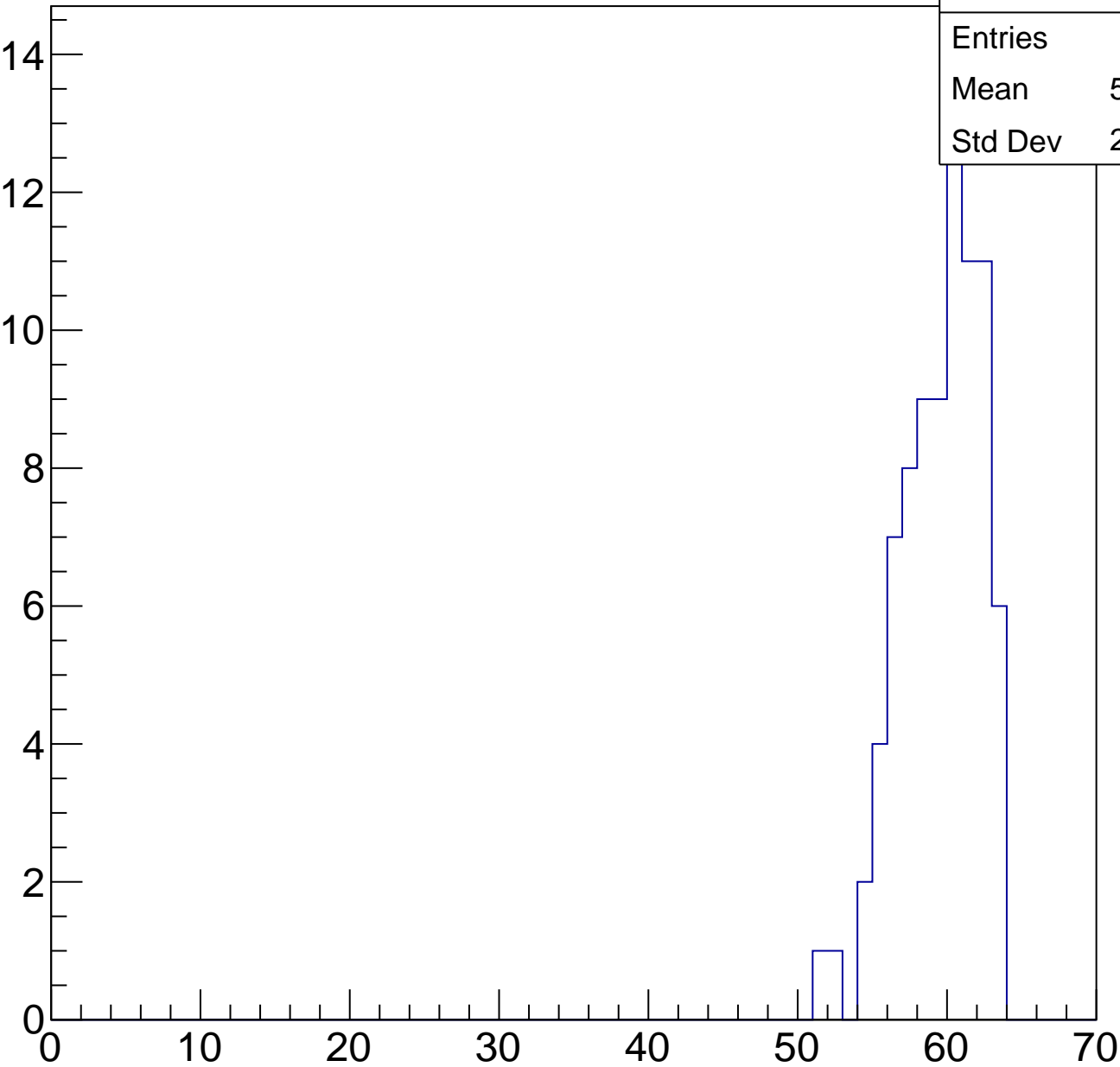
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	83
Mean	59.07
Std Dev	2.652

ampl



B0L101S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	29
Mean	59.9
Std Dev	11.38

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch47, adc0

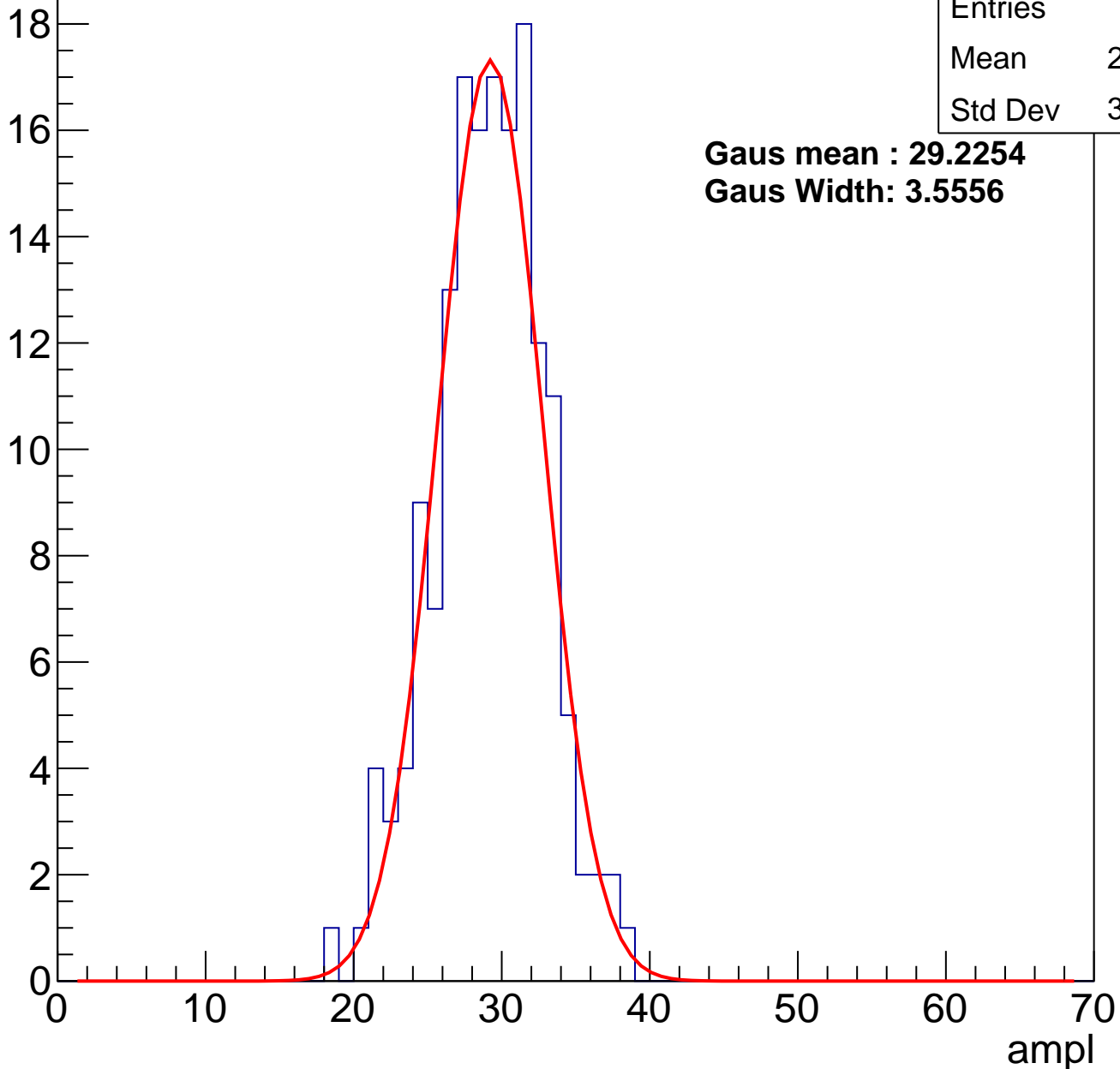
calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	28.68
Std Dev	3.652

Gaus mean : 29.2254

Gaus Width: 3.5556

Entry

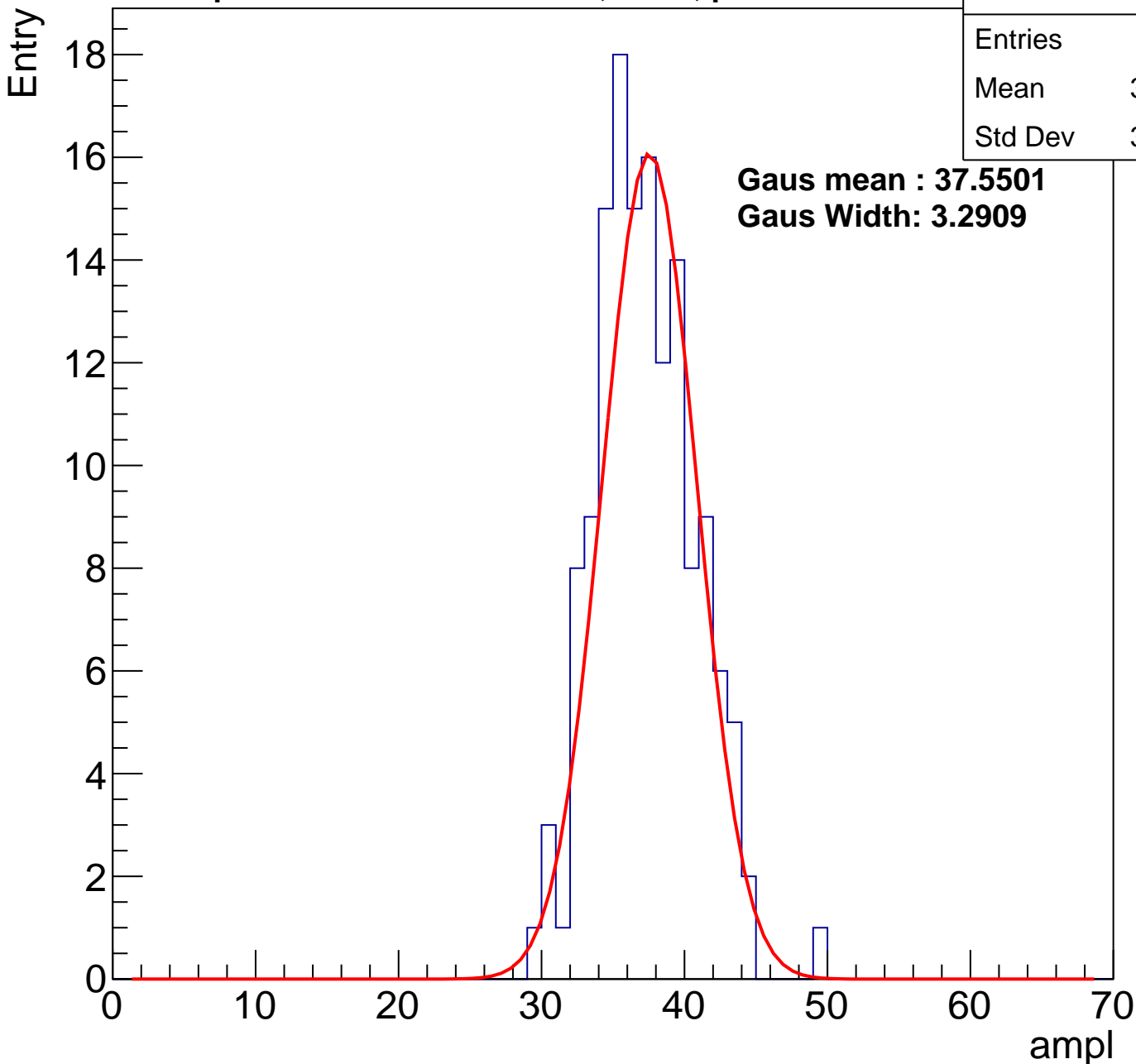


B0L101S, U15-ch47, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	36.85
Std Dev	3.413

Gaus mean : 37.5501
Gaus Width: 3.2909



B0L101S, U15-ch47, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

108

Mean

42.45

Std Dev

3.071

Gaus mean : 42.8105

Gaus Width: 2.9244

0

10

20

30

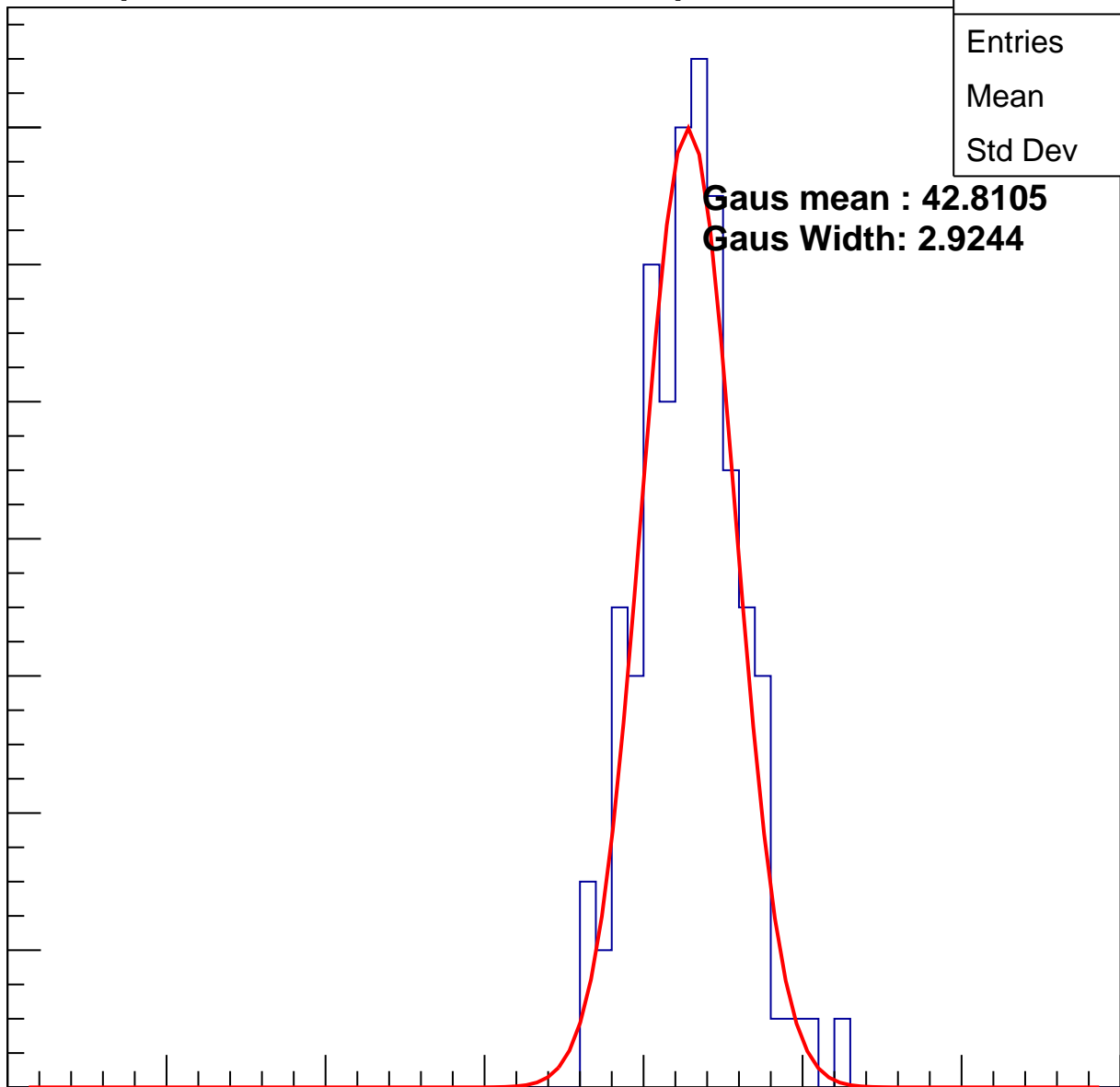
40

50

60

70

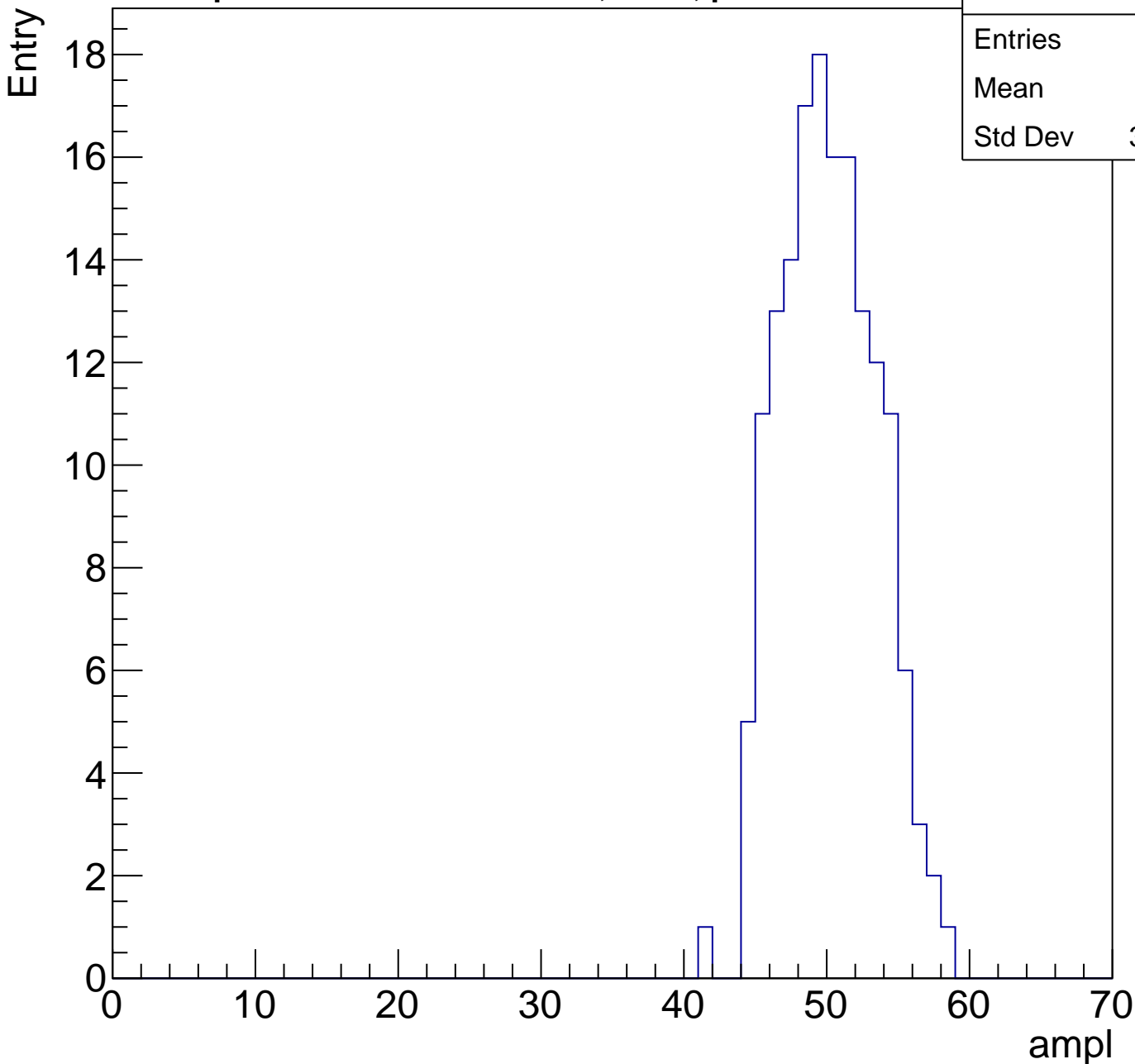
ampl



B0L101S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	159
Mean	49.7
Std Dev	3.283



B0L101S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

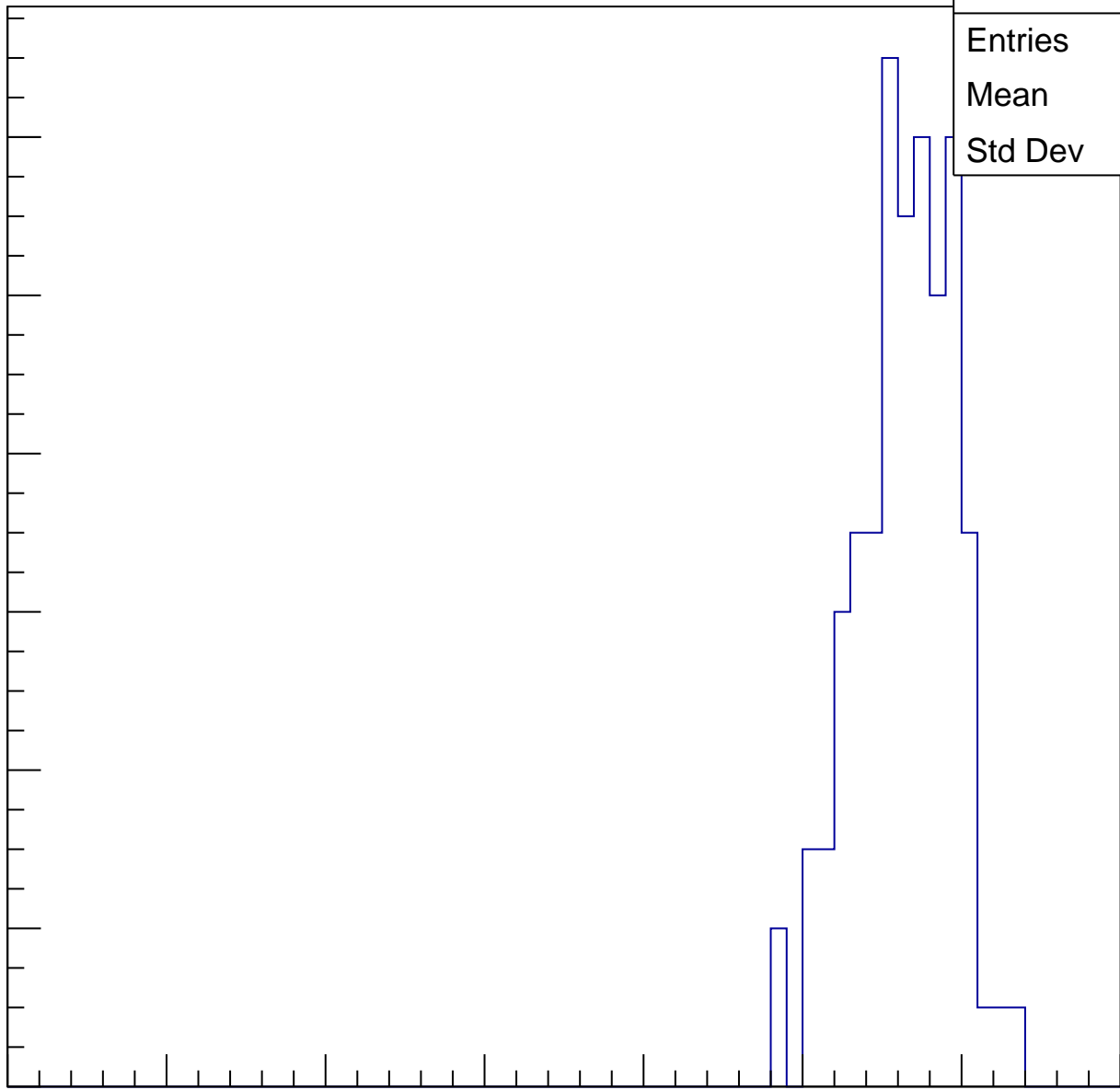
2

0

Entries	96
Mean	55.93
Std Dev	3.049

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch47, adc5

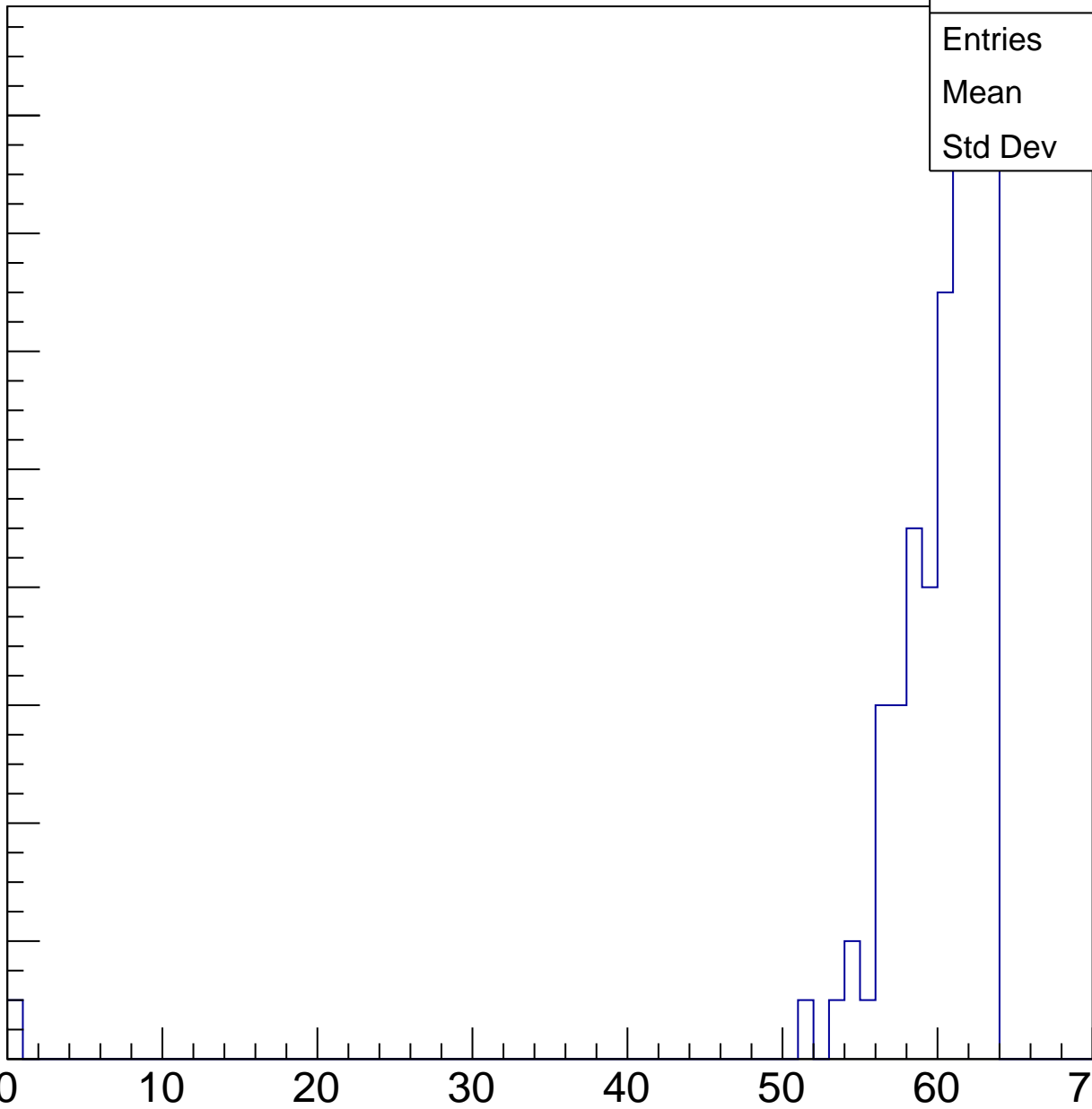
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	59.37
Std Dev	6.555

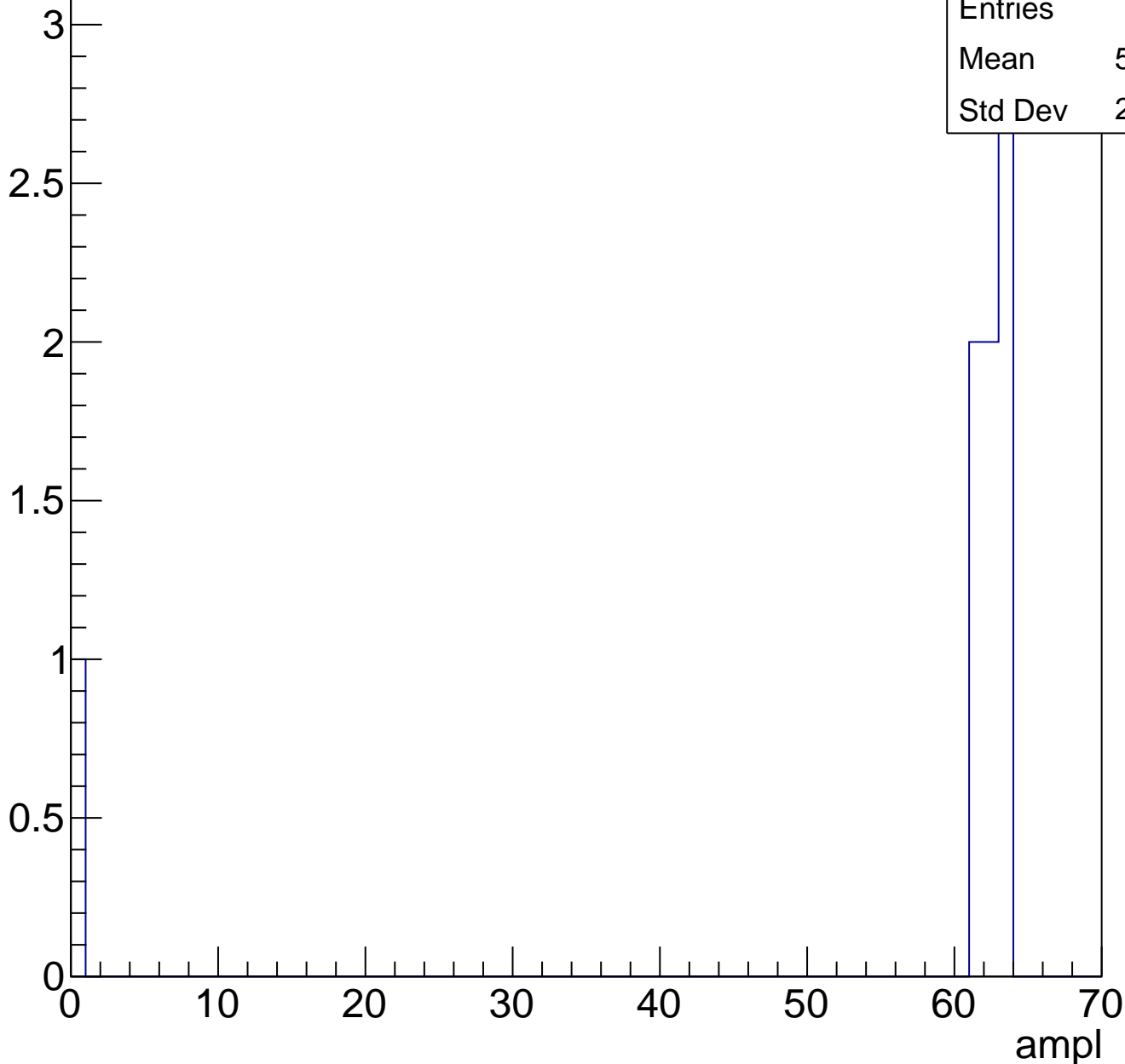
ampl



B0L101S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch48, adc0

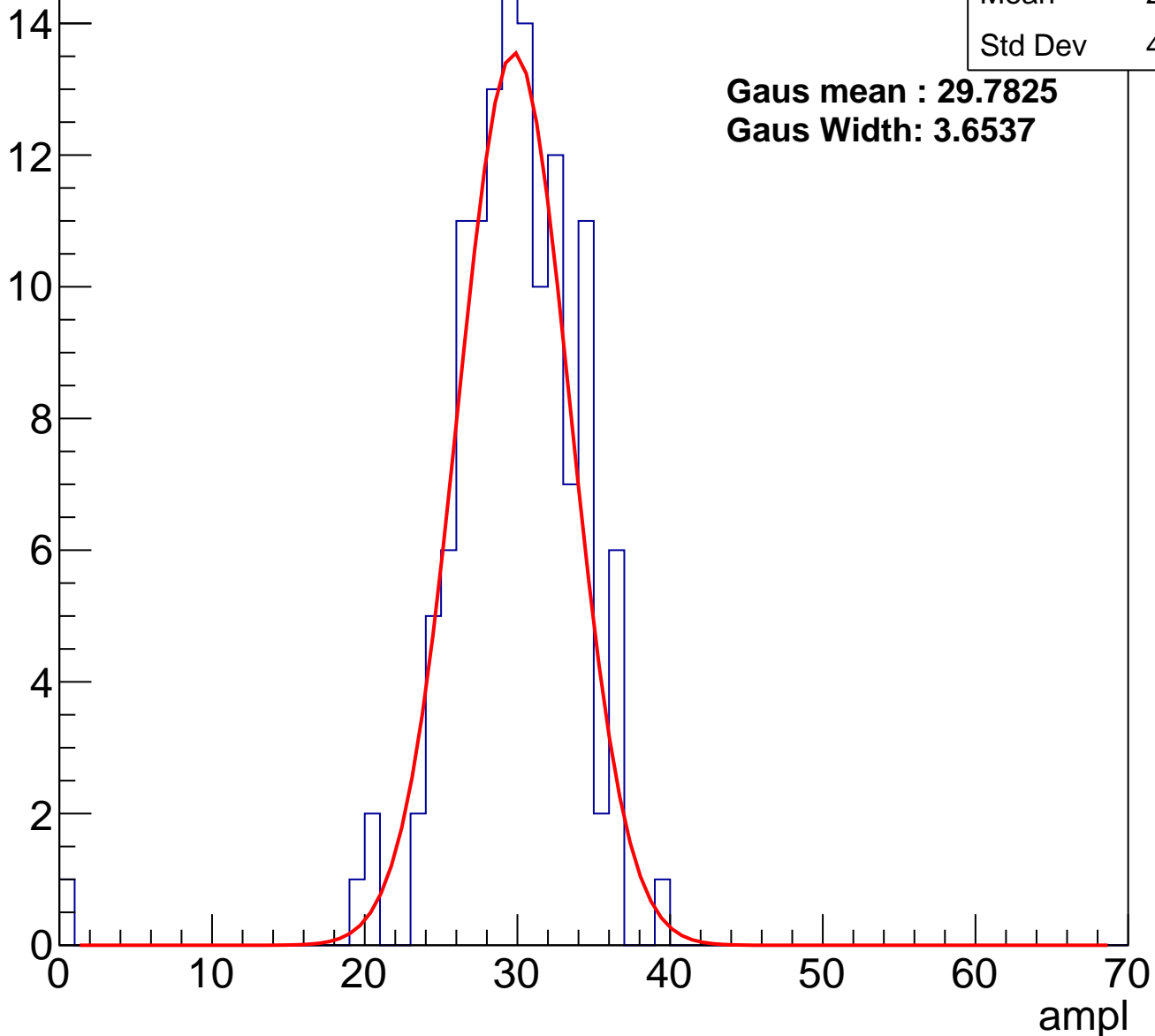
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	29.24
Std Dev	4.437

Gaus mean : 29.7825

Gaus Width: 3.6537

Entry



B0L101S, U15-ch48, adc1

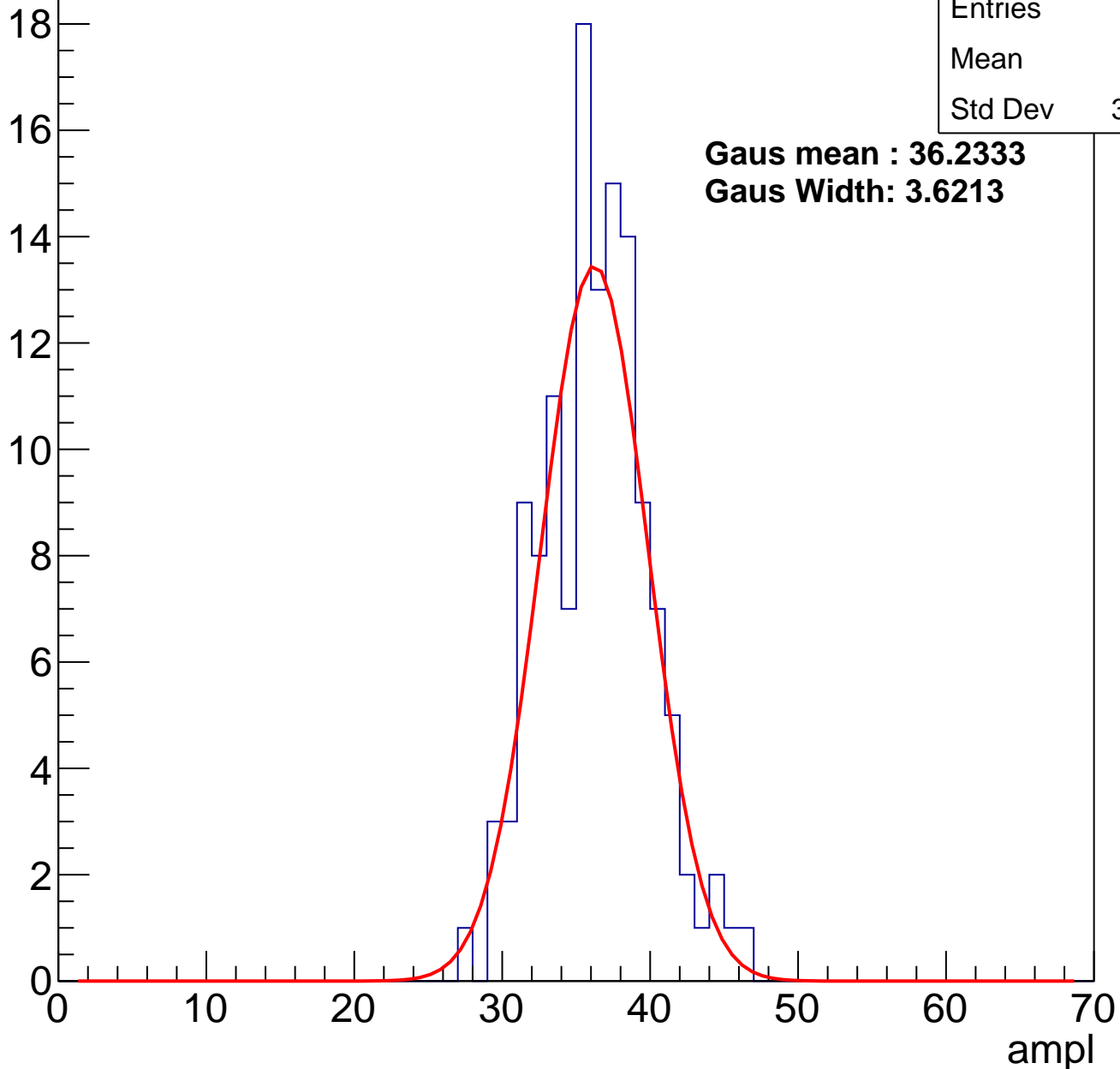
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	35.9
Std Dev	3.564

Gaus mean : 36.2333

Gaus Width: 3.6213

Entry



B0L101S, U15-ch48, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	42.36
Std Dev	3.716

Gaus mean : 42.8027

Gaus Width: 3.3840

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

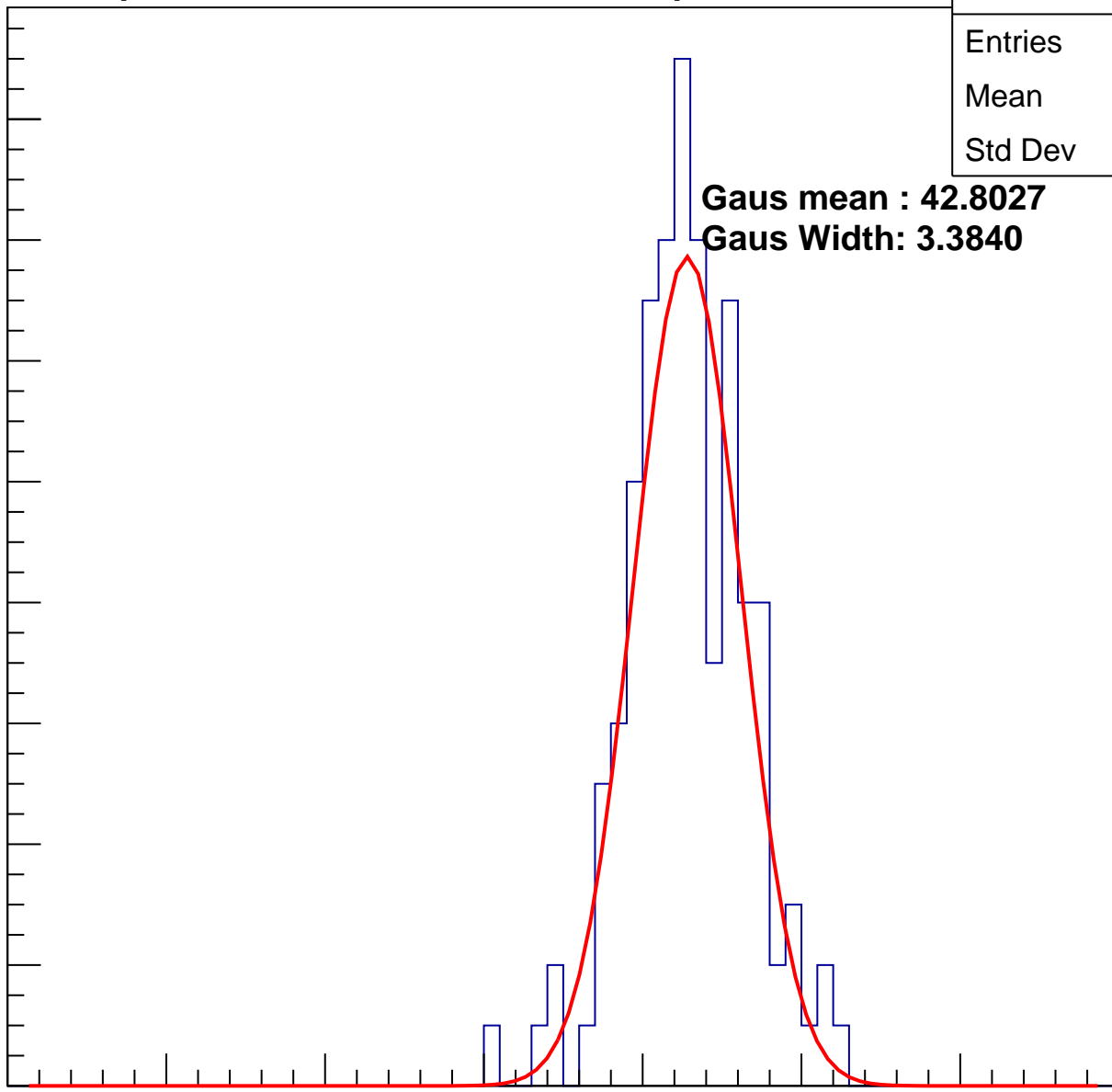
40

50

60

70

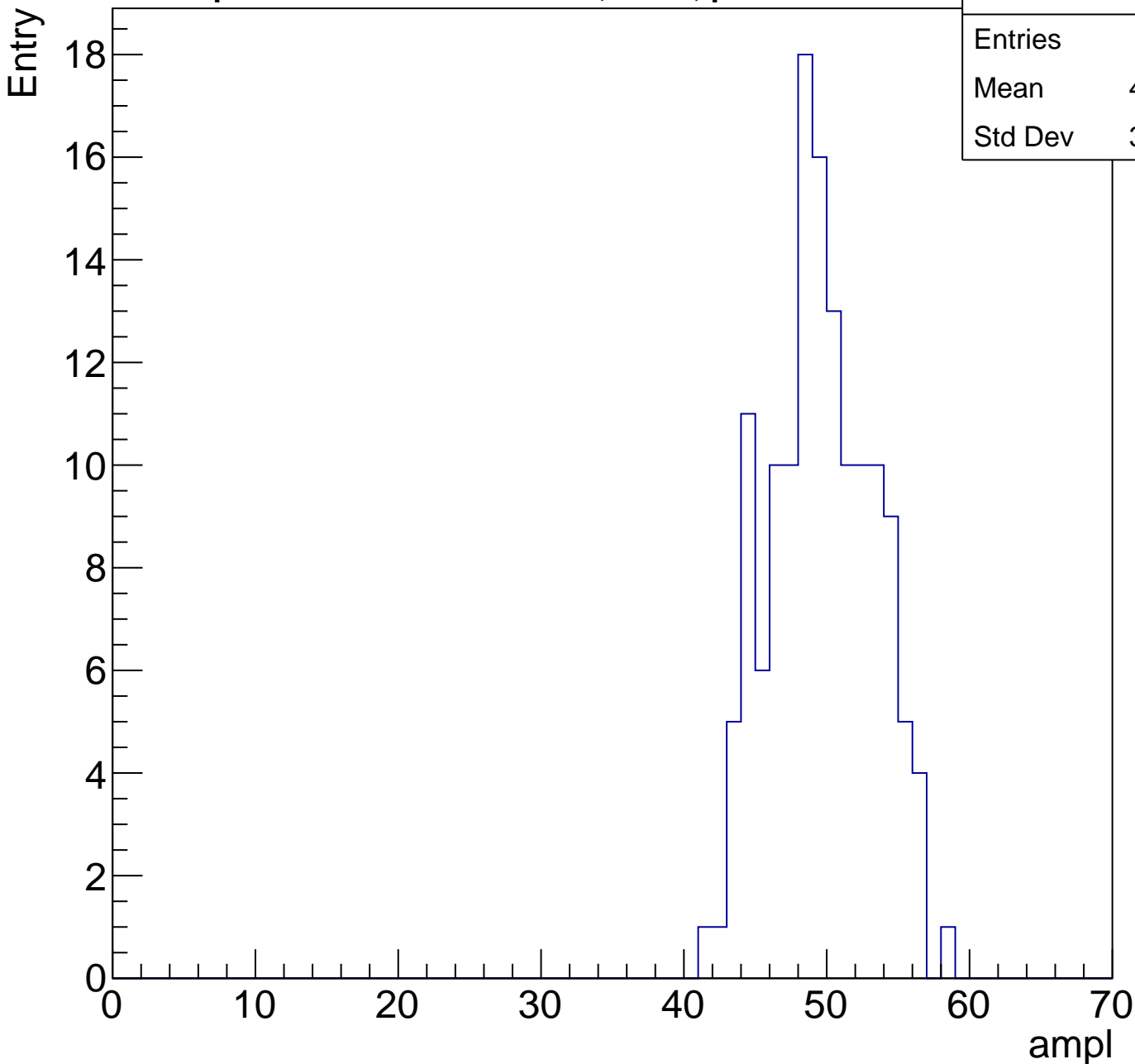
ampl



B0L101S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	49.16
Std Dev	3.579

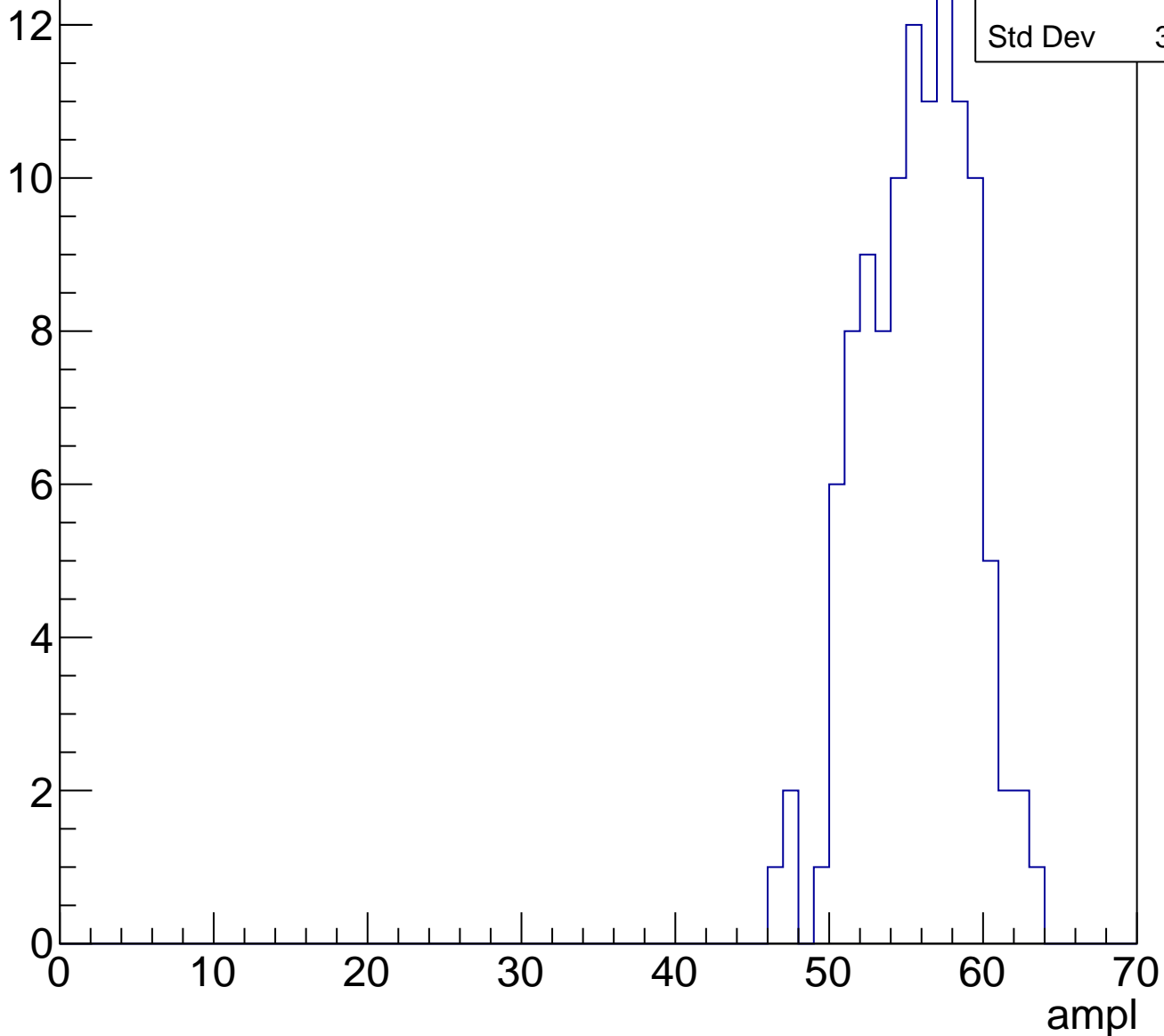


B0L101S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	55.21
Std Dev	3.436

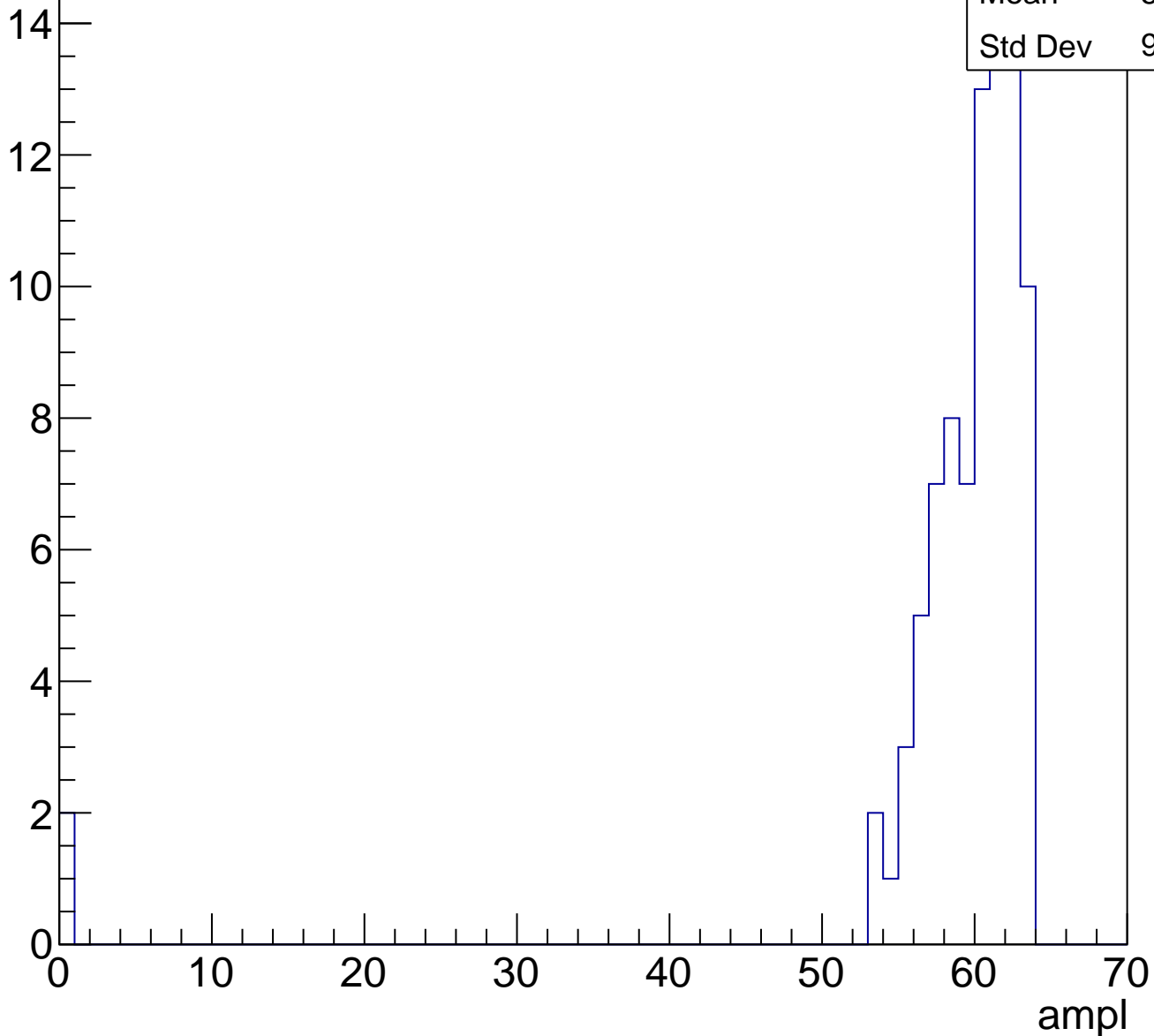
Entry



B0L101S, U15-ch48, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

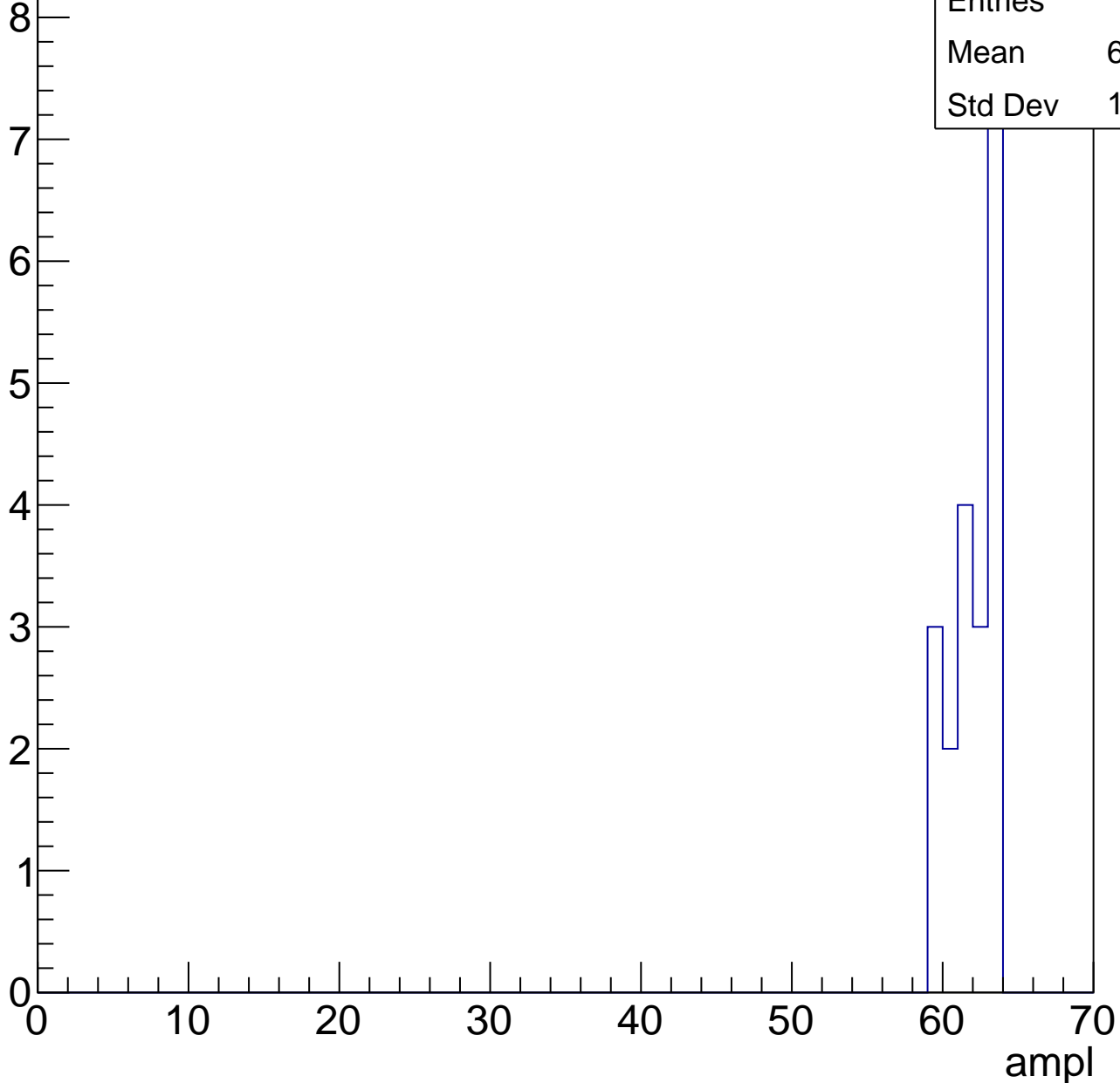


B0L101S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

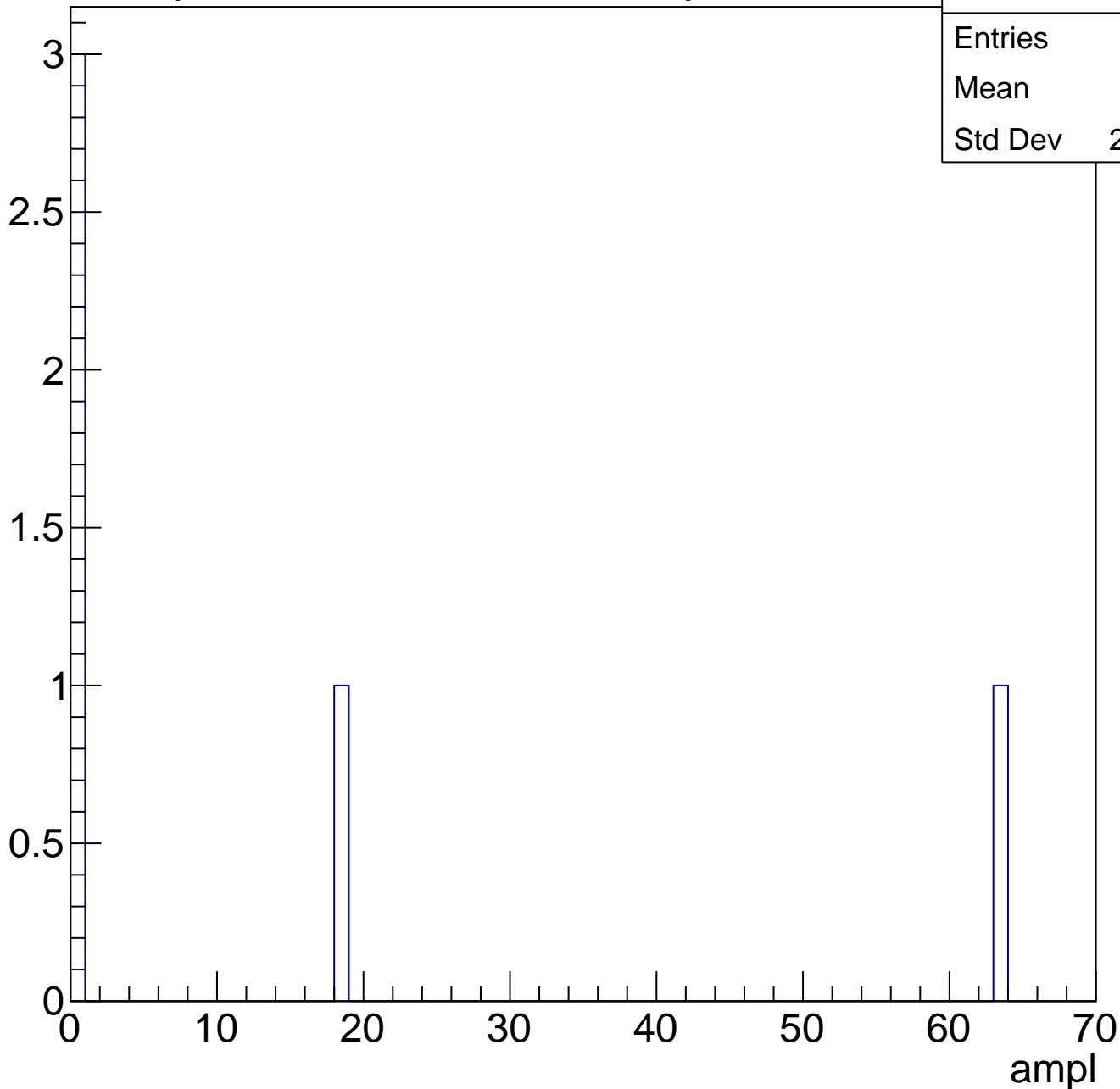
Entries	20
Mean	61.55
Std Dev	1.465



B0L101S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	16.2
Std Dev	24.42

B0L101S, U15-ch49, adc0

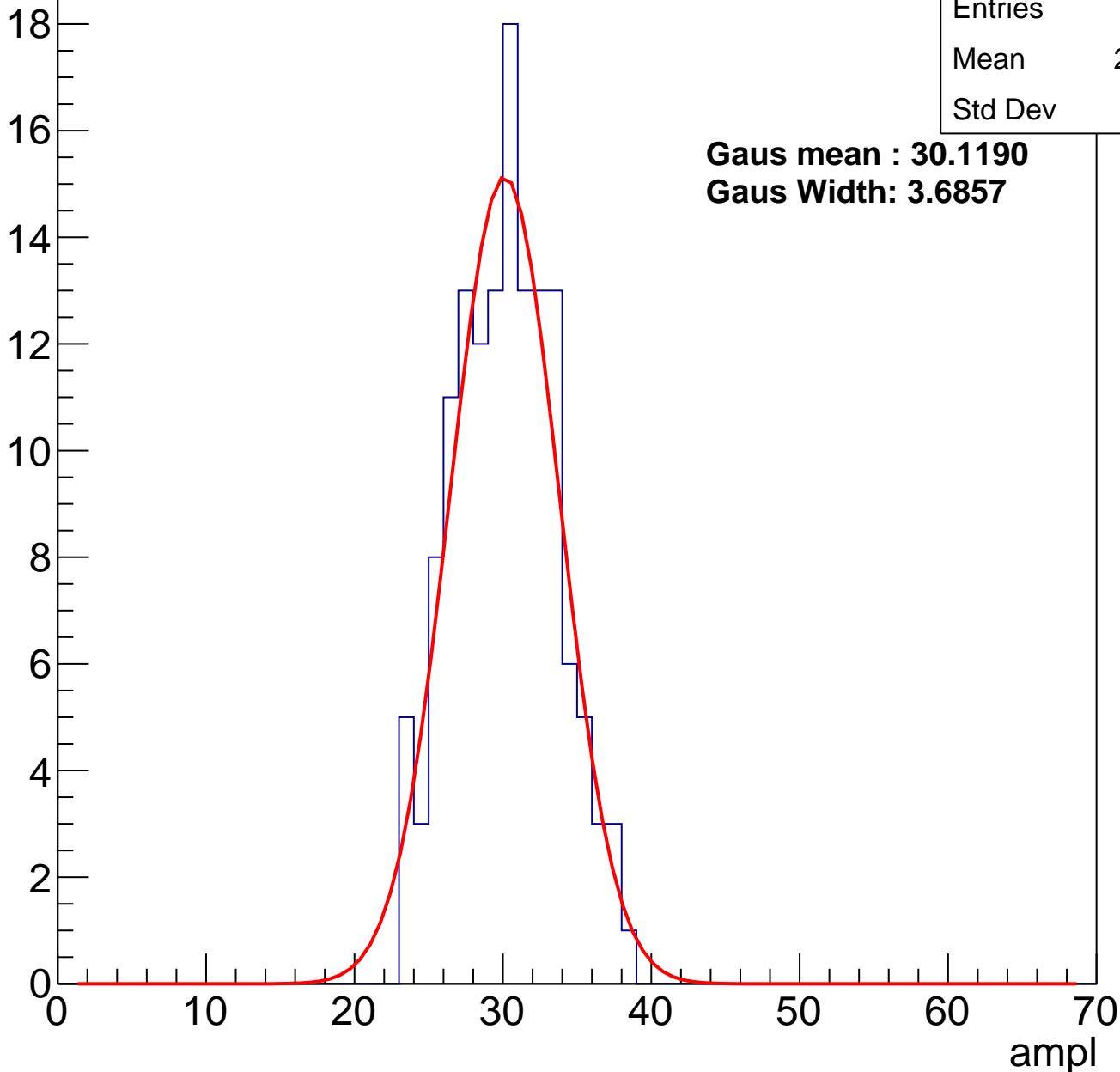
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	29.72
Std Dev	3.4

Gaus mean : 30.1190

Gaus Width: 3.6857

Entry



B0L101S, U15-ch49, adc1

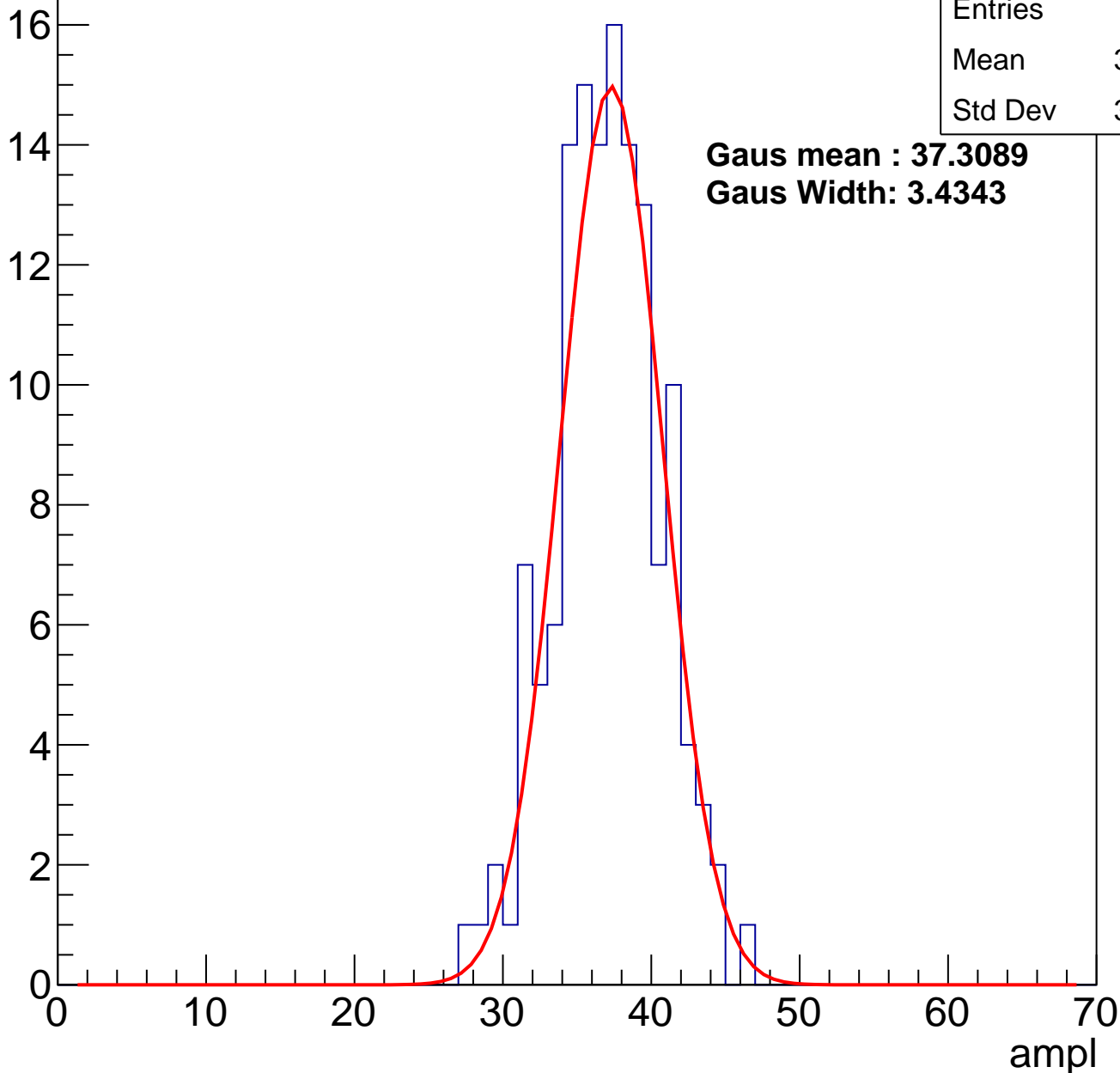
calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	36.58
Std Dev	3.518

Gaus mean : 37.3089

Gaus Width: 3.4343

Entry



B0L101S, U15-ch49, adc2

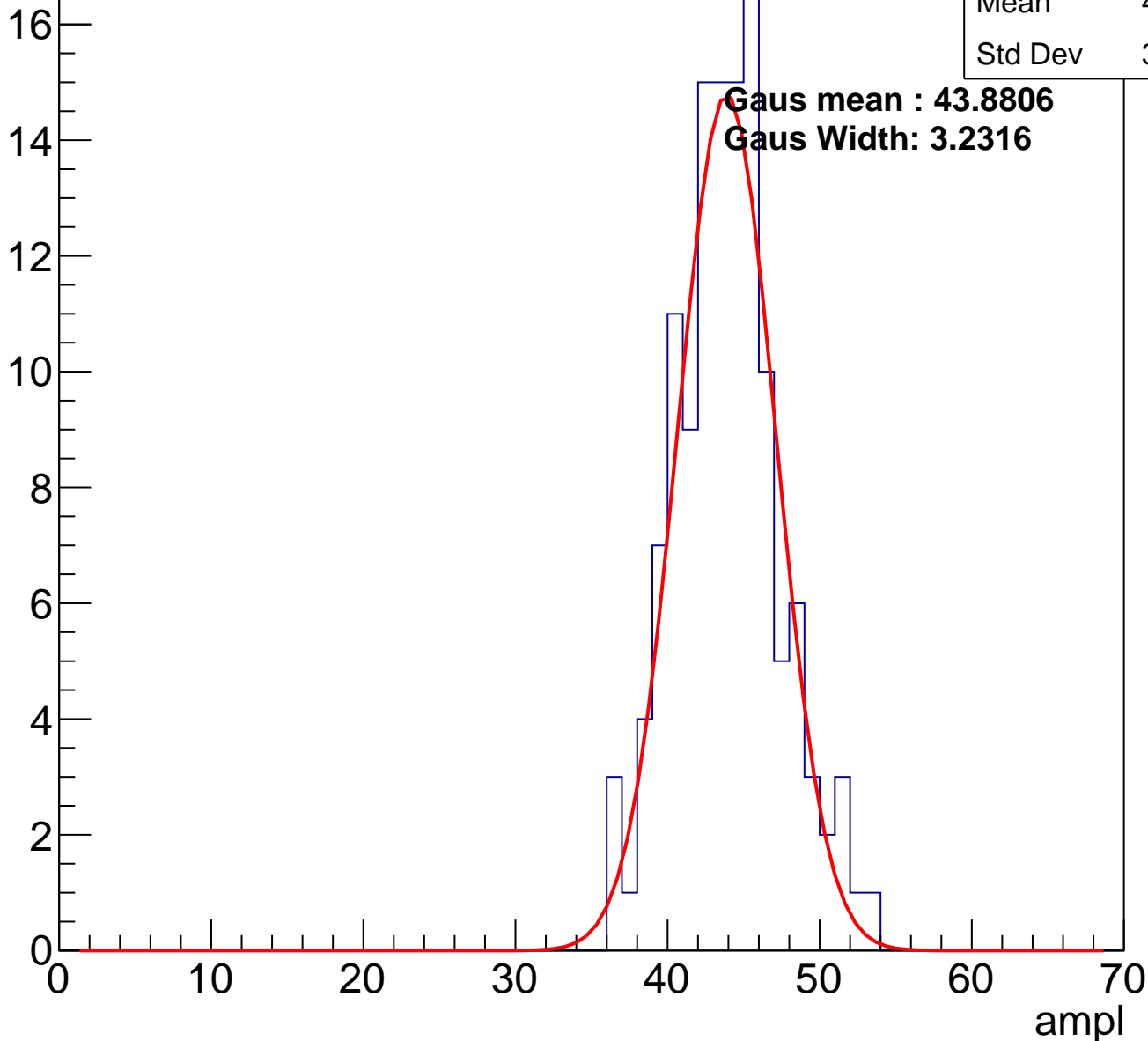
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	128
Mean	43.49
Std Dev	3.419

Gaus mean : 43.8806

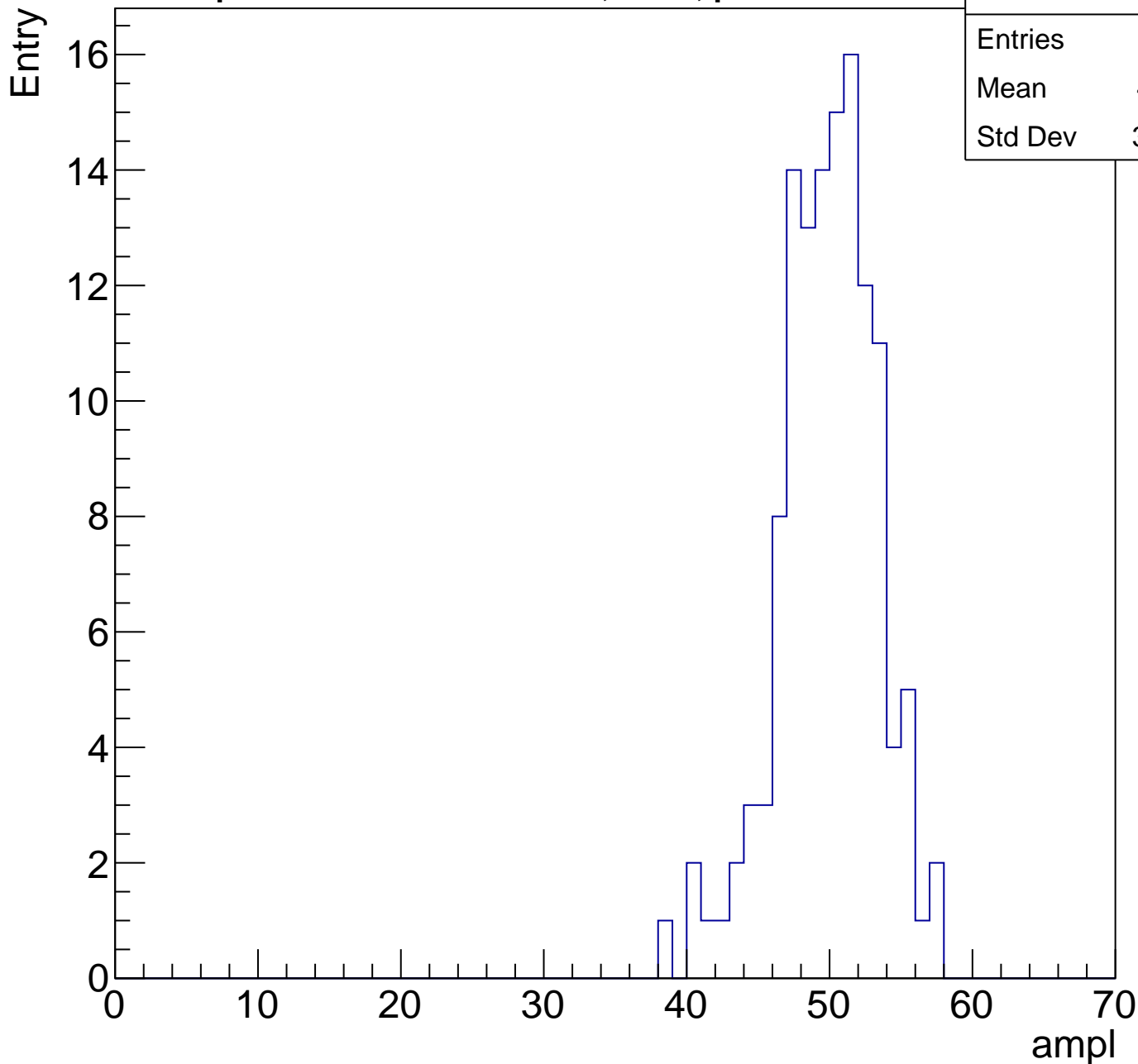
Gaus Width: 3.2316



B0L101S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	49.41
Std Dev	3.458



B0L101S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#1, port C1

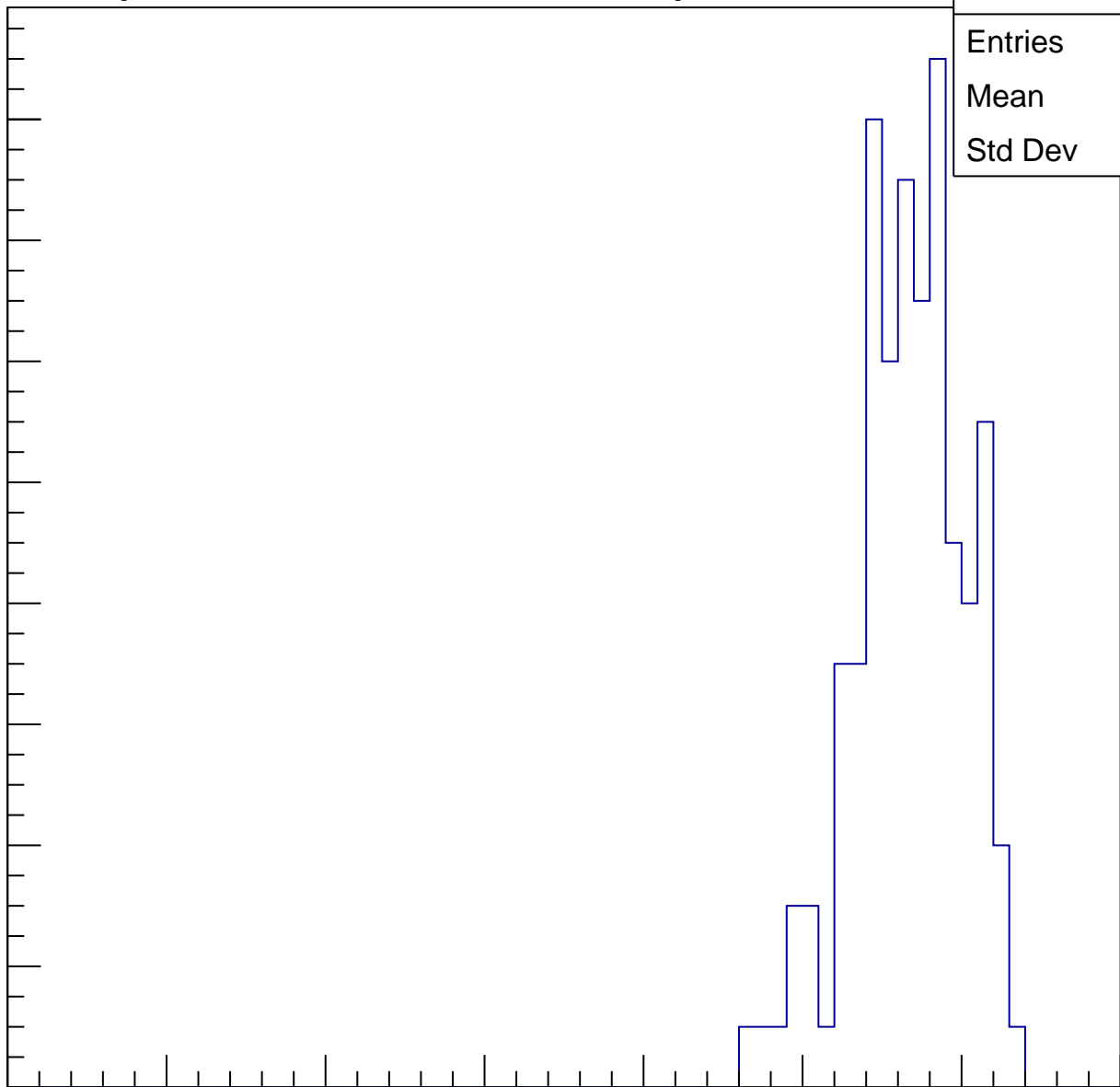
Entries	130
Mean	56.22
Std Dev	3.428

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

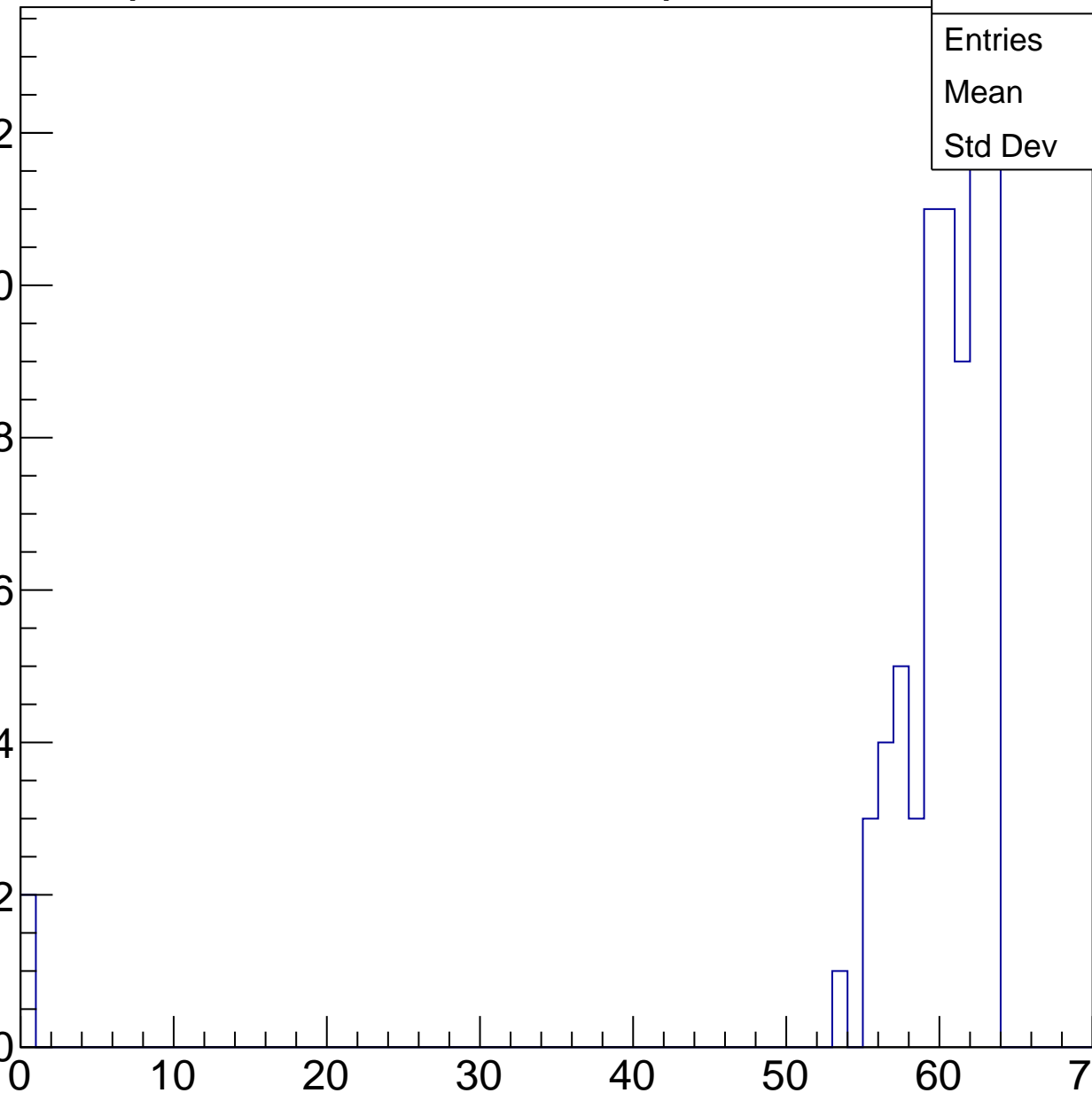
4

2

0

Entries	75
Mean	58.45
Std Dev	9.97

ampl

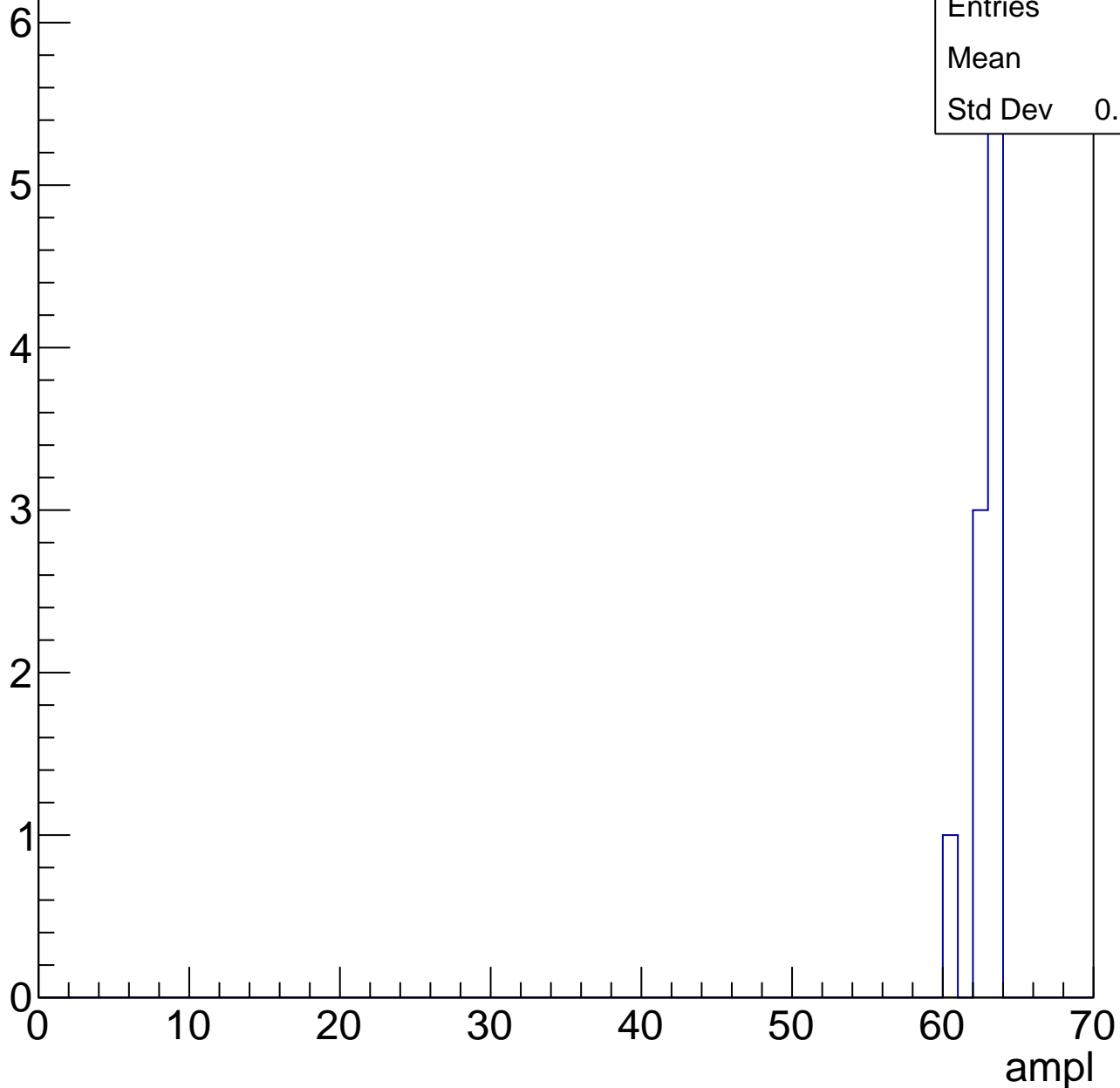


B0L101S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	10
Mean	62.4
Std Dev	0.9165



B0L101S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U15-ch50, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	27.54
Std Dev	4.764

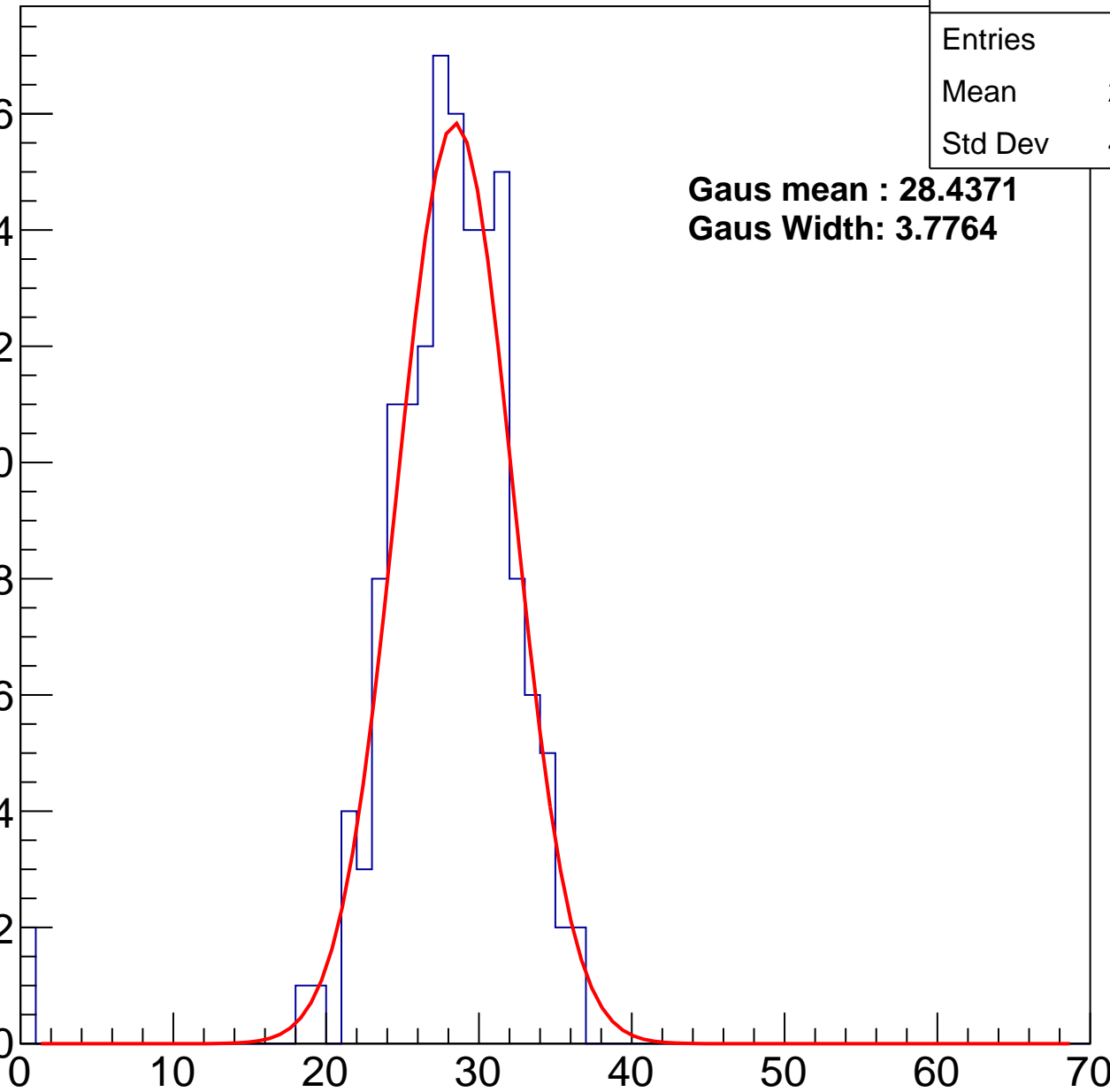
Gaus mean : 28.4371

Gaus Width: 3.7764

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch50, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

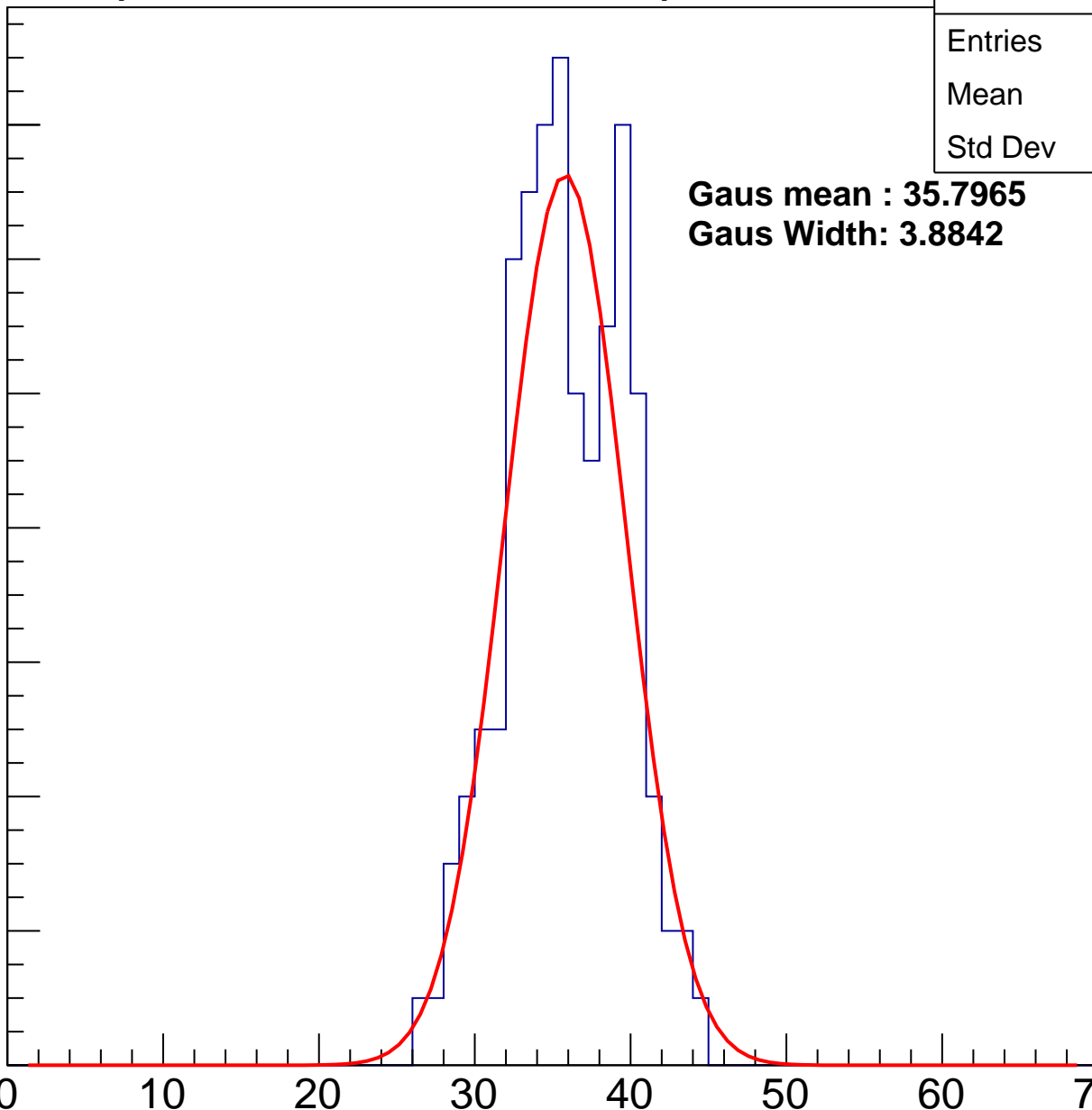
14
12
10
8
6
4
2
0

Entries	136
Mean	35.35
Std Dev	3.707

Gaus mean : 35.7965

Gaus Width: 3.8842

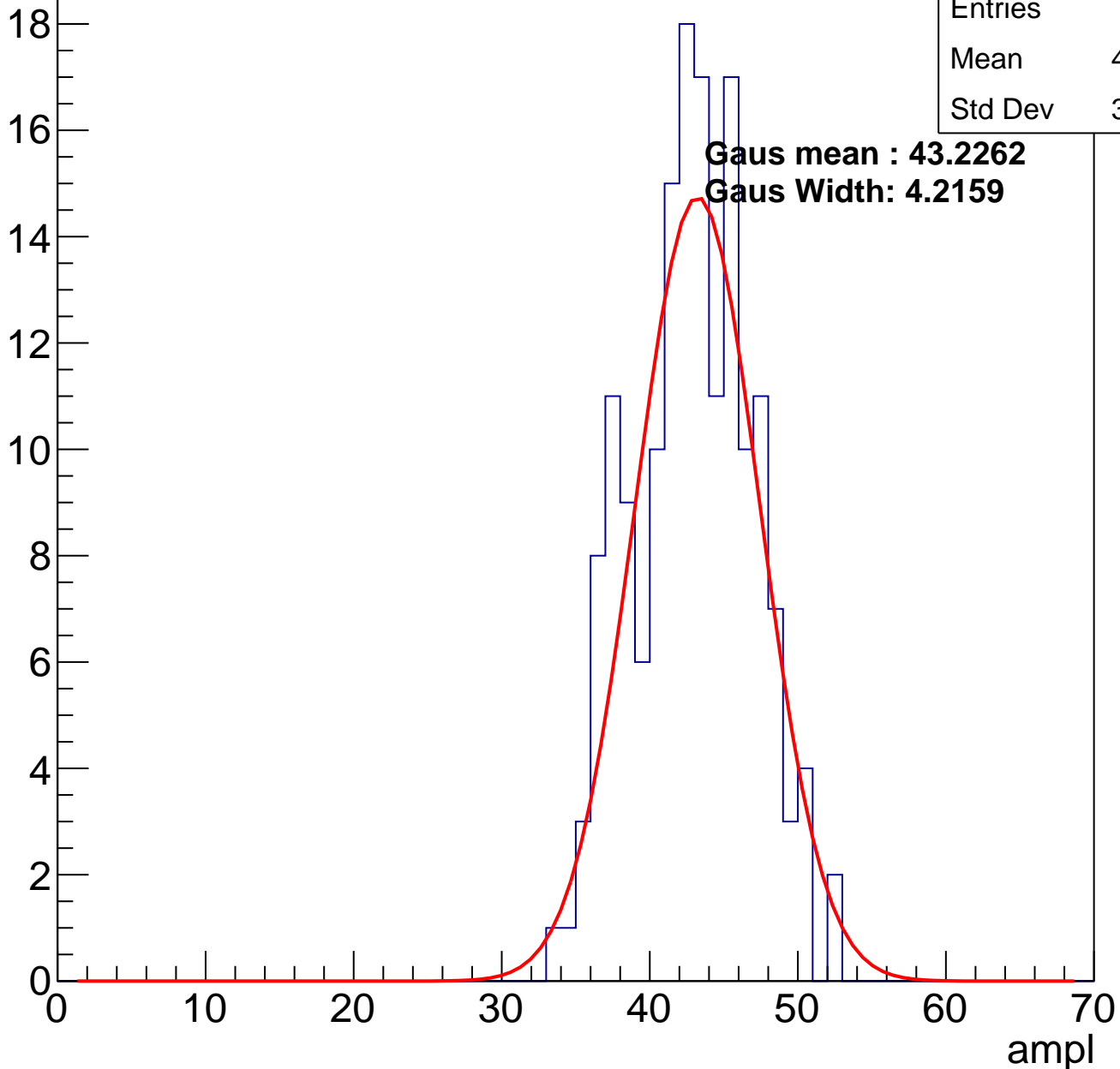
ampl



B0L101S, U15-ch50, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	49.17
Std Dev	3.244

Entry

12

10

8

6

4

2

0

0

10

20

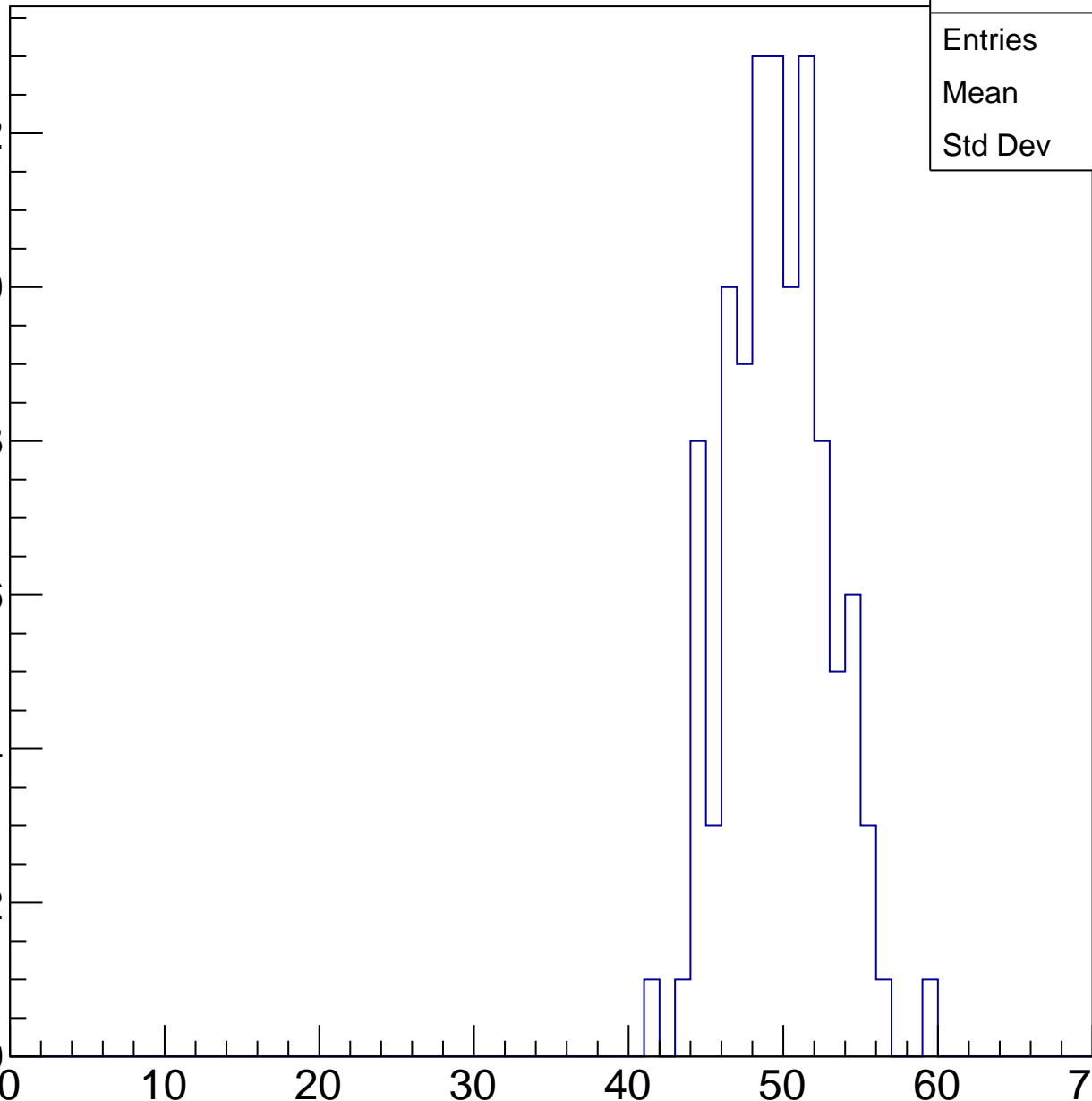
30

40

50

60

ampl



B0L101S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#1, port C1

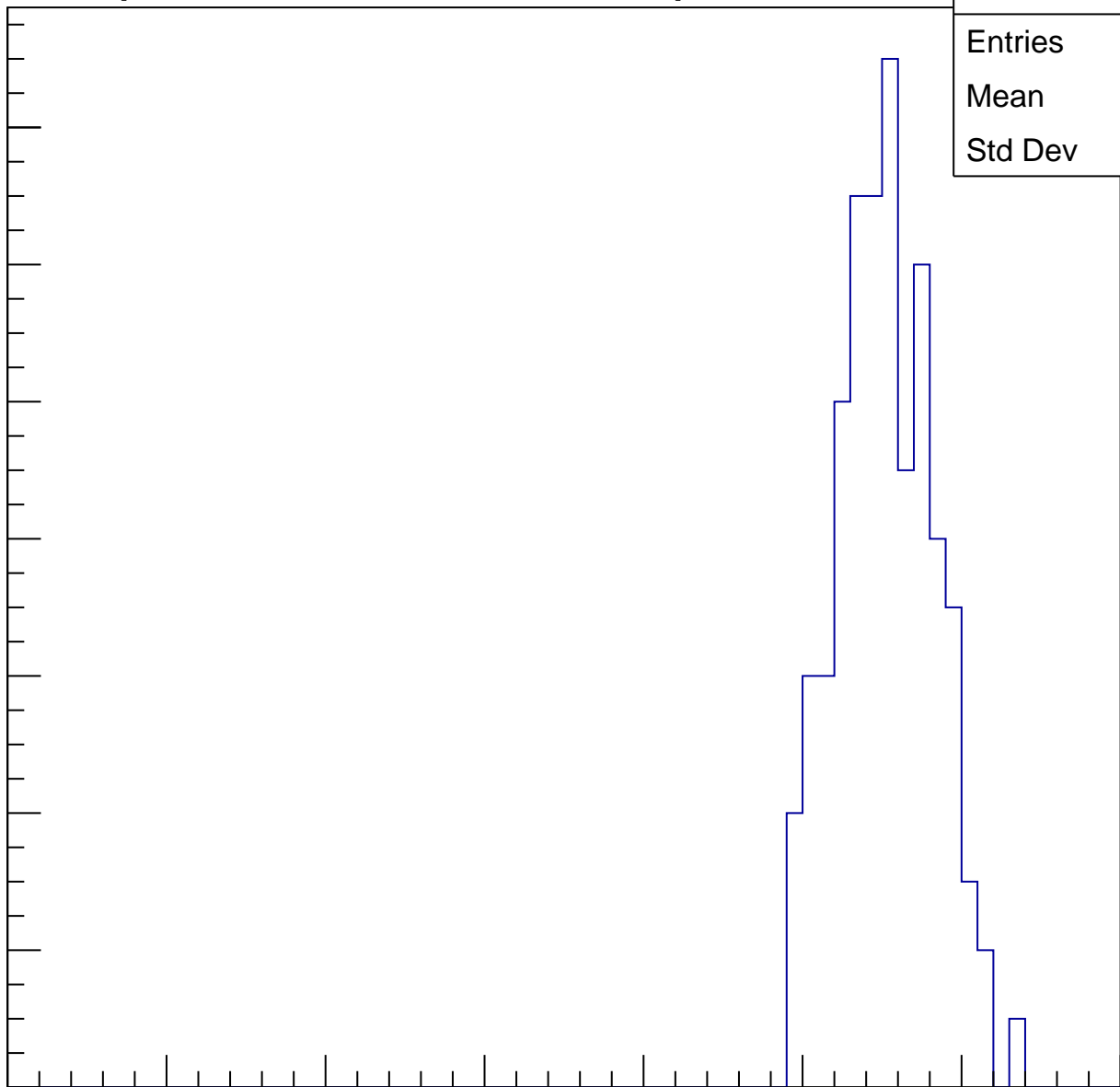
Entries	109
Mean	54.75
Std Dev	3.014

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch50, adc5

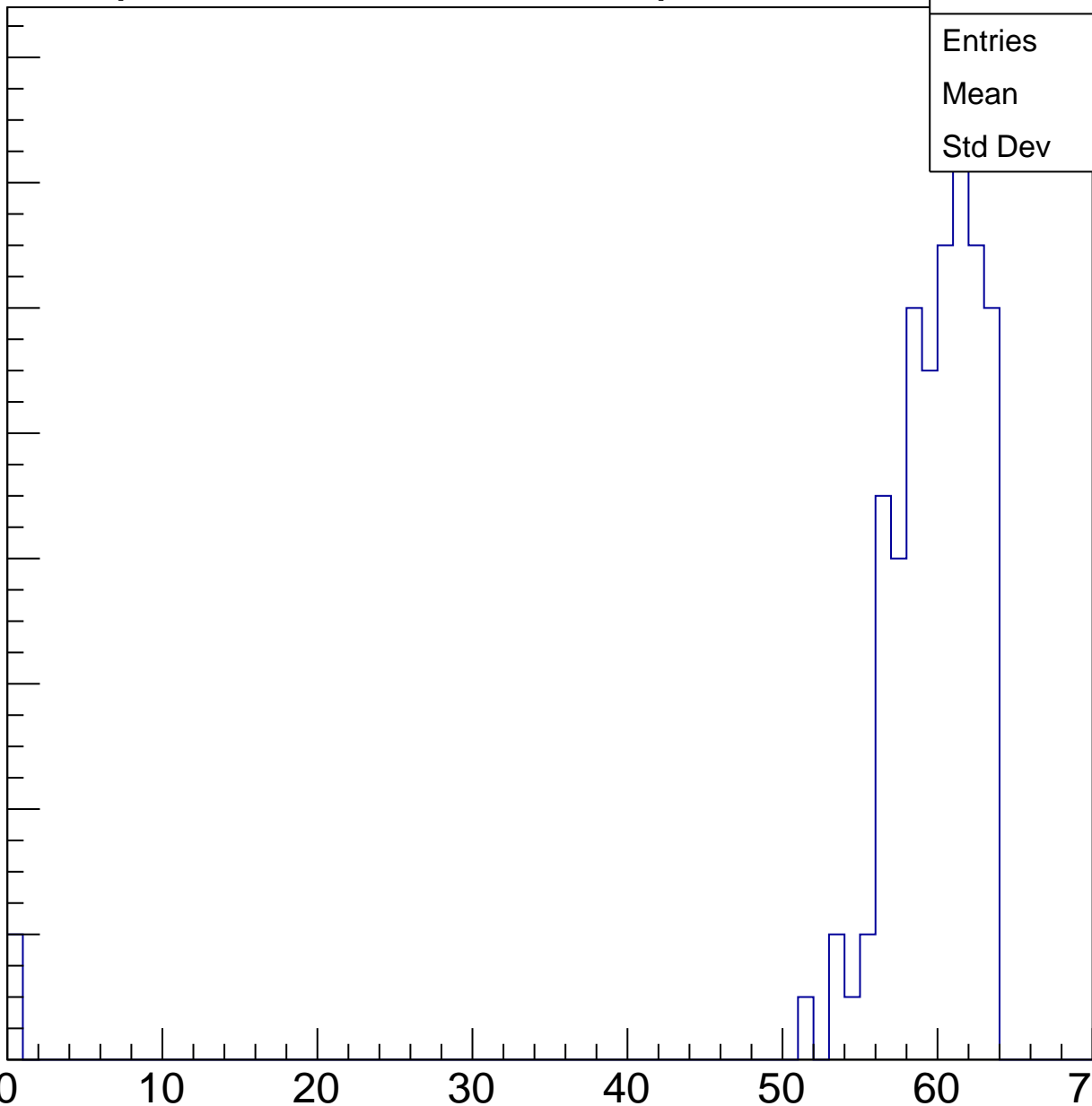
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.27
Std Dev	8.639

ampl

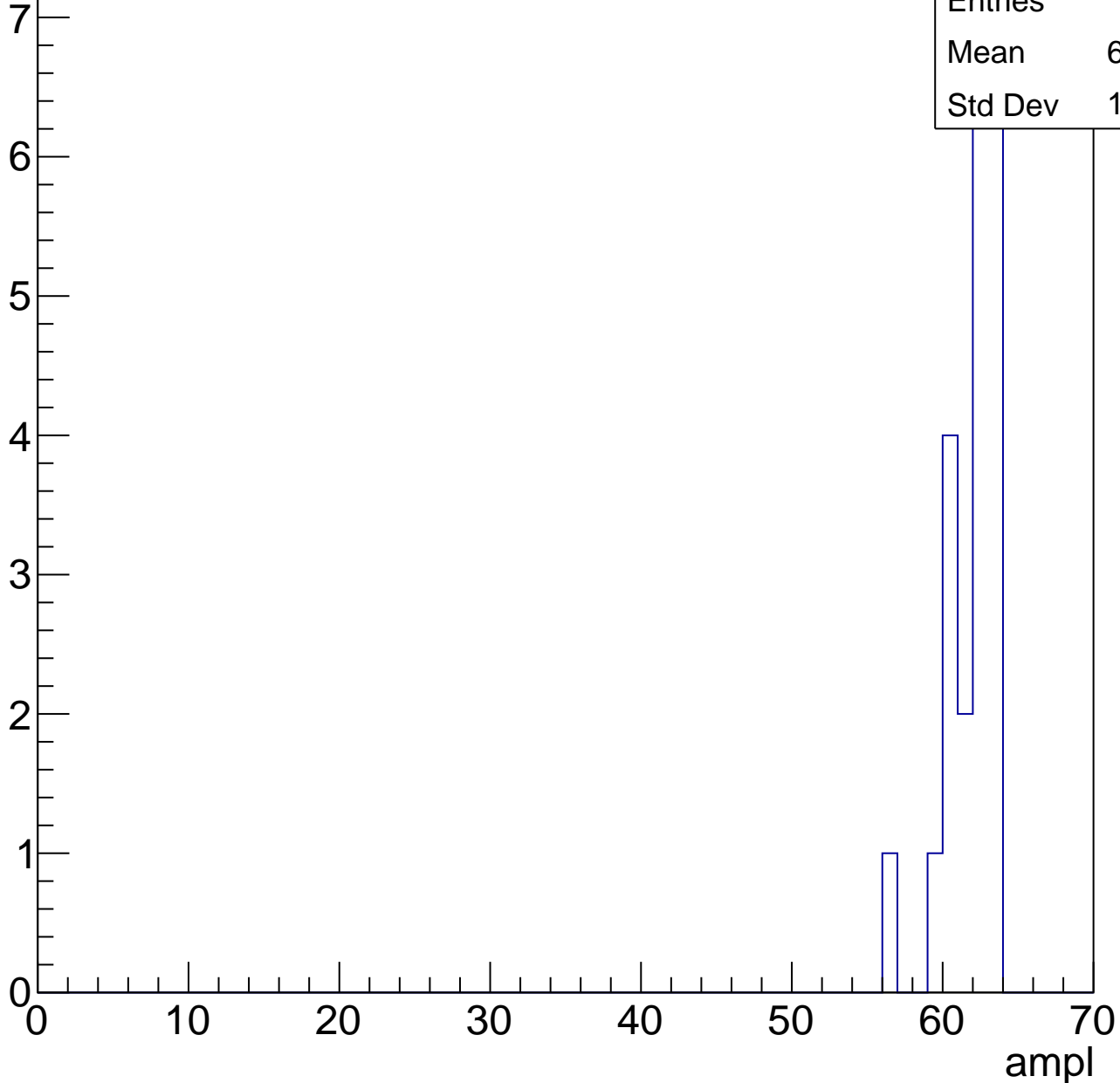


B0L101S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	61.45
Std Dev	1.698



B0L101S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch51, adc0

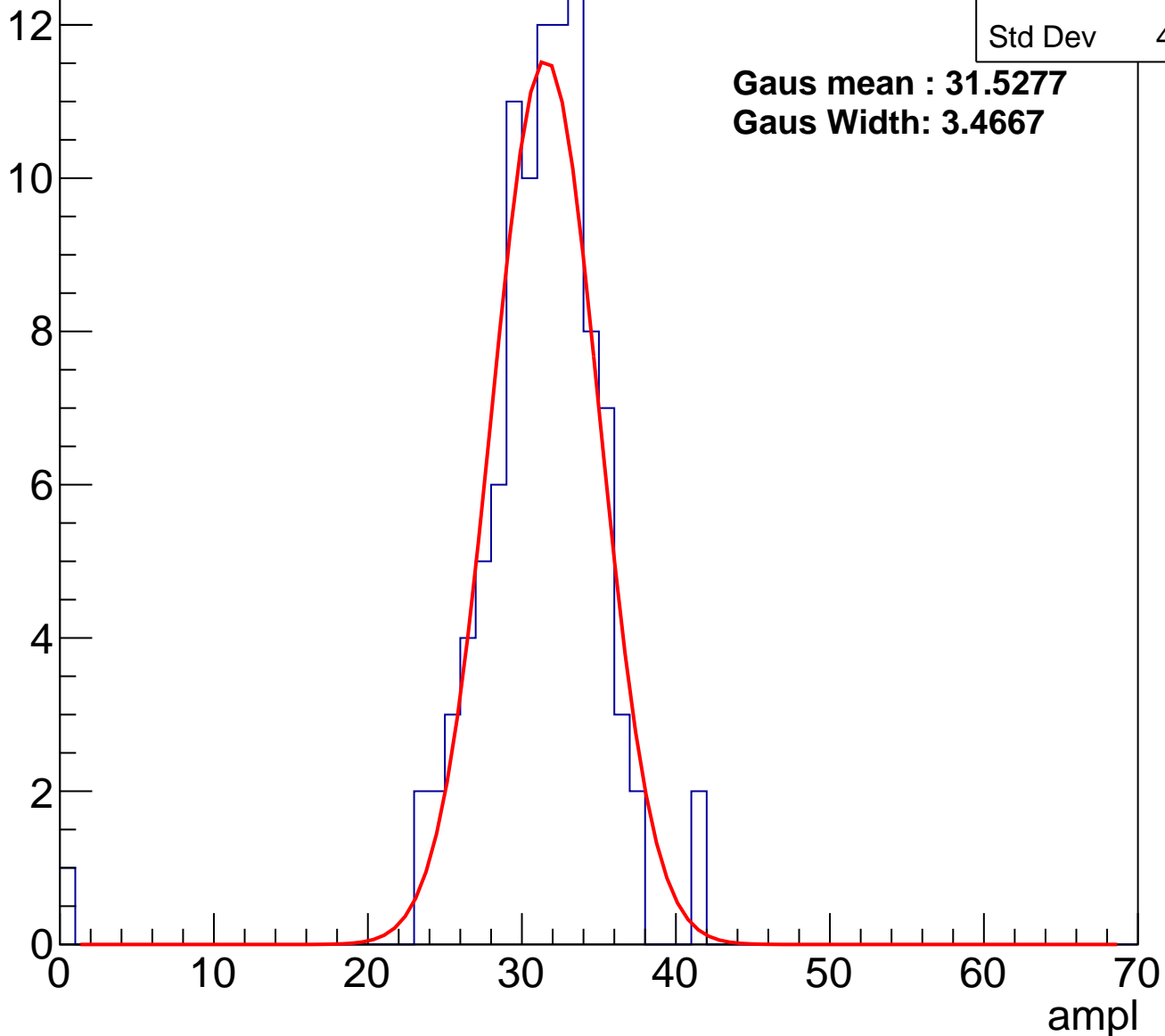
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	30.69
Std Dev	4.588

Gaus mean : 31.5277

Gaus Width: 3.4667

Entry



B0L101S, U15-ch51, adc1

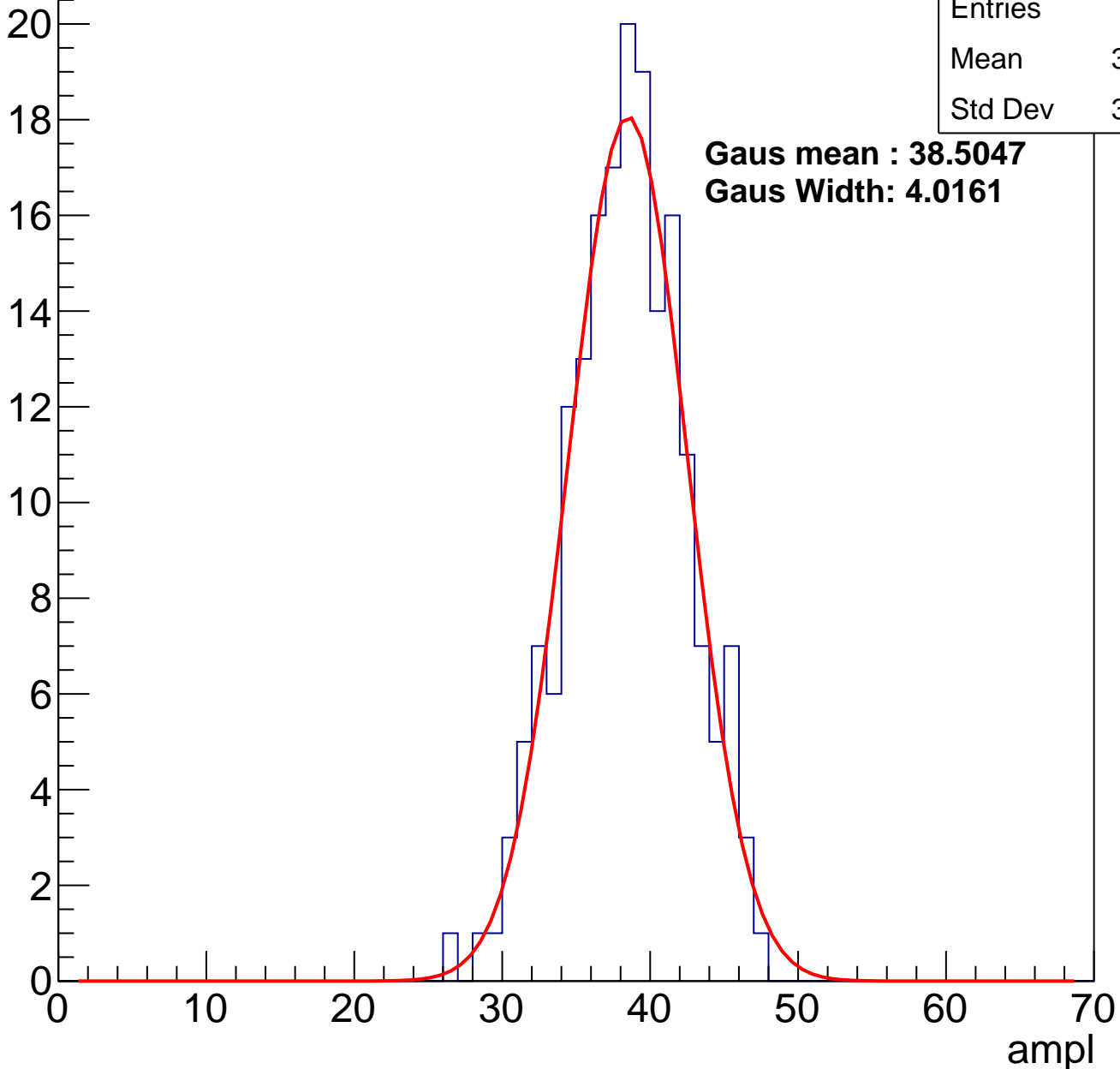
calib_packv5_042523_0143.root, FC#1, port C1

Entries	185
Mean	37.94
Std Dev	3.972

Gaus mean : 38.5047

Gaus Width: 4.0161

Entry



B0L101S, U15-ch51, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	87
Mean	44.76
Std Dev	3.169

Gaus mean : 45.2632

Gaus Width: 2.4861

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

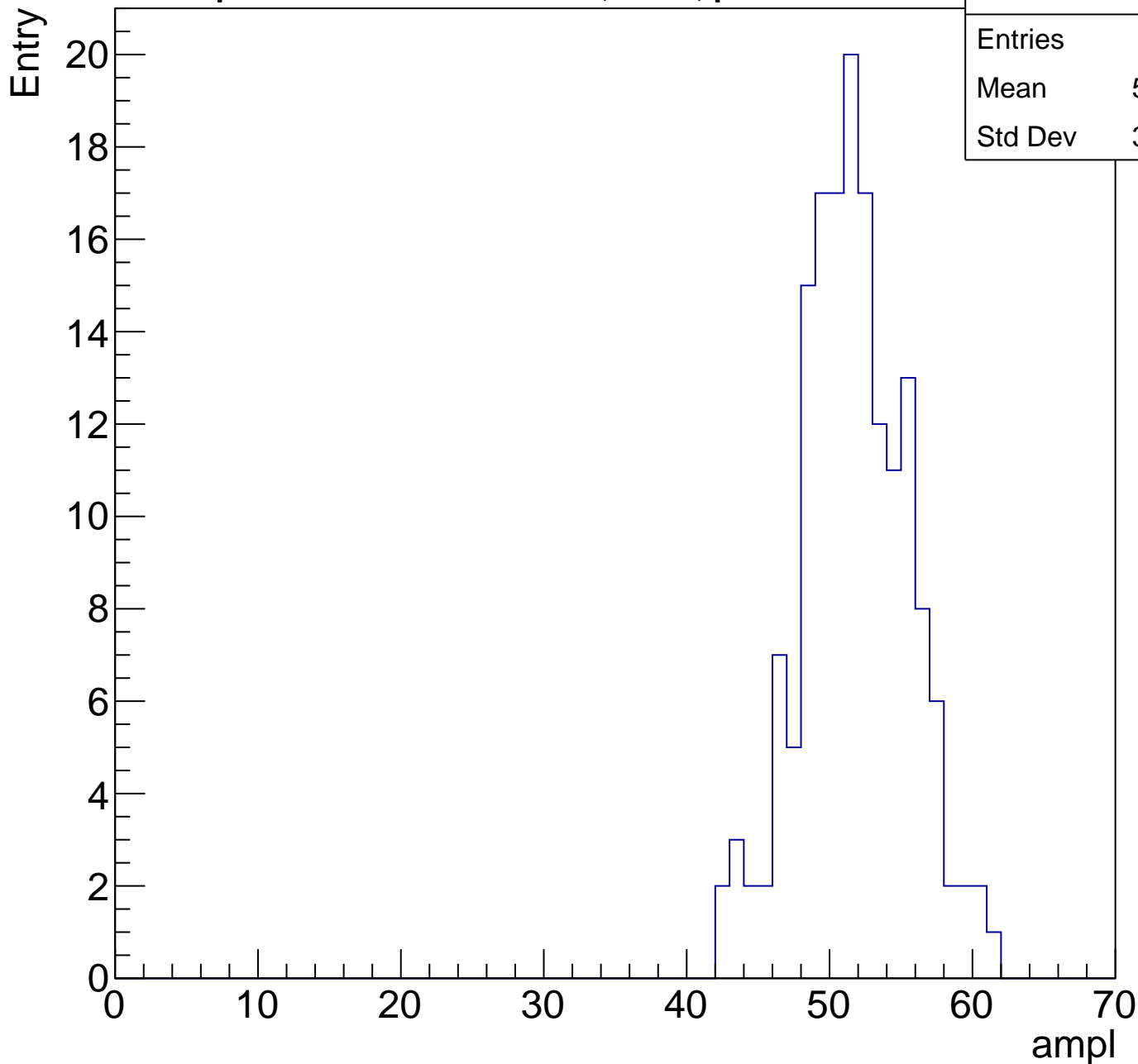
60

70

B0L101S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	51.25
Std Dev	3.716

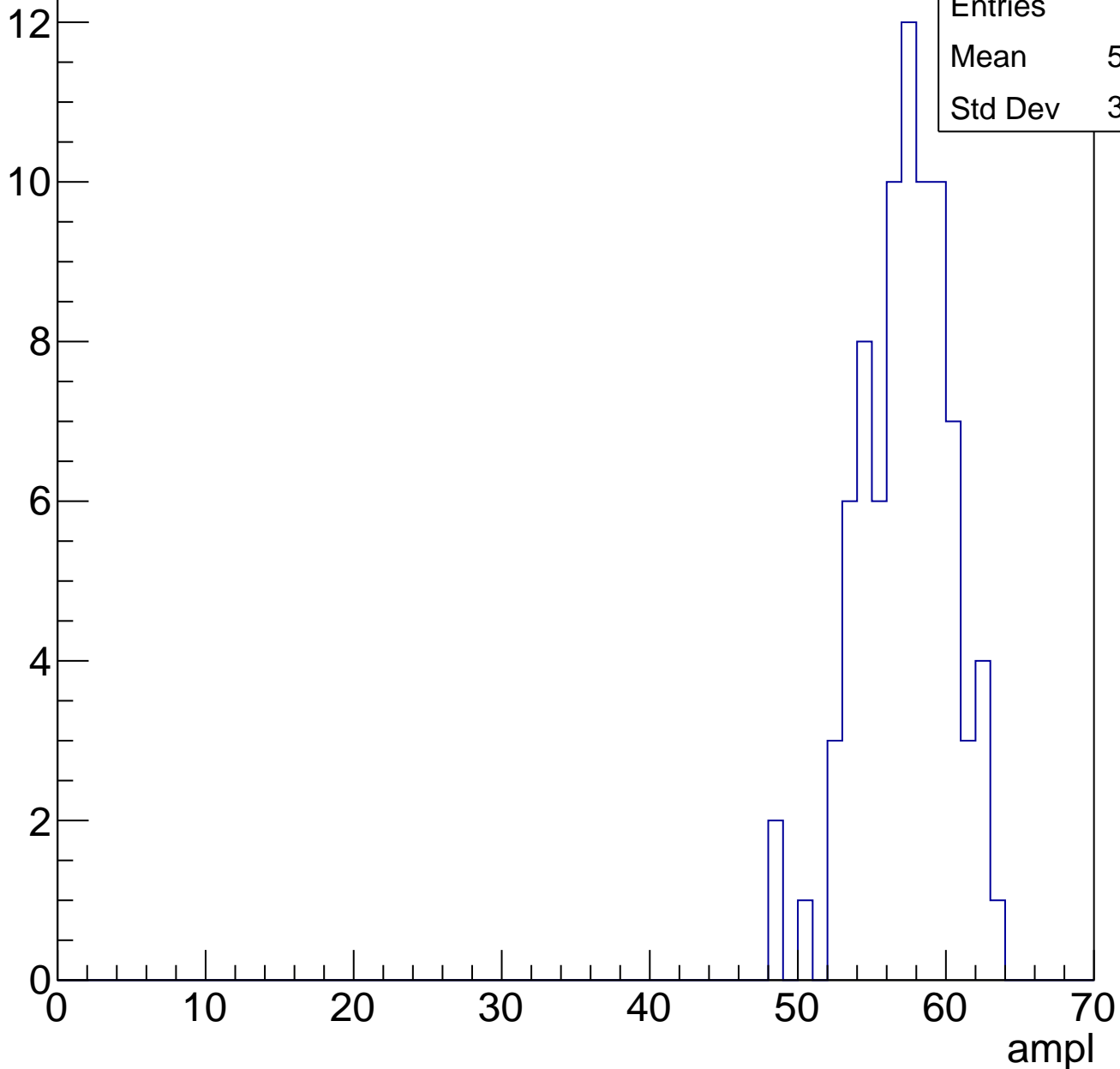


B0L101S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	83
Mean	56.75
Std Dev	3.065

Entry



B0L101S, U15-ch51, adc5

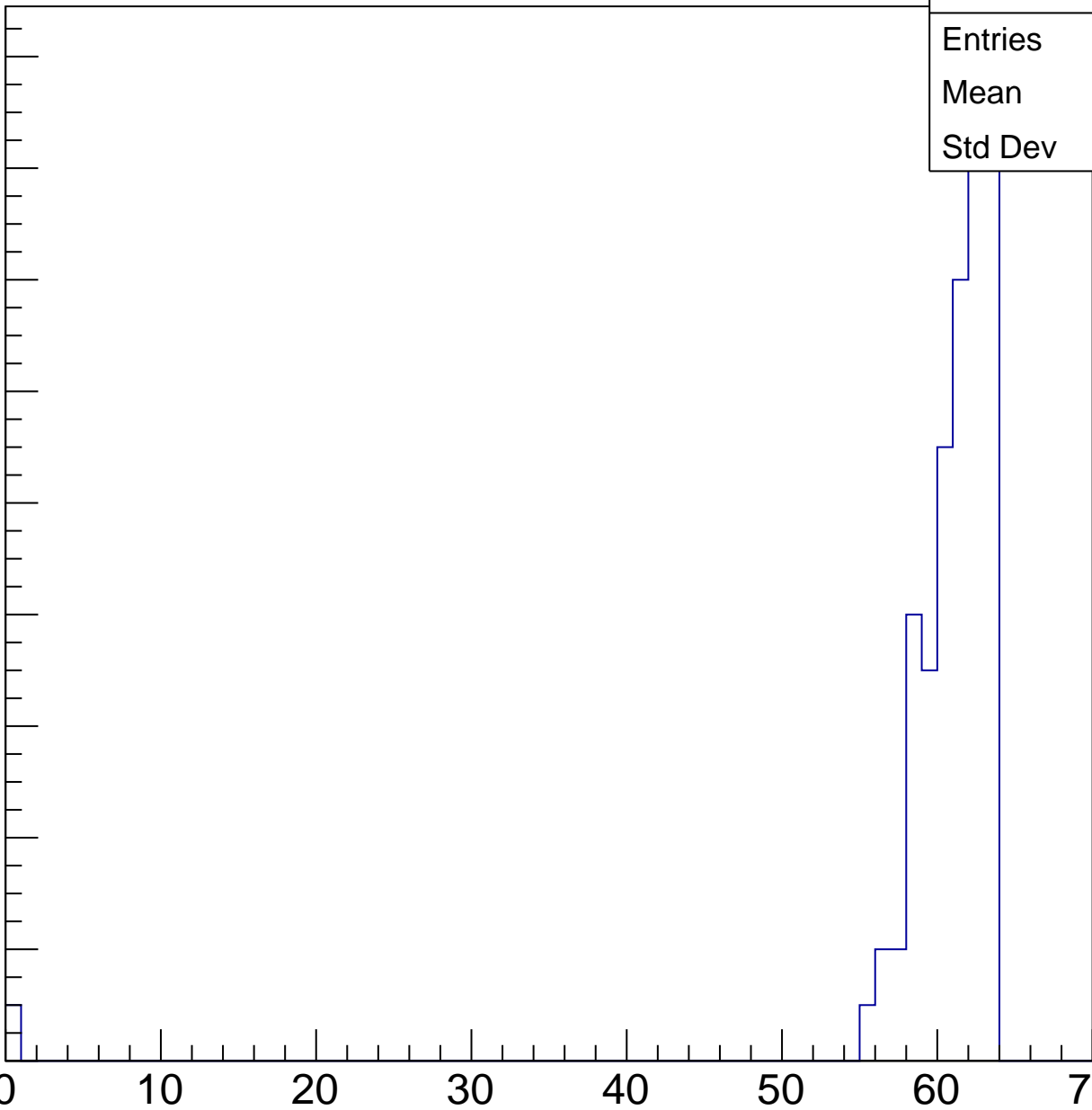
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.98
Std Dev	7.03

ampl



B0L101S, U15-ch51, adc6

calib_packv5_042523_0143.root, FC#1, port C1

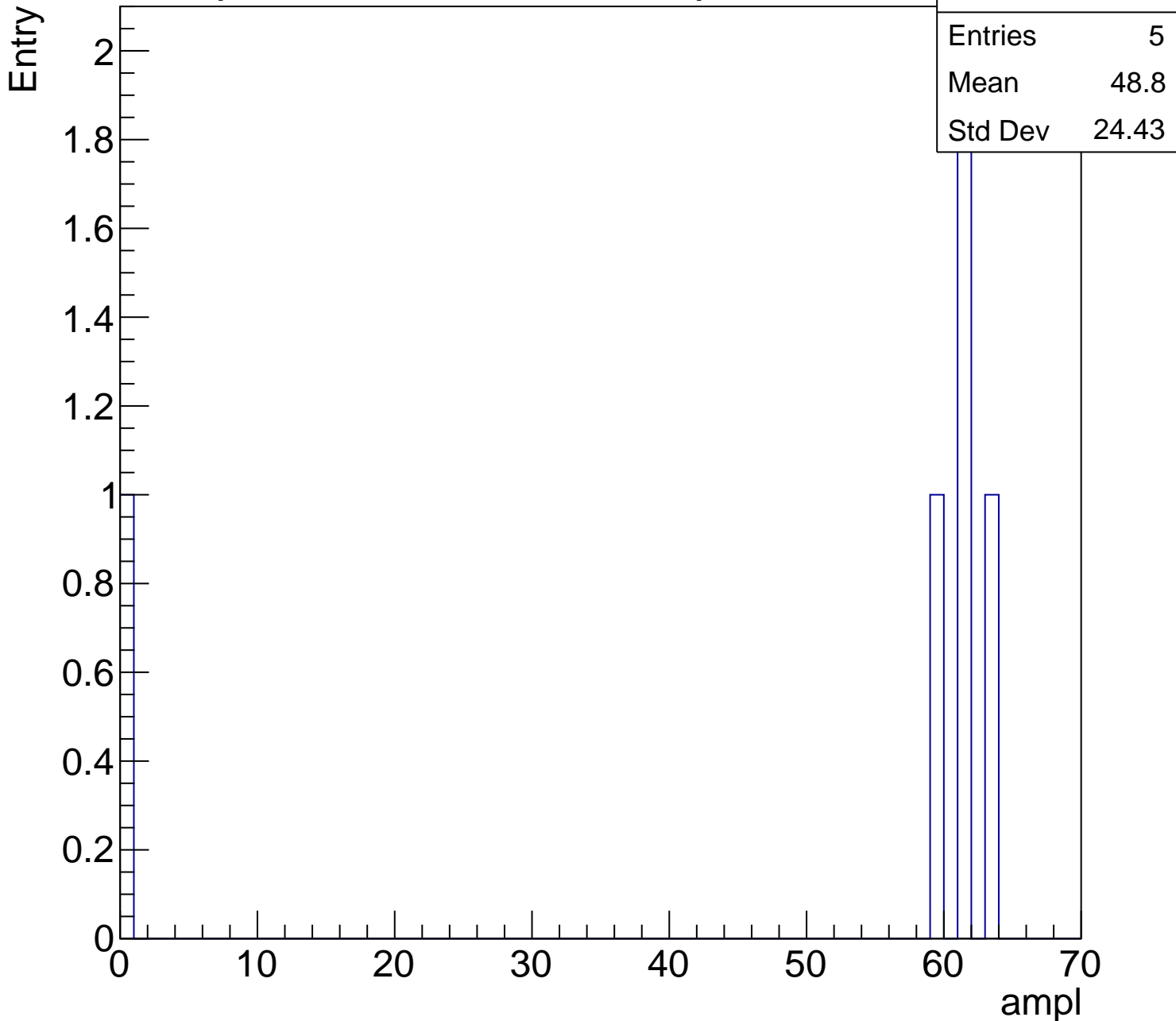
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	48.8
Std Dev	24.43

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch52, adc0

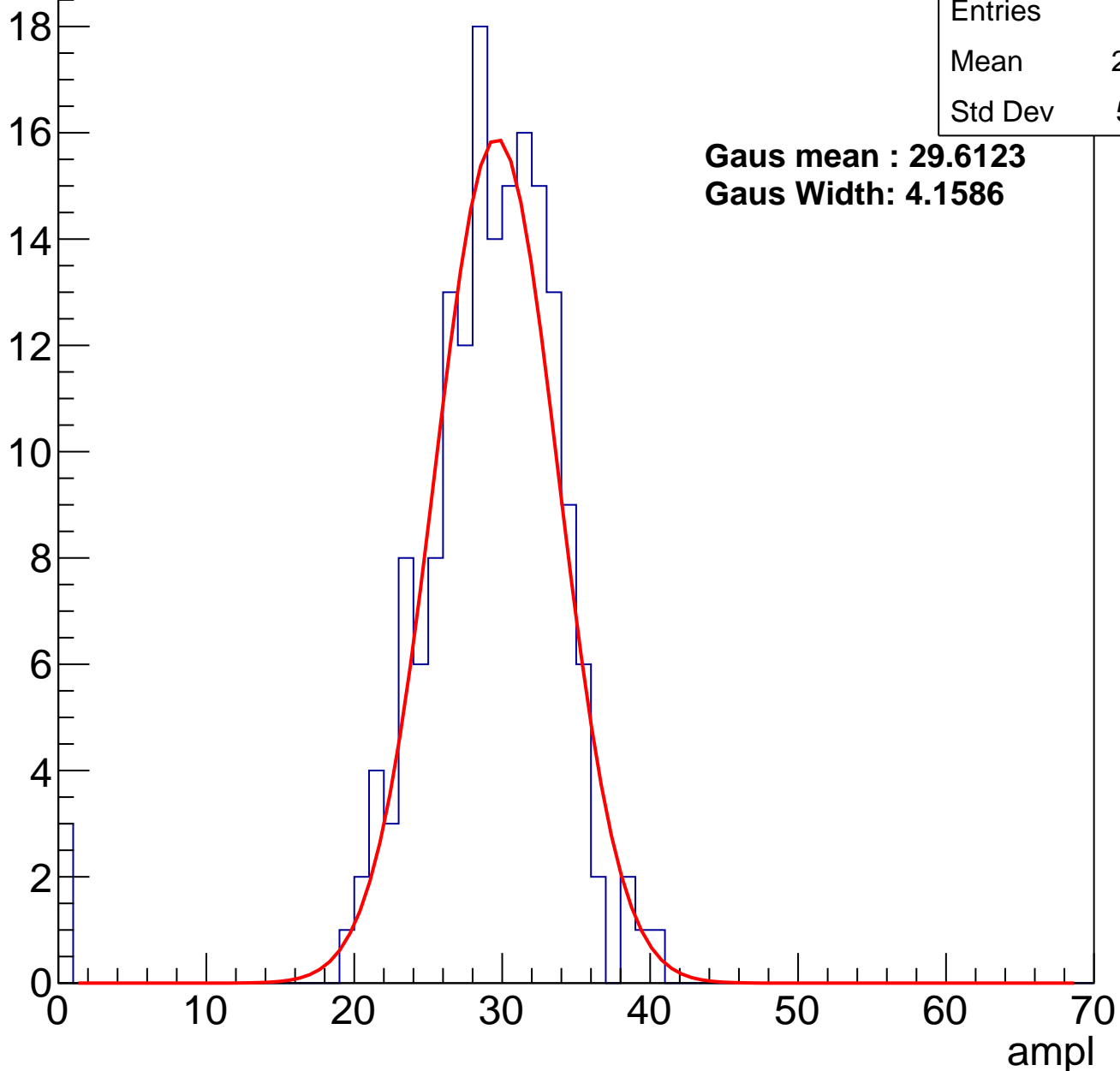
calib_packv5_042523_0143.root, FC#1, port C1

Entries	172
Mean	28.53
Std Dev	5.521

Gaus mean : 29.6123

Gaus Width: 4.1586

Entry



B0L101S, U15-ch52, adc1

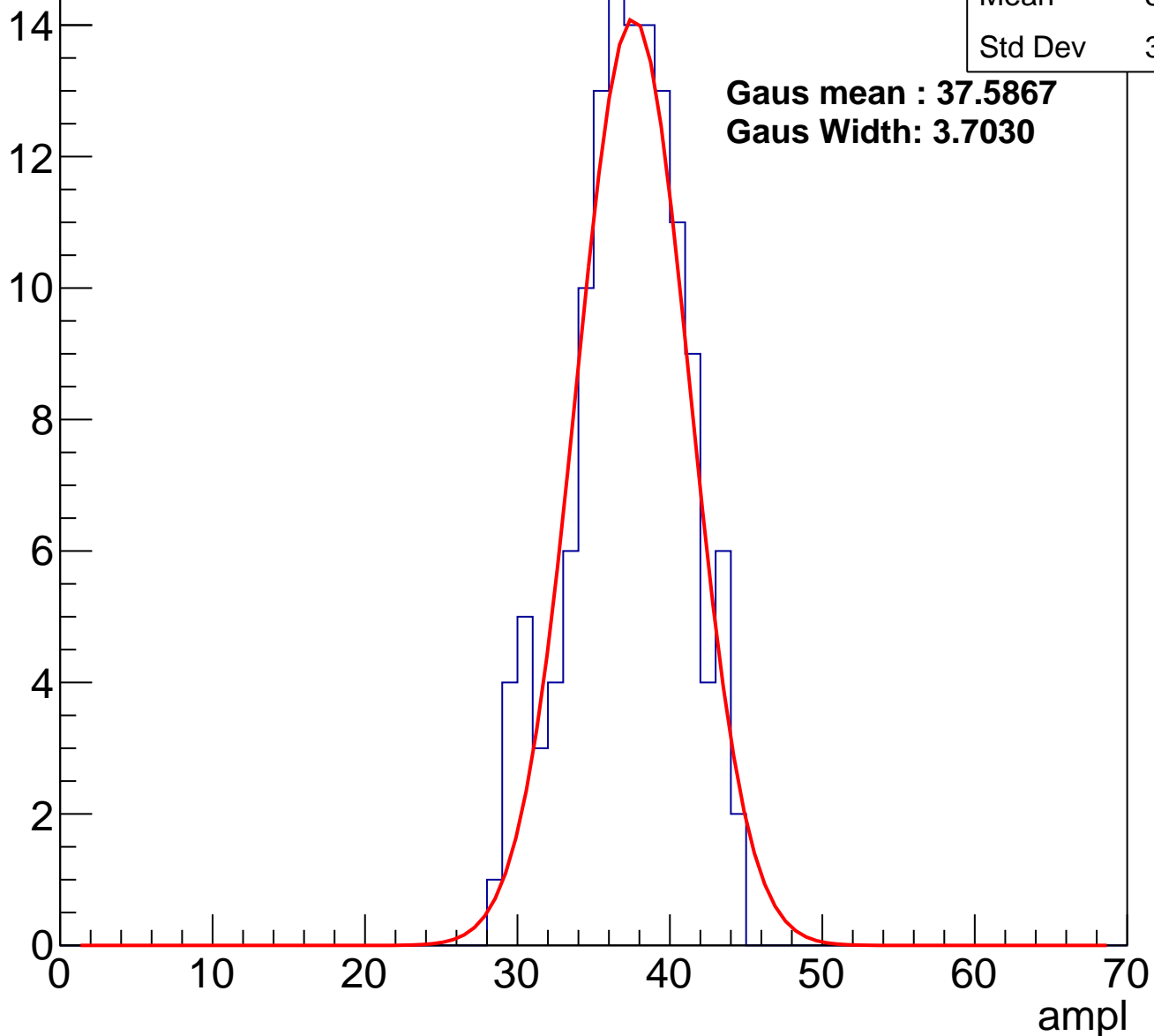
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	36.78
Std Dev	3.623

Gaus mean : 37.5867

Gaus Width: 3.7030

Entry



B0L101S, U15-ch52, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

114

Mean

42.7

Std Dev

3.69

Gaus mean : 43.3913

Gaus Width: 3.6602

0

10

20

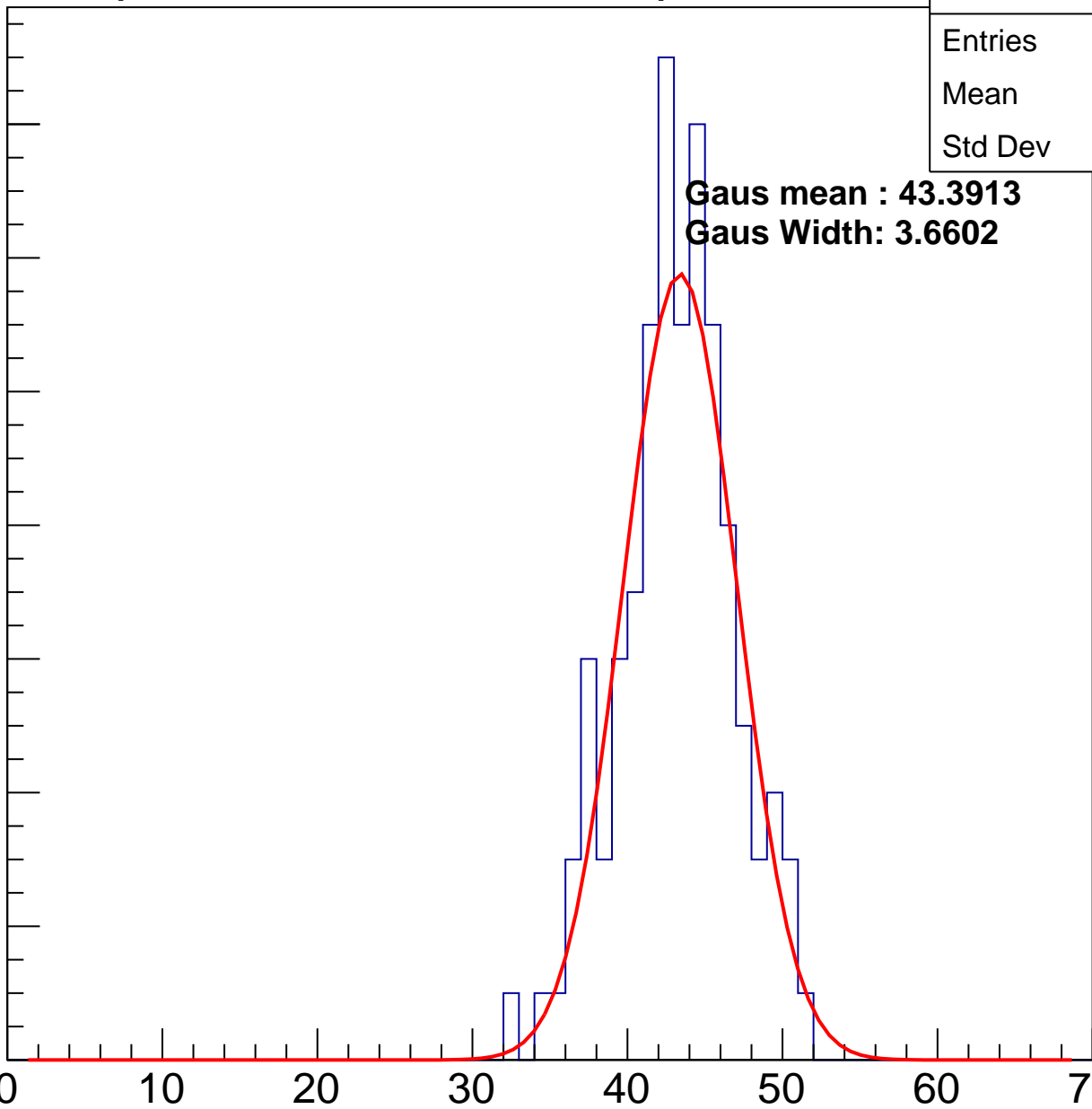
30

40

50

60

ampl



B0L101S, U15-ch52, adc3

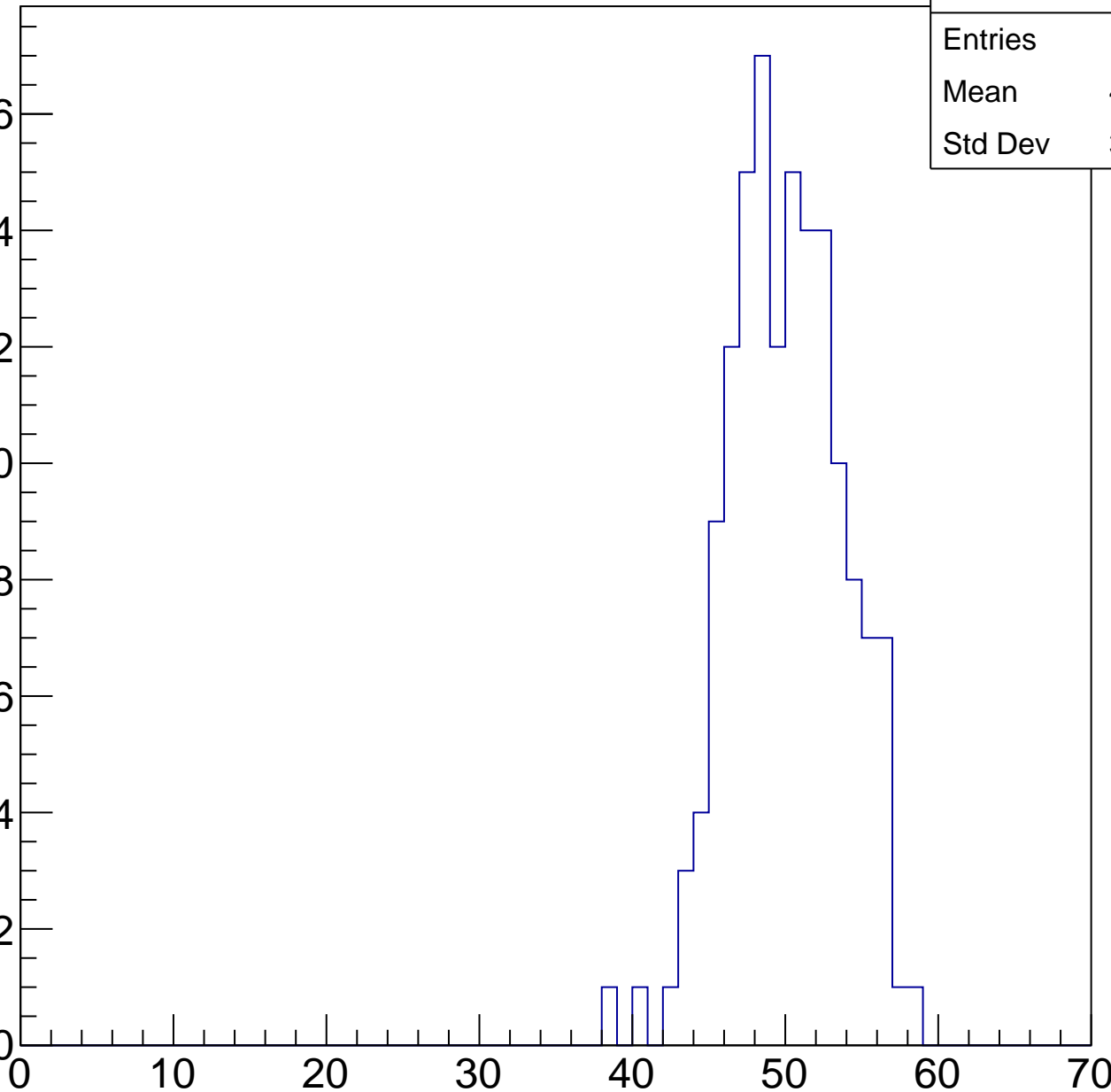
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	49.59
Std Dev	3.657

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

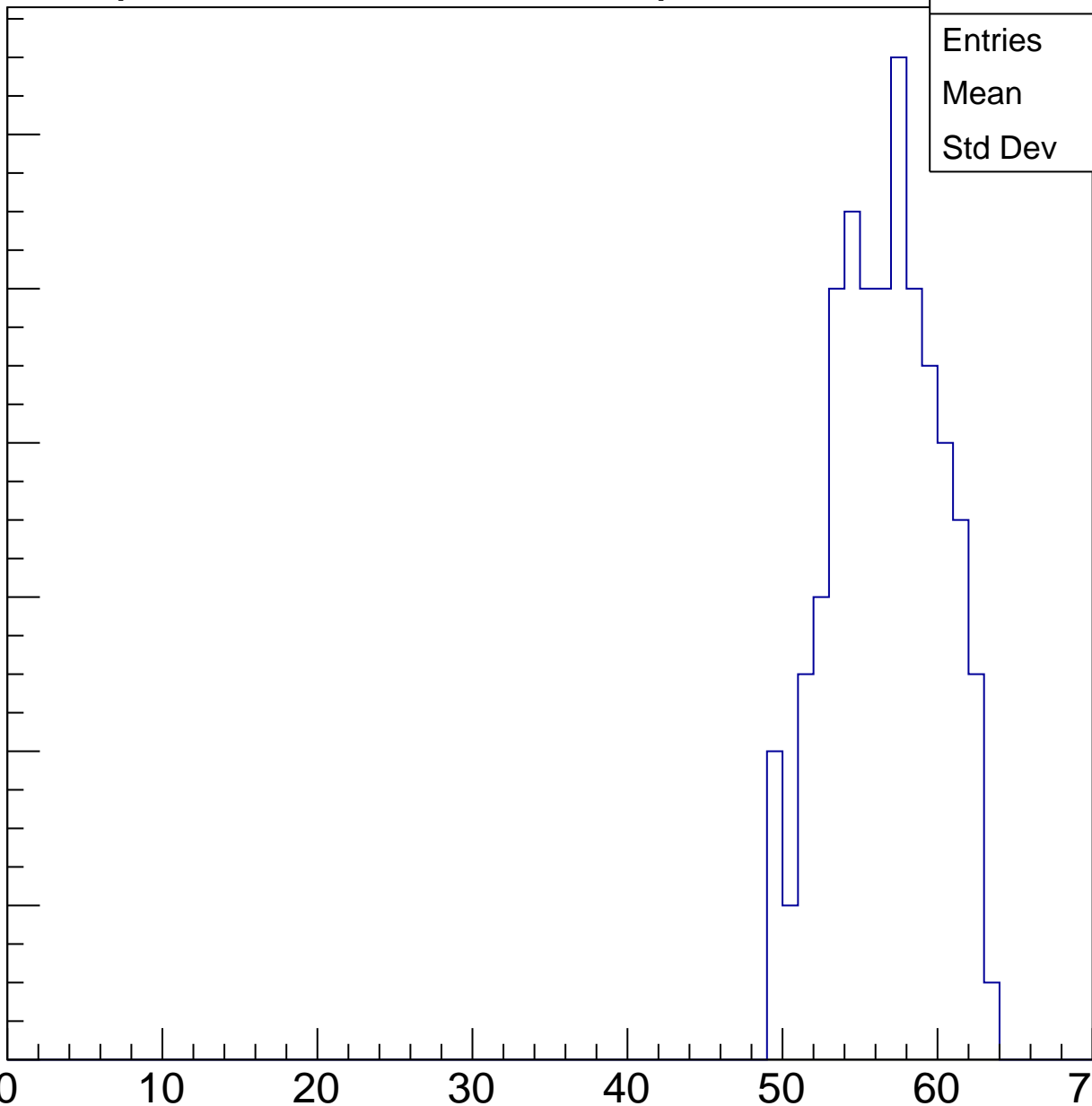
4

2

0

Entries	111
Mean	56.12
Std Dev	3.419

ampl



B0L101S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

72

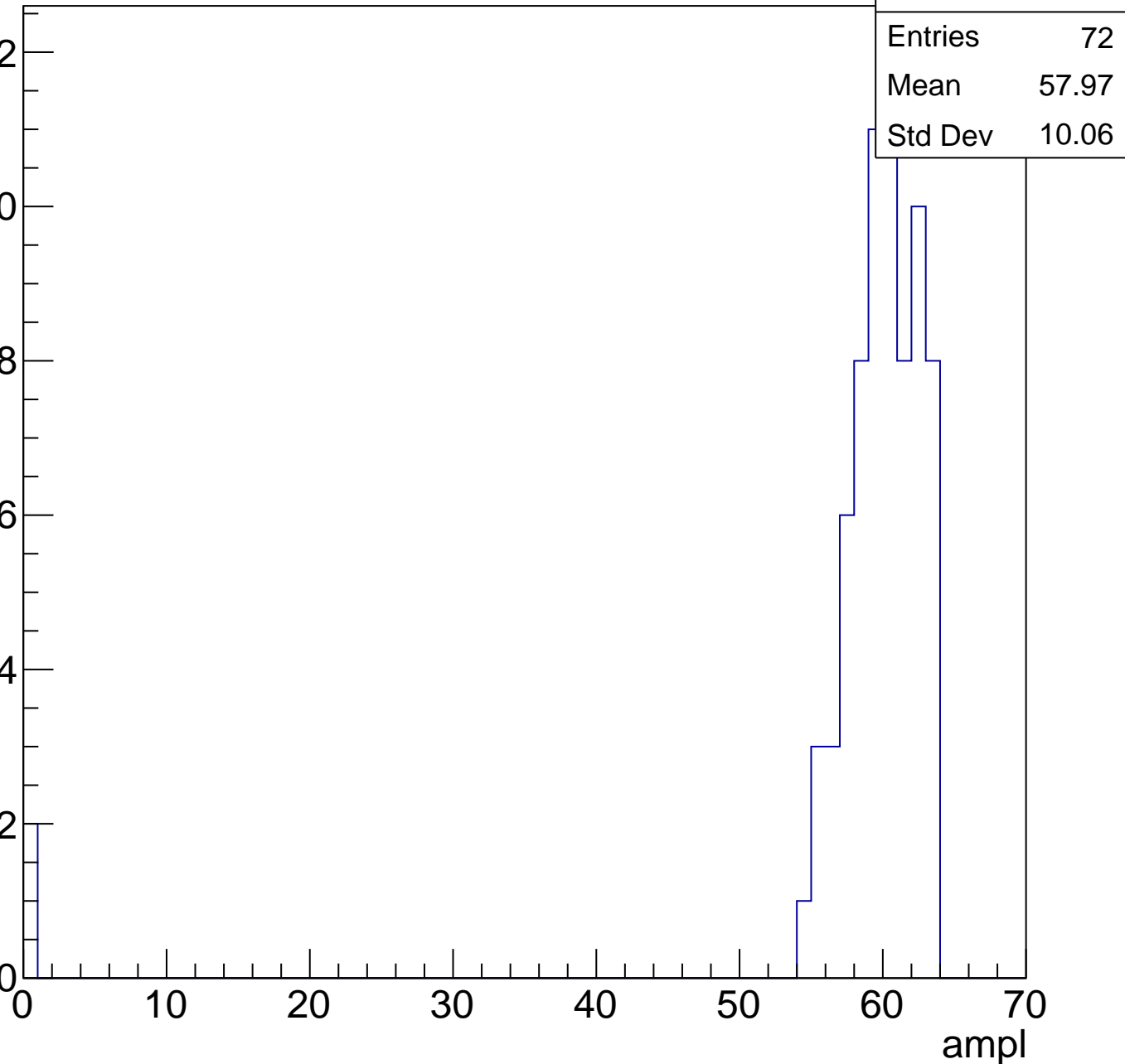
Mean

57.97

Std Dev

10.06

ampl

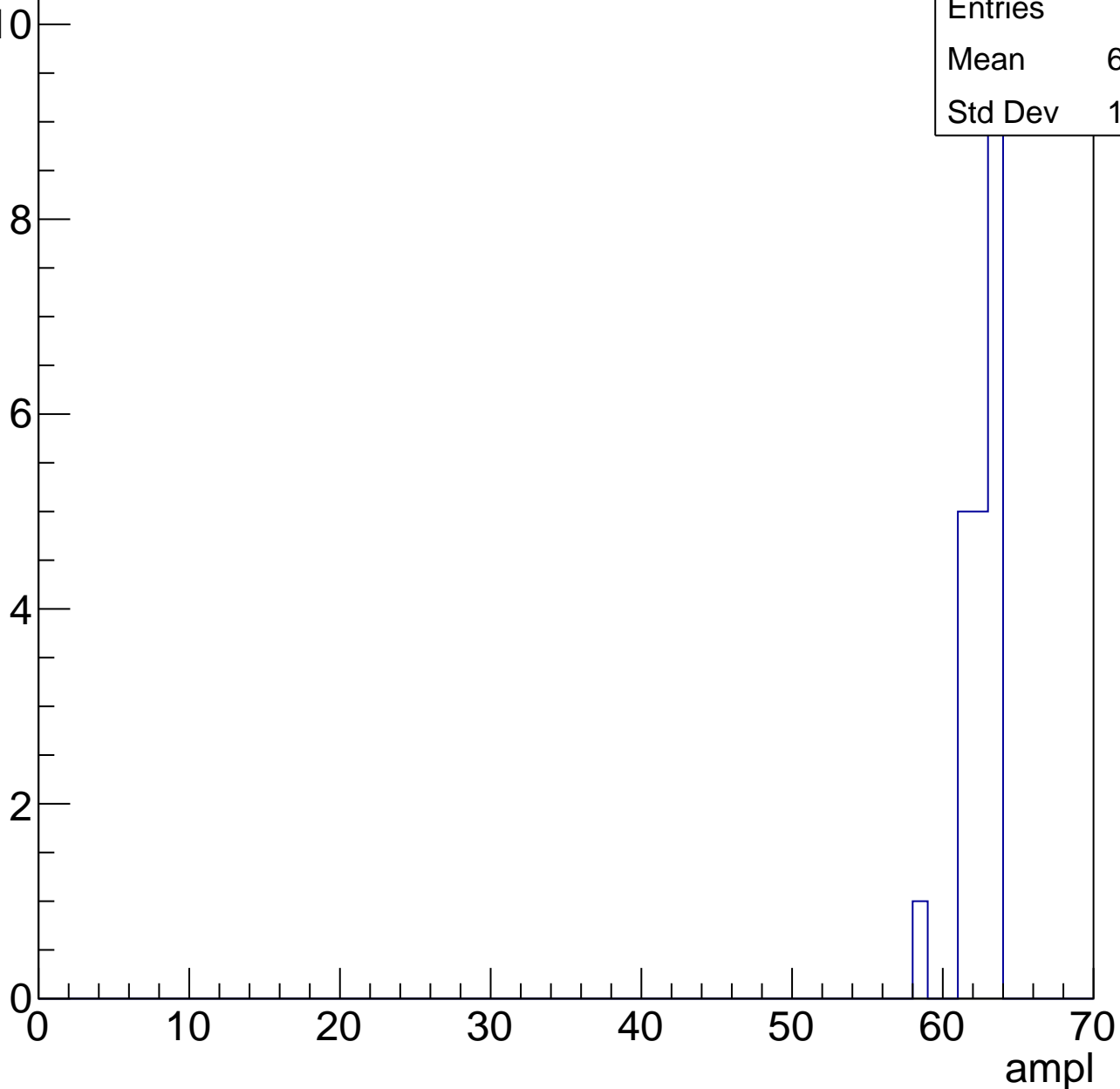


B0L101S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	62.05
Std Dev	1.214



B0L101S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch53, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	29.46
Std Dev	3.777

Gaus mean : 29.7865

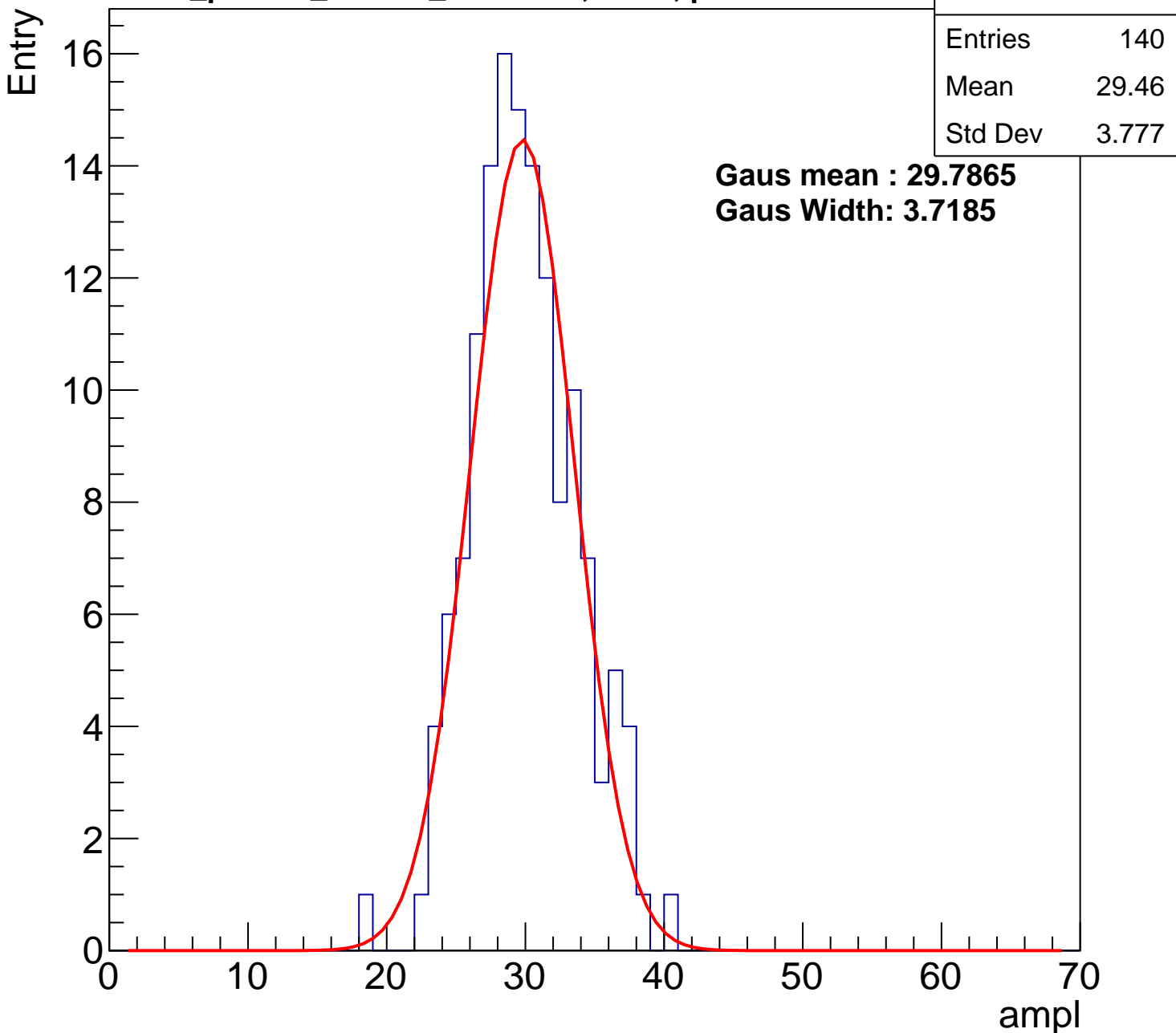
Gaus Width: 3.7185

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch53, adc1

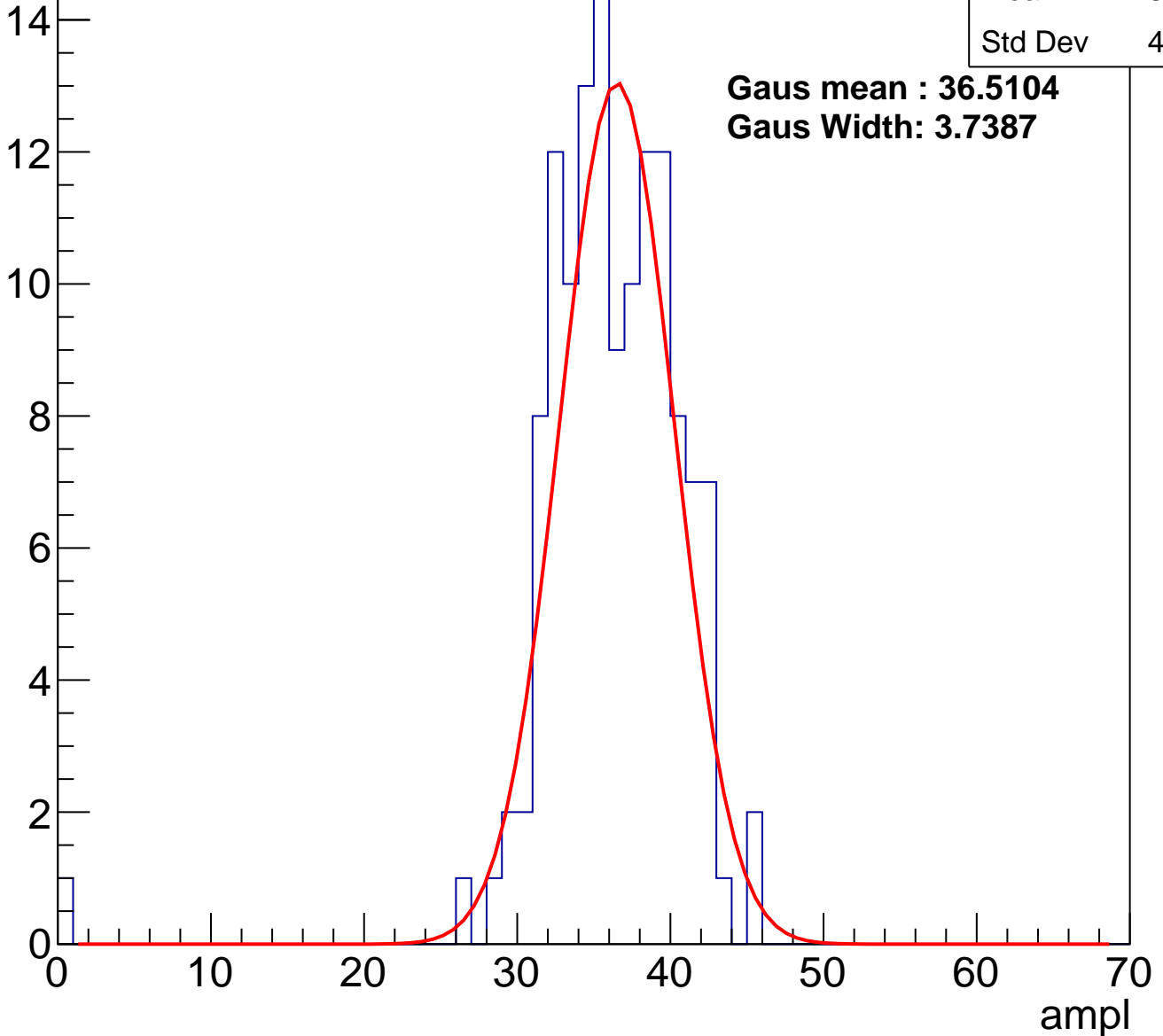
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	133
Mean	35.74
Std Dev	4.829

Gaus mean : 36.5104

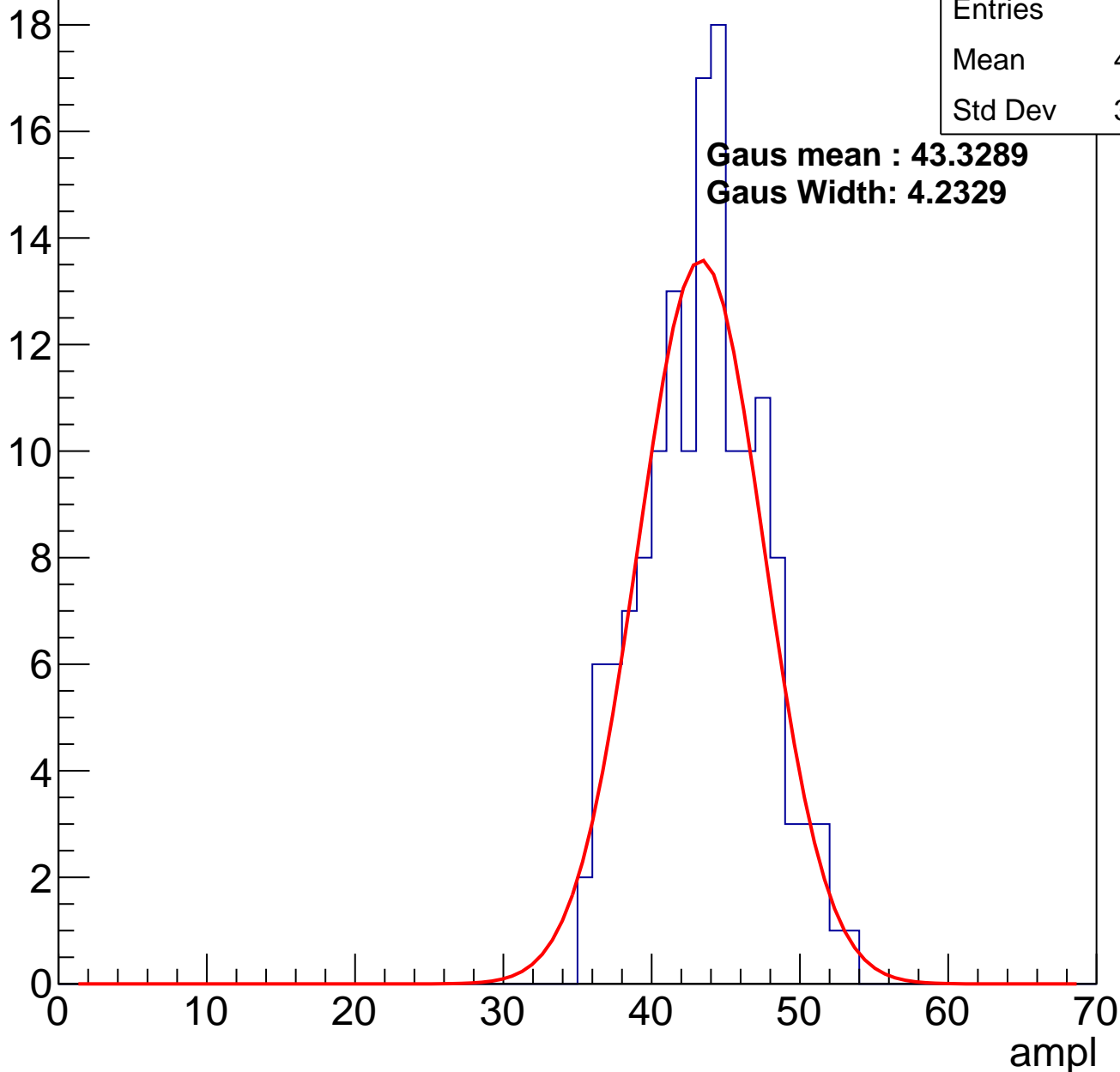
Gaus Width: 3.7387



B0L101S, U15-ch53, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	49.37
Std Dev	3.424

Entry

12

10

8

6

4

2

0

0

10

20

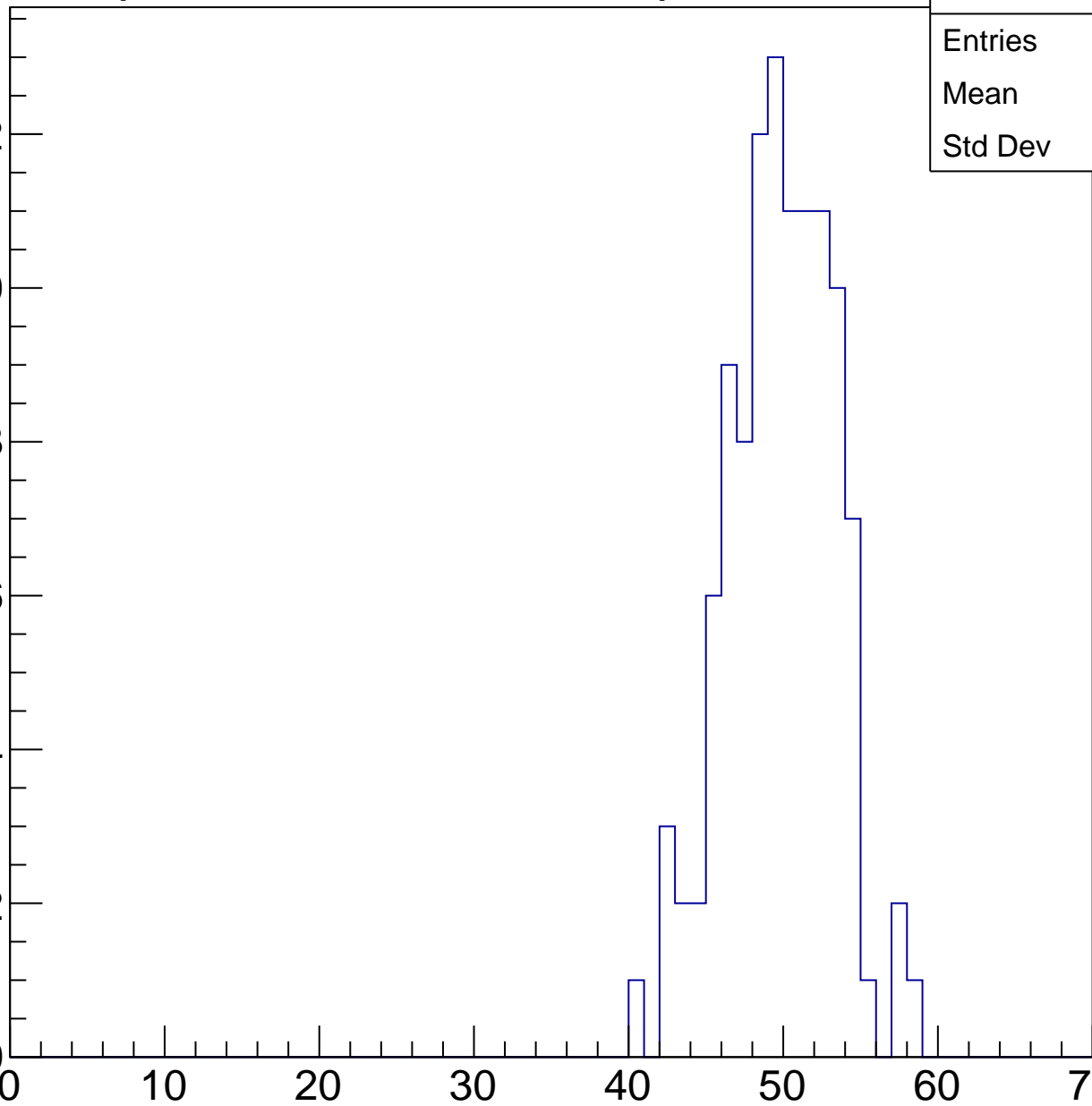
30

40

50

60

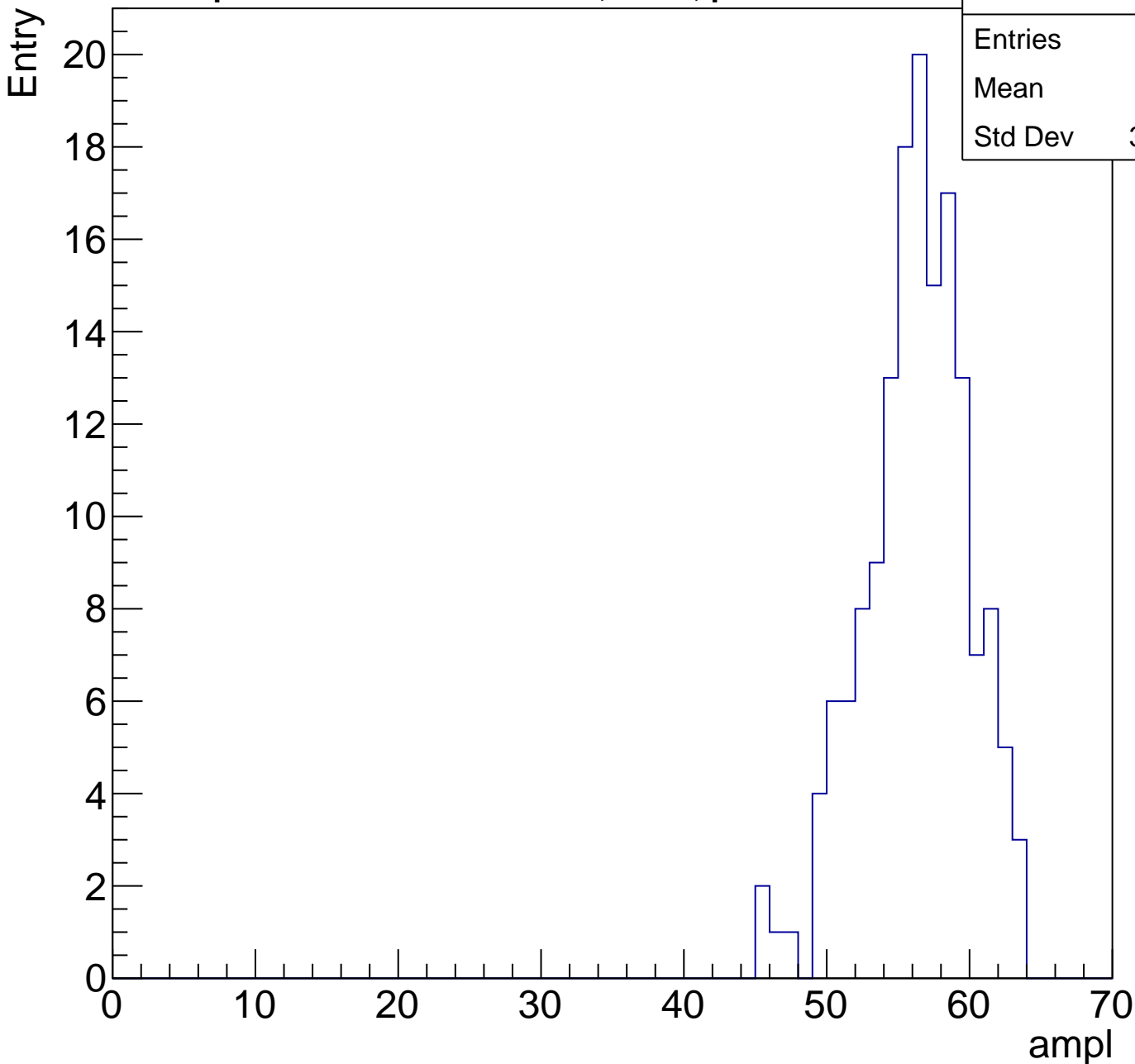
ampl



B0L101S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	55.8
Std Dev	3.666



B0L101S, U15-ch53, adc5

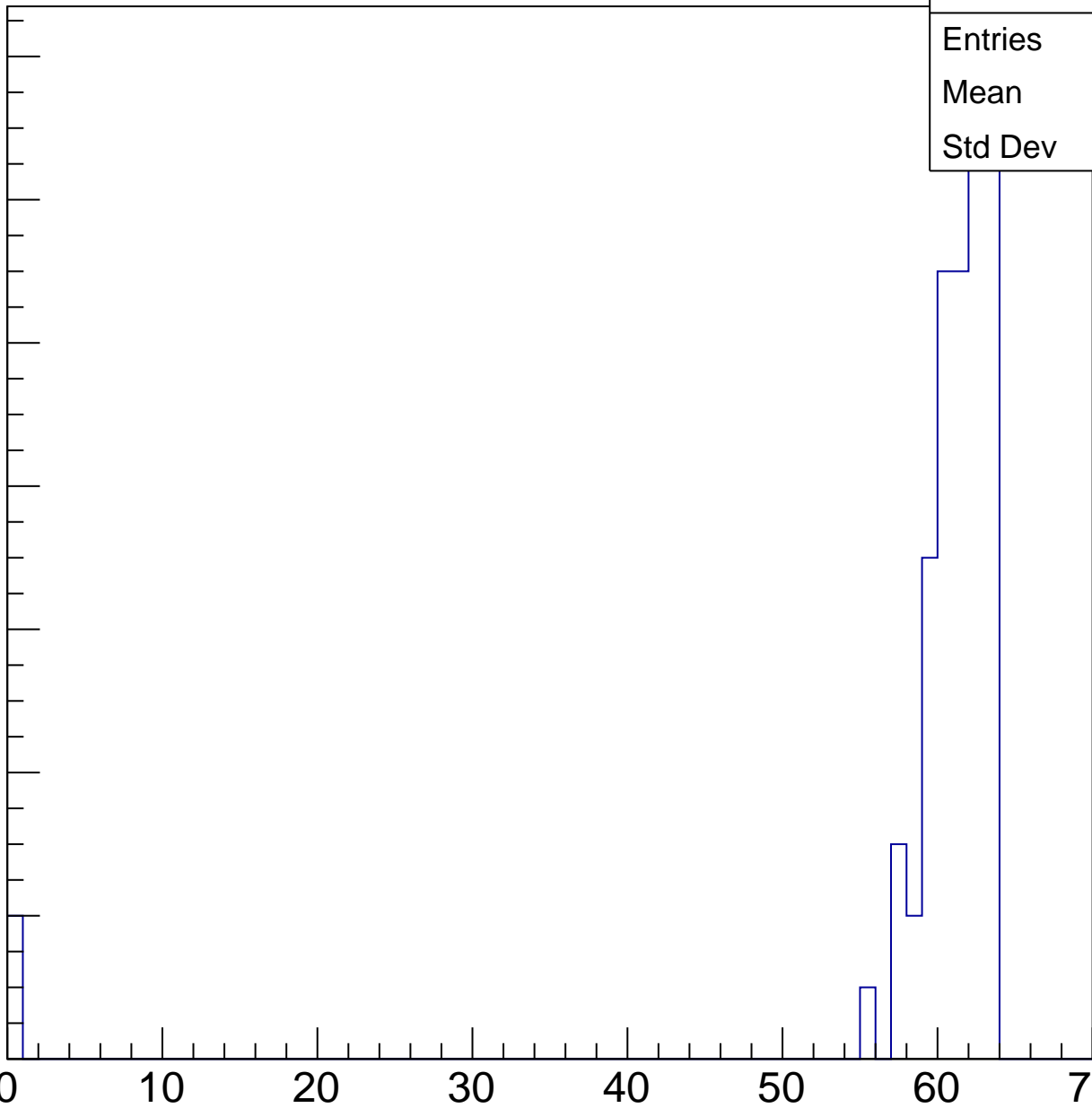
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	64
Mean	58.95
Std Dev	10.74

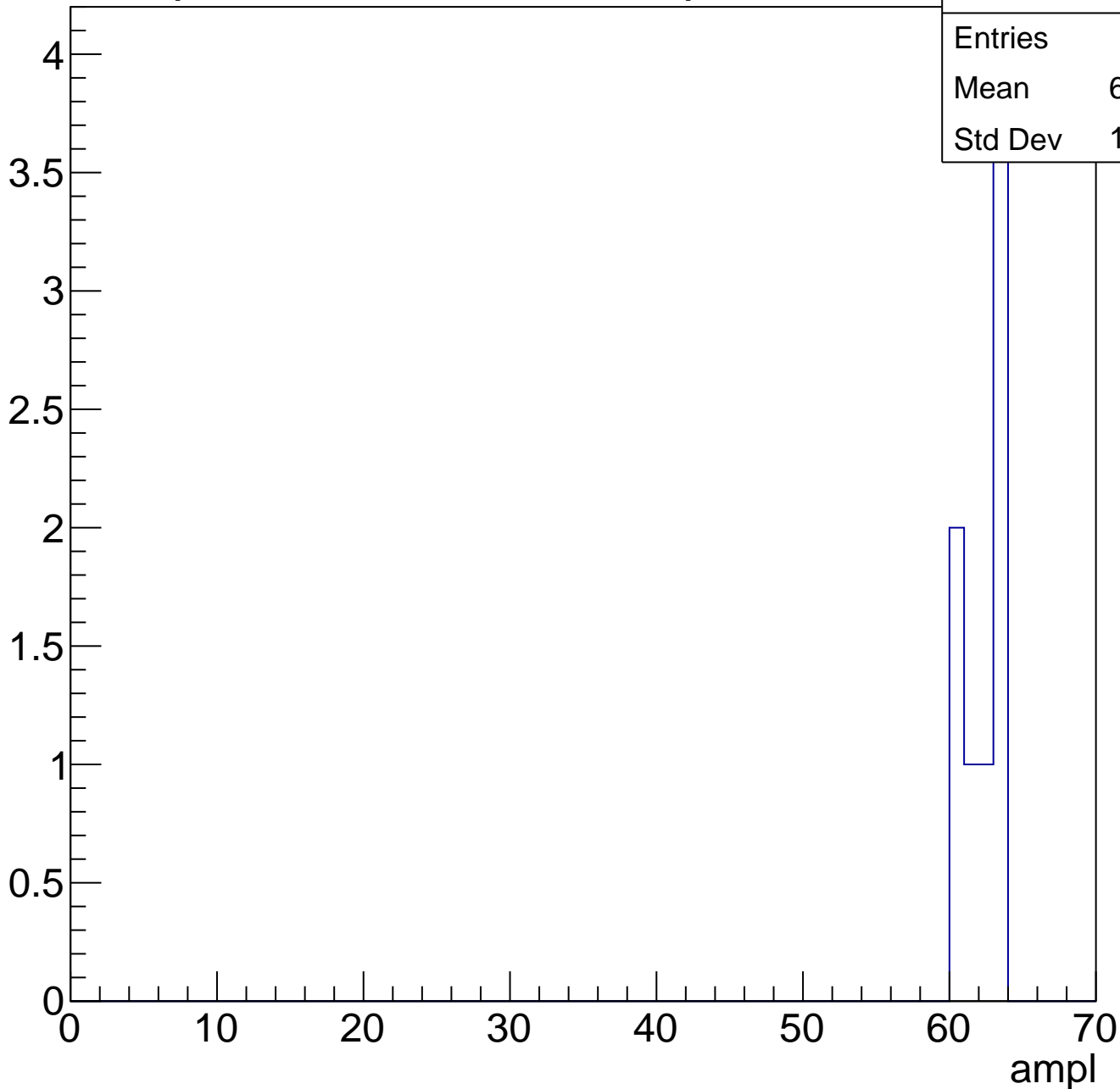
ampl



B0L101S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#1, port C1

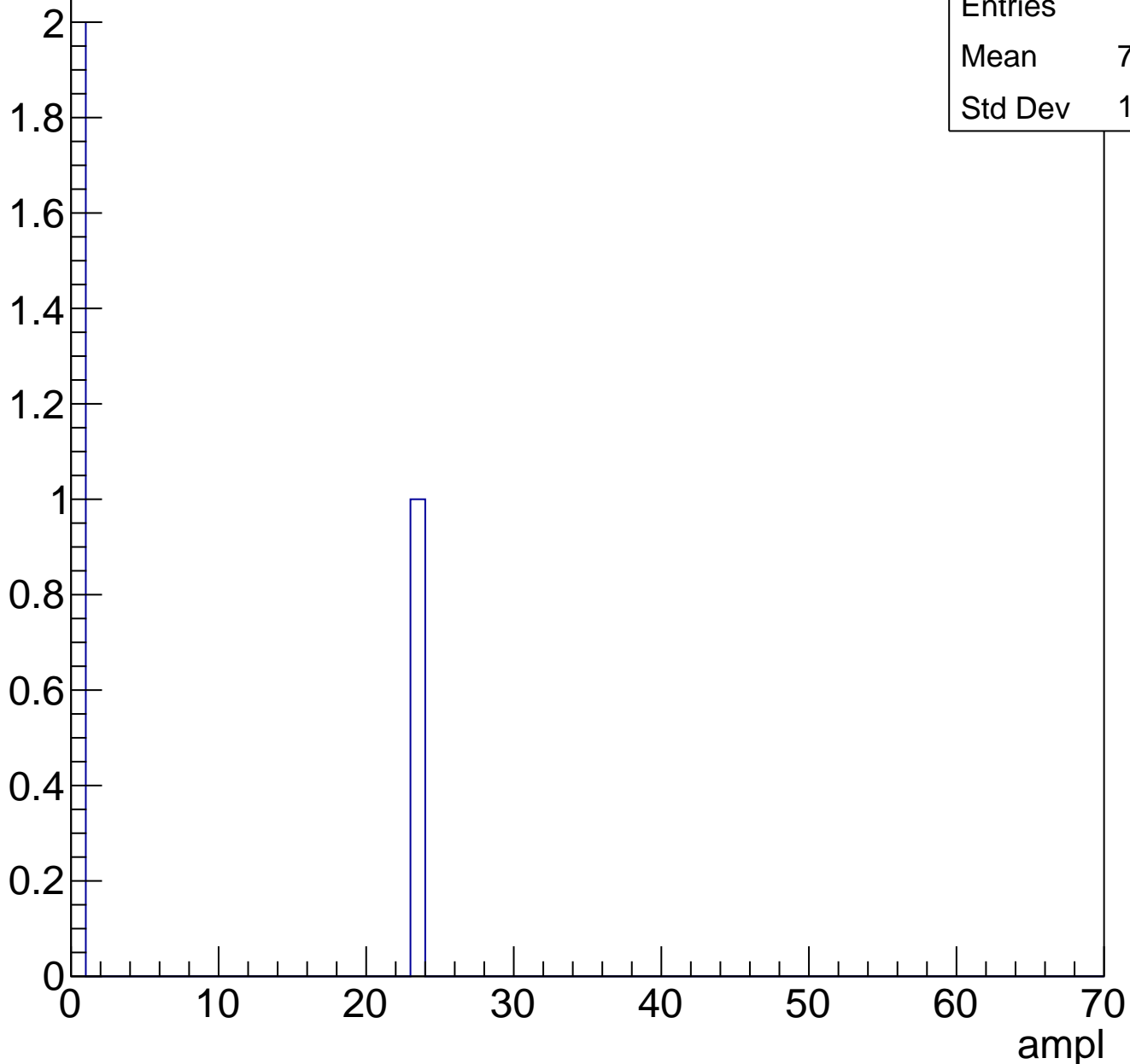
Entry



B0L101S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L101S, U15-ch54, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	28.08
Std Dev	4.346

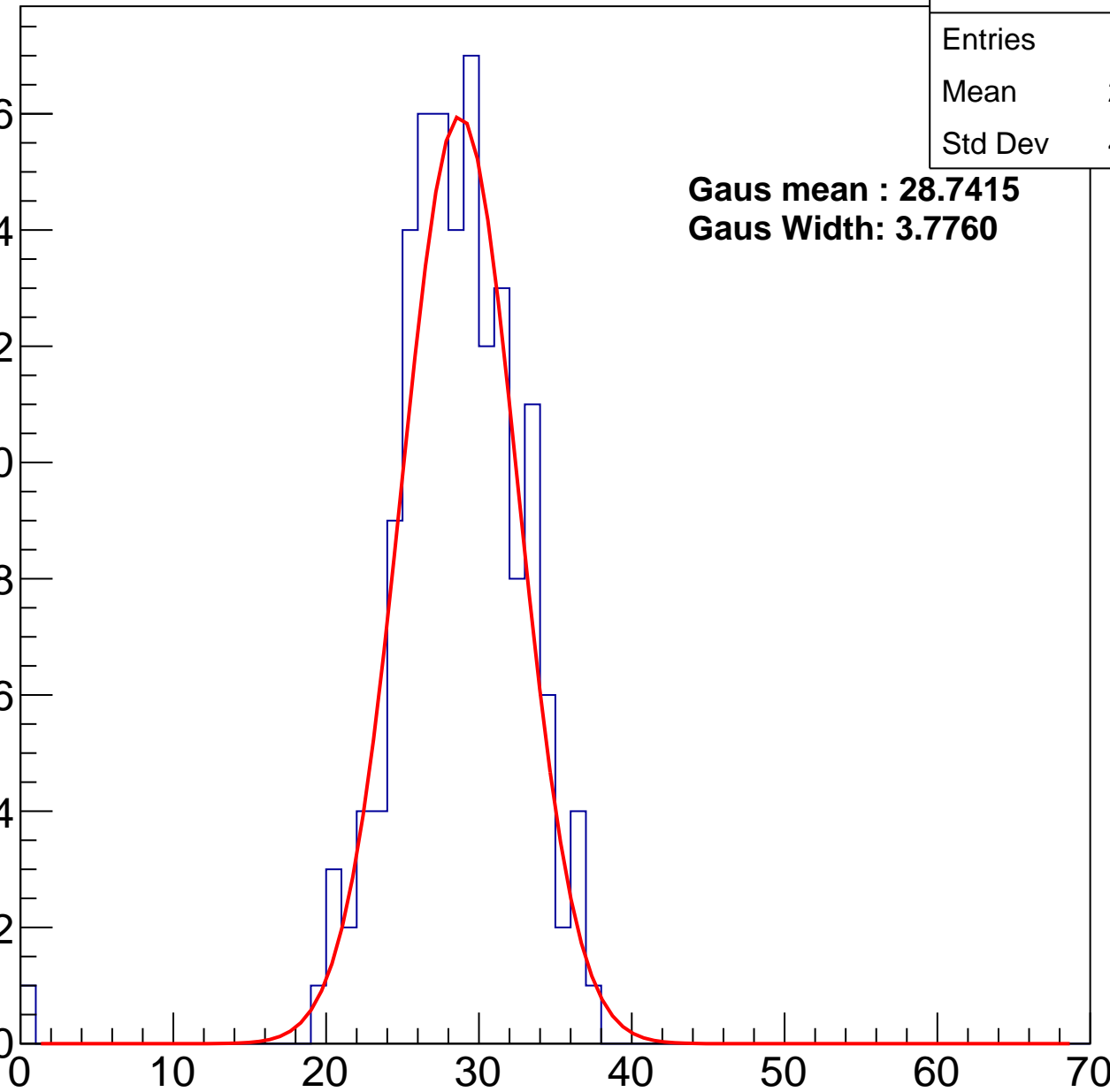
Gaus mean : 28.7415

Gaus Width: 3.7760

Entry

16
14
12
10
8
6
4
2
0

ampl



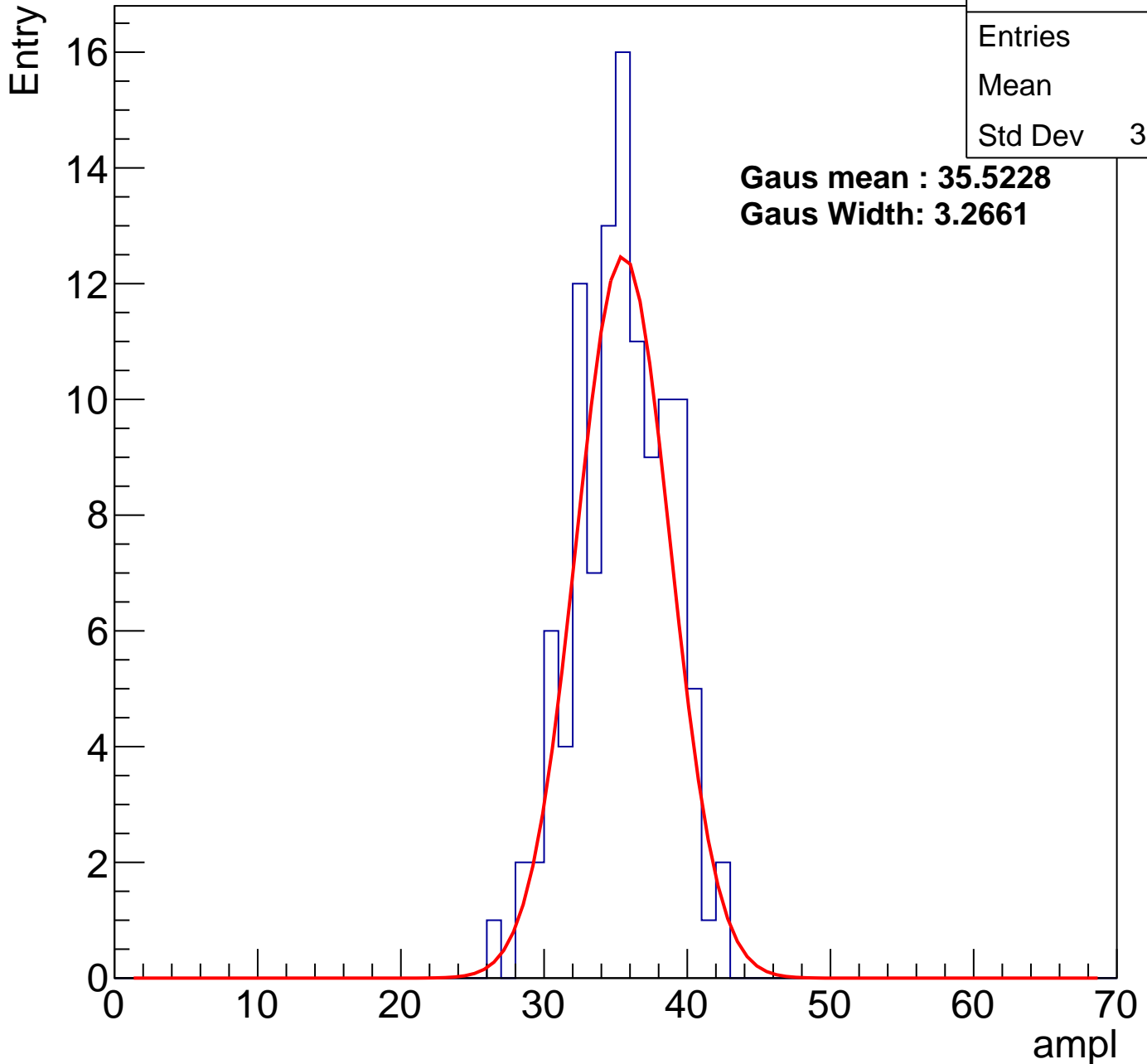
B0L101S, U15-ch54, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	35
Std Dev	3.247

Gaus mean : 35.5228

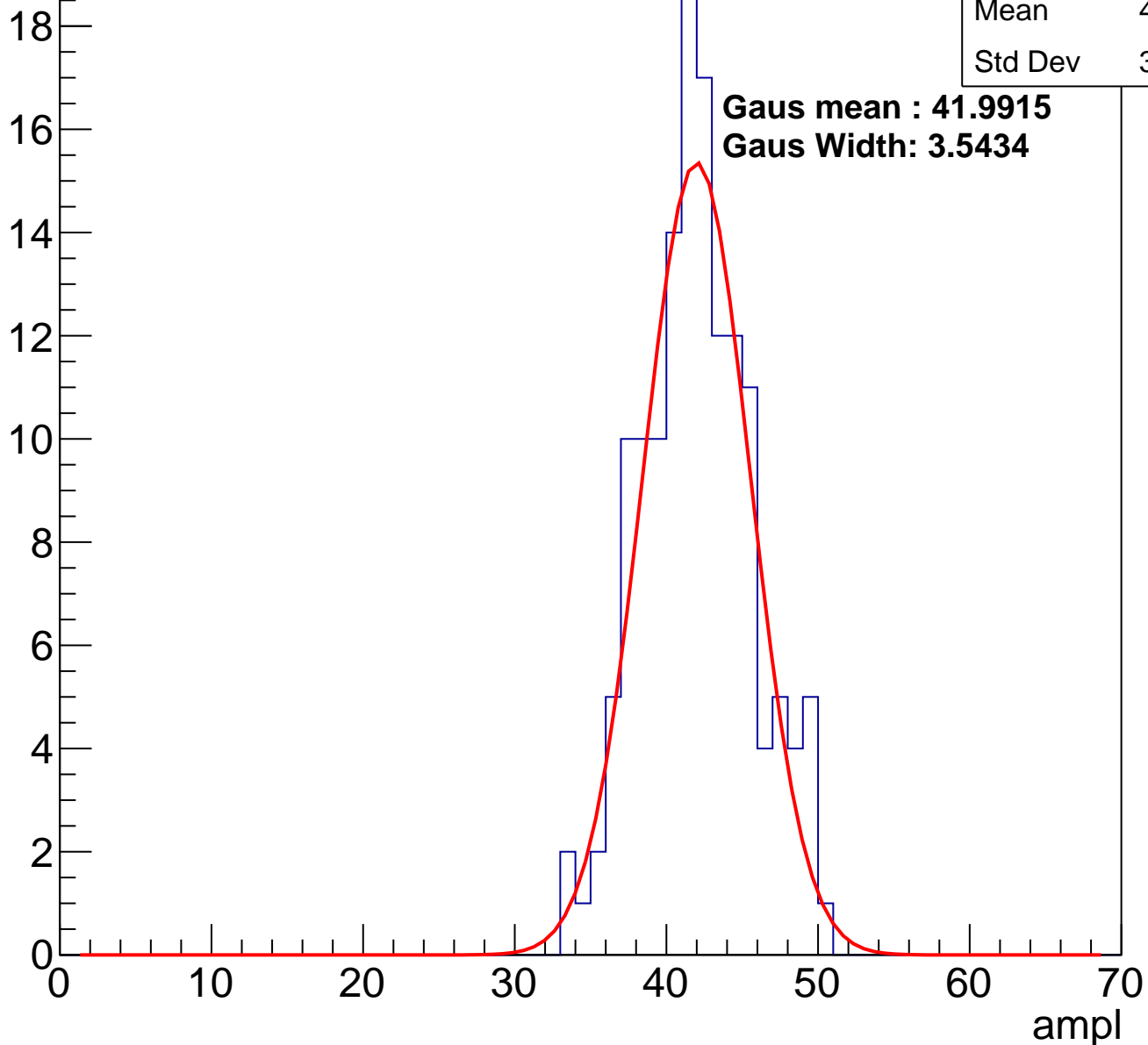
Gaus Width: 3.2661



B0L101S, U15-ch54, adc2

calib_packv5_042523_0143.root, FC#1, port C1

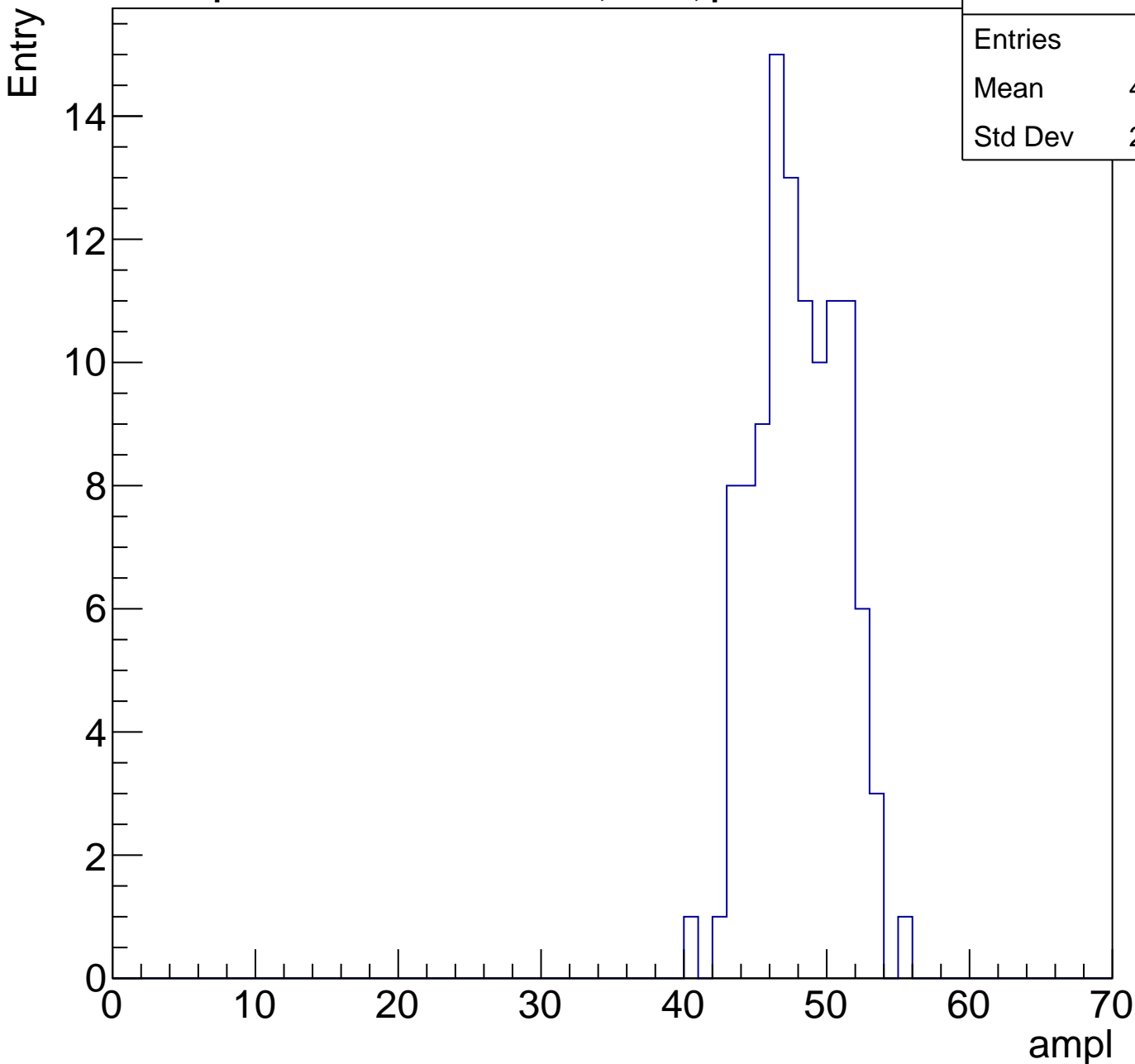
Entry



B0L101S, U15-ch54, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	108
Mean	47.58
Std Dev	2.944



B0L101S, U15-ch54, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	54.38
Std Dev	3.498

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

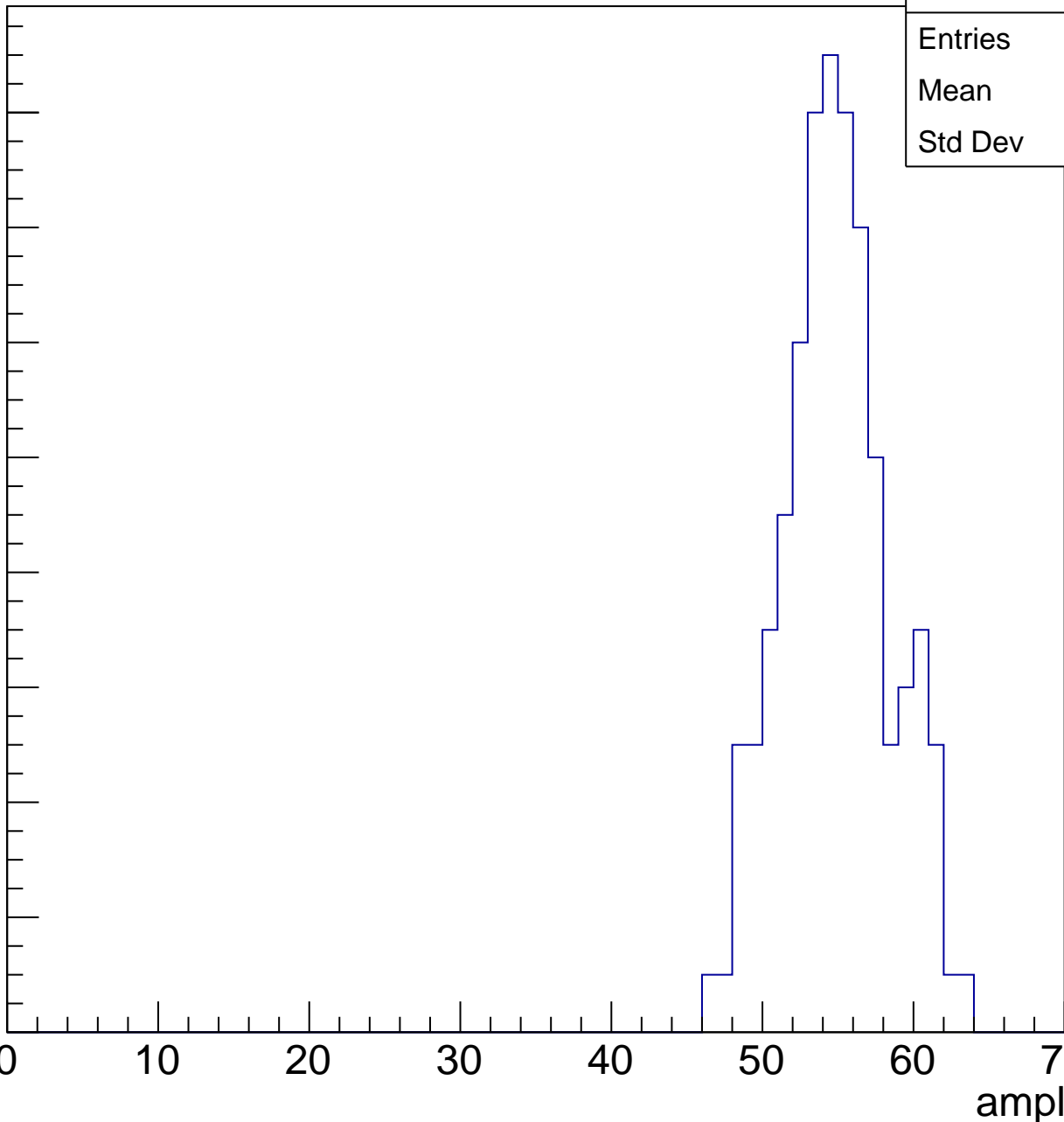
40

50

60

70

ampl



B0L101S, U15-ch54, adc5

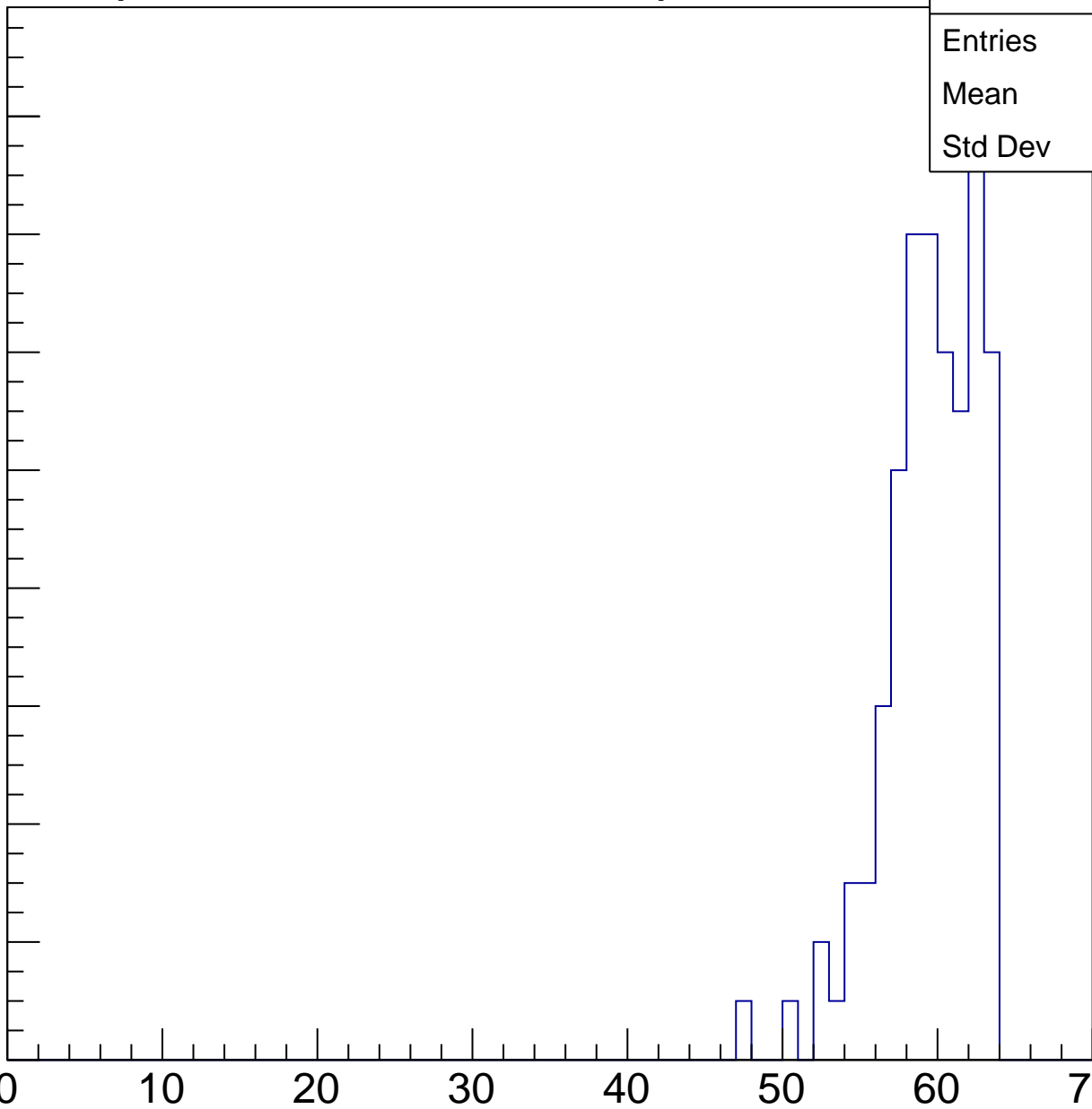
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	59.12
Std Dev	3.041

ampl

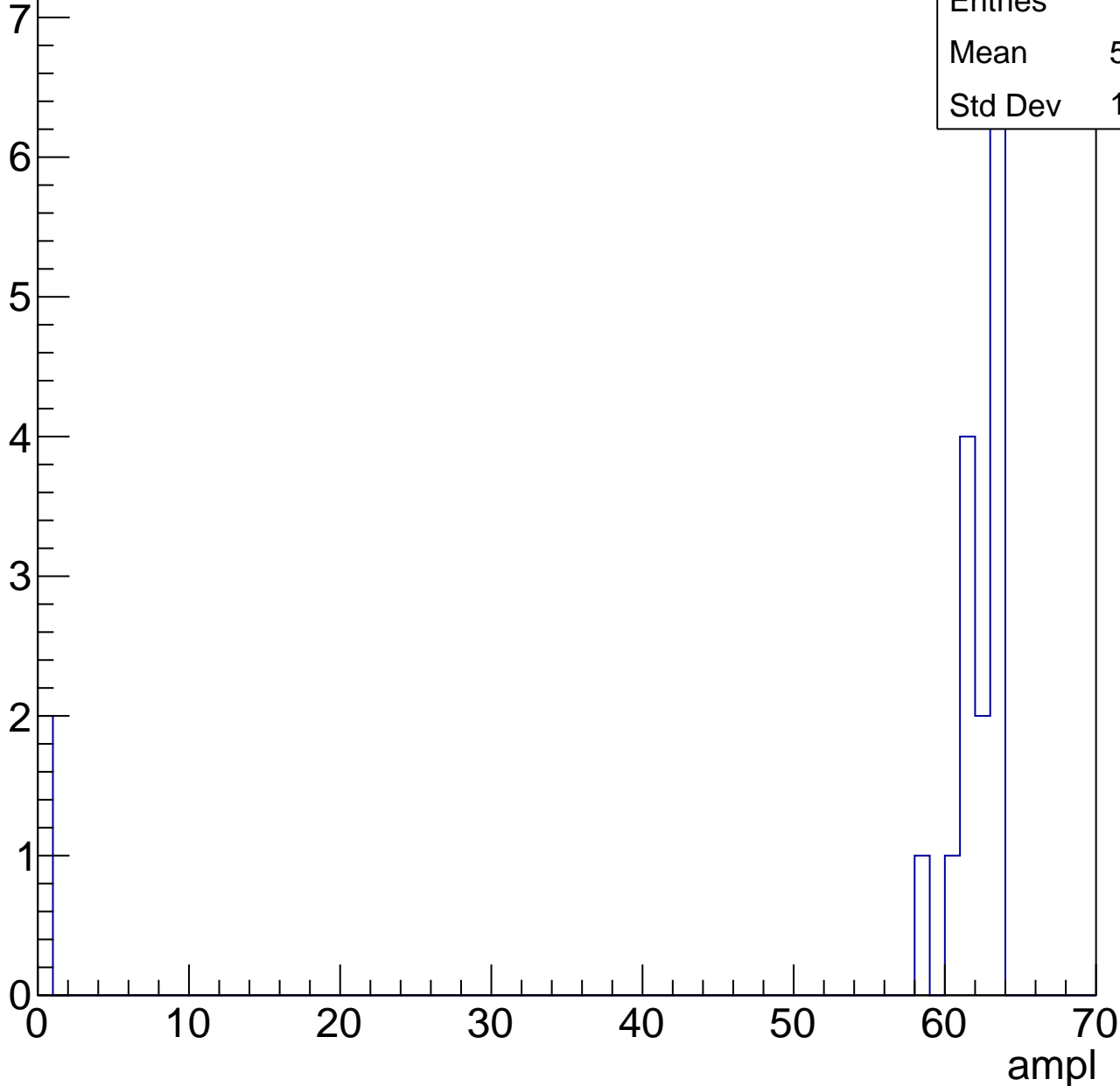


B0L101S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	54.53
Std Dev	19.96



B0L101S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch55, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	27.8
Std Dev	3.678

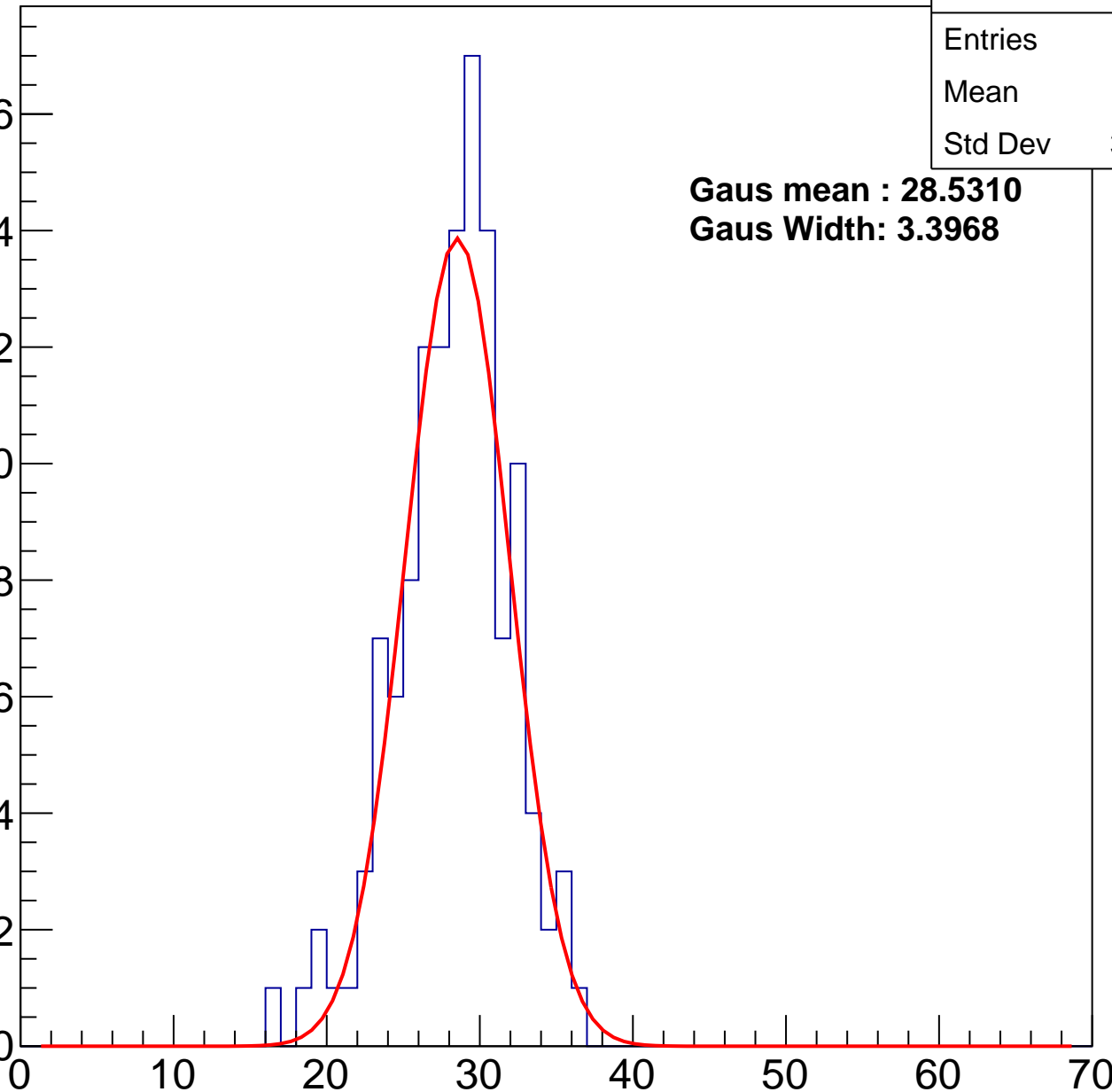
Gaus mean : 28.5310

Gaus Width: 3.3968

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch55, adc1

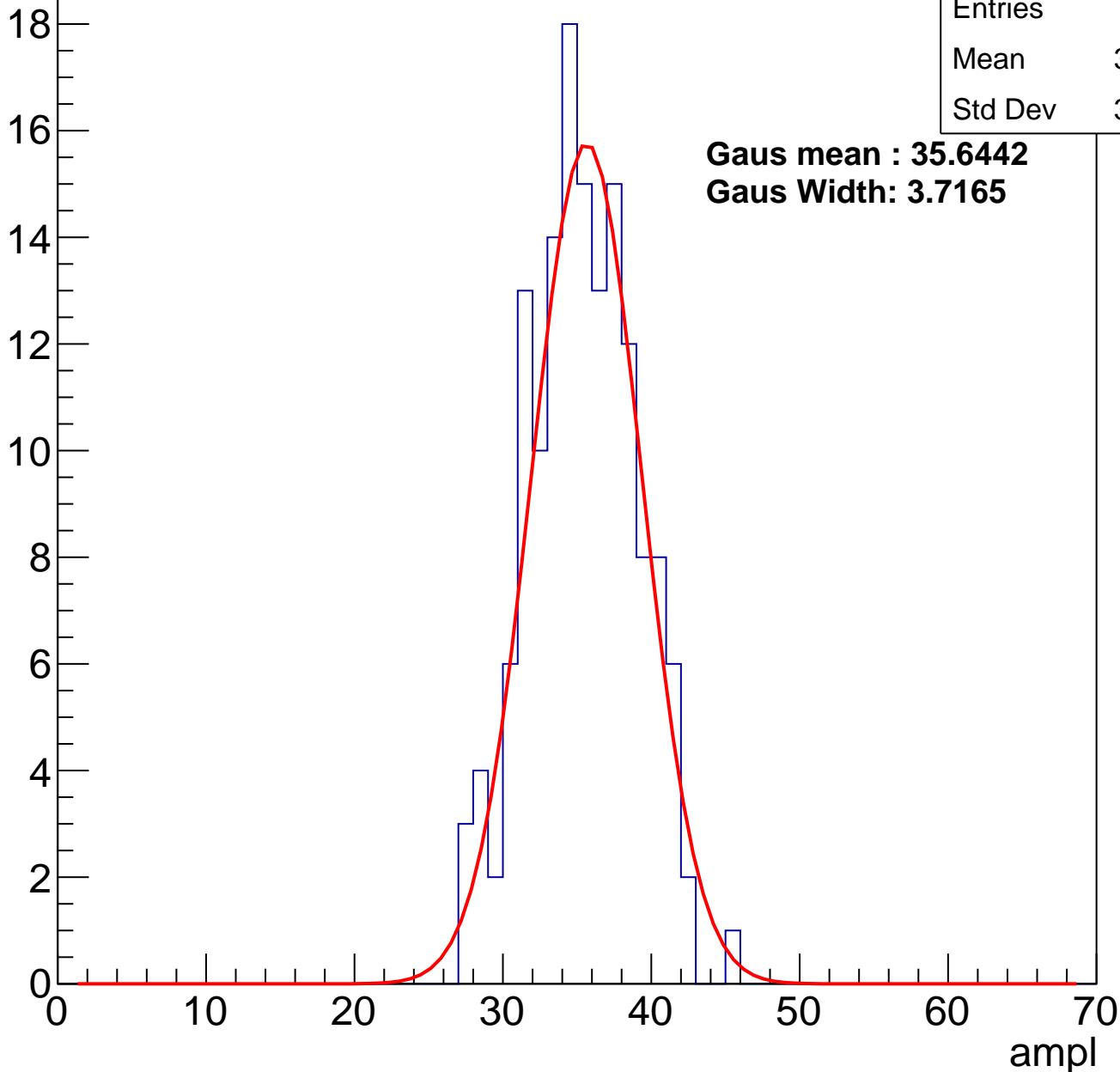
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	34.93
Std Dev	3.563

Gaus mean : 35.6442

Gaus Width: 3.7165

Entry



B0L101S, U15-ch55, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	41.97
Std Dev	3.488

Gaus mean : 42.5702

Gaus Width: 3.5111

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

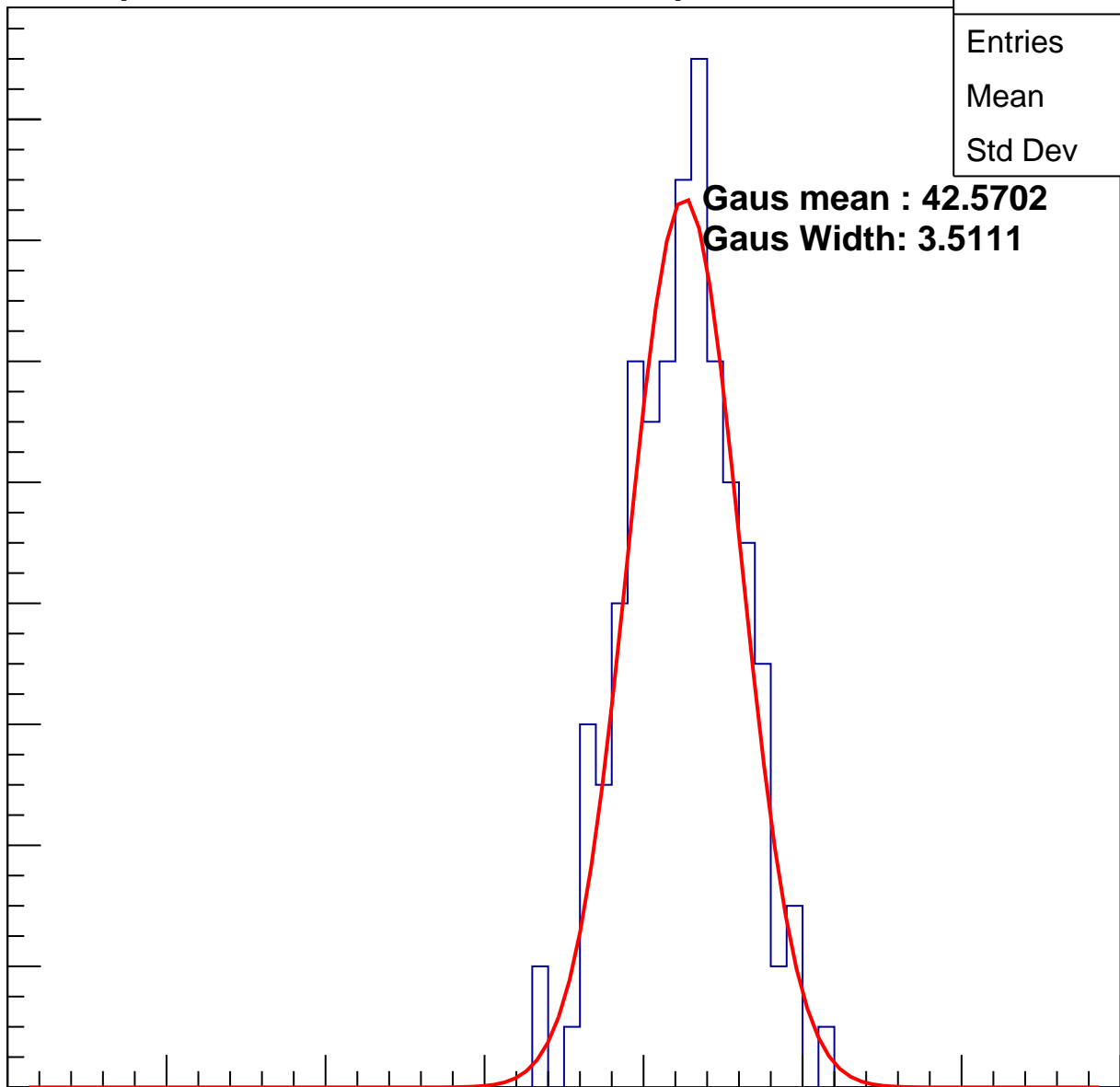
40

50

60

70

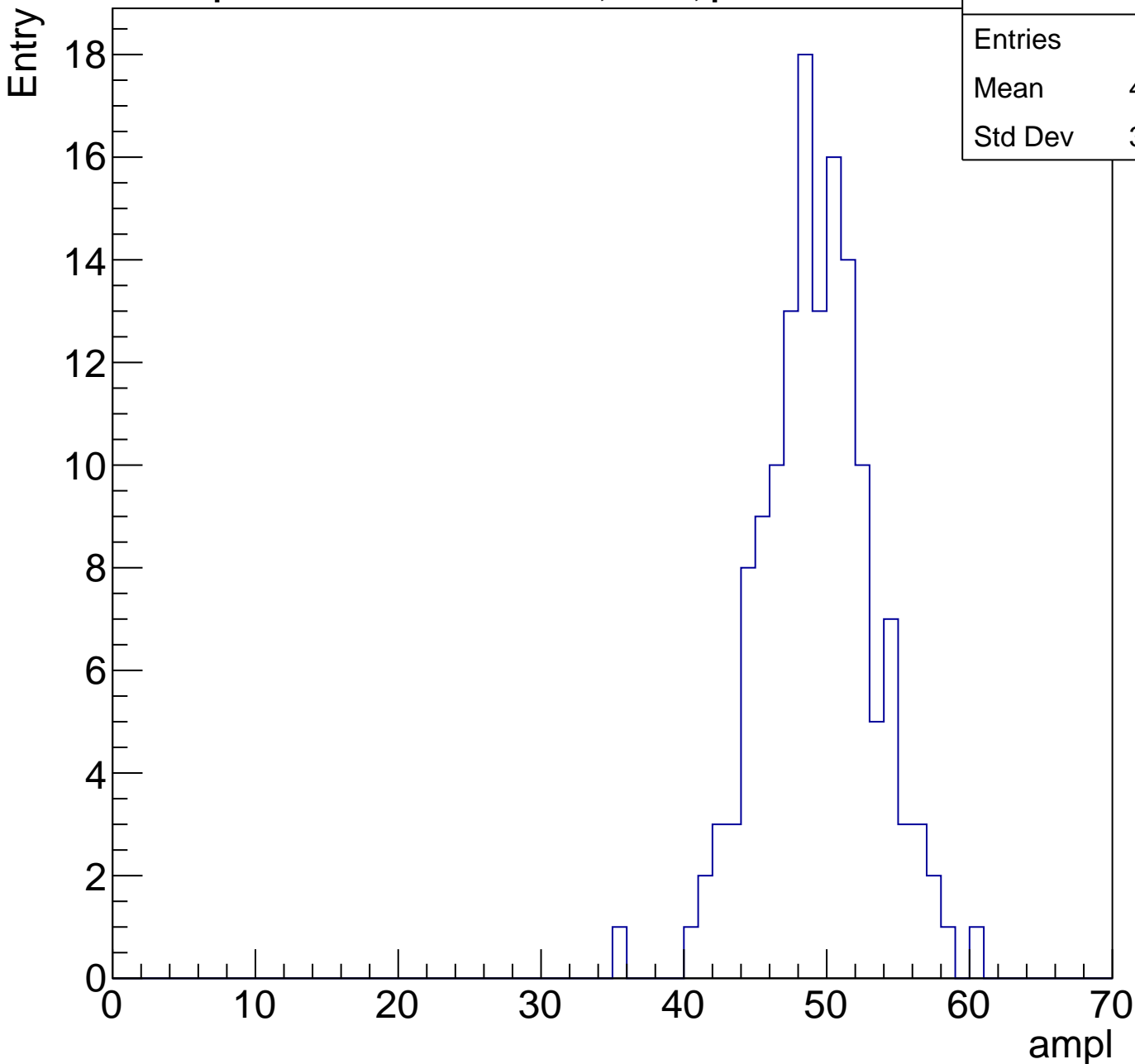
ampl



B0L101S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#1, port C1

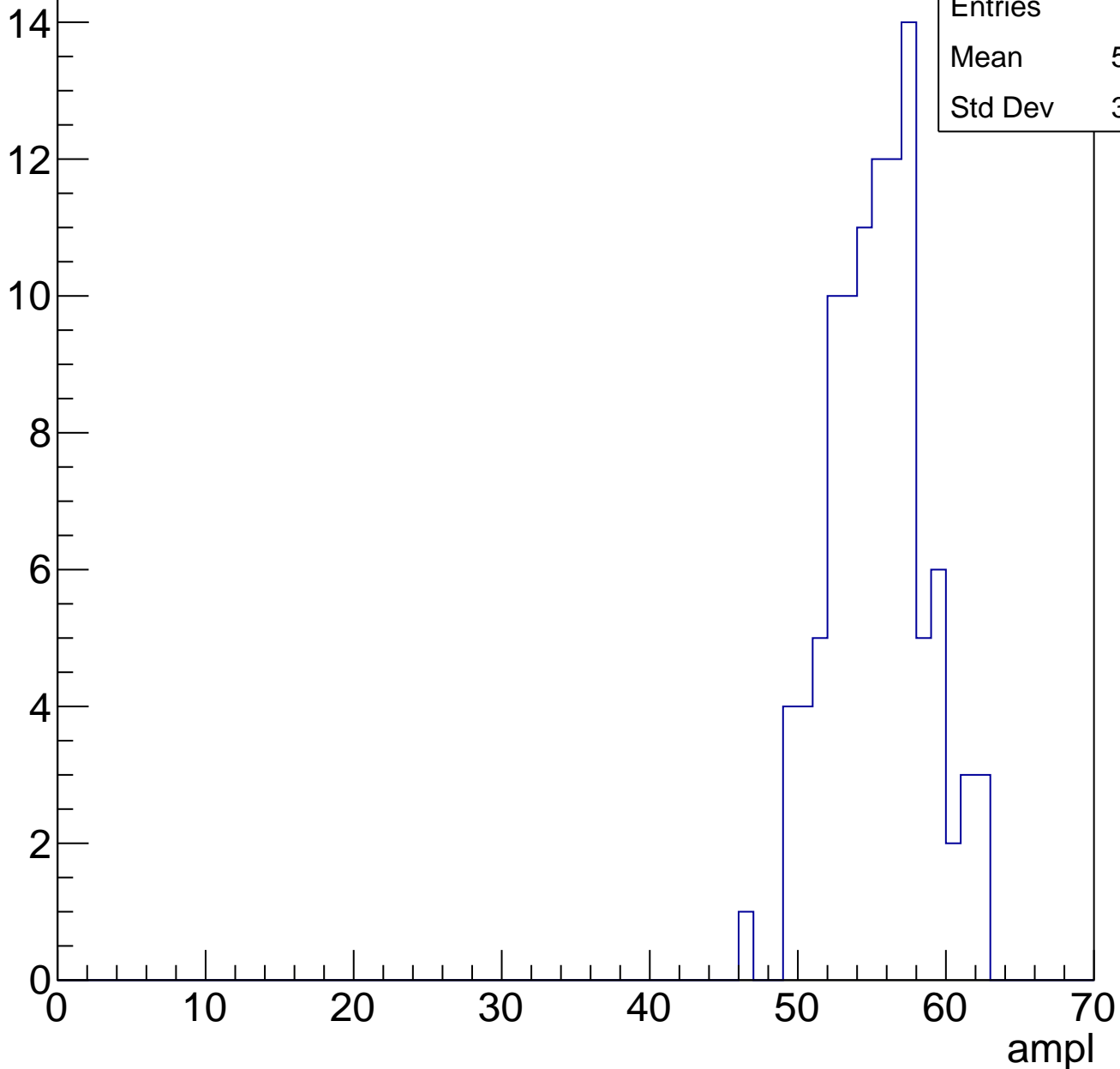
Entries	143
Mean	48.83
Std Dev	3.888



B0L101S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch55, adc5

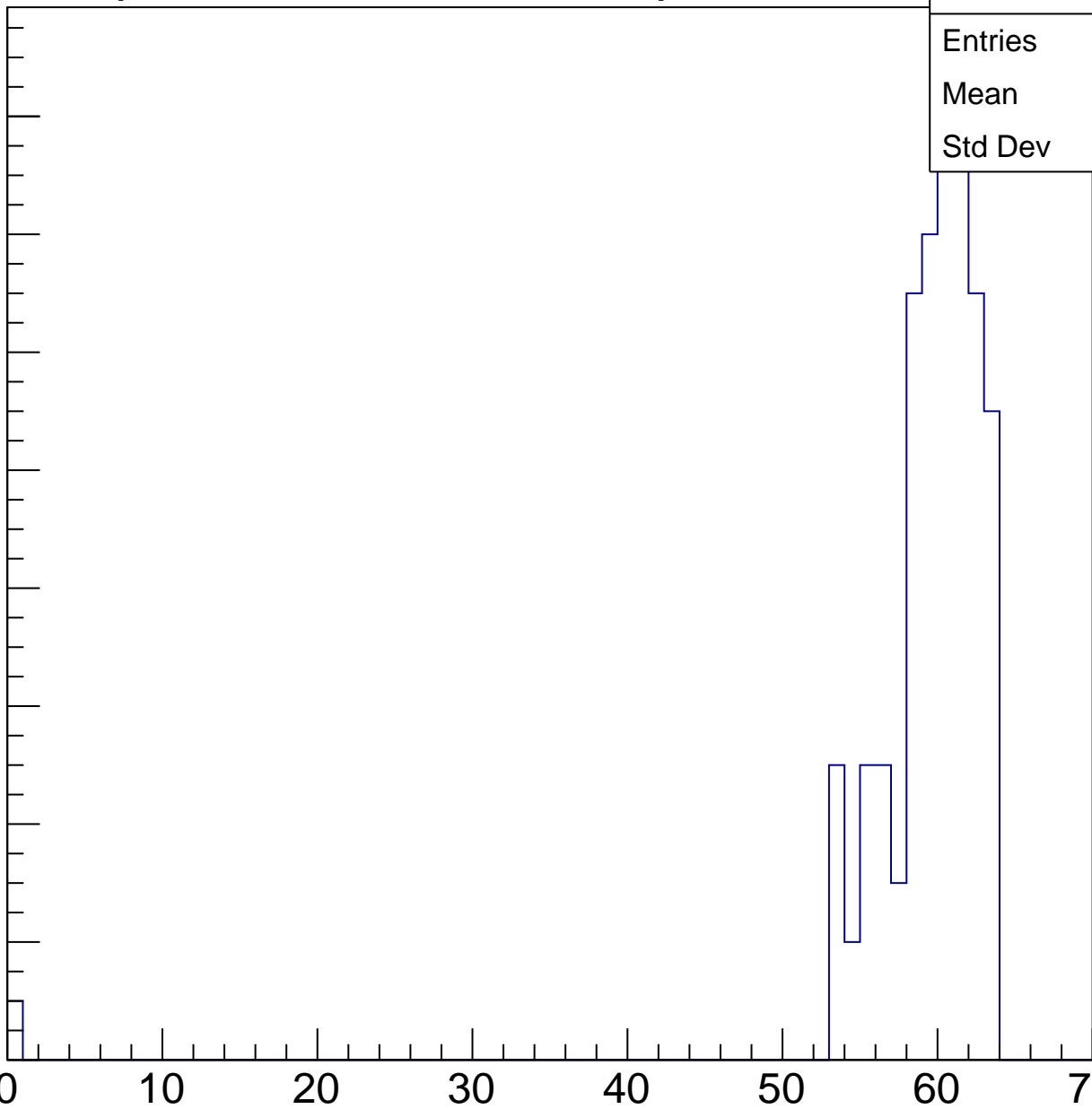
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.8
Std Dev	6.348

ampl

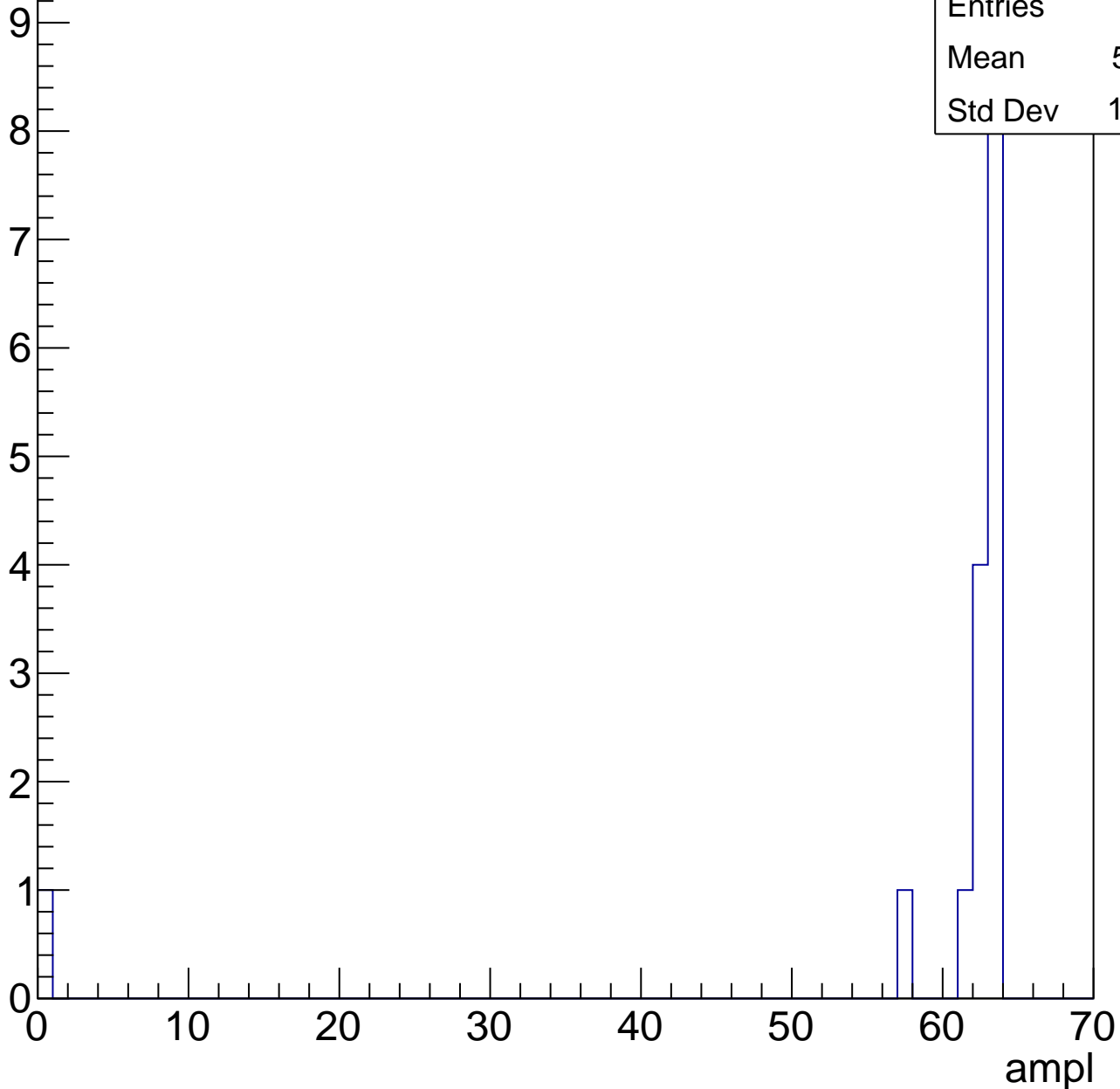


B0L101S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

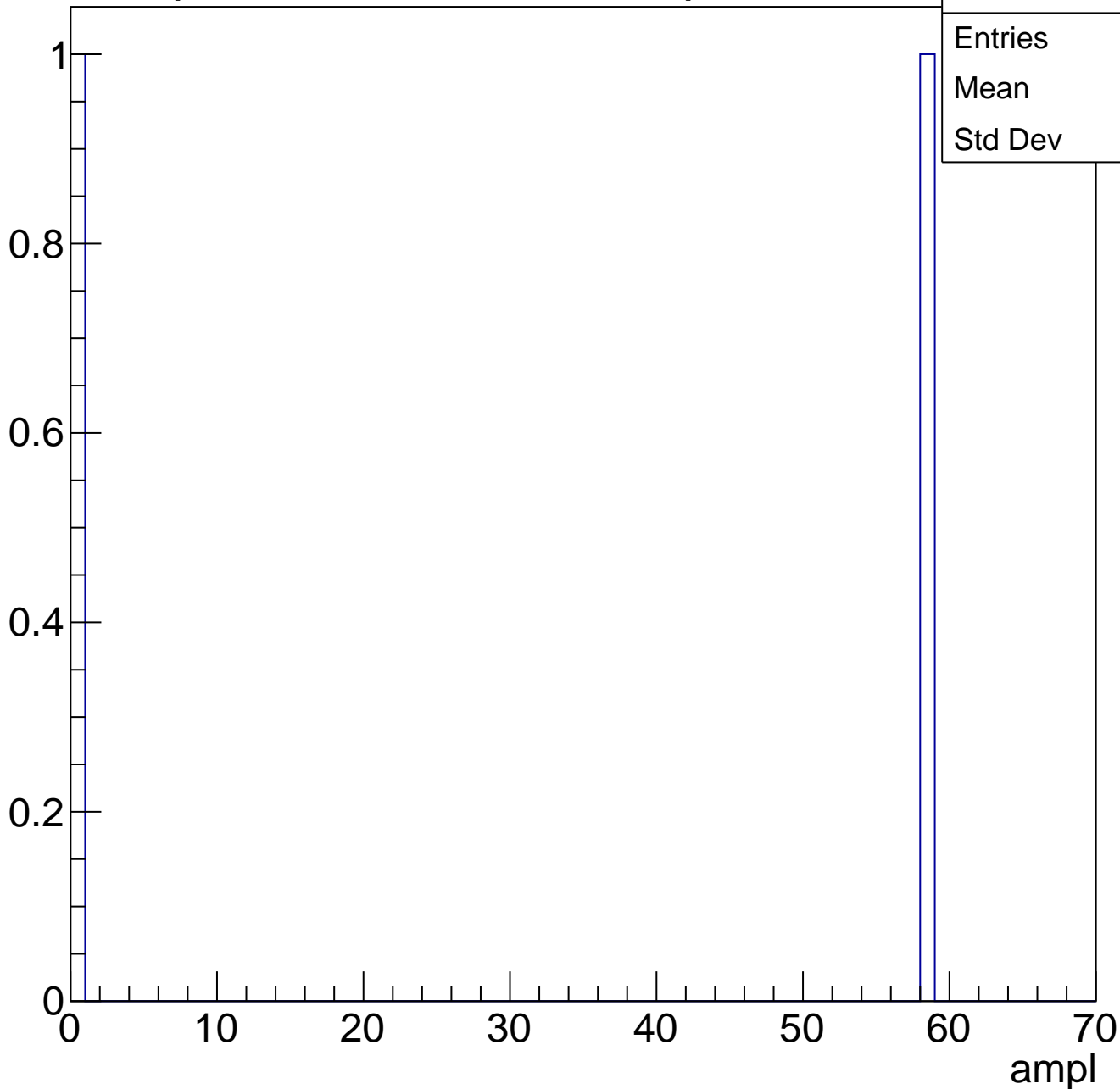
Entries	16
Mean	58.31
Std Dev	15.13



B0L101S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



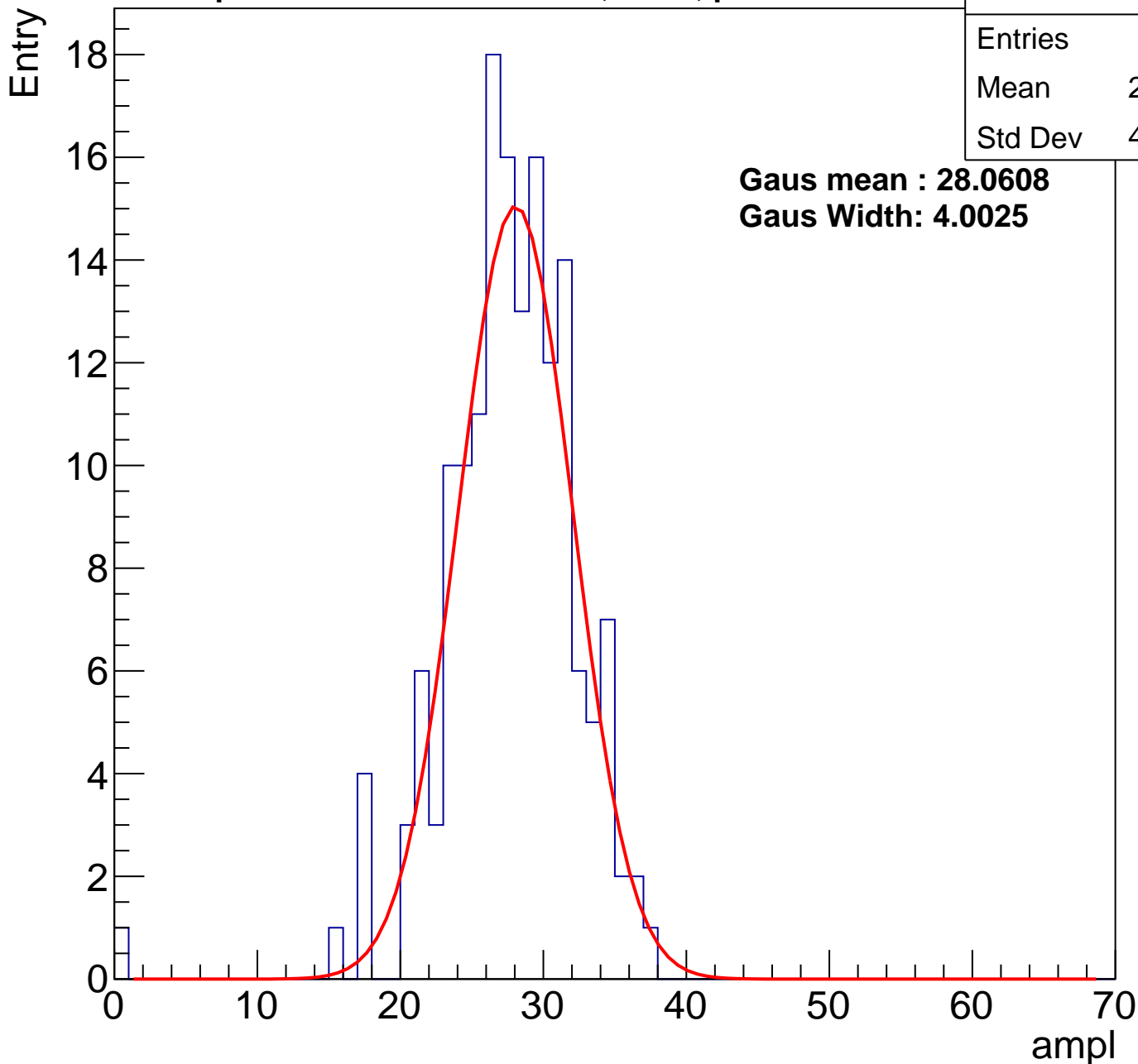
B0L101S, U15-ch56, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	161
Mean	27.18
Std Dev	4.656

Gaus mean : 28.0608

Gaus Width: 4.0025



B0L101S, U15-ch56, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	34.42
Std Dev	3.416

Gaus mean : 34.9515

Gaus Width: 3.4060

Entry

12

10

8

6

4

2

0

0

10

20

30

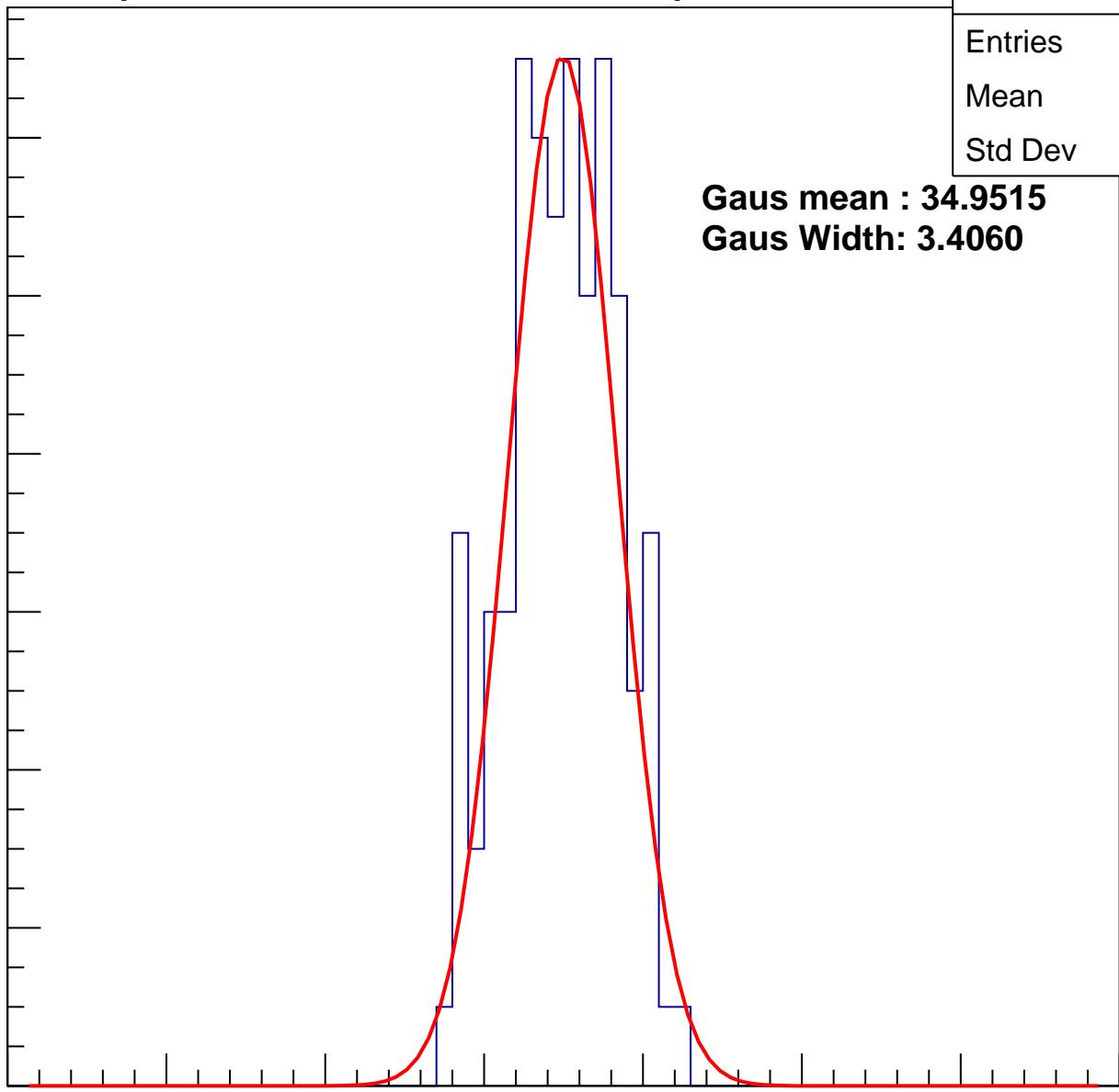
40

50

60

70

ampl



B0L101S, U15-ch56, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

130

Mean

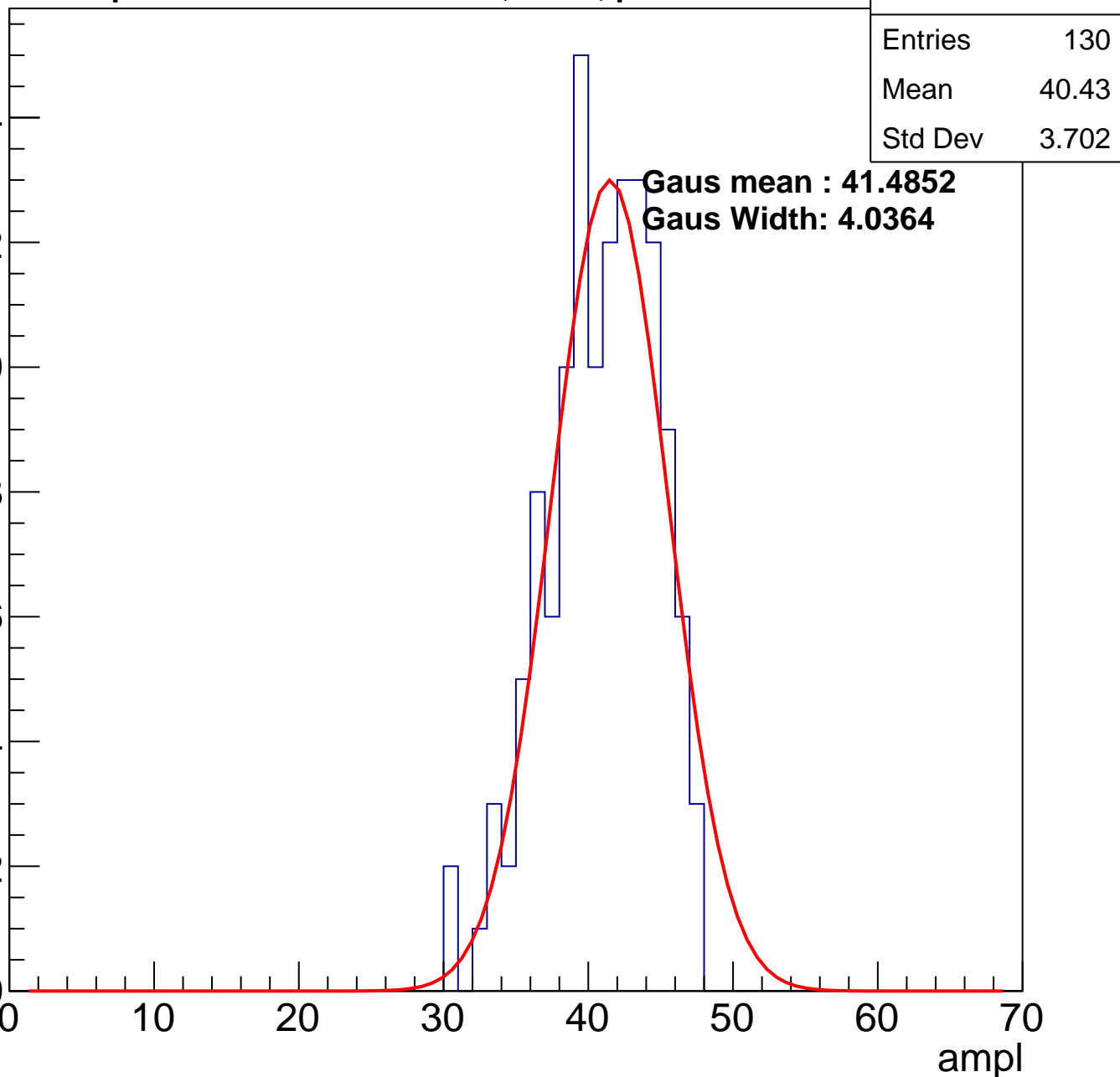
40.43

Std Dev

3.702

Gaus mean : 41.4852

Gaus Width: 4.0364



B0L101S, U15-ch56, adc3

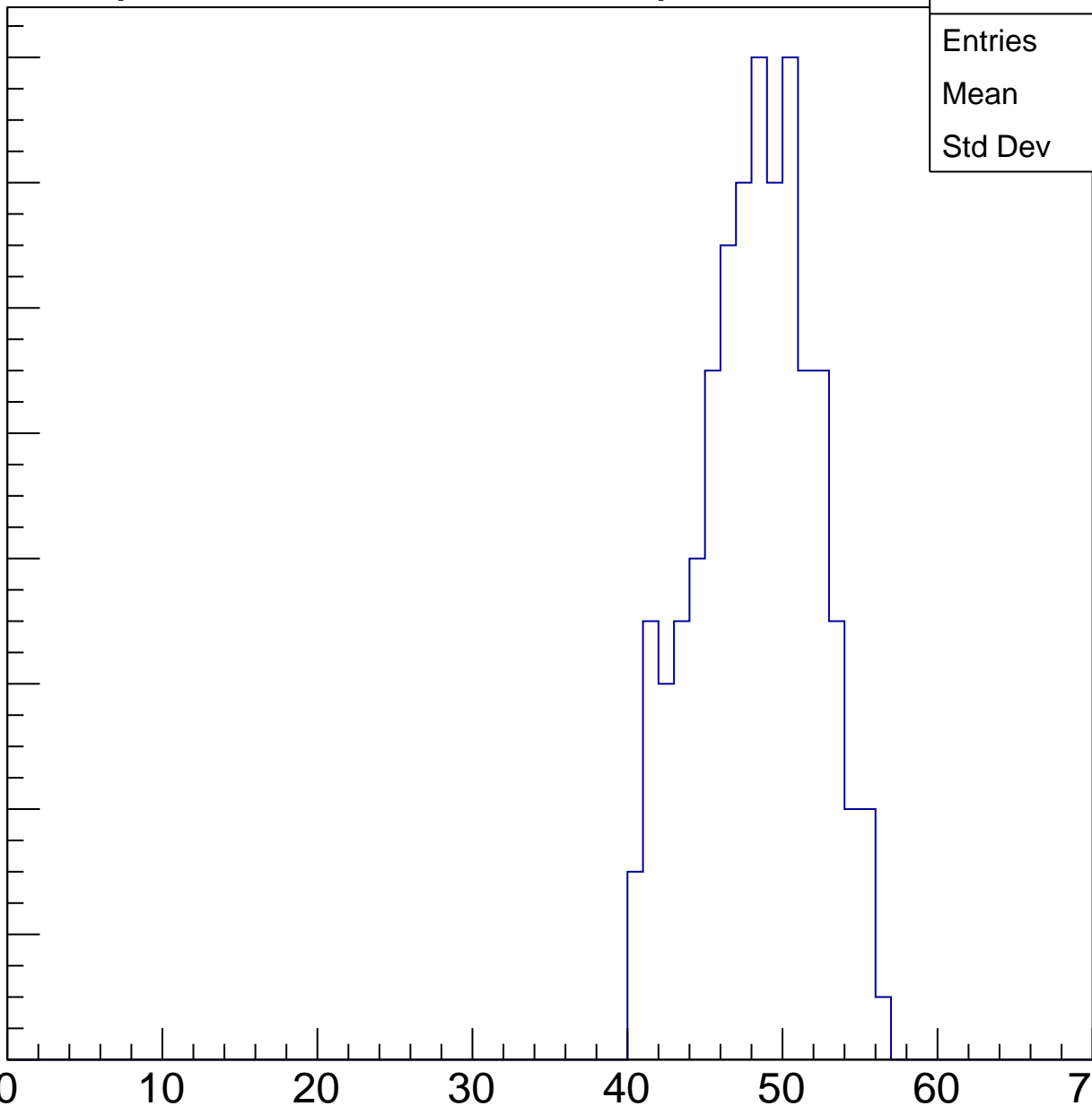
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	153
Mean	47.8
Std Dev	3.744

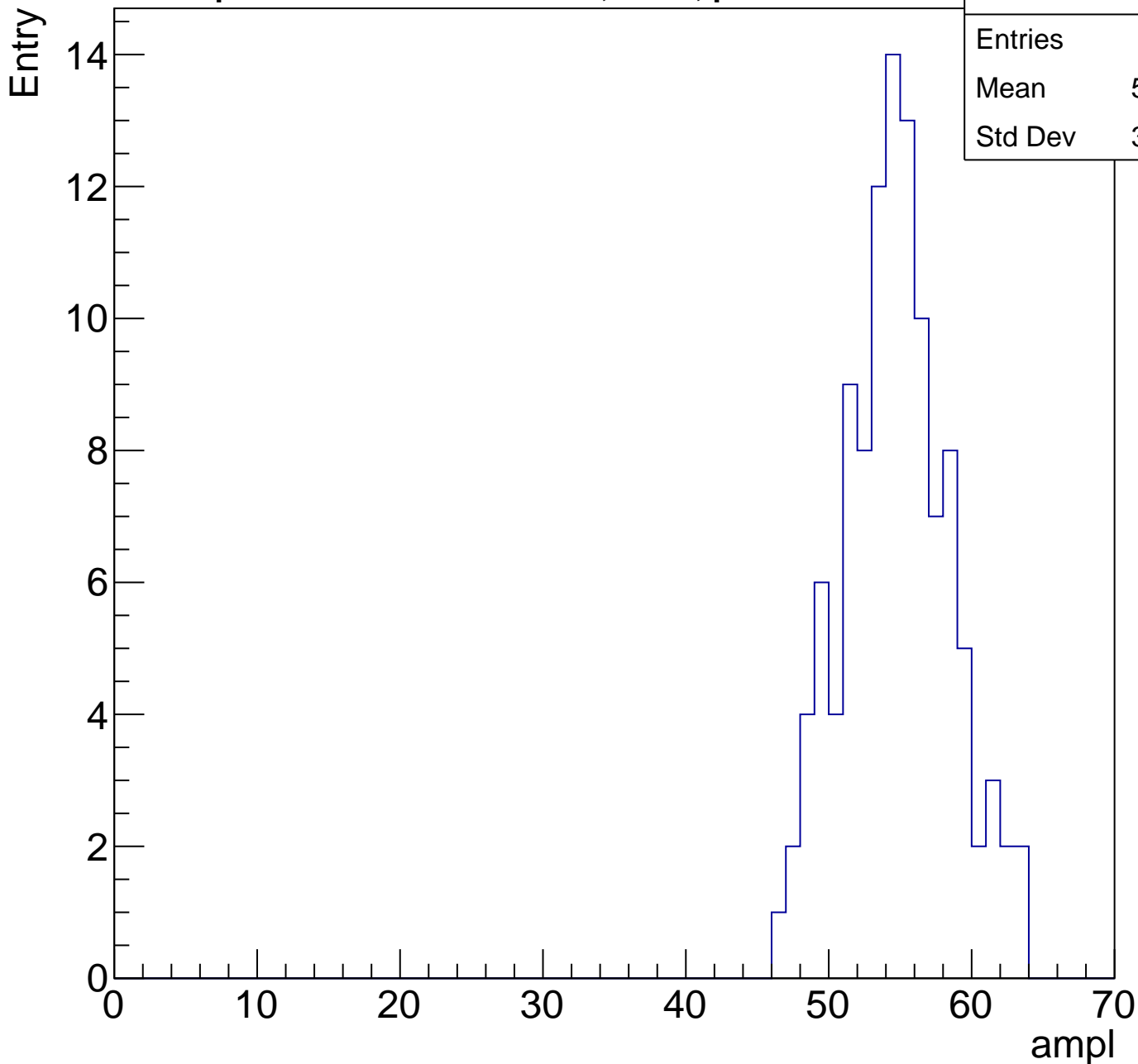
ampl



B0L101S, U15-ch56, adc4

calib_packv5_042523_0143.root, FC#1, port C1

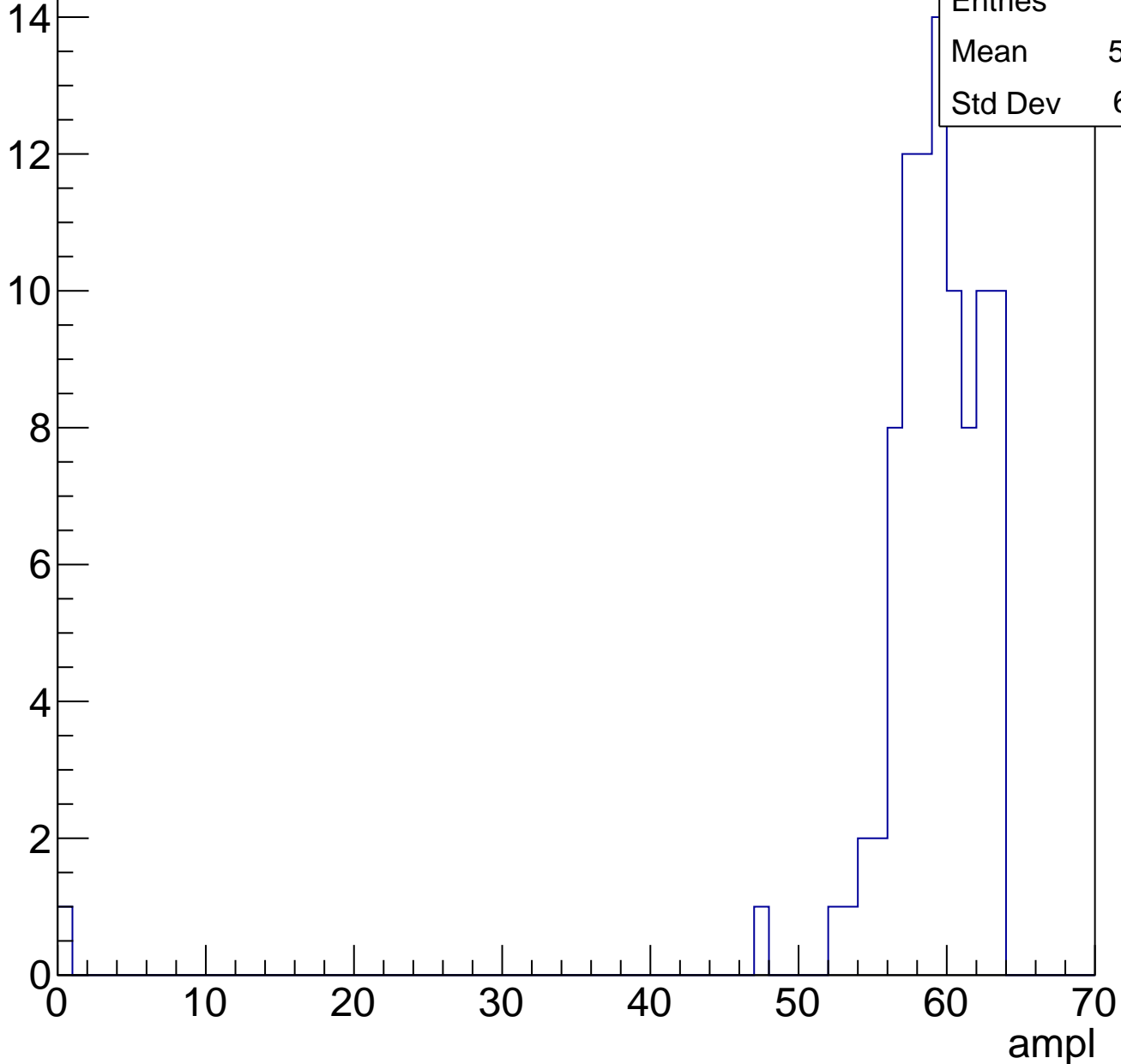
Entries	112
Mean	54.28
Std Dev	3.677



B0L101S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#1, port C1

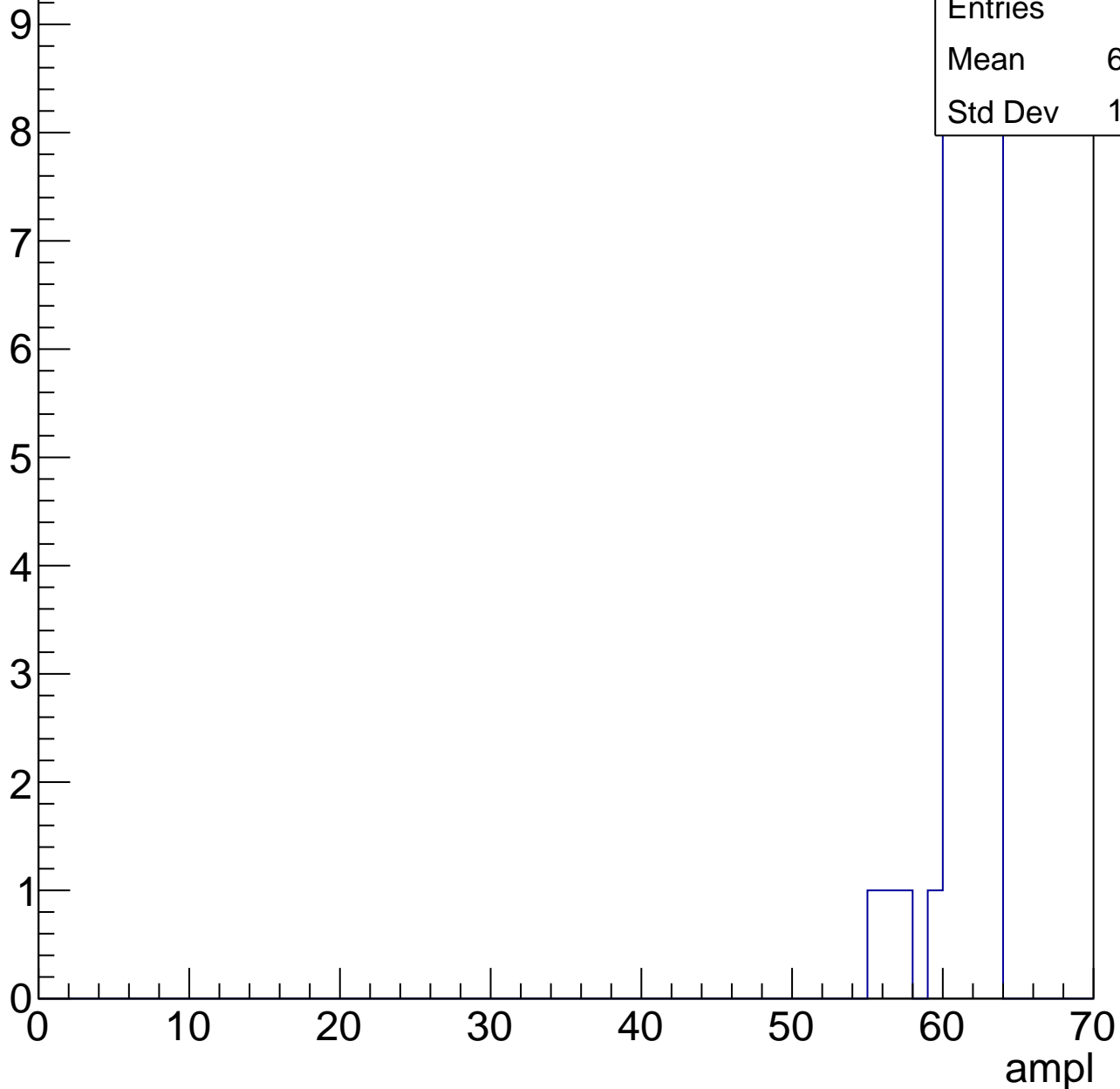
Entry



B0L101S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L101S, U15-ch57, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	27.17
Std Dev	3.204

Gaus mean : 27.5970

Gaus Width: 3.7627

Entry

14

12

10

8

6

4

2

0

0

10

20

30

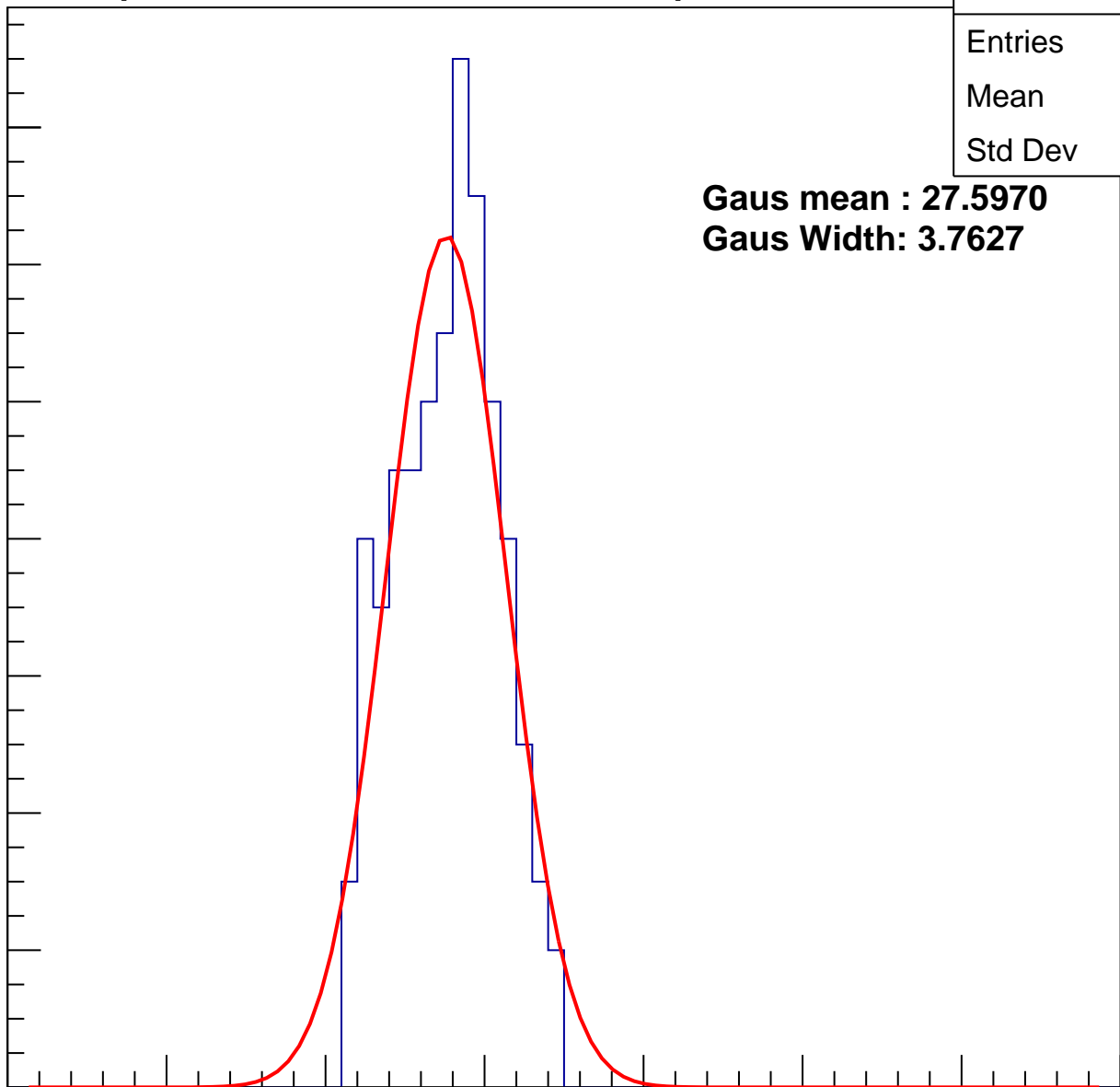
40

50

60

70

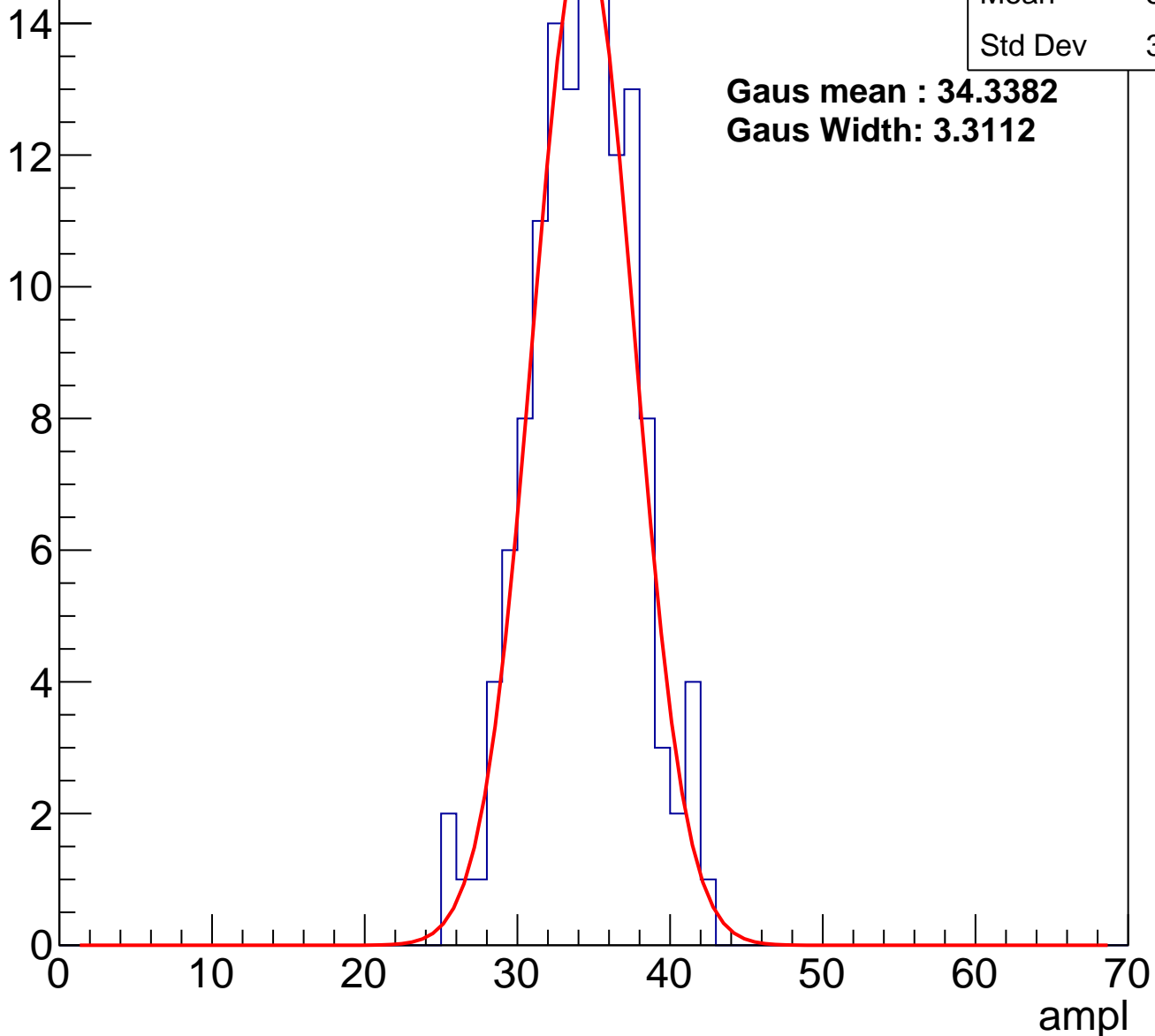
ampl



B0L101S, U15-ch57, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

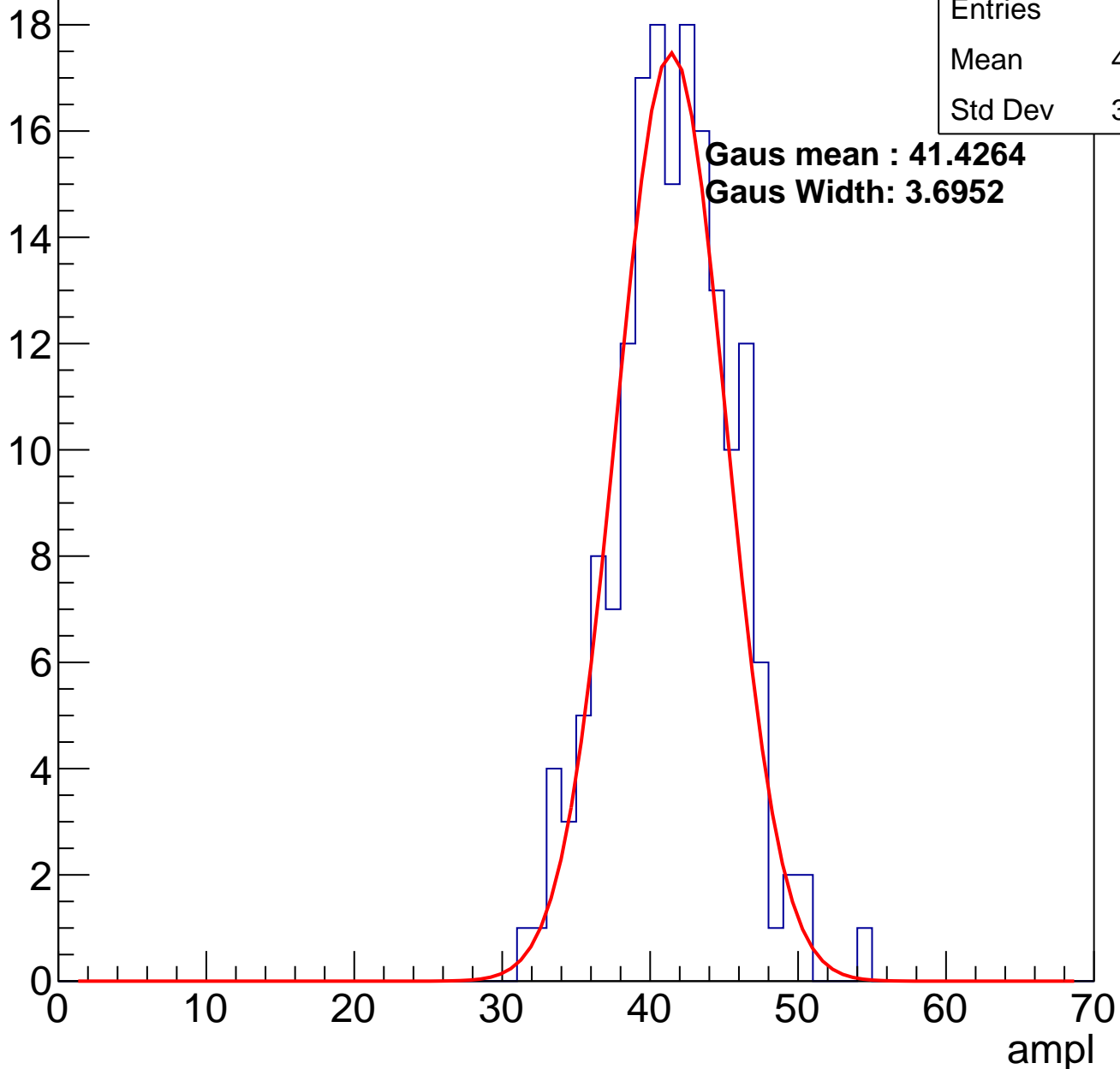


Entries	133
Mean	33.85
Std Dev	3.443

B0L101S, U15-ch57, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	48.78
Std Dev	3.232

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

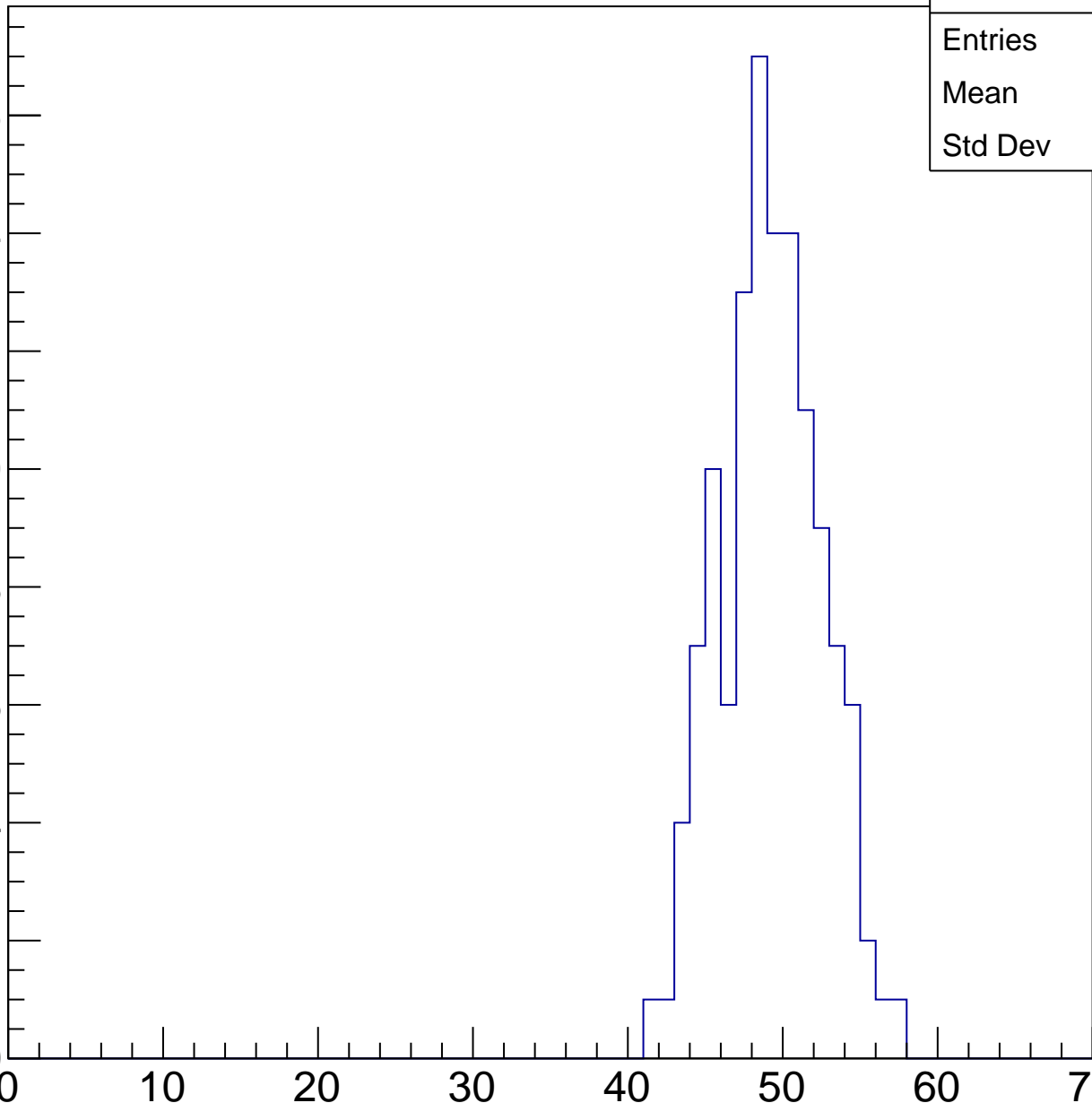
40

50

60

70

ampl



B0L101S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	104
Mean	54.33
Std Dev	3.409

0

2

4

6

8

10

12

B0L101S, U15-ch57, adc5

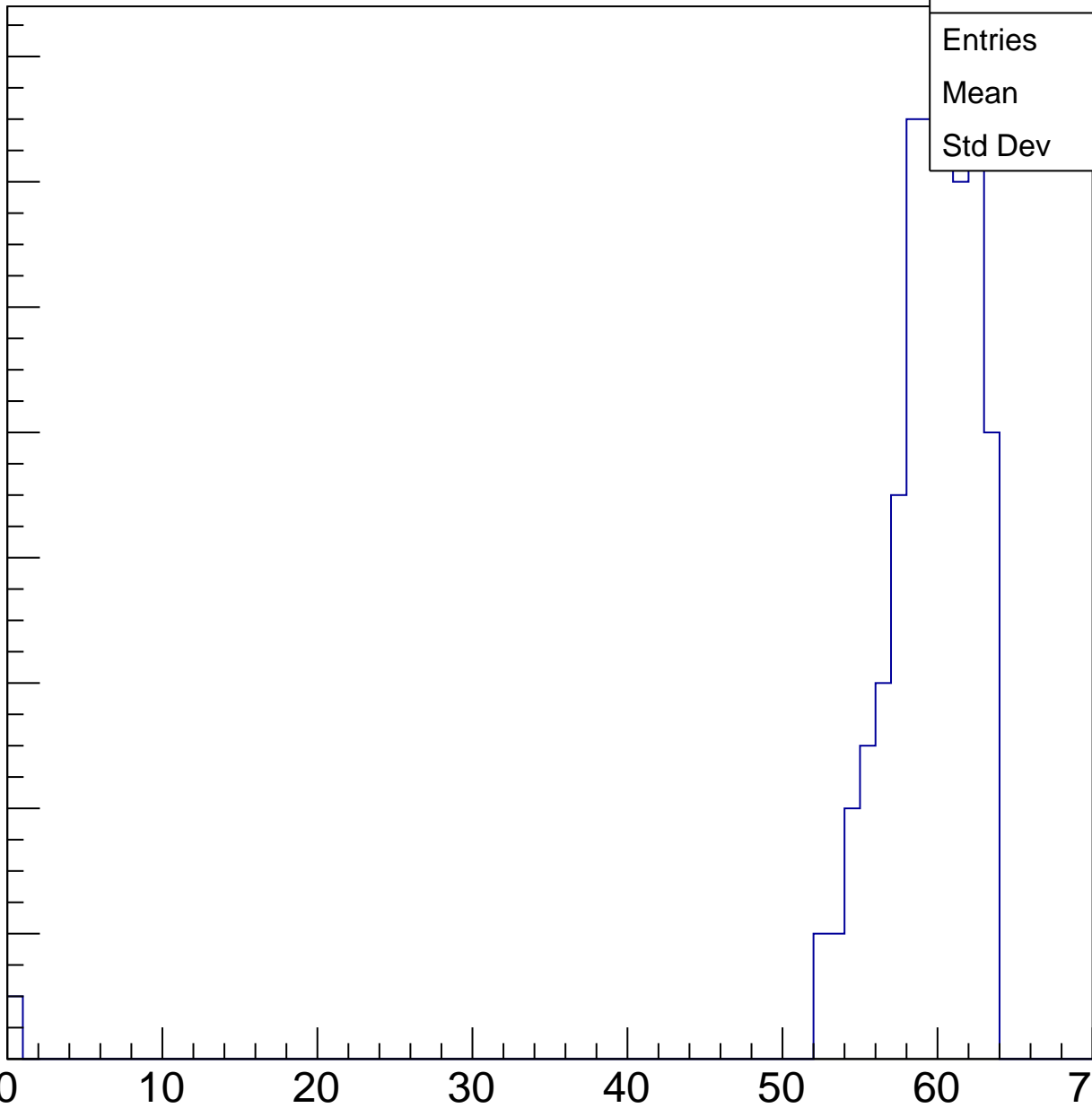
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	58.62
Std Dev	6.111

ampl

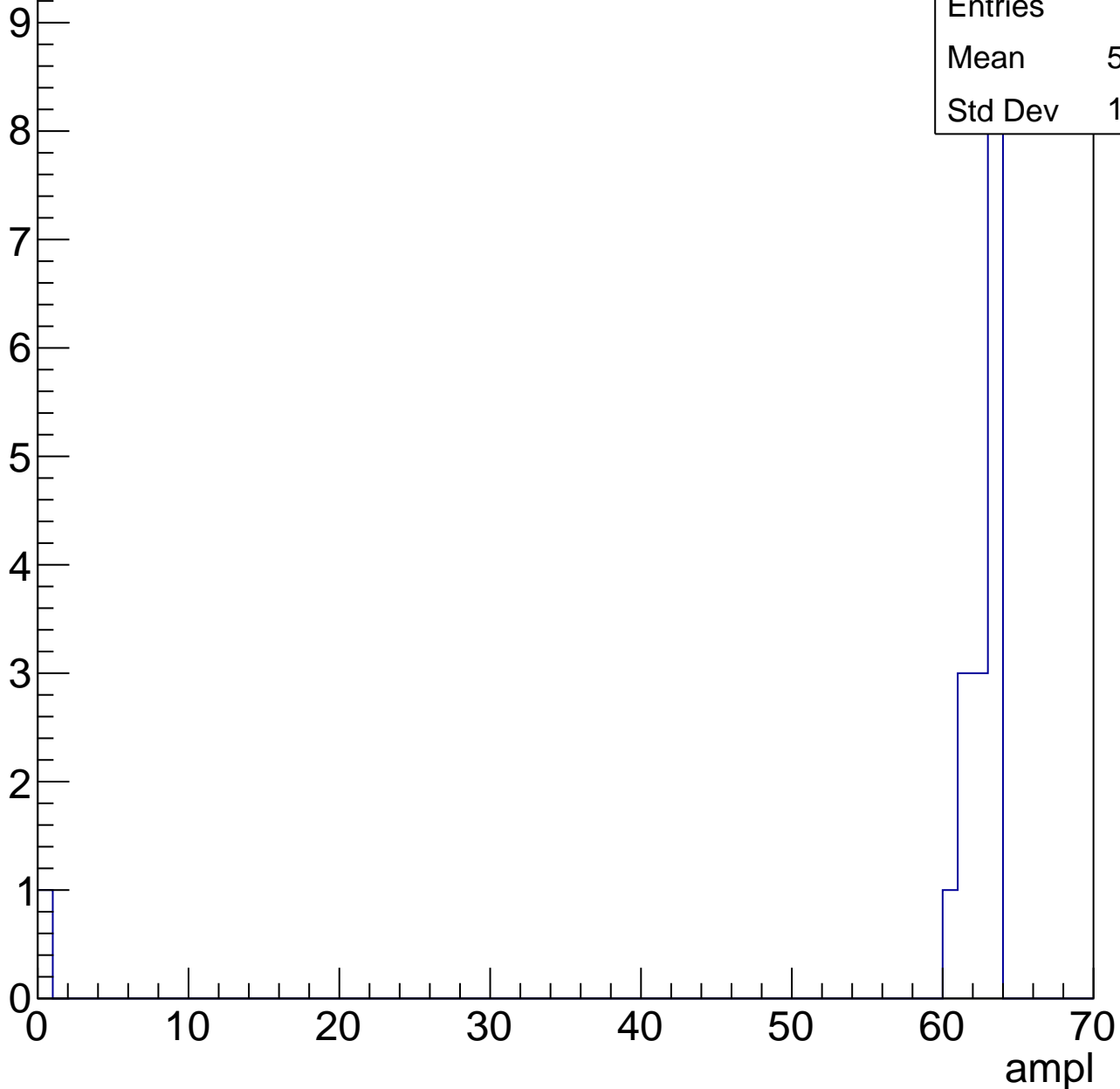


B0L101S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

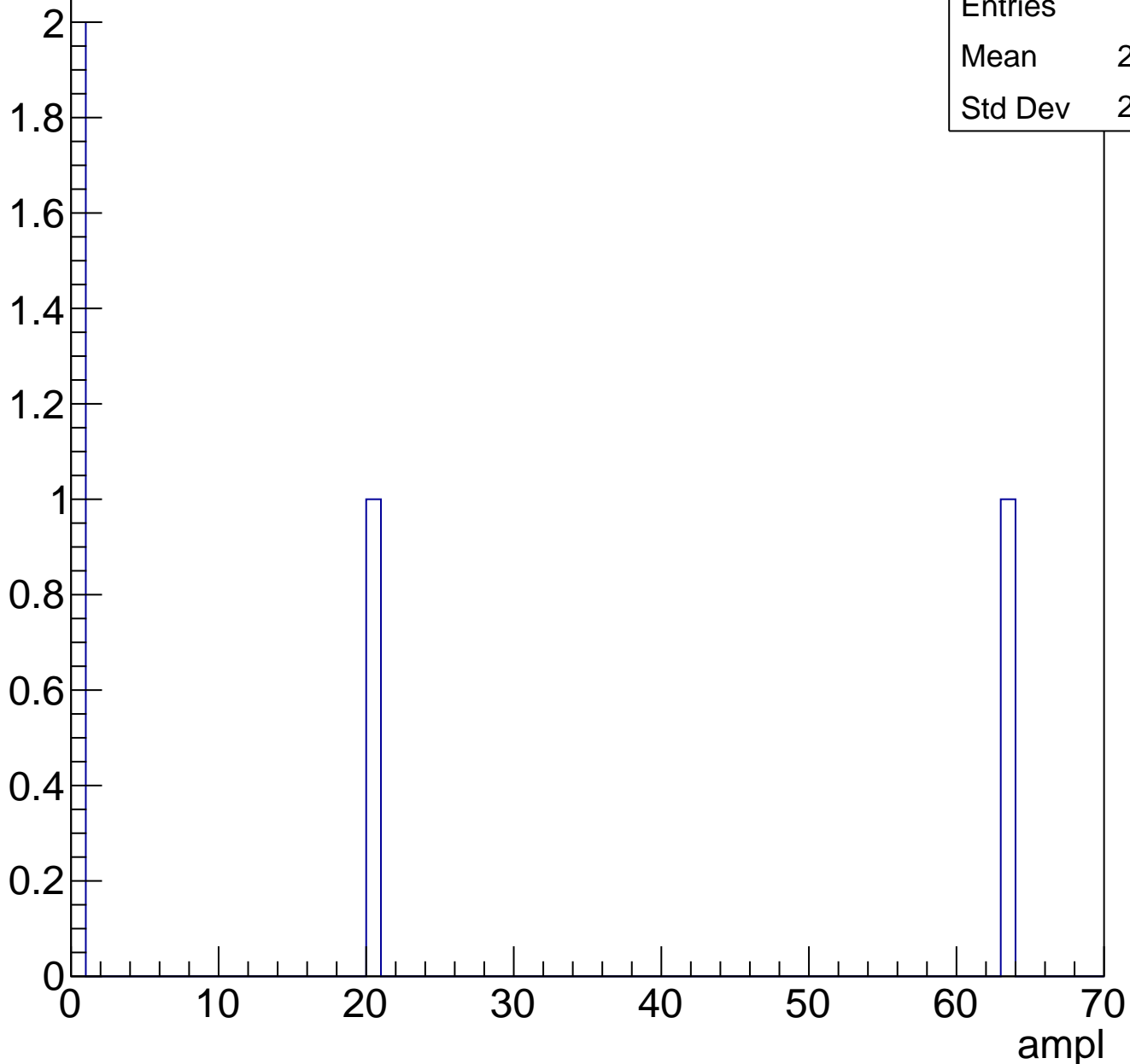
Entries	17
Mean	58.59
Std Dev	14.68



B0L101S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	20.75
Std Dev	25.72

B0L101S, U15-ch58, adc0

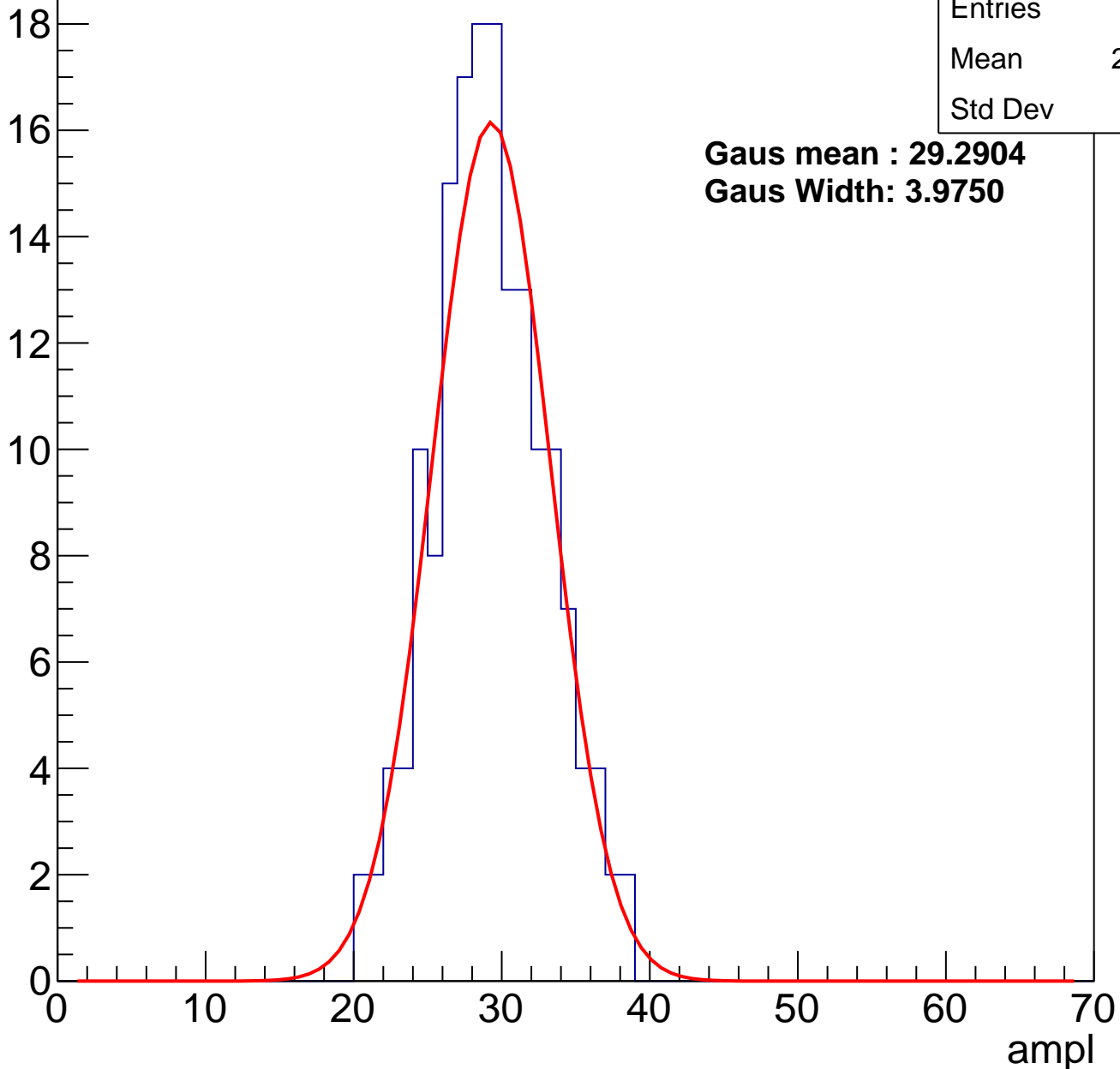
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.79
Std Dev	3.79

Gaus mean : 29.2904

Gaus Width: 3.9750

Entry



B0L101S, U15-ch58, adc1

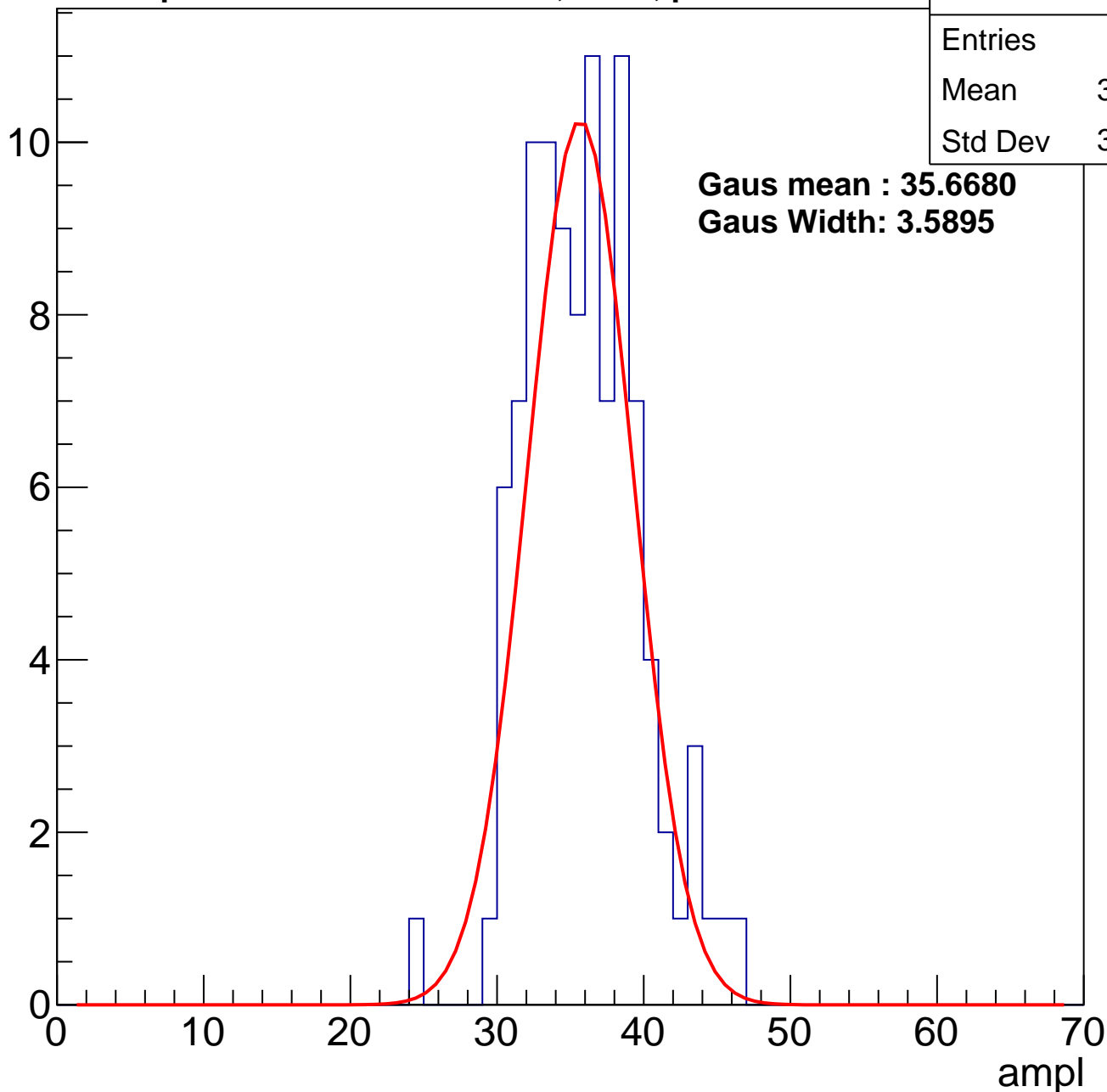
calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	35.45
Std Dev	3.858

Gaus mean : 35.6680

Gaus Width: 3.5895

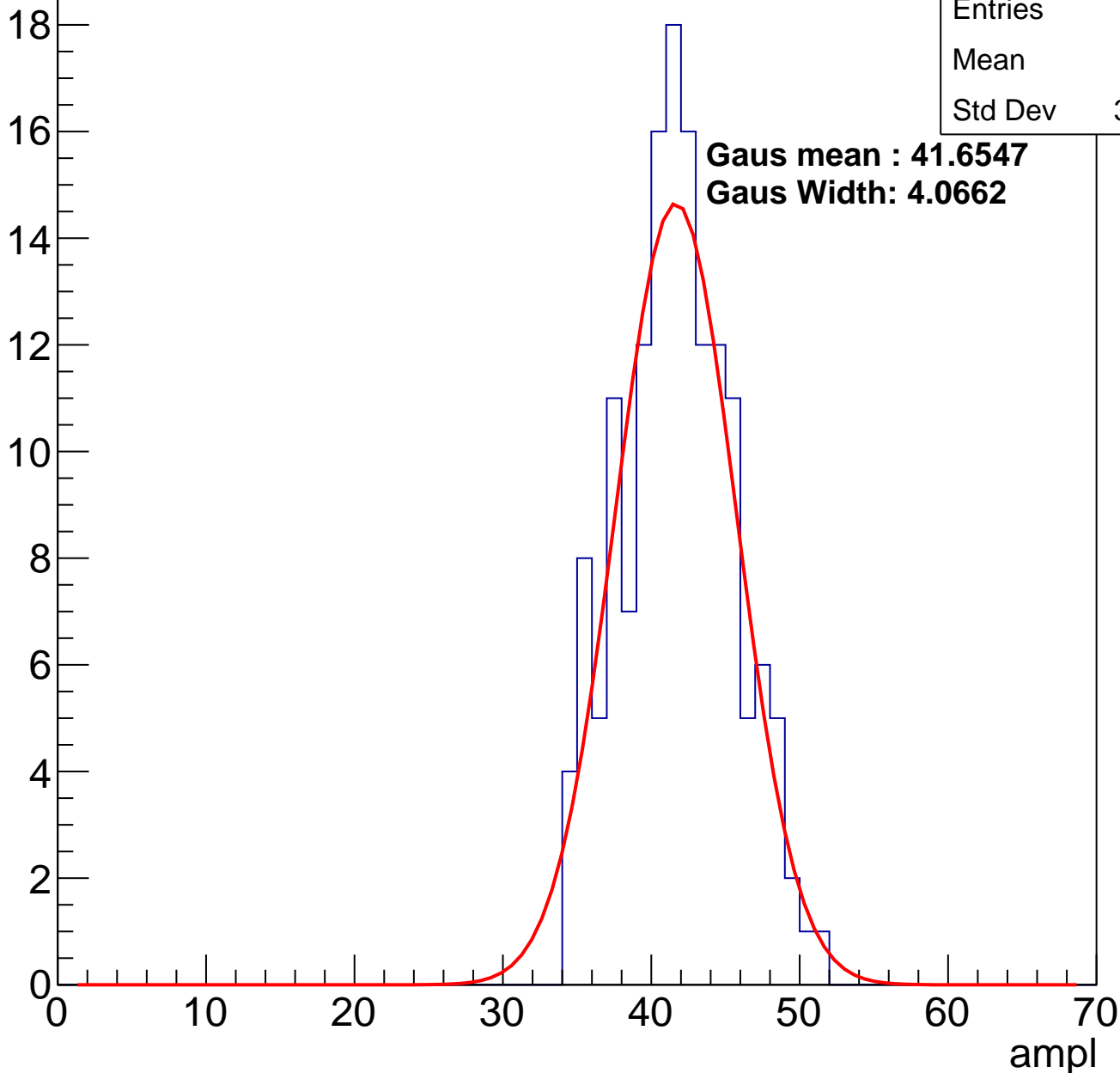
Entry



B0L101S, U15-ch58, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch58, adc3

calib_packv5_042523_0143.root, FC#1, port C1

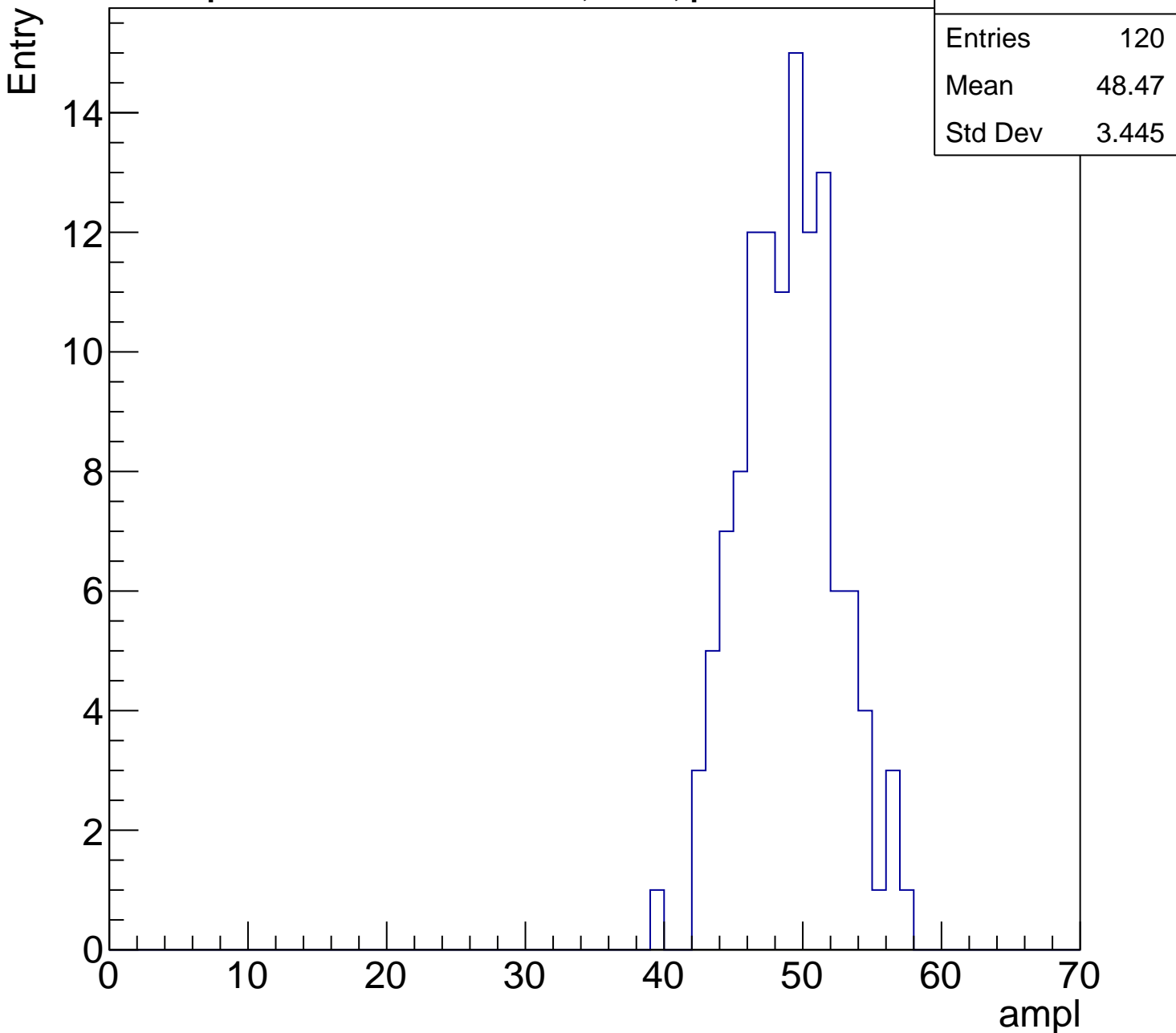
Entries	120
Mean	48.47
Std Dev	3.445

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

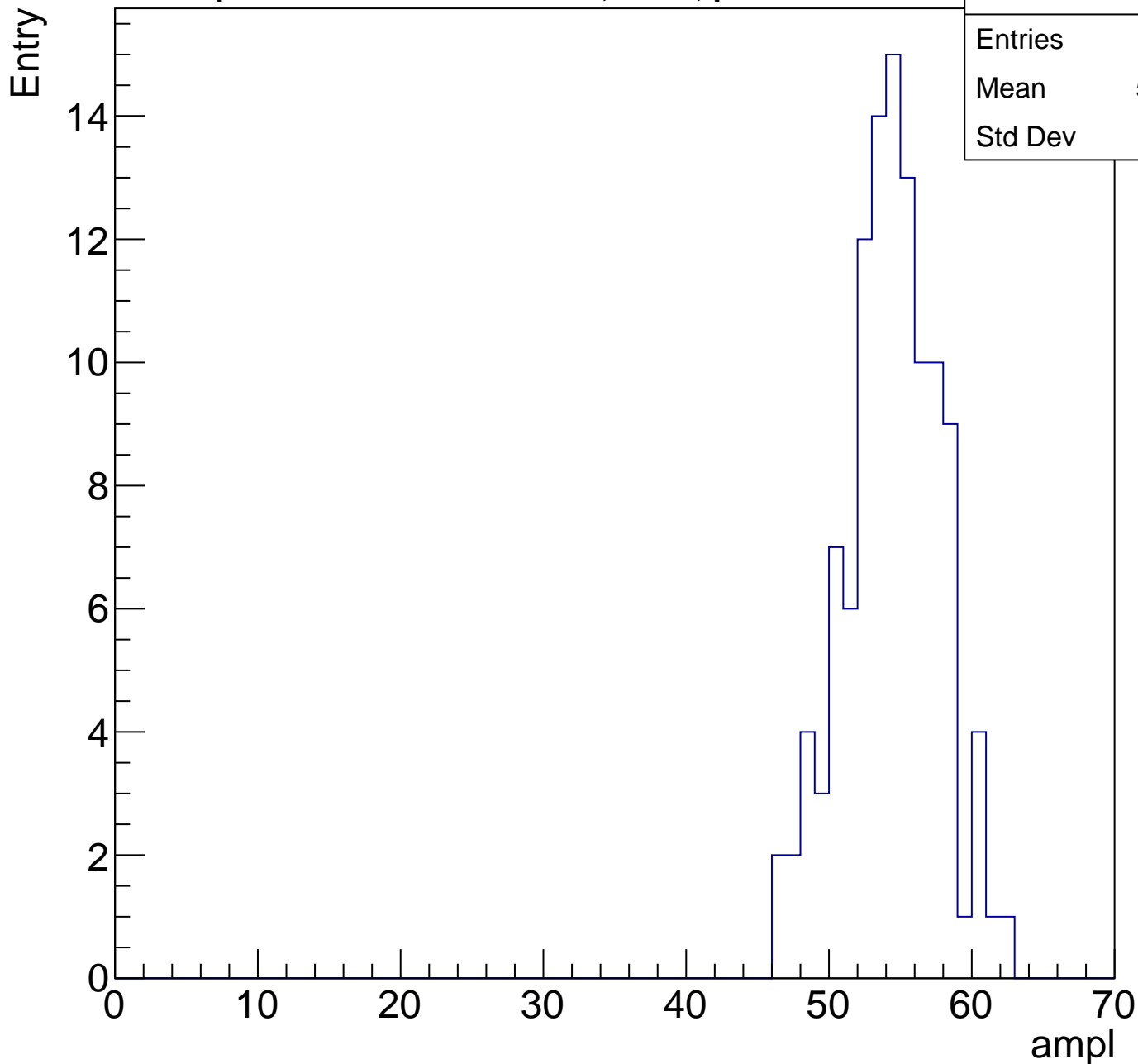
ampl



B0L101S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	53.91
Std Dev	3.3



B0L101S, U15-ch58, adc5

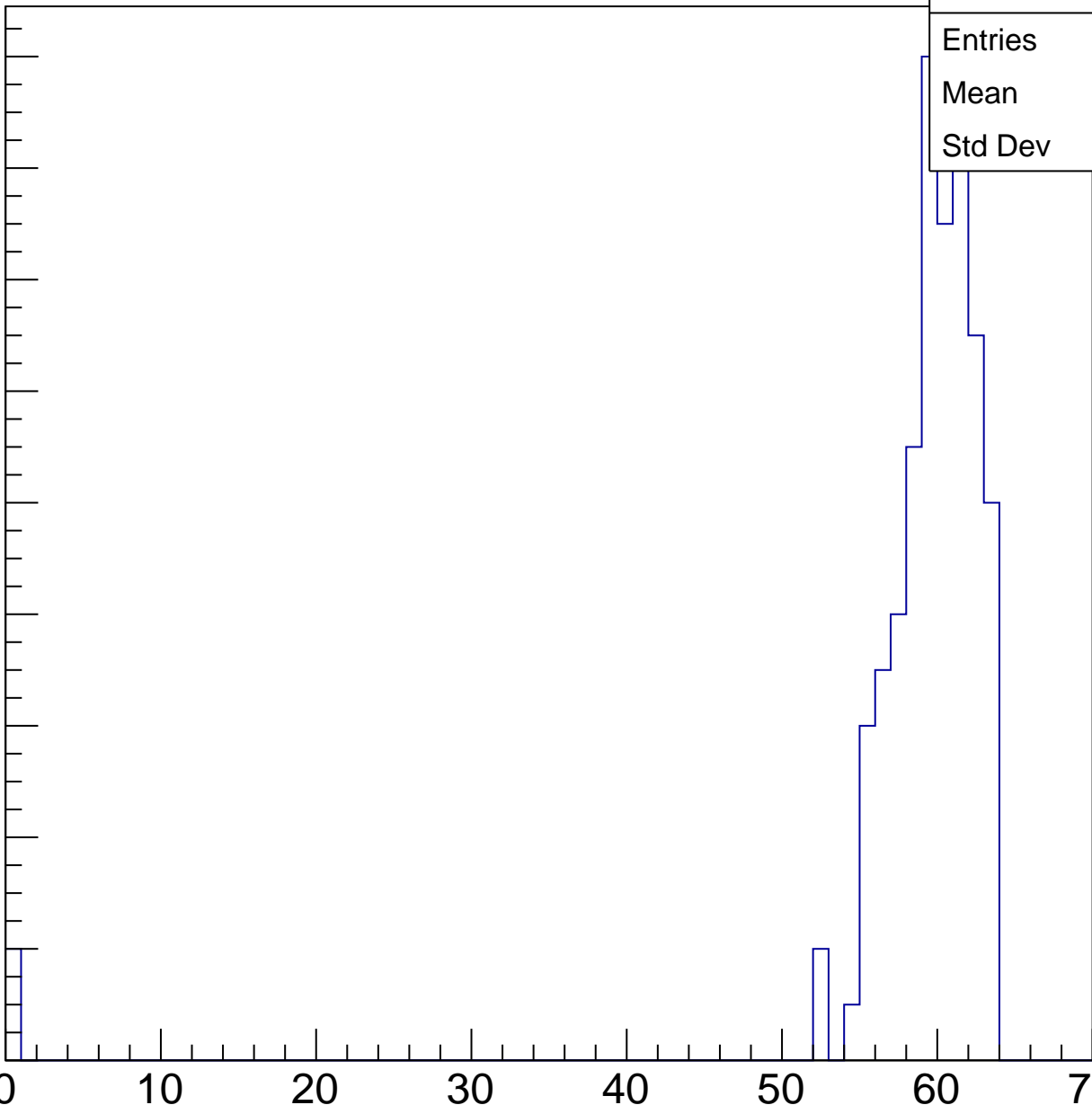
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	110
Mean	58.26
Std Dev	8.305

ampl

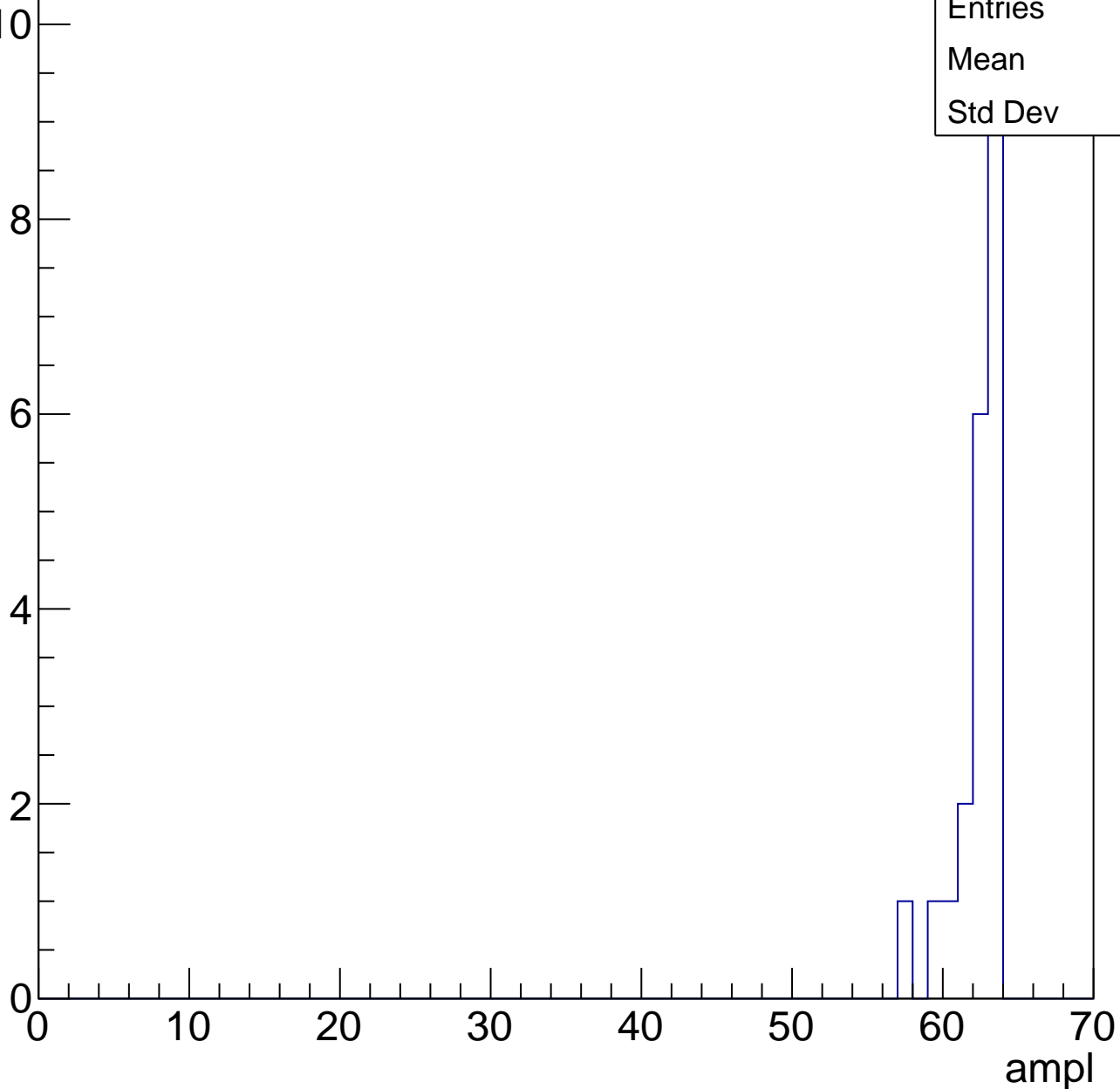


B0L101S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61.9
Std Dev	1.54



B0L101S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch59, adc0

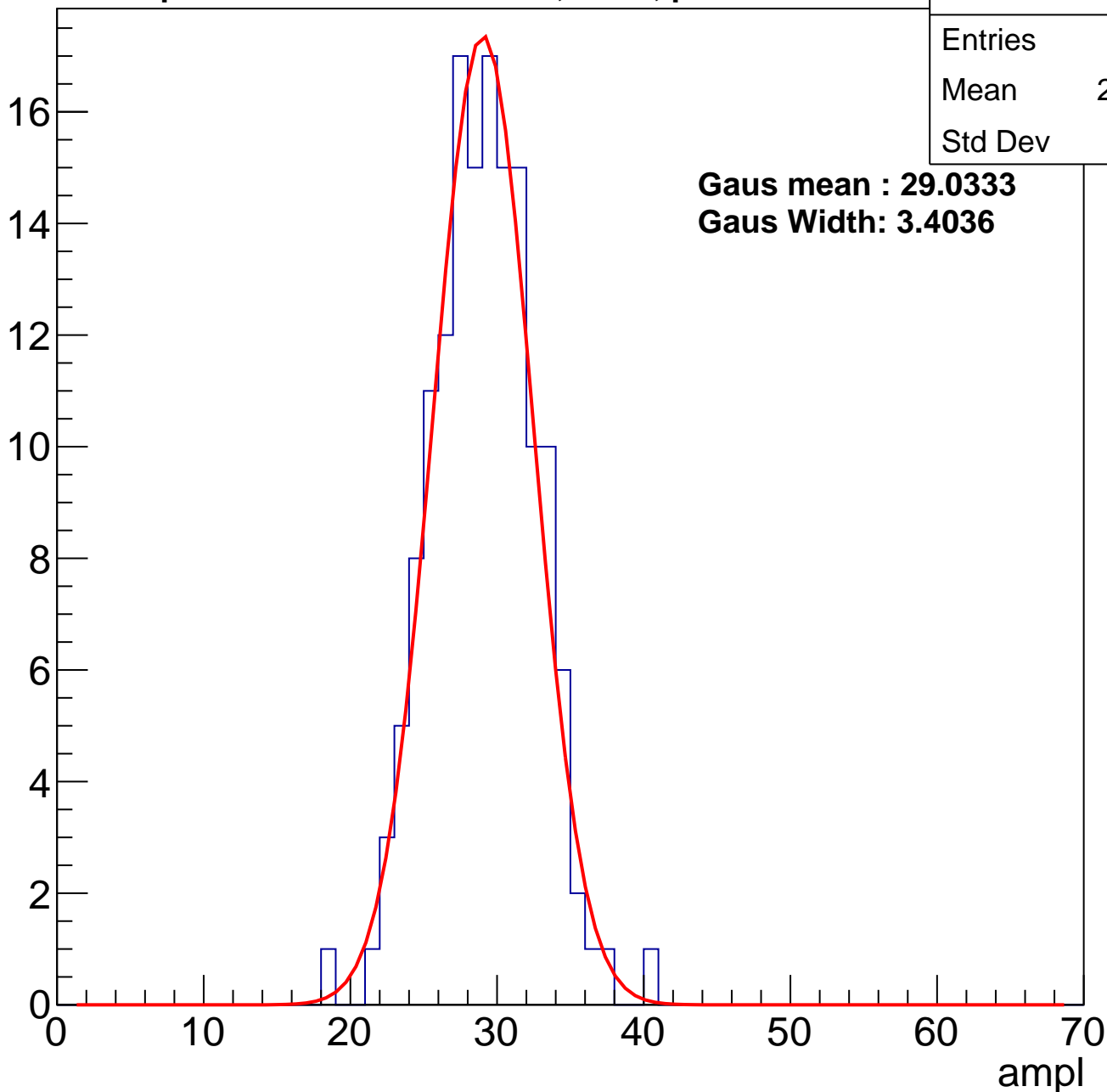
calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	28.63
Std Dev	3.49

Gaus mean : 29.0333

Gaus Width: 3.4036

Entry



B0L101S, U15-ch59, adc1

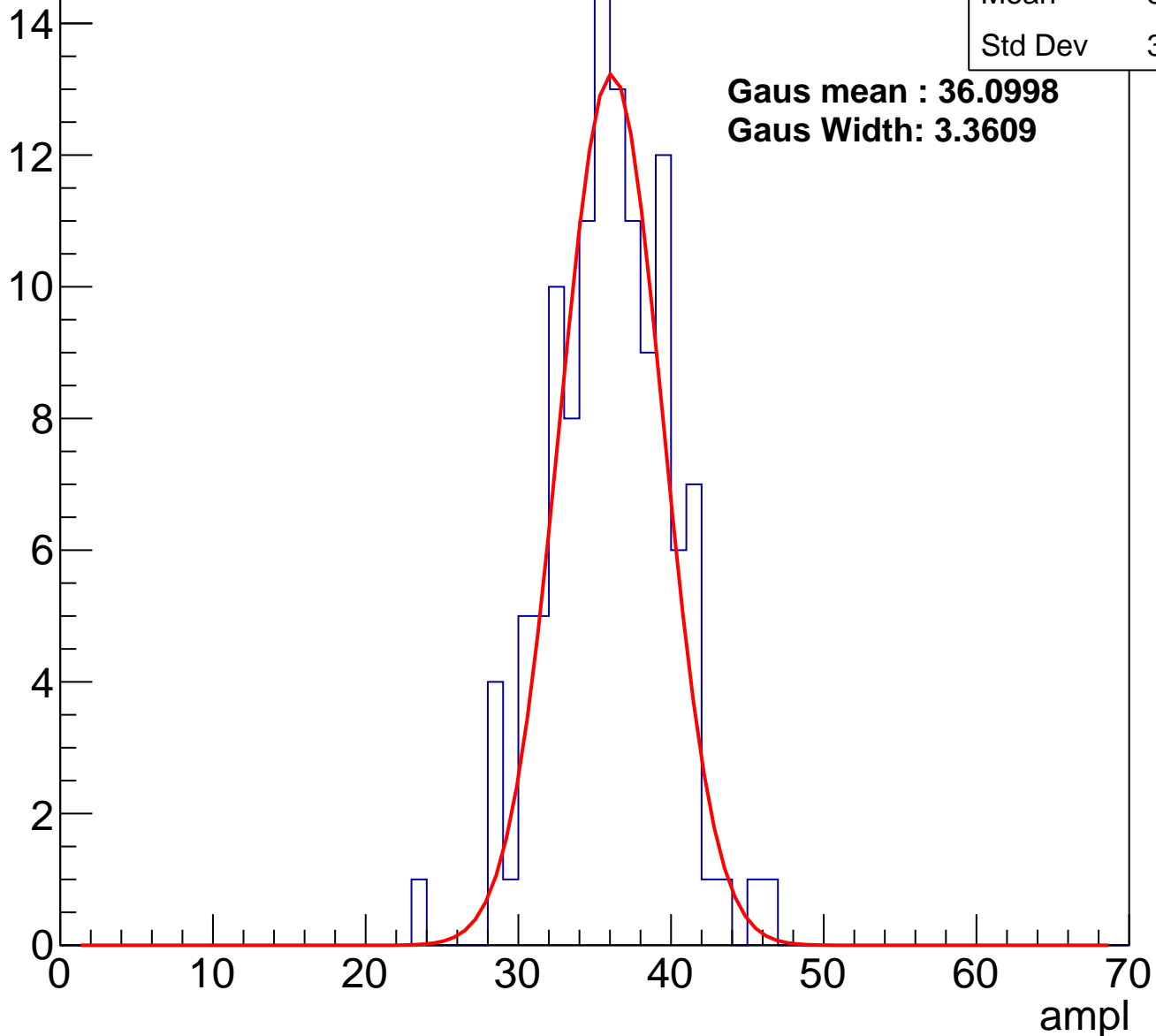
calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	35.57
Std Dev	3.776

Gaus mean : 36.0998

Gaus Width: 3.3609

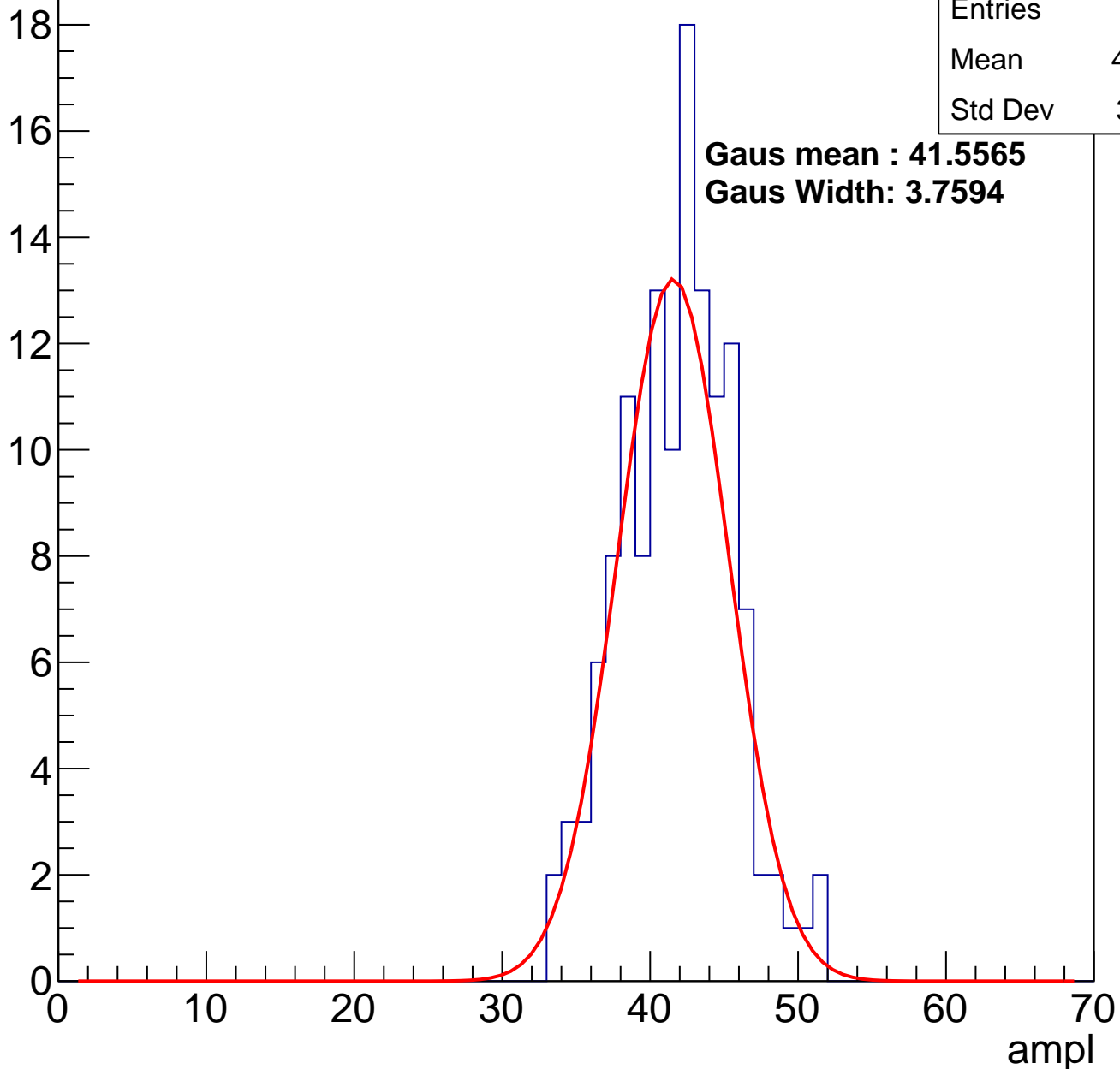
Entry



B0L101S, U15-ch59, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	48.51
Std Dev	3.478

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

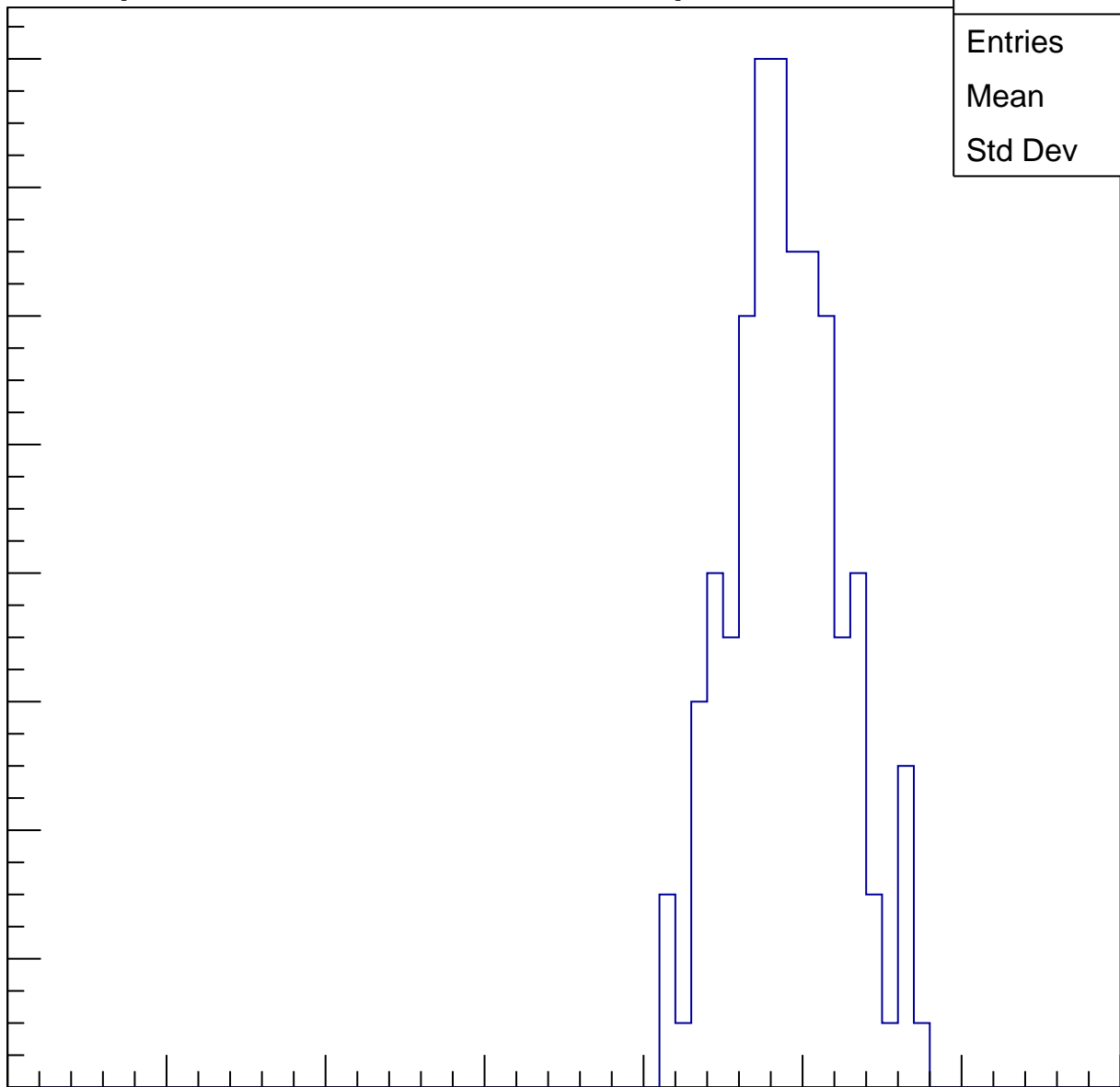
40

50

60

70

ampl

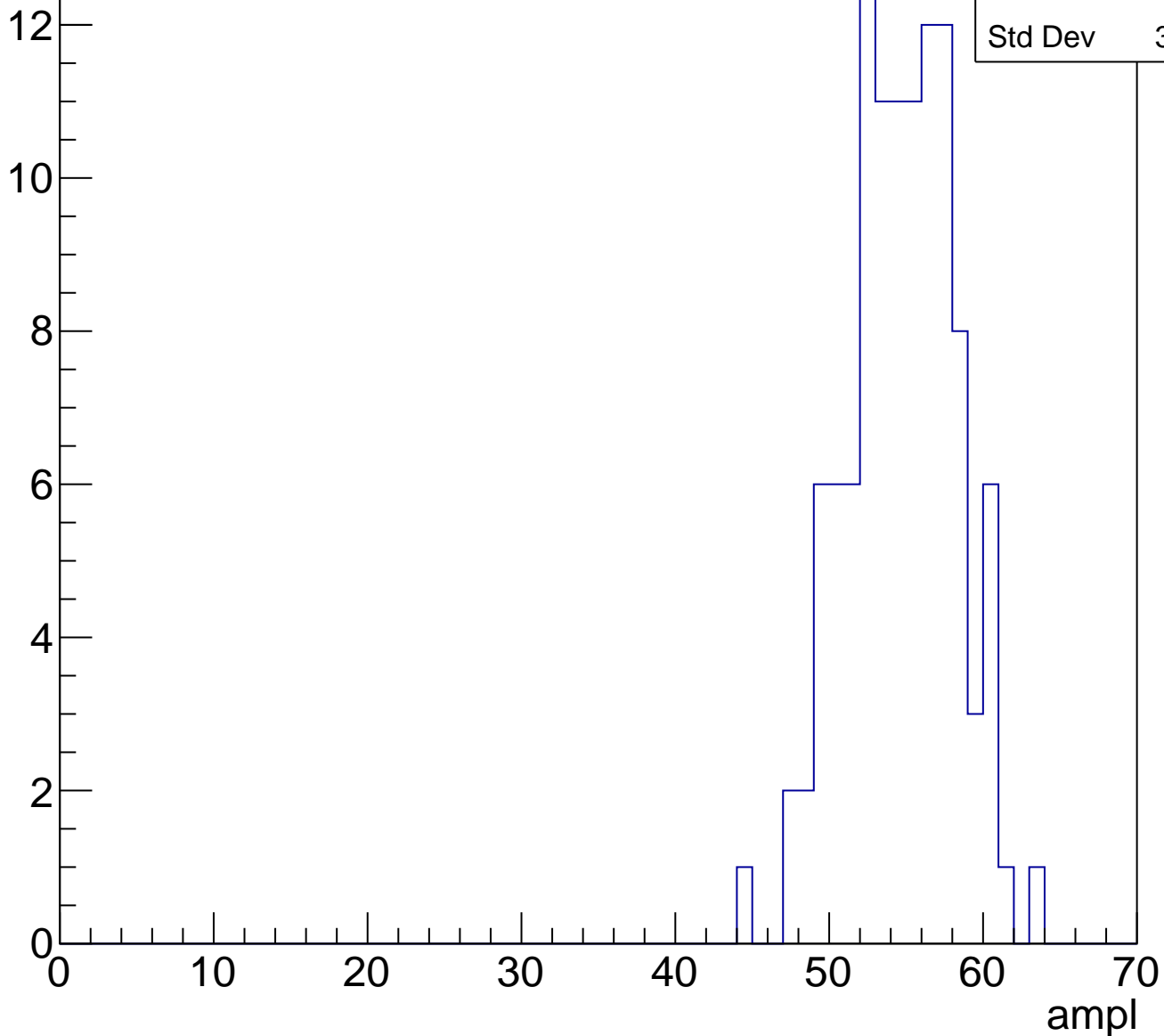


B0L101S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	54.22
Std Dev	3.463

Entry



B0L101S, U15-ch59, adc5

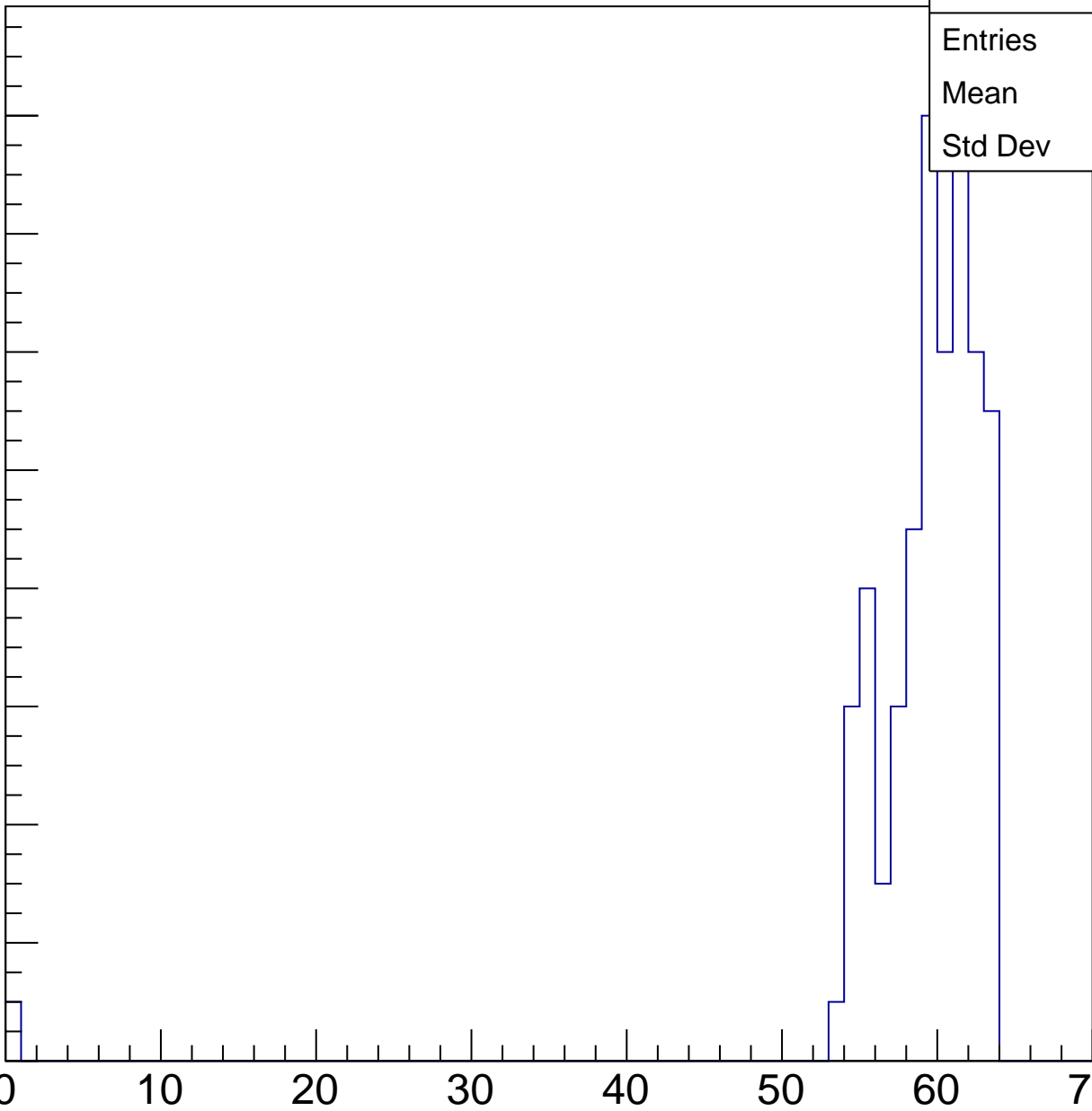
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	58.7
Std Dev	6.424

ampl

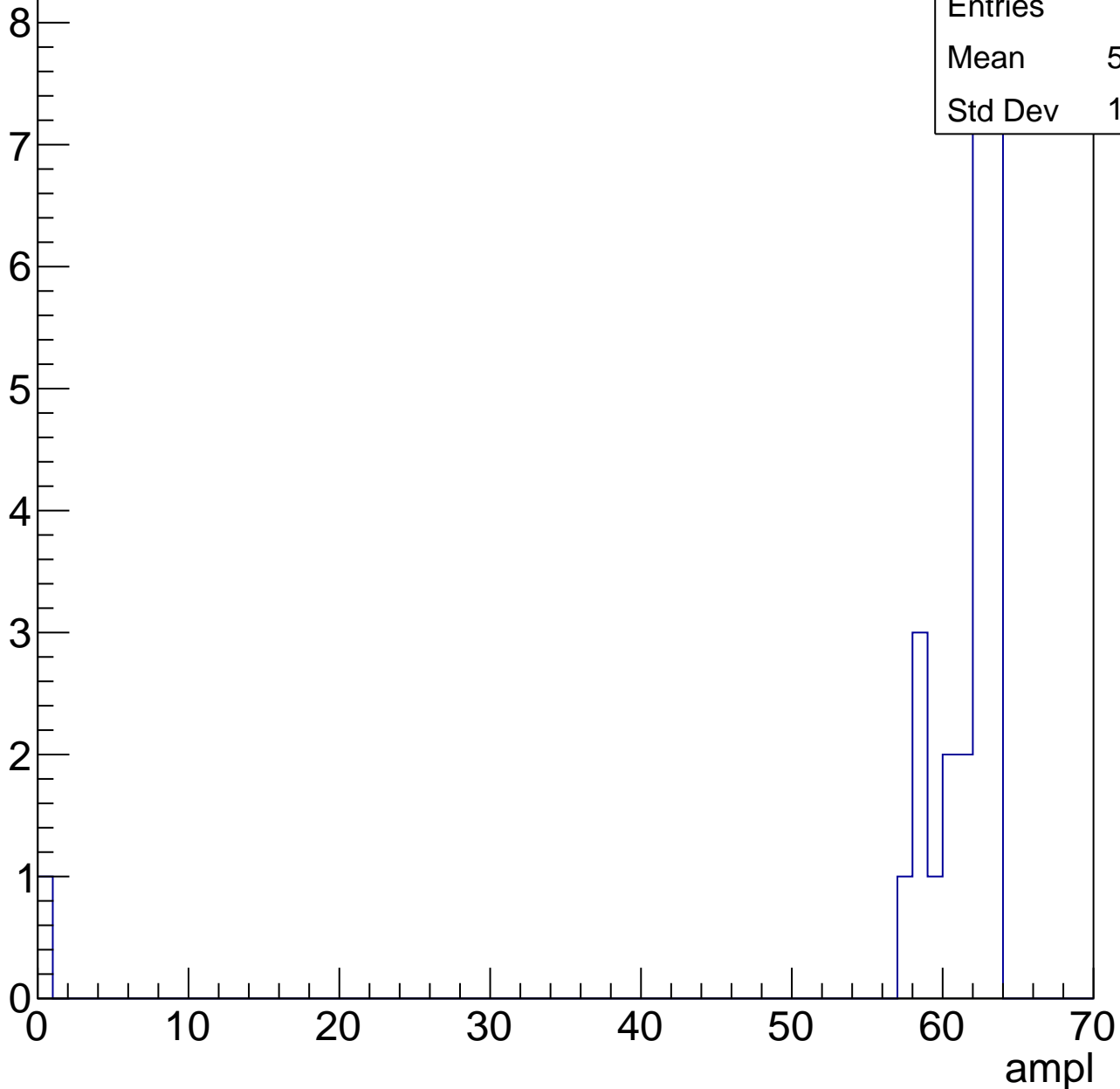


B0L101S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	26
Mean	58.92
Std Dev	11.93



B0L101S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch60, adc0

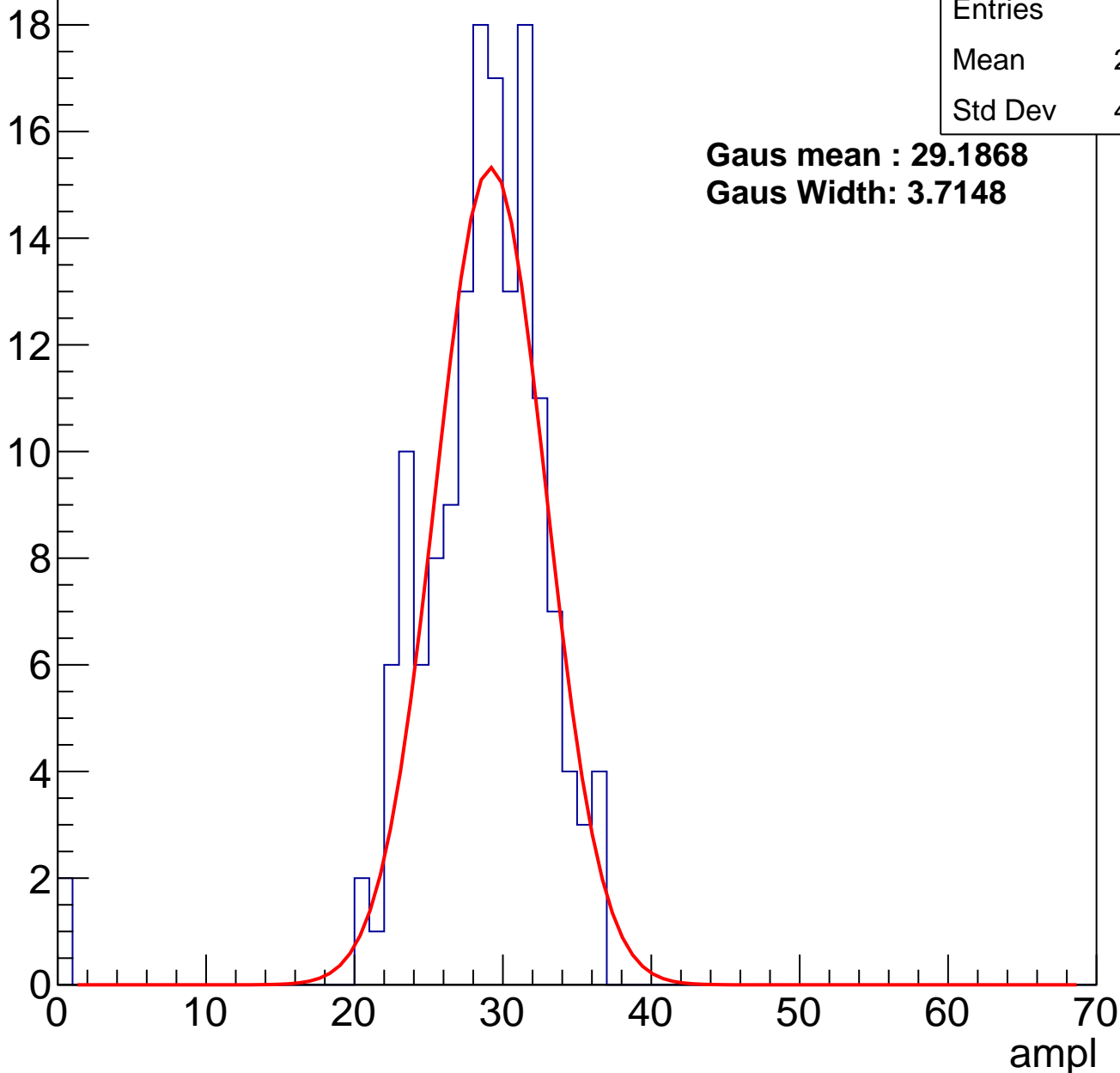
calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.06
Std Dev	4.837

Gaus mean : 29.1868

Gaus Width: 3.7148

Entry



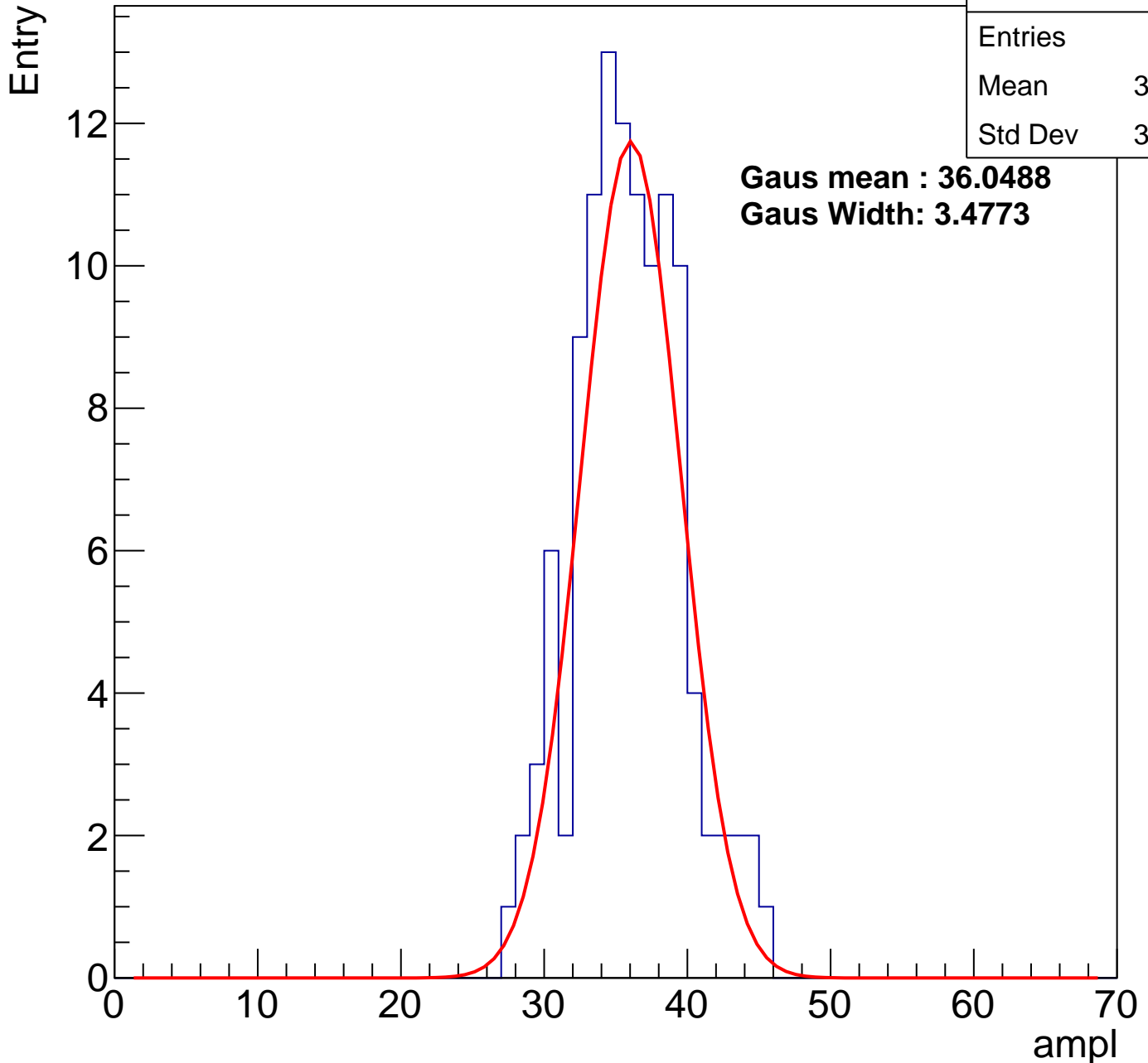
B0L101S, U15-ch60, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	35.47
Std Dev	3.669

Gaus mean : 36.0488

Gaus Width: 3.4773



B0L101S, U15-ch60, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	40.79
Std Dev	3.299

Gaus mean : 41.4262

Gaus Width: 3.6018

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

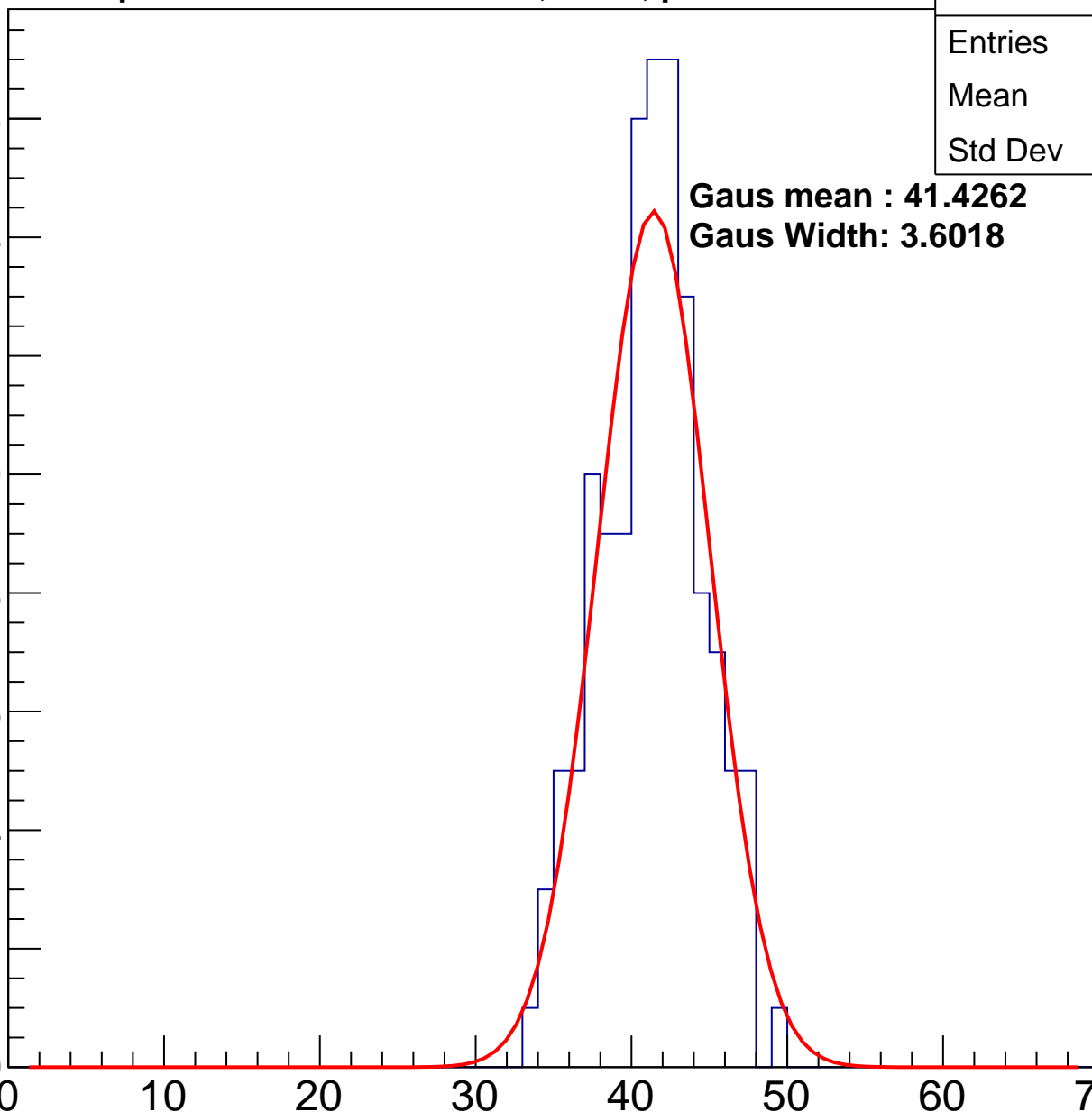
40

50

60

70

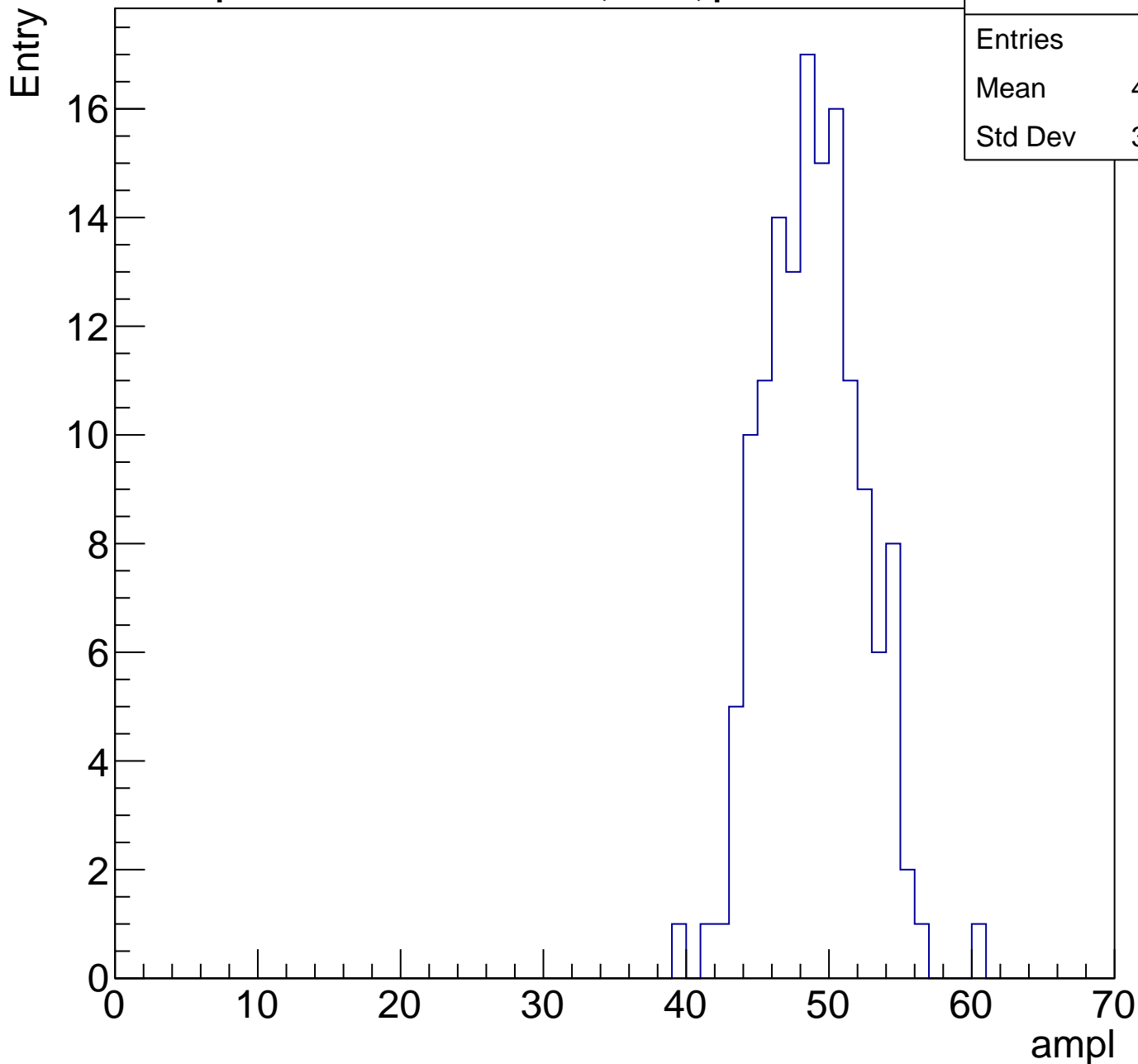
ampl



B0L101S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	48.47
Std Dev	3.412



B0L101S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

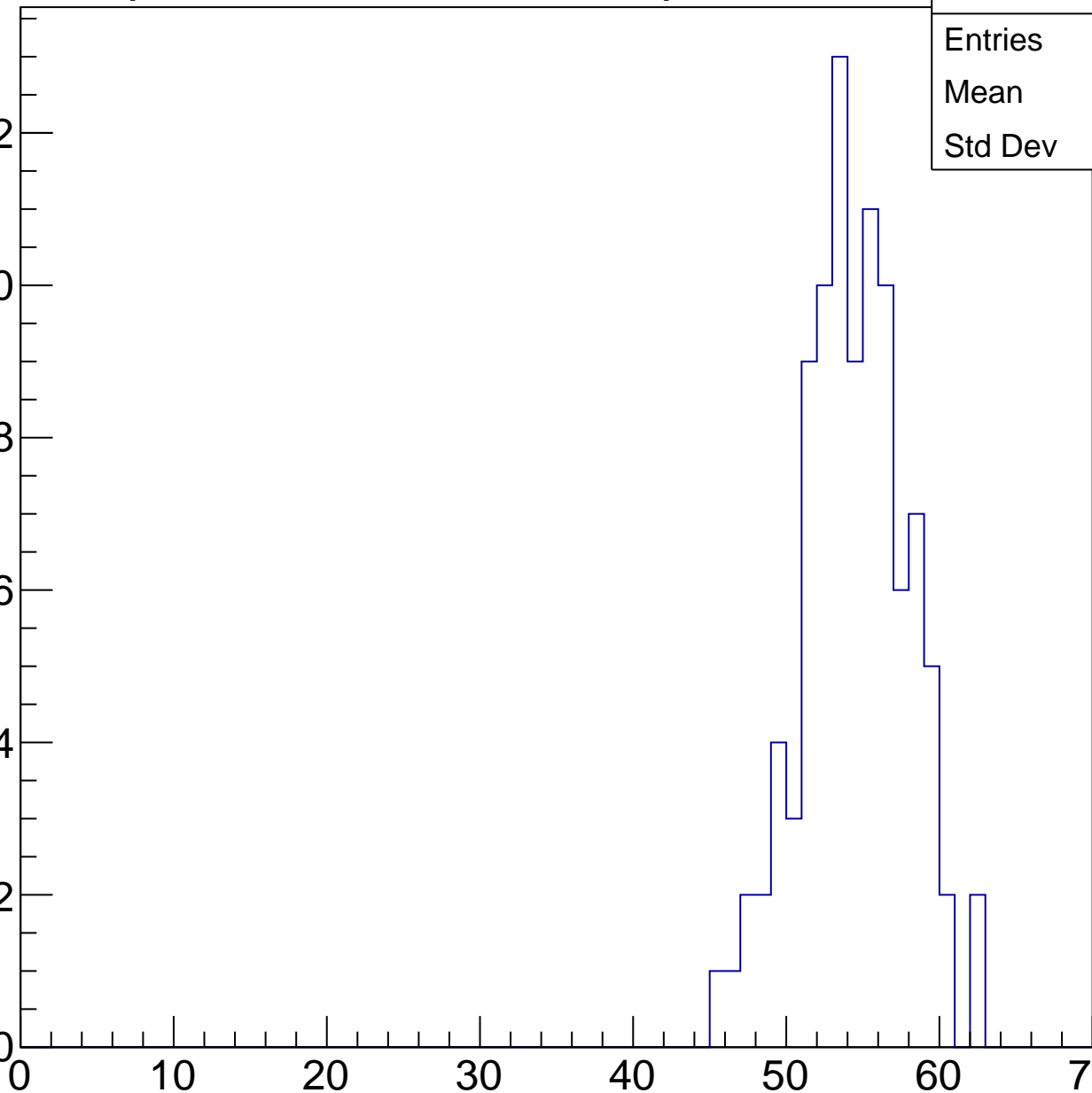
4

2

0

Entries	97
Mean	53.95
Std Dev	3.426

ampl



B0L101S, U15-ch60, adc5

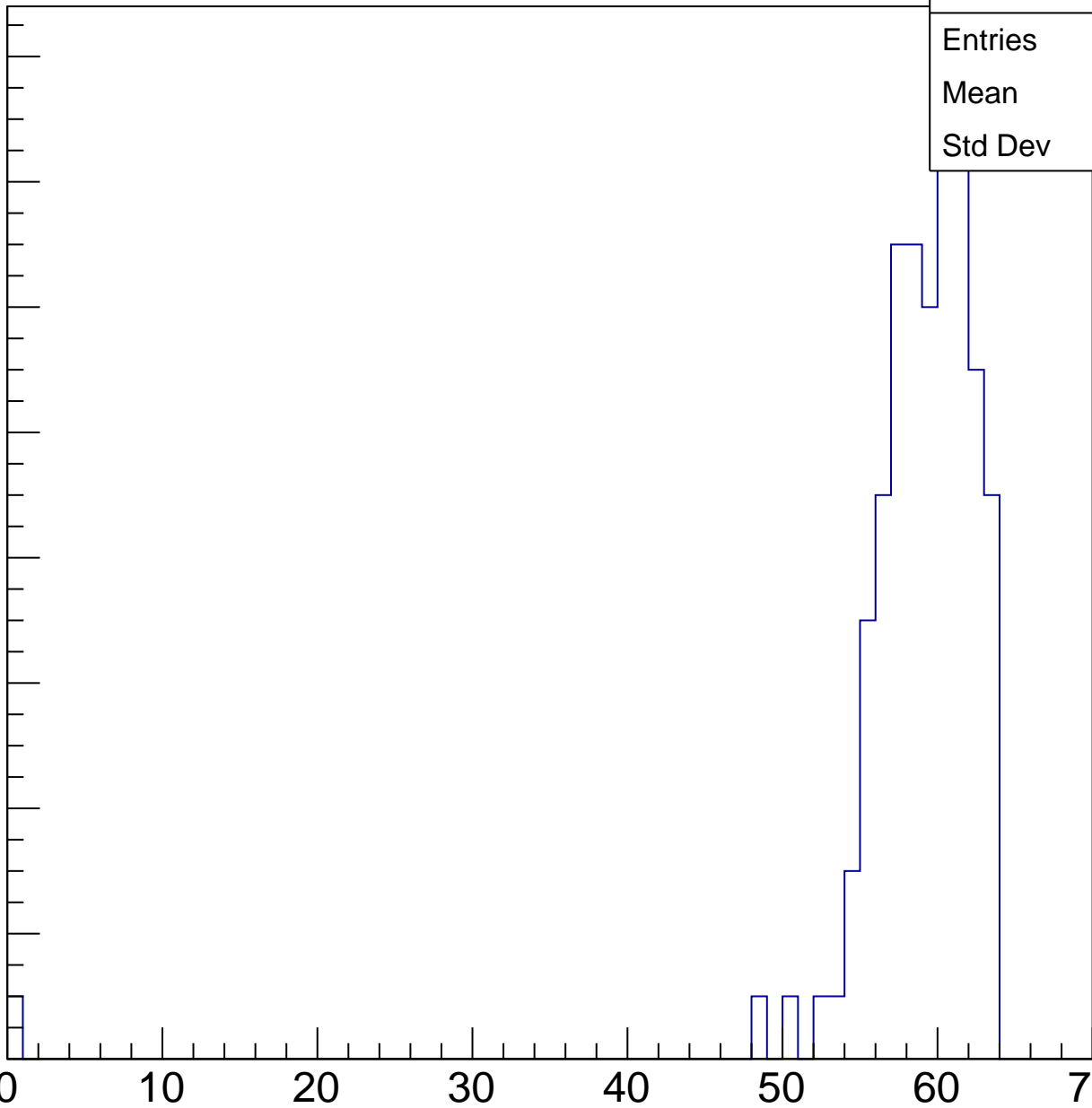
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	113
Mean	58.25
Std Dev	6.211

ampl



B0L101S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

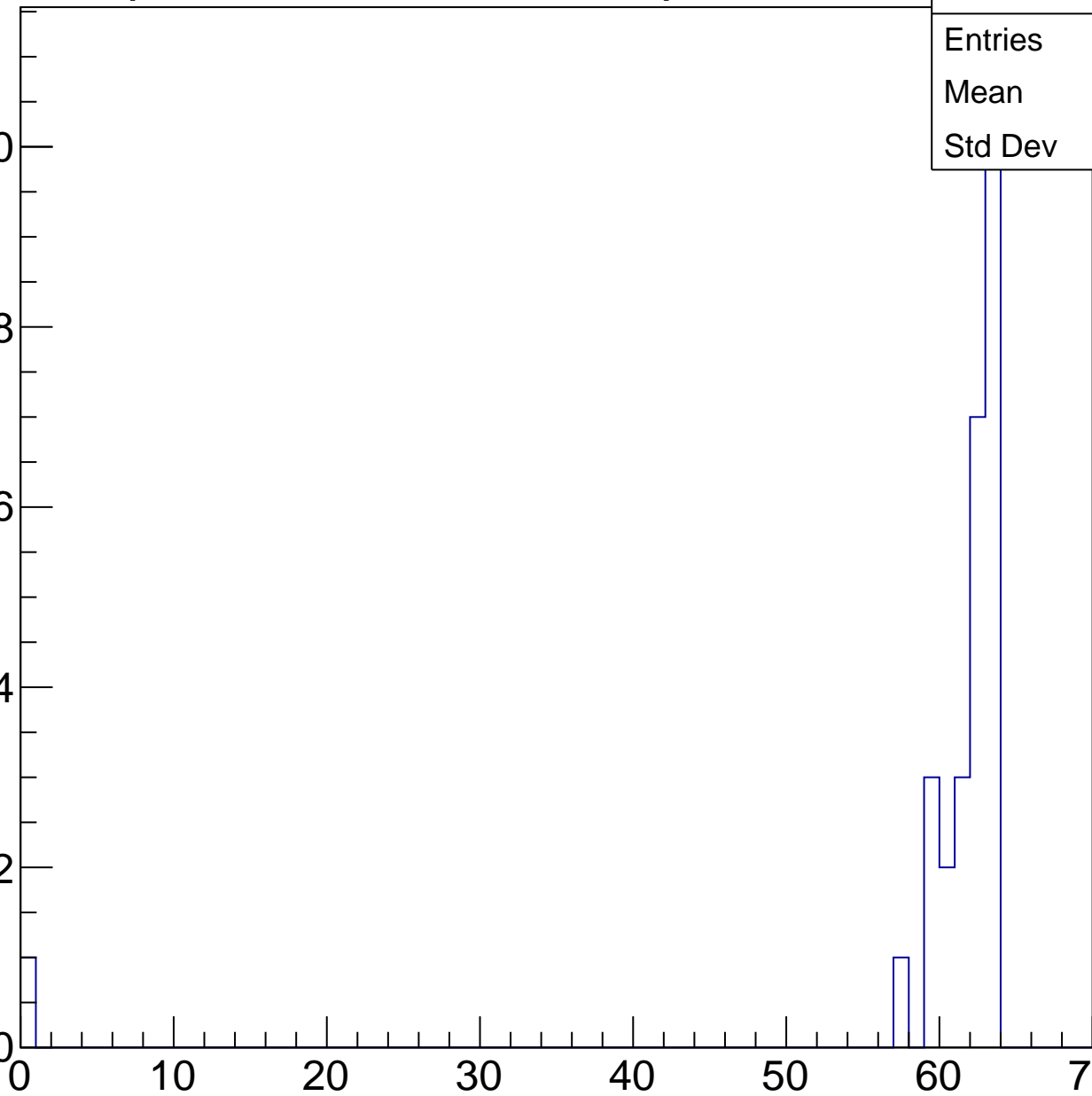
4

2

0

Entries	28
Mean	59.43
Std Dev	11.55

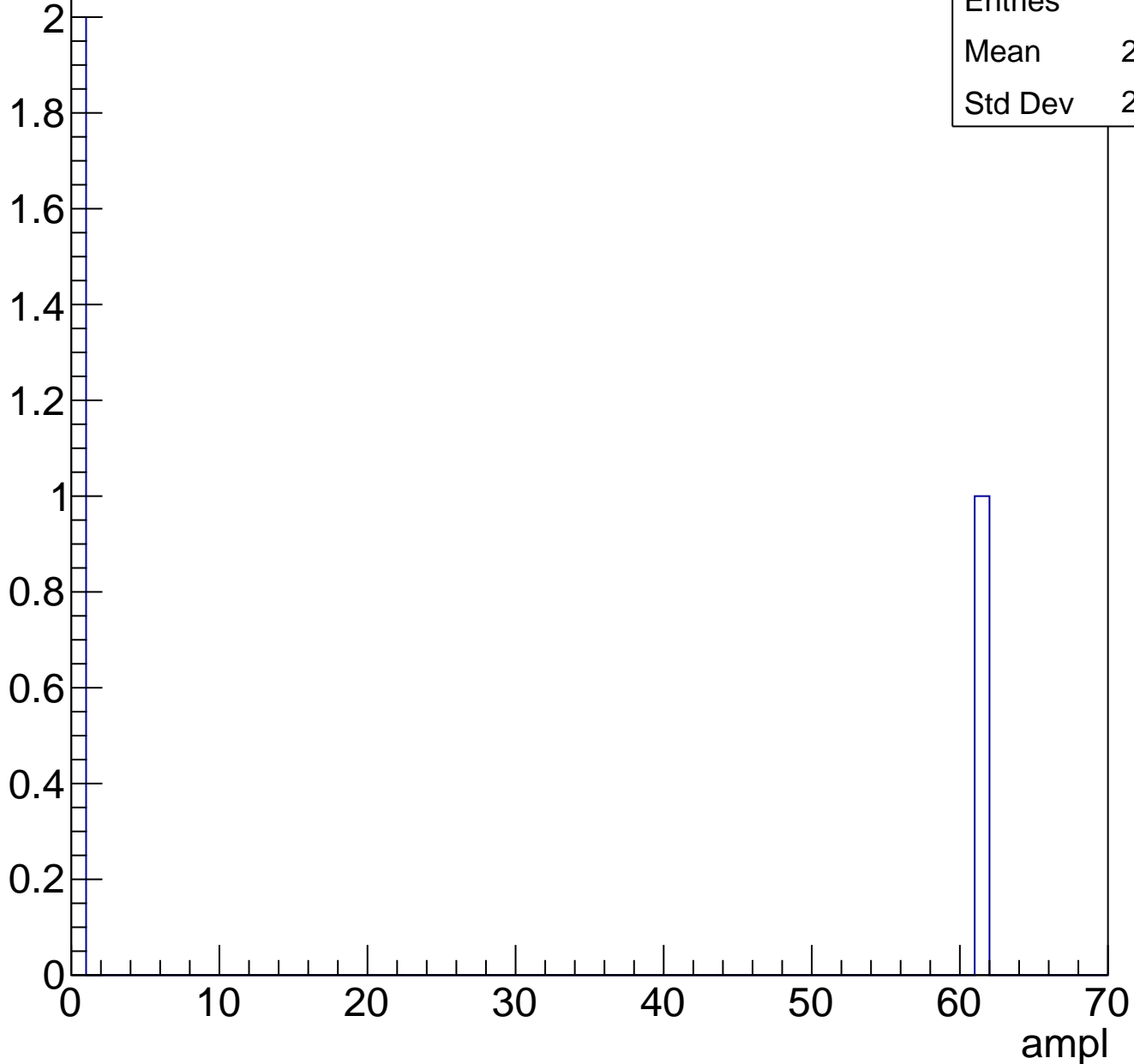
ampl



B0L101S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch61, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	26.62
Std Dev	4.252

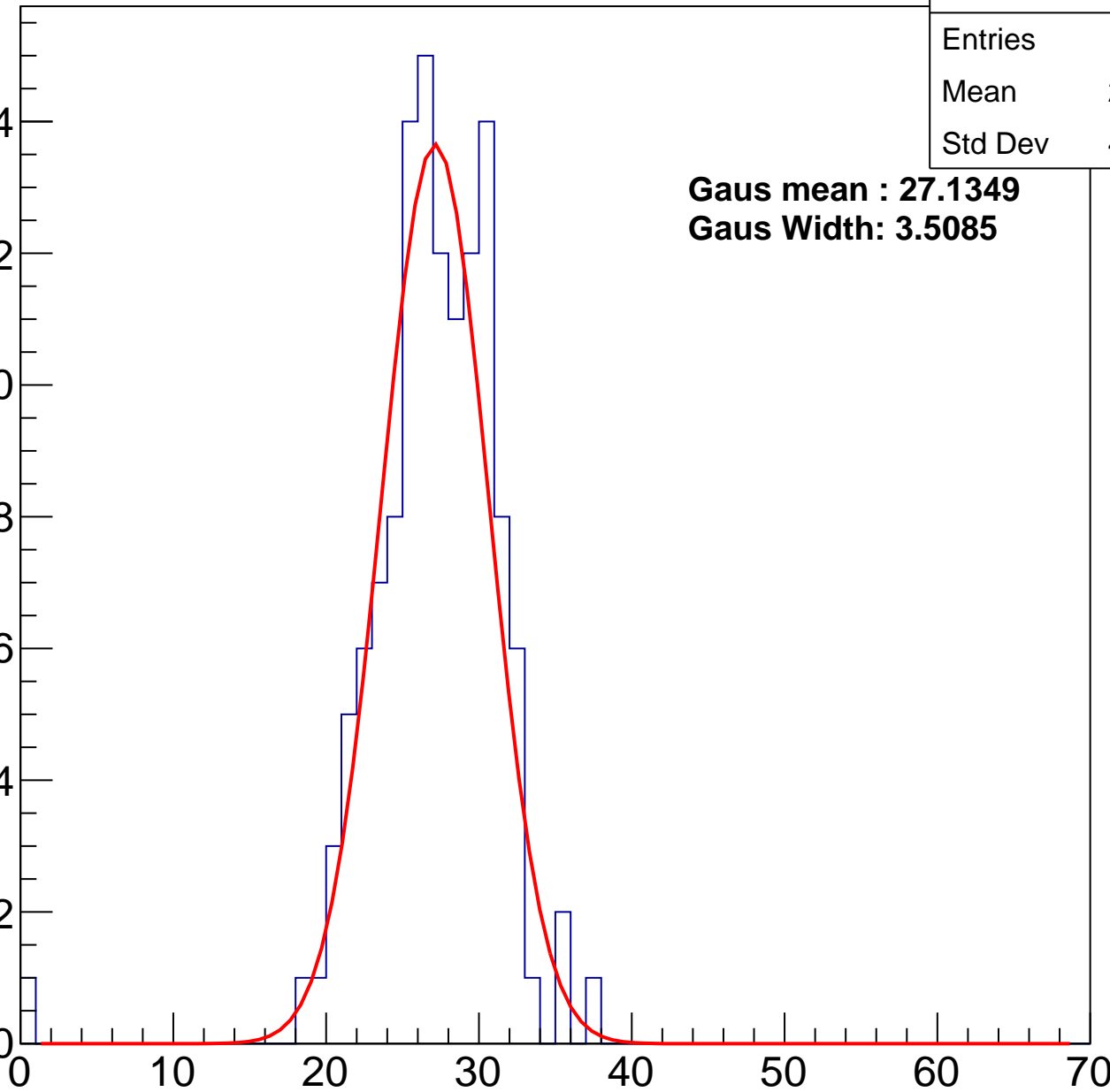
Gaus mean : 27.1349

Gaus Width: 3.5085

Entry

14
12
10
8
6
4
2
0

ampl



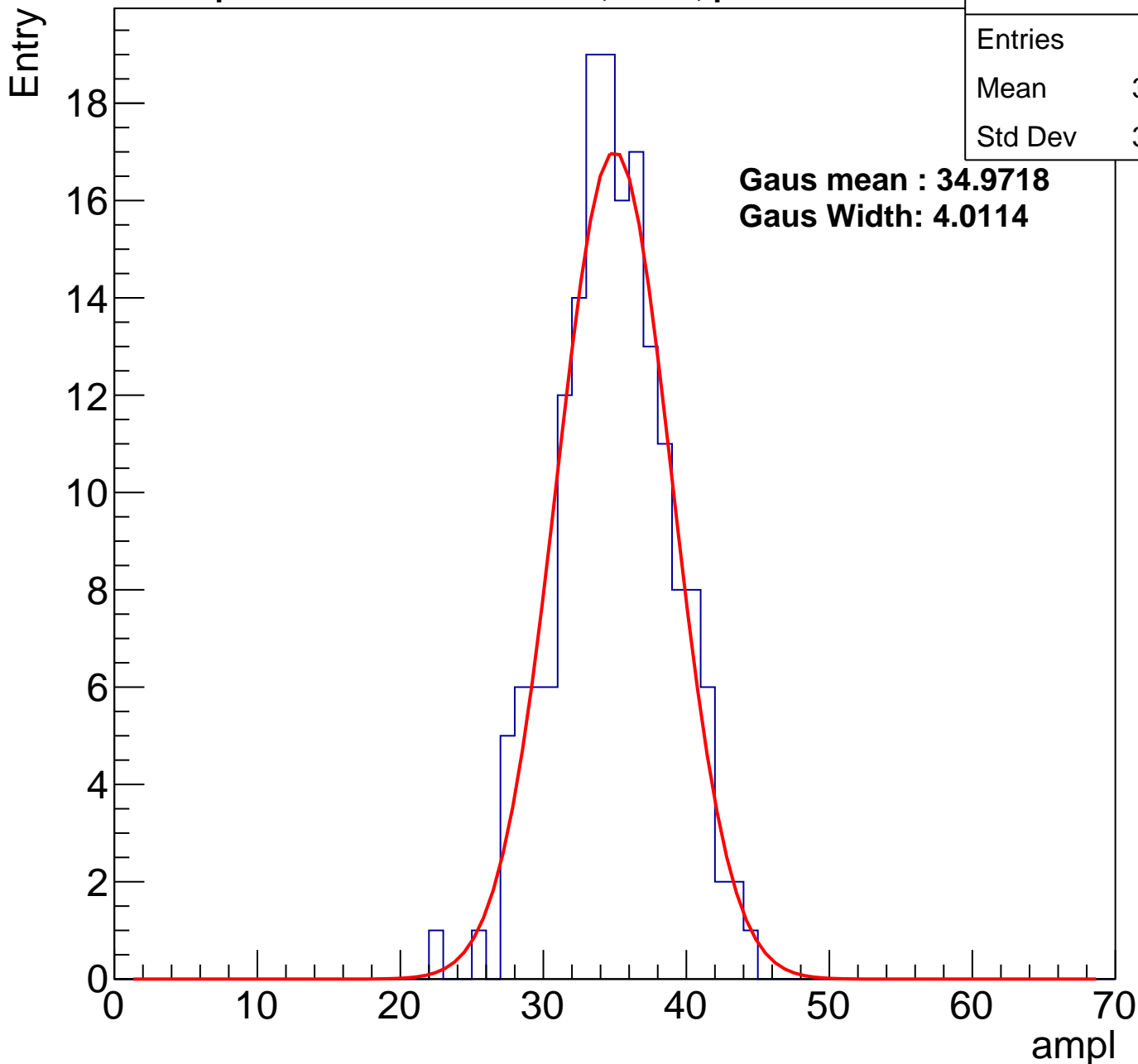
B0L101S, U15-ch61, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	173
Mean	34.45
Std Dev	3.898

Gaus mean : 34.9718

Gaus Width: 4.0114



B0L101S, U15-ch61, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

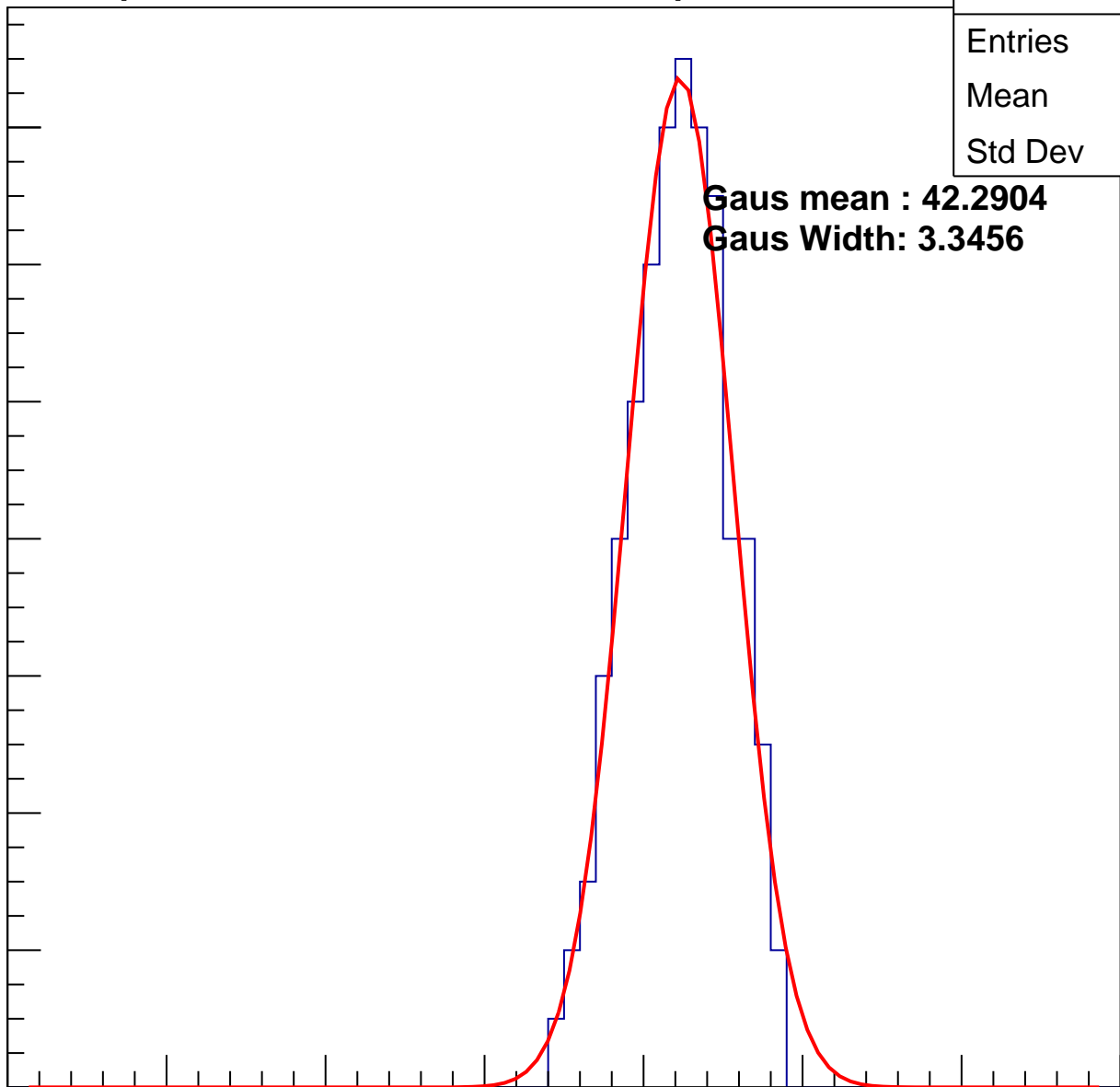
70

ampl

Entries	121
Mean	41.69
Std Dev	3.085

Gaus mean : 42.2904

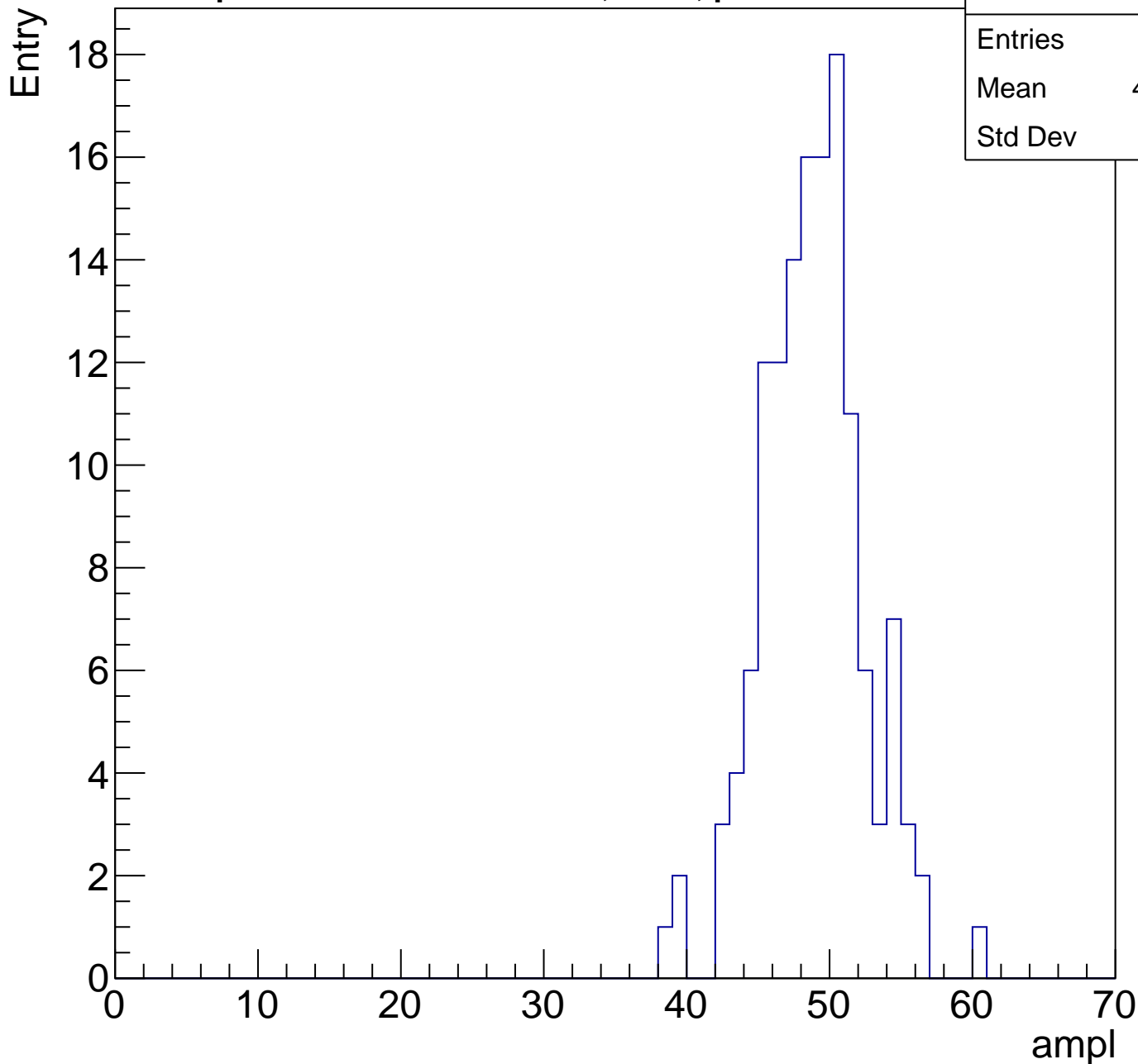
Gaus Width: 3.3456



B0L101S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	48.37
Std Dev	3.57



B0L101S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#1, port C1

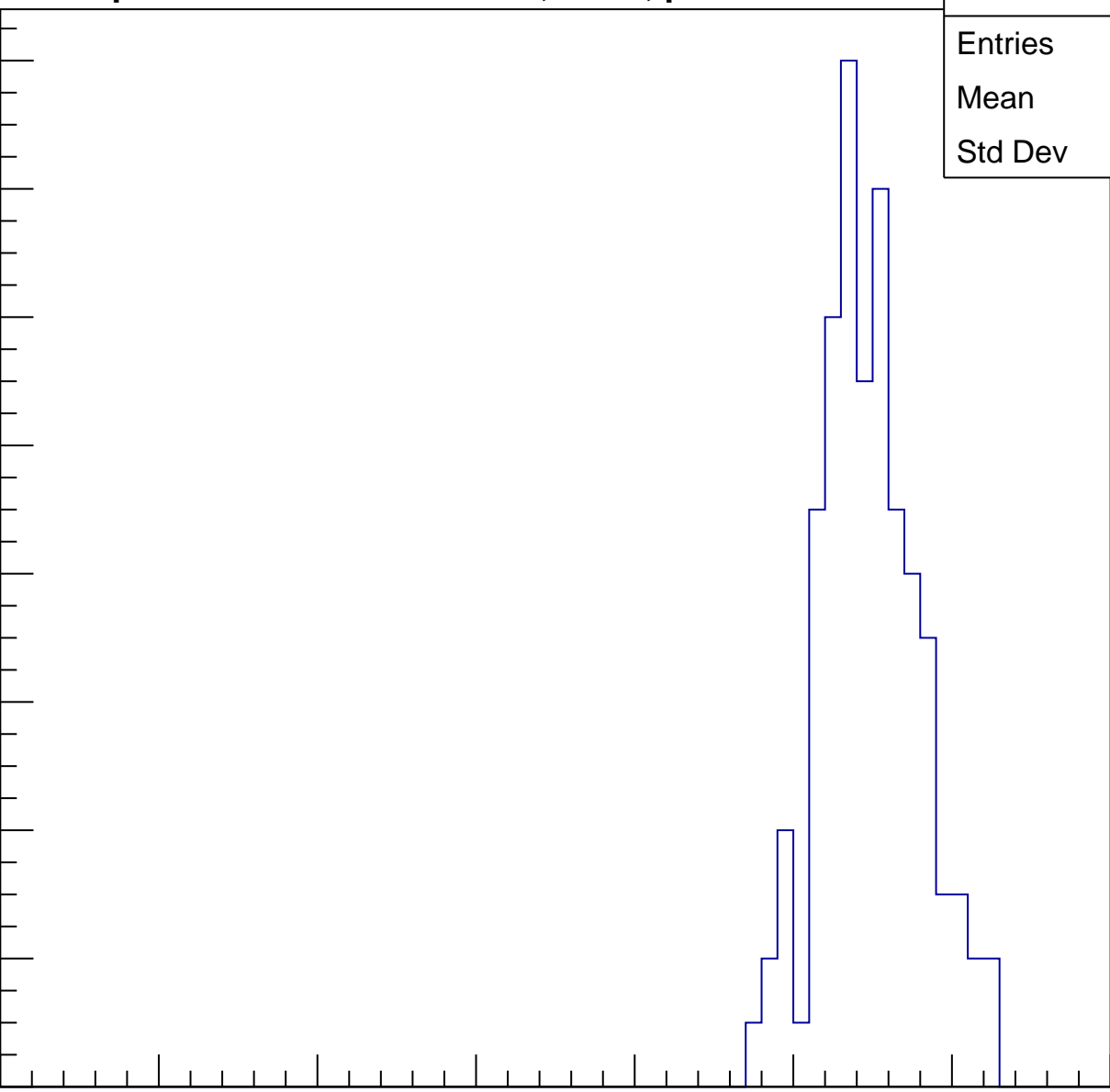
Entries	104
Mean	54.36
Std Dev	3.119

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch61, adc5

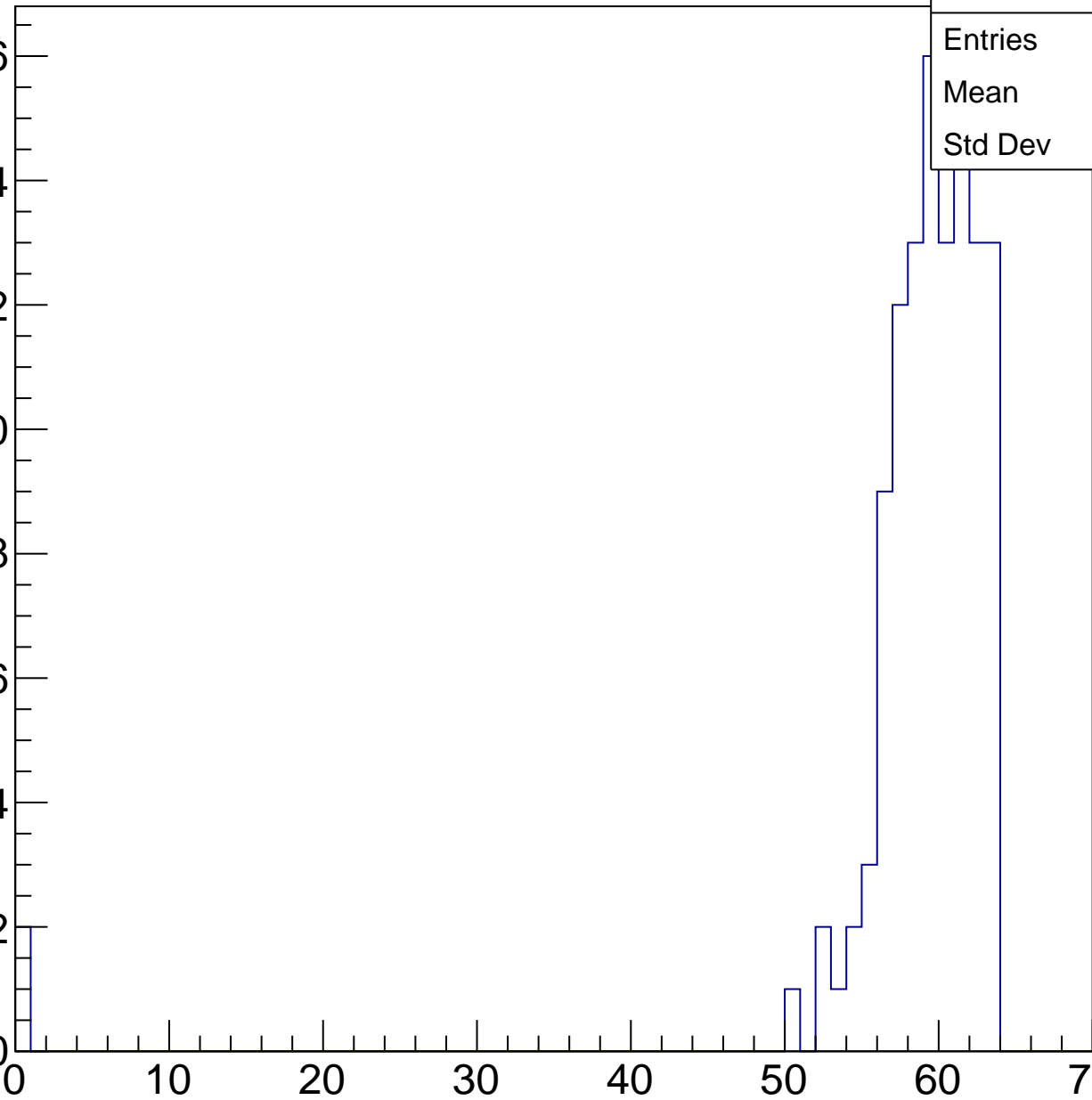
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	115
Mean	58.14
Std Dev	8.199

ampl

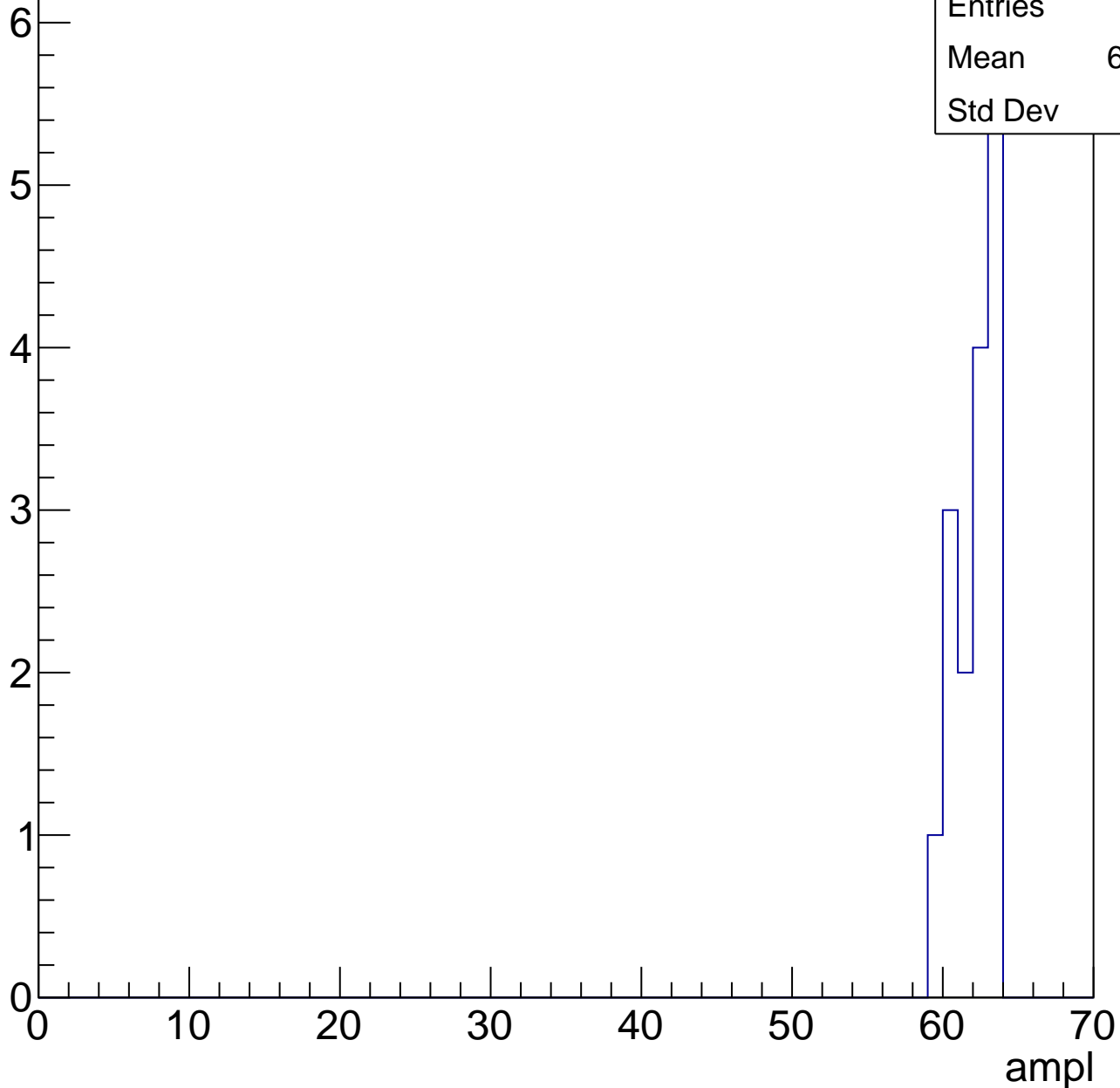


B0L101S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

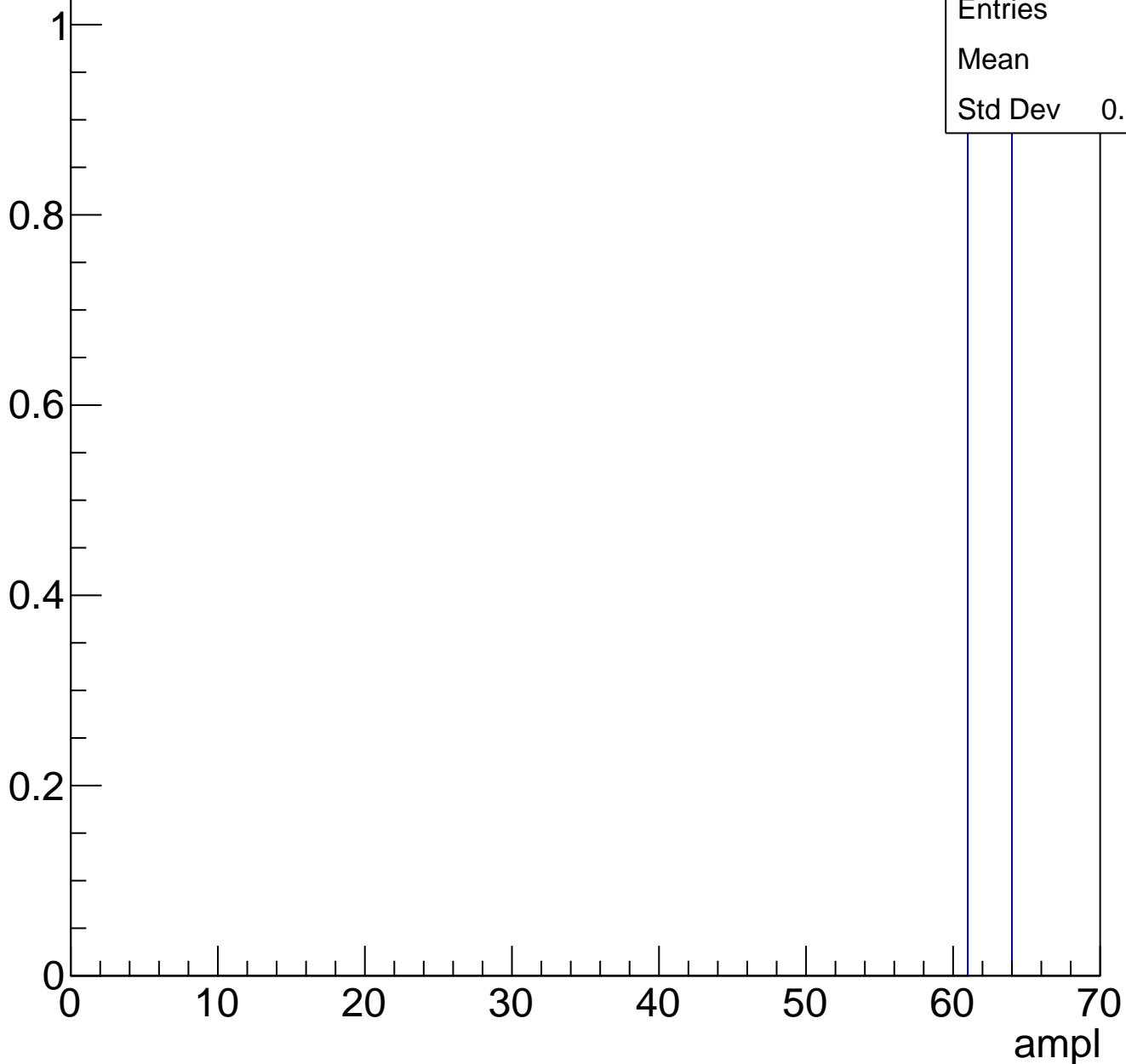
Entries	16
Mean	61.69
Std Dev	1.31



B0L101S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch62, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	26.96
Std Dev	4.974

Gaus mean : 27.7334

Gaus Width: 3.5632

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

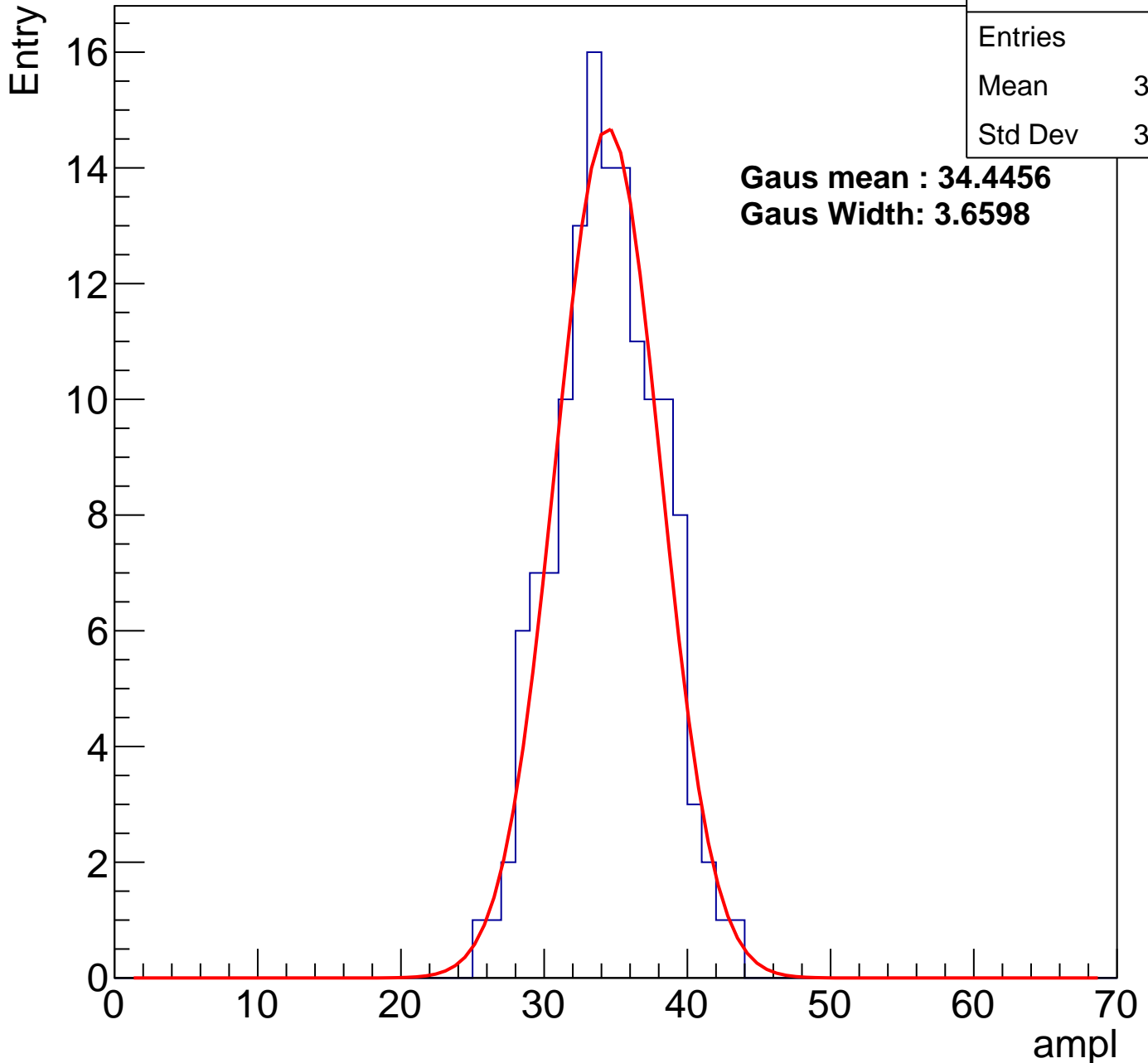
B0L101S, U15-ch62, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	33.95
Std Dev	3.587

Gaus mean : 34.4456

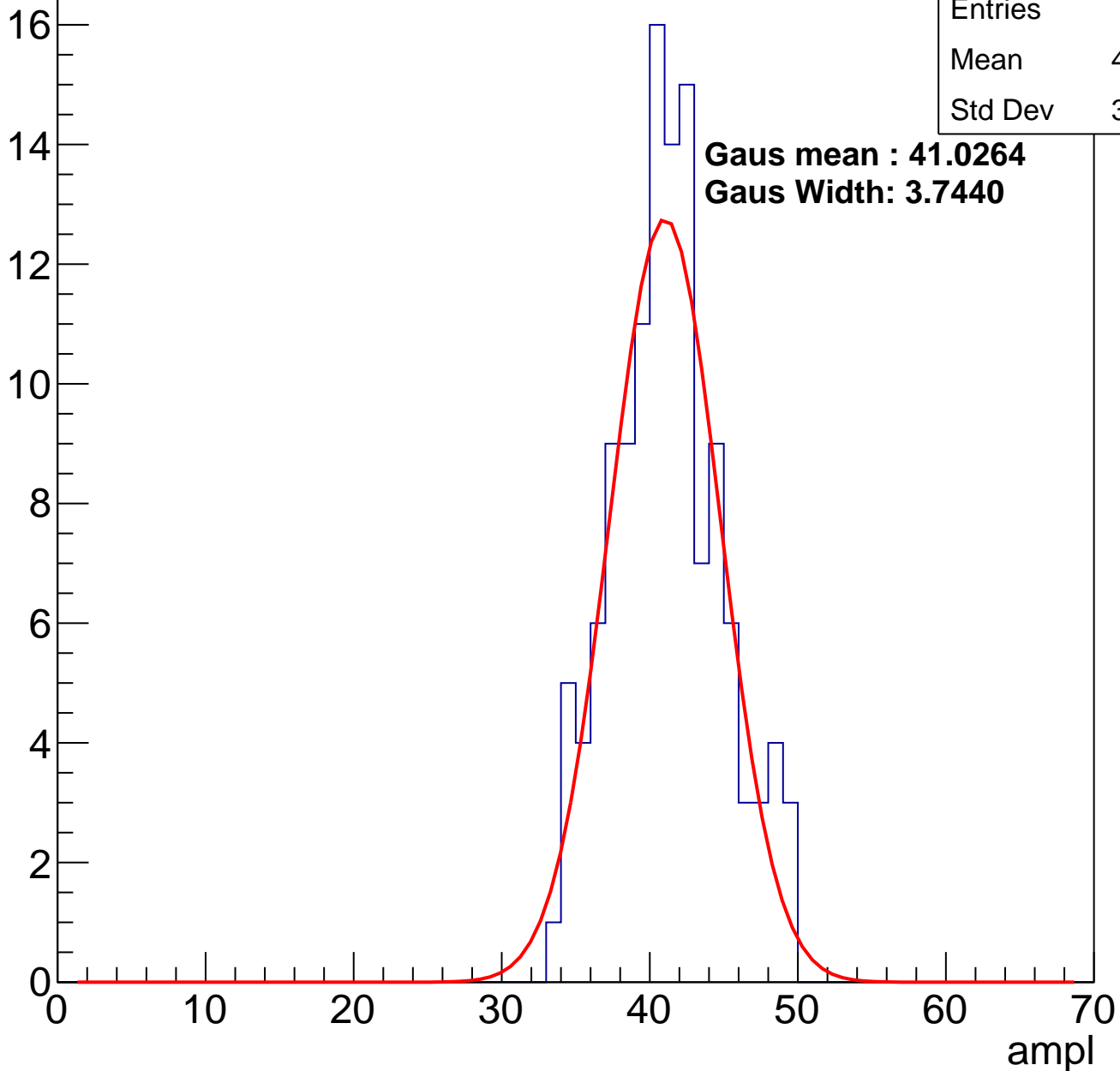
Gaus Width: 3.6598



B0L101S, U15-ch62, adc2

calib_packv5_042523_0143.root, FC#1, port C1

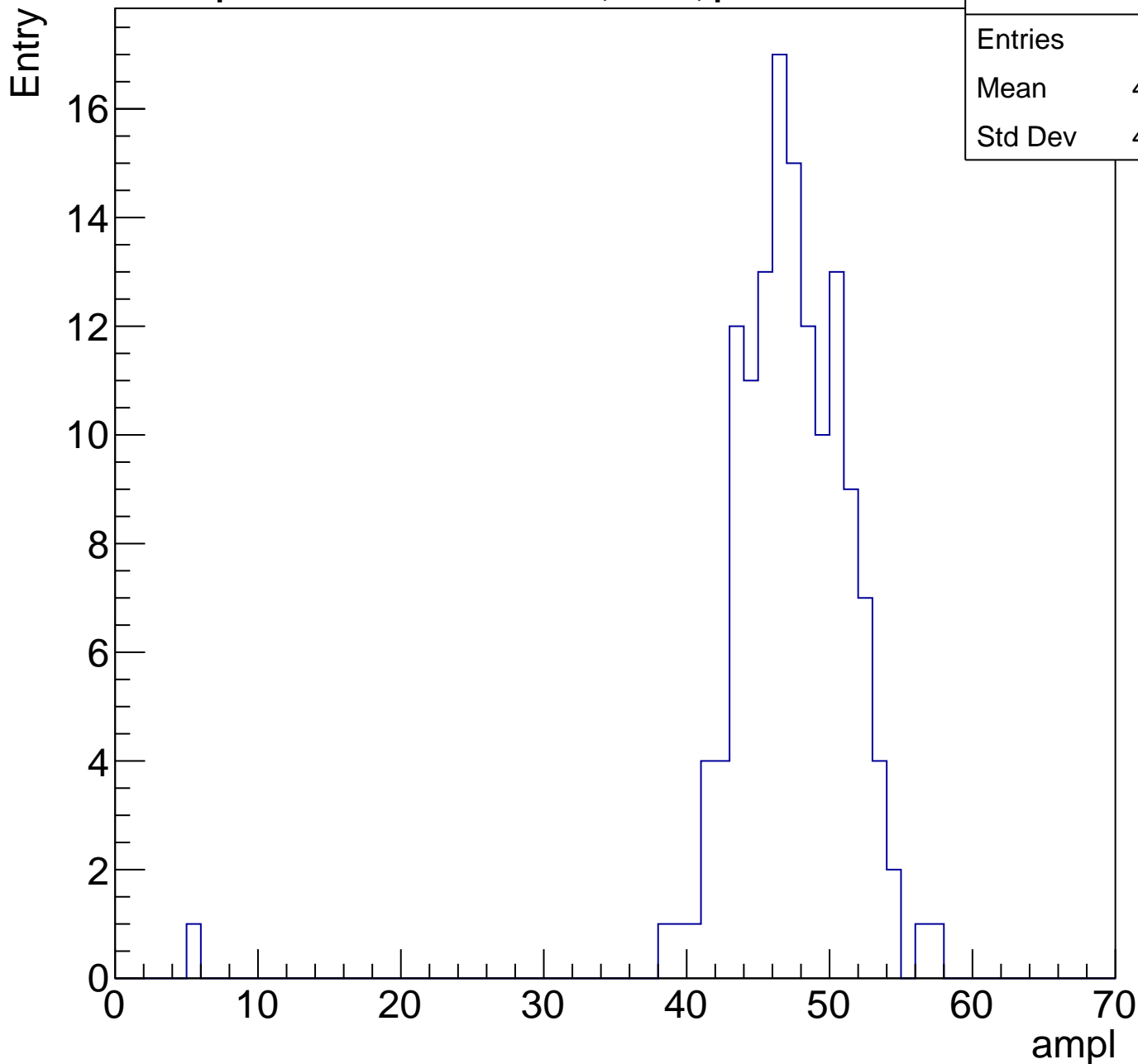
Entry



B0L101S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#1, port C1

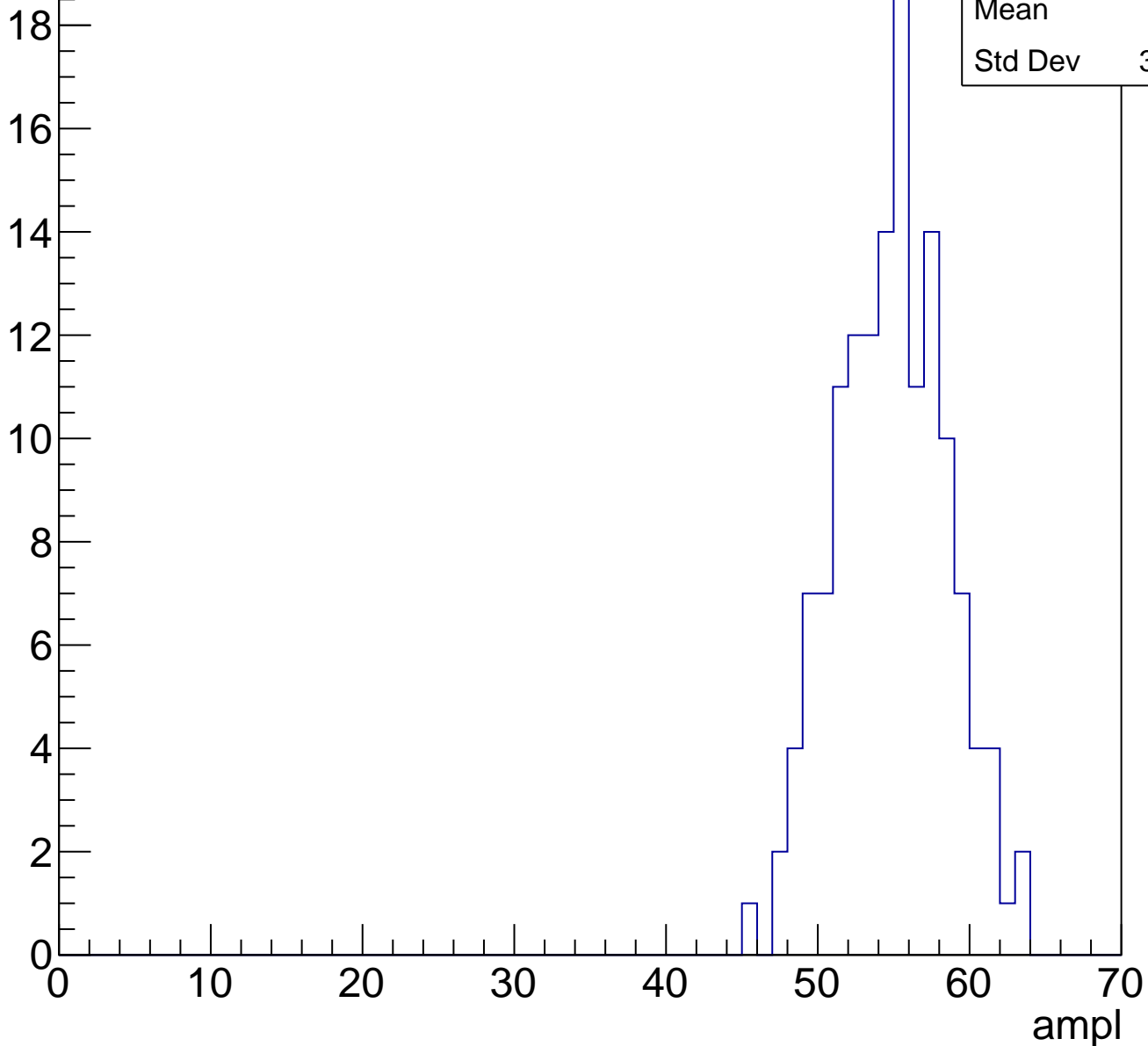
Entries	139
Mean	46.75
Std Dev	4.992



B0L101S, U15-ch62, adc4

calib_packv5_042523_0143.root, FC#1, port C1

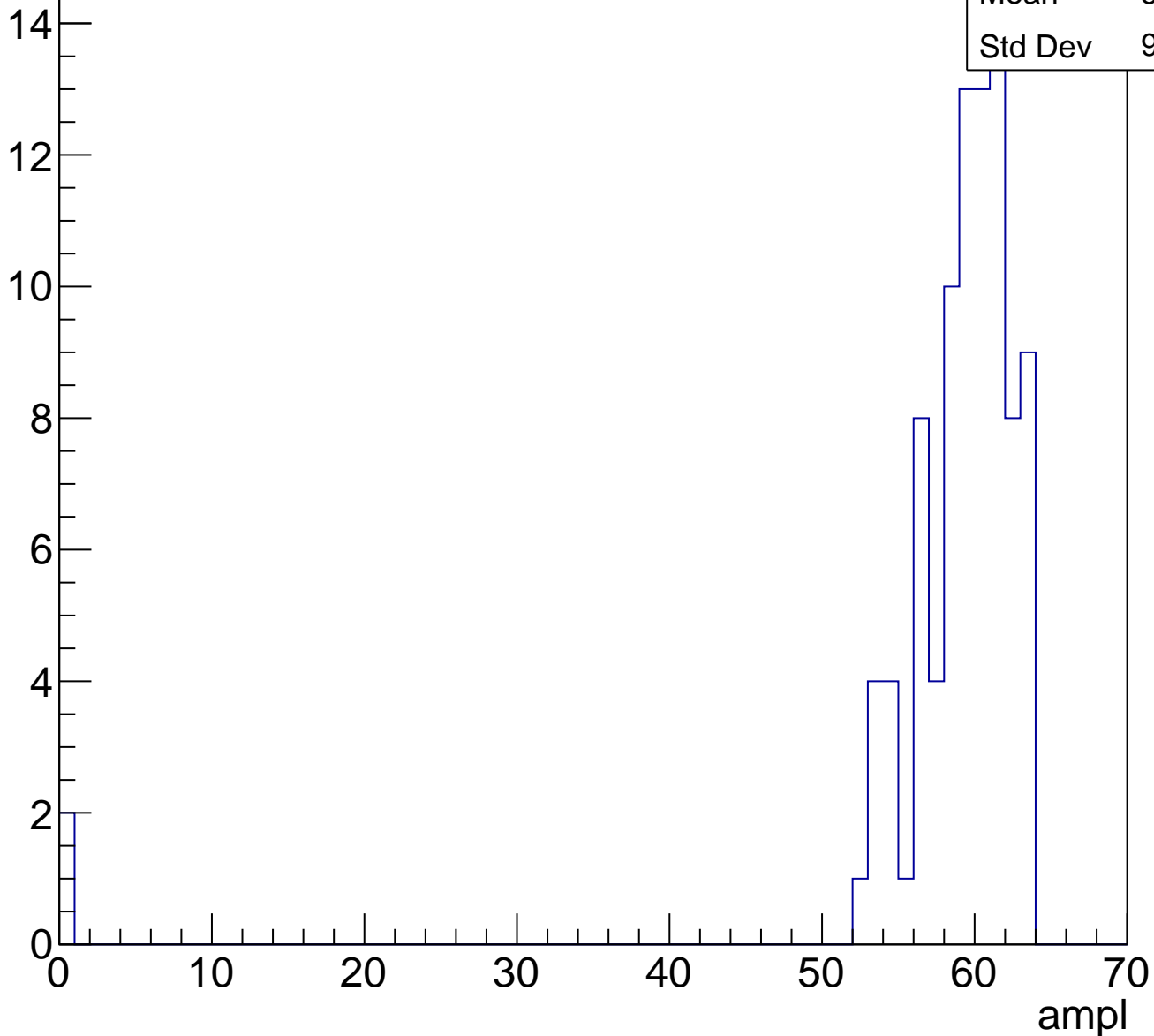
Entry



B0L101S, U15-ch62, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

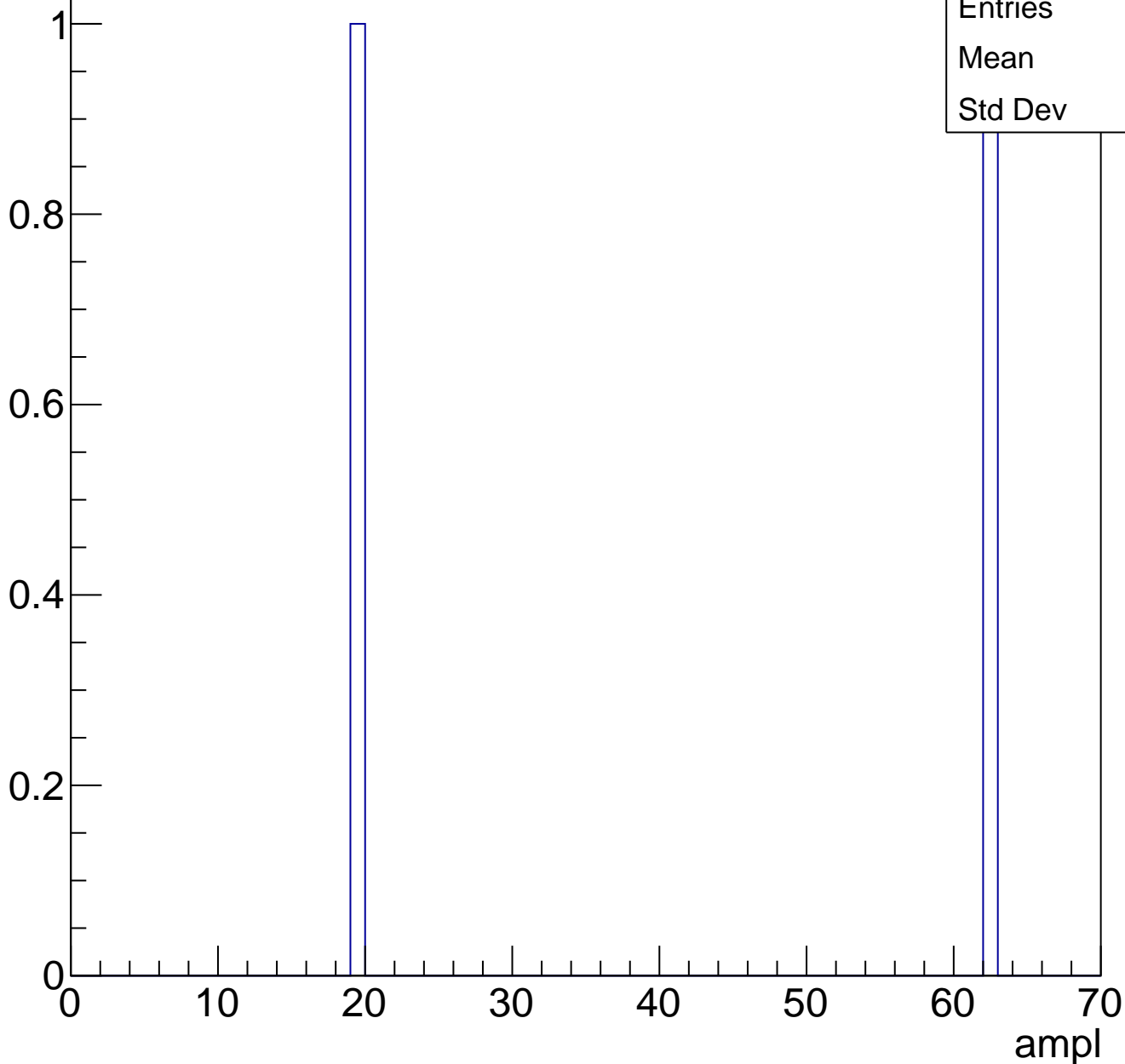
ampl

Entries	24
Mean	61.88
Std Dev	1.394

B0L101S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch63, adc0

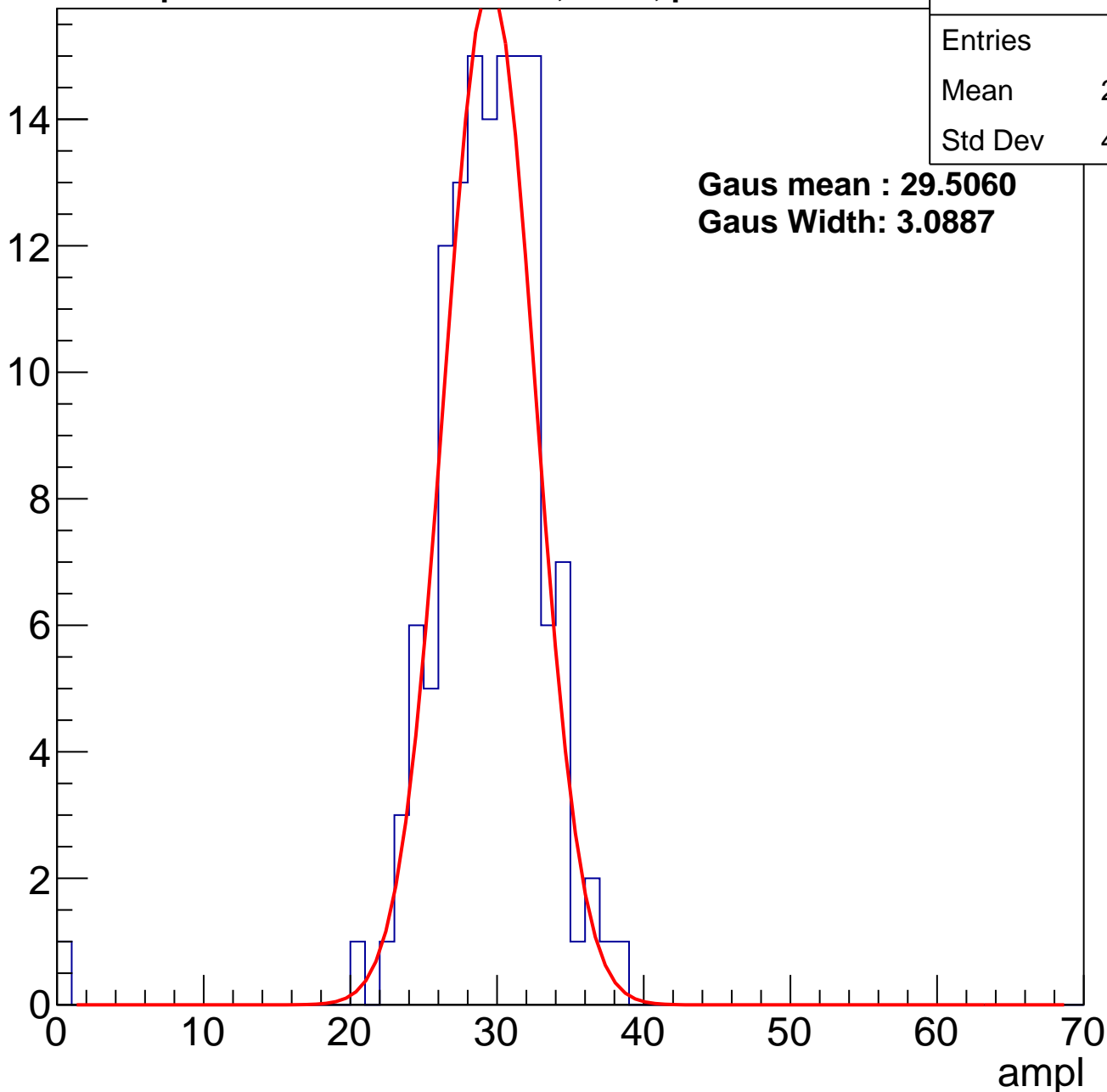
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	28.97
Std Dev	4.092

Gaus mean : 29.5060

Gaus Width: 3.0887

Entry



B0L101S, U15-ch63, adc1

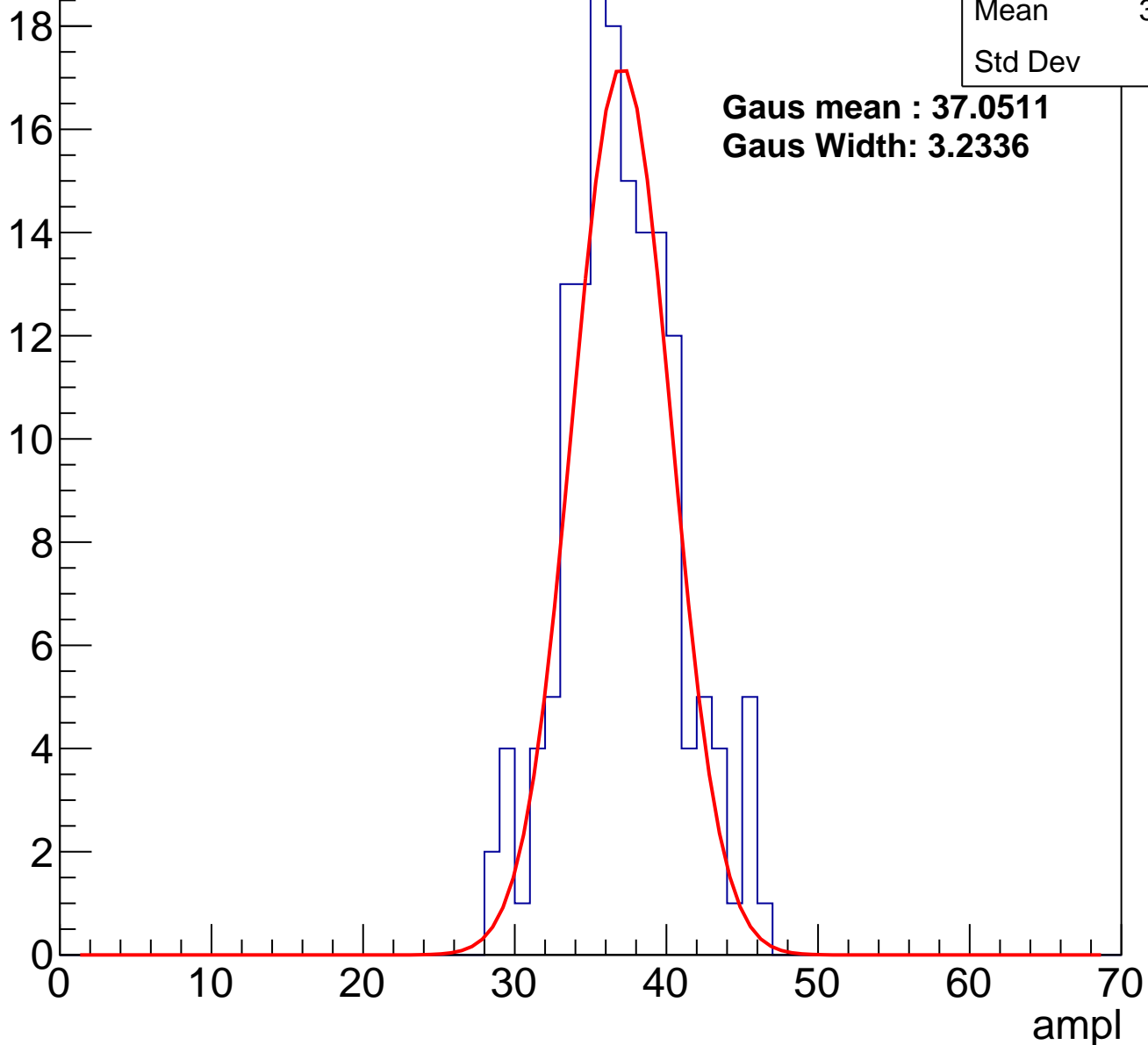
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.65
Std Dev	3.69

Gaus mean : 37.0511

Gaus Width: 3.2336

Entry



B0L101S, U15-ch63, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

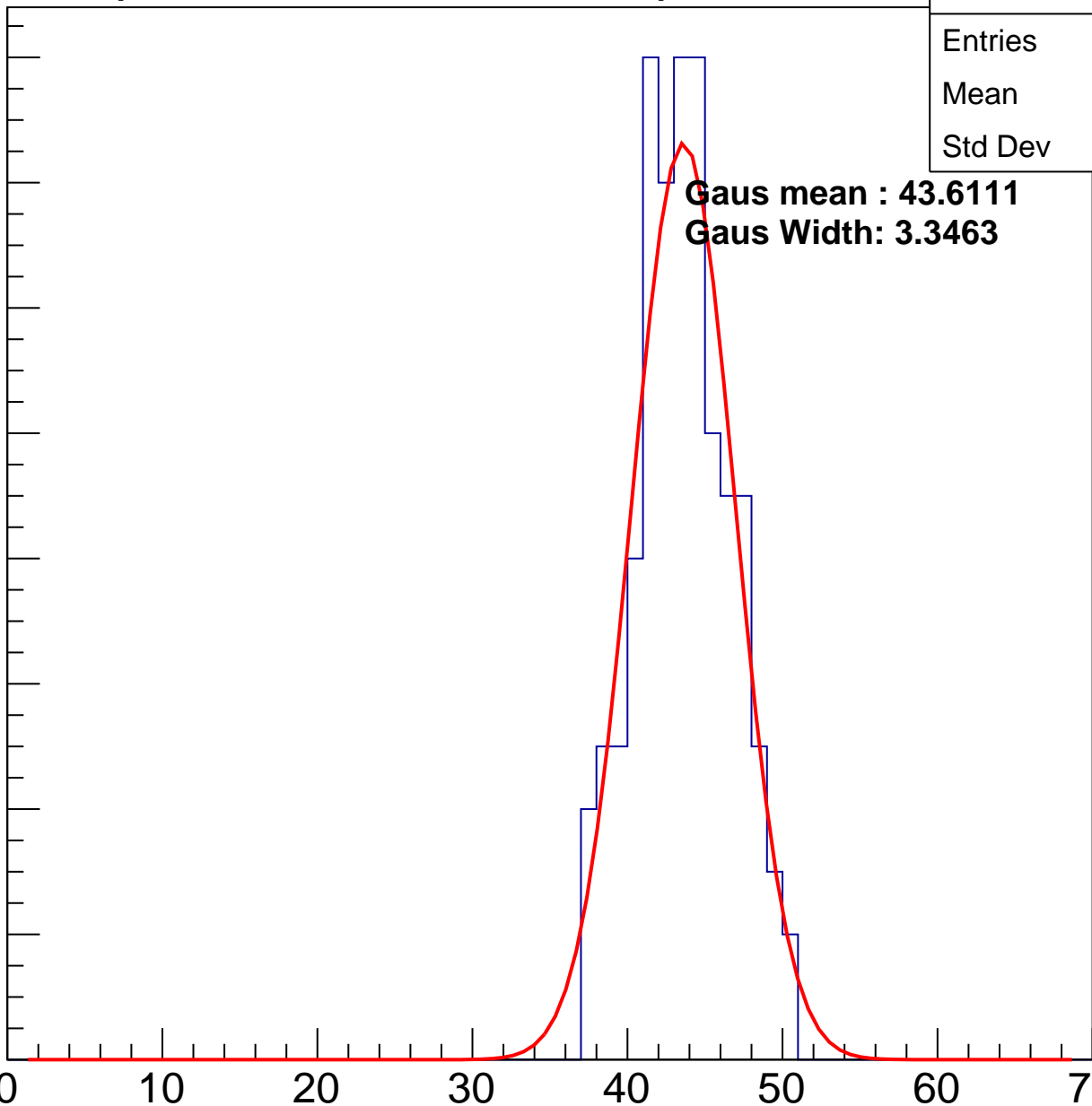
16
14
12
10
8
6
4
2
0

Entries	122
Mean	43.14
Std Dev	3.014

Gaus mean : 43.6111

Gaus Width: 3.3463

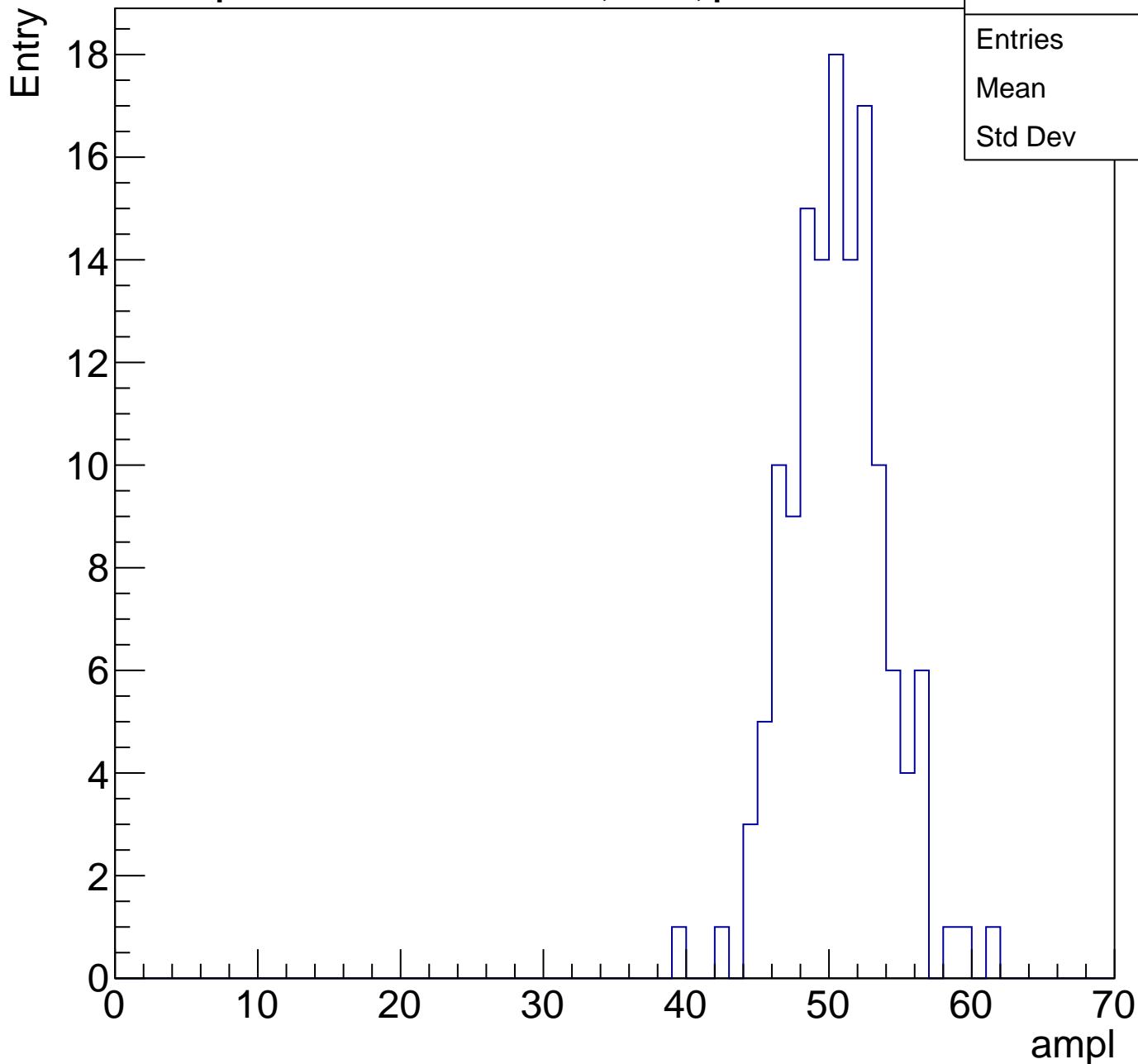
ampl



B0L101S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	50.1
Std Dev	3.41



B0L101S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	99
Mean	55.68
Std Dev	3.222

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

ampl

B0L101S, U15-ch63, adc5

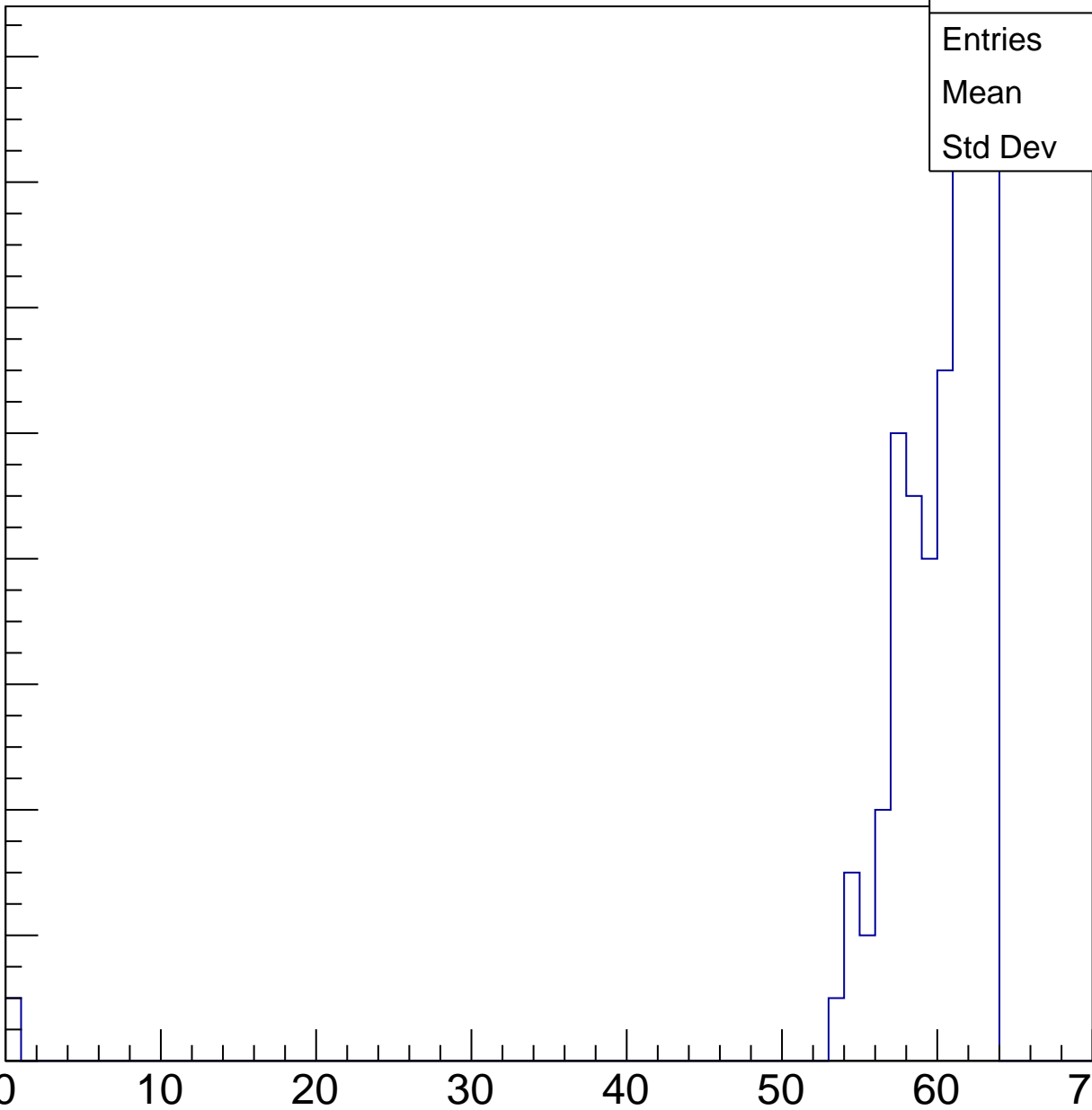
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.23
Std Dev	6.585

ampl

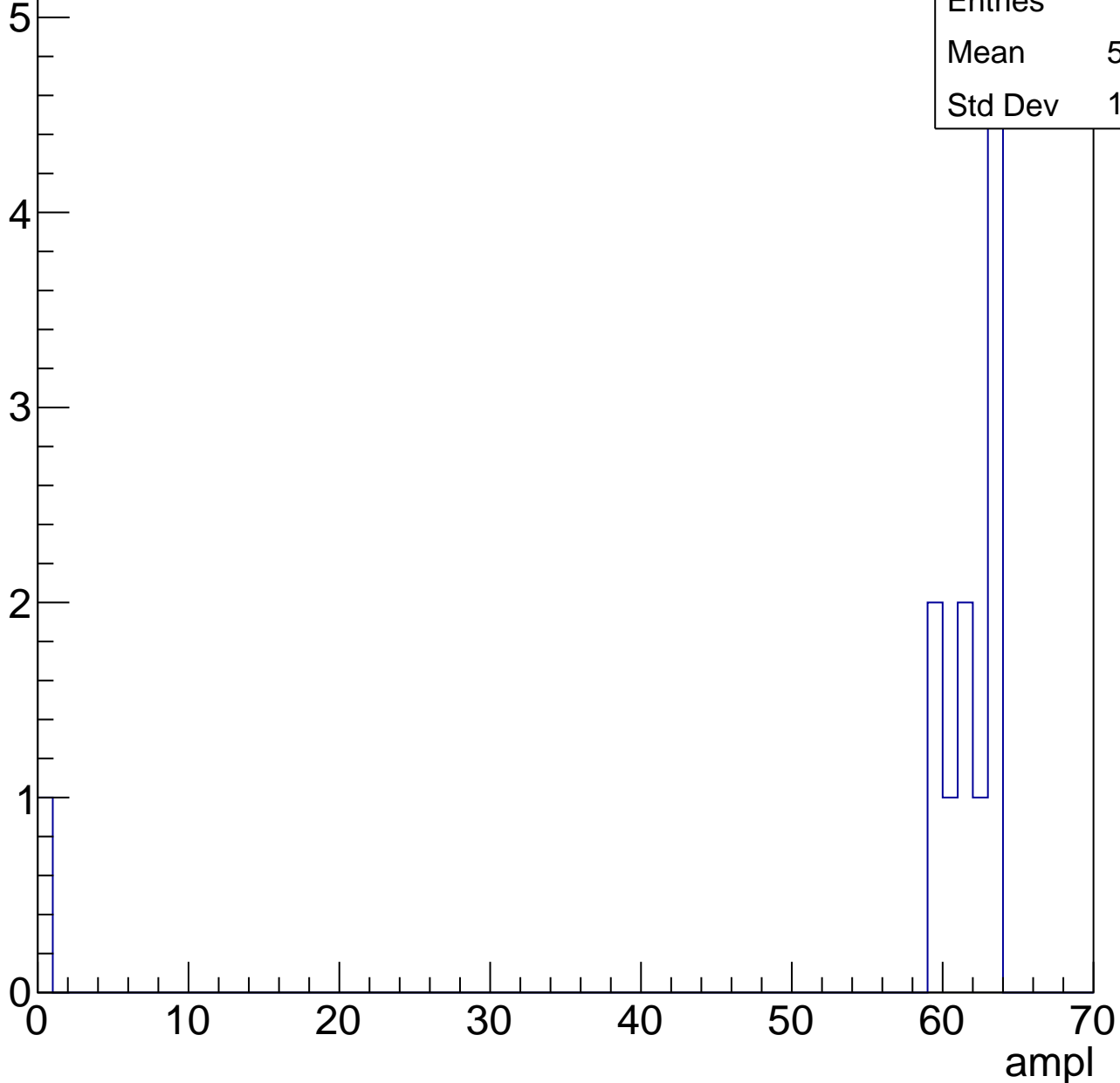


B0L101S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	12
Mean	56.42
Std Dev	17.08



B0L101S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch64, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	27.28
Std Dev	3.29

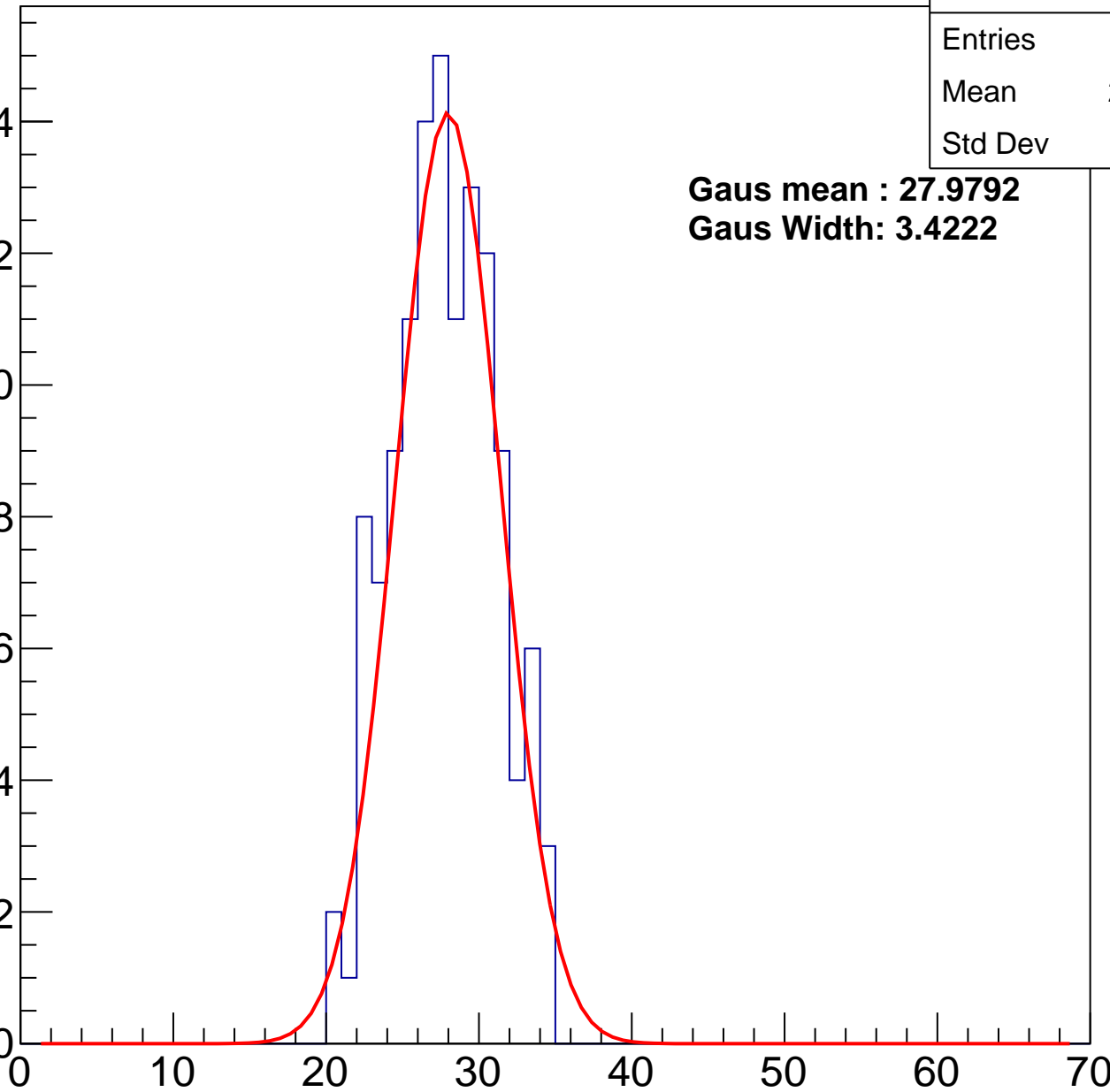
Gaus mean : 27.9792

Gaus Width: 3.4222

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch64, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	34.27
Std Dev	3.813

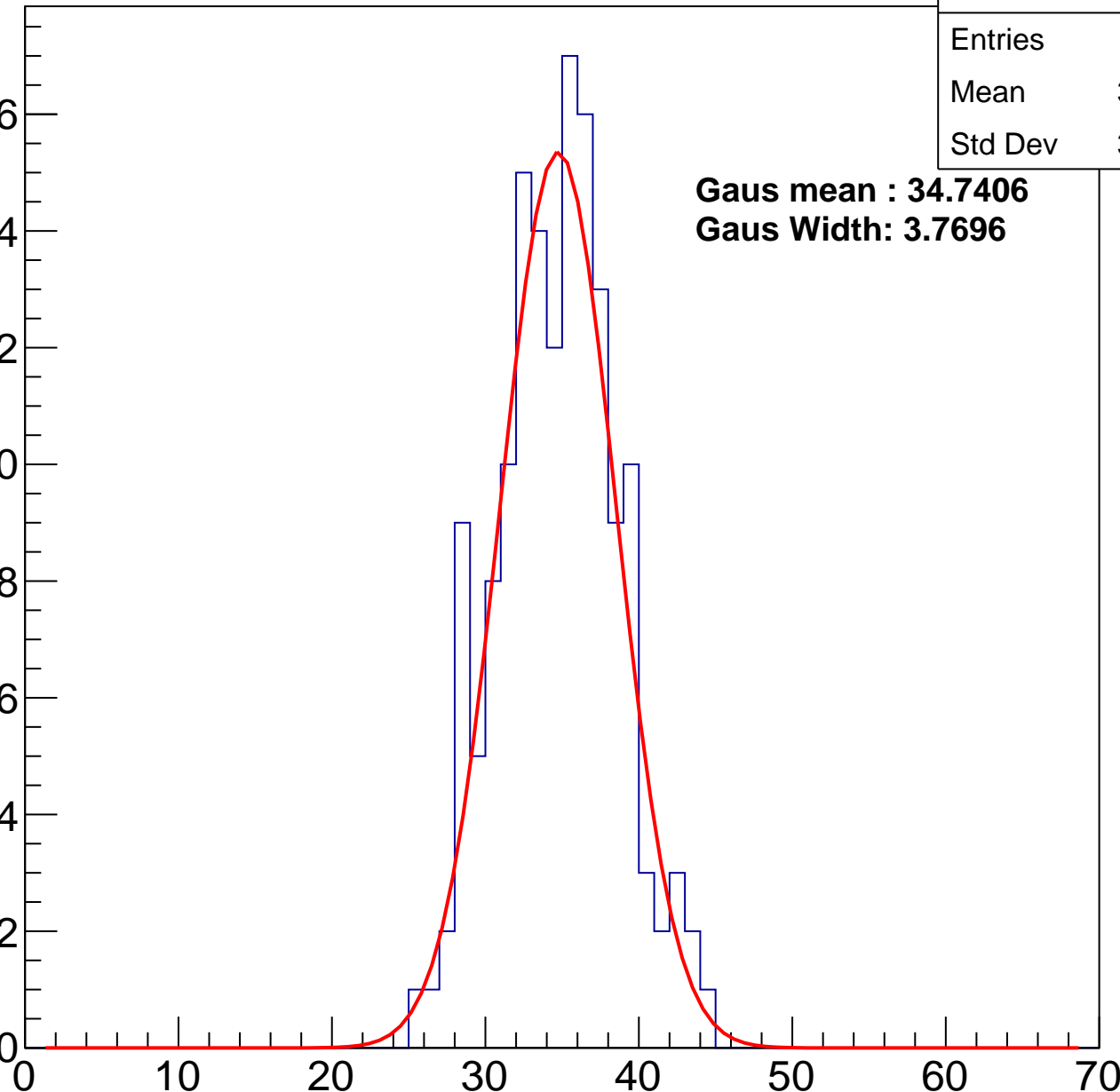
Gaus mean : 34.7406

Gaus Width: 3.7696

Entry

16
14
12
10
8
6
4
2
0

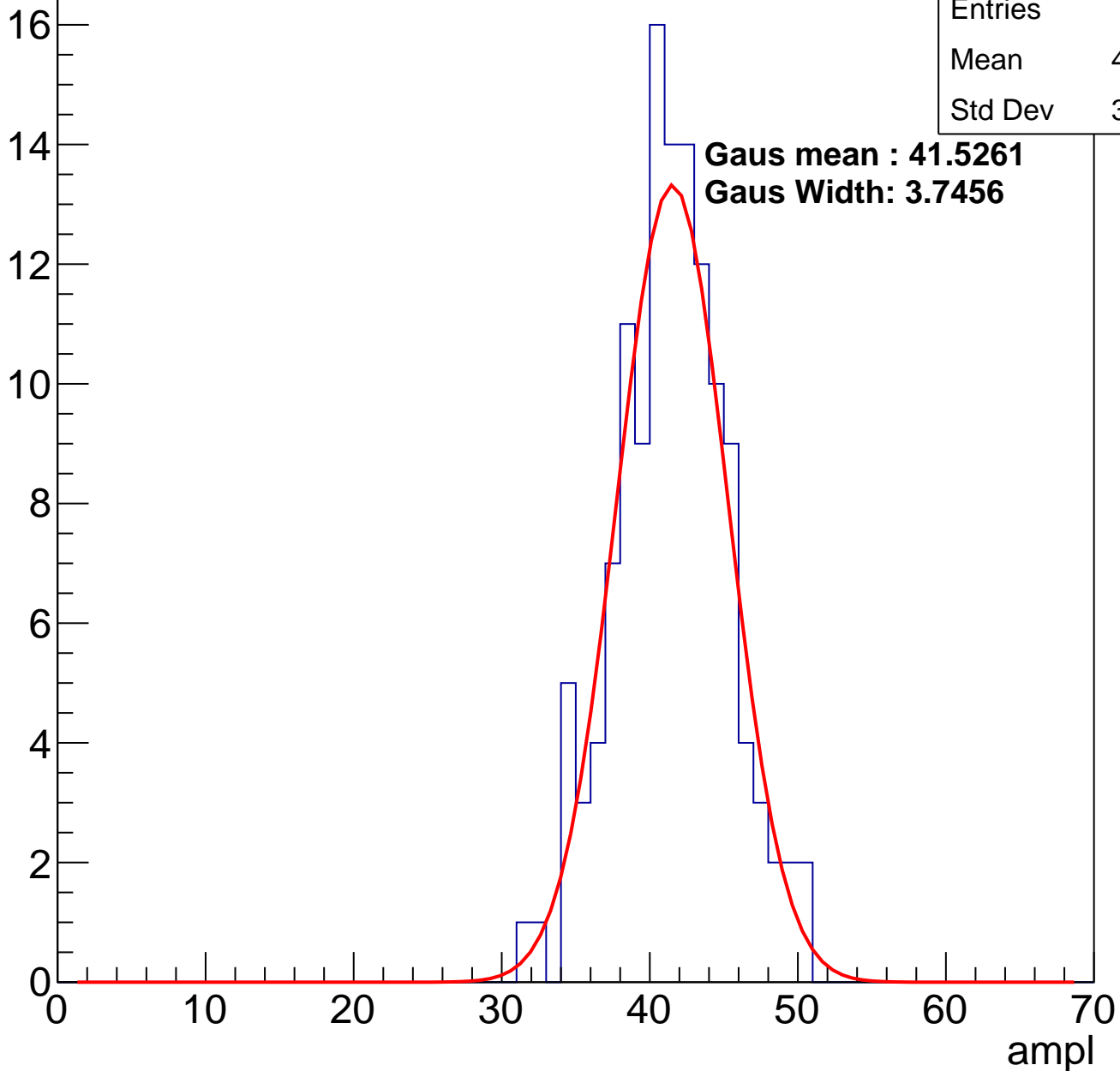
ampl



B0L101S, U15-ch64, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch64, adc3

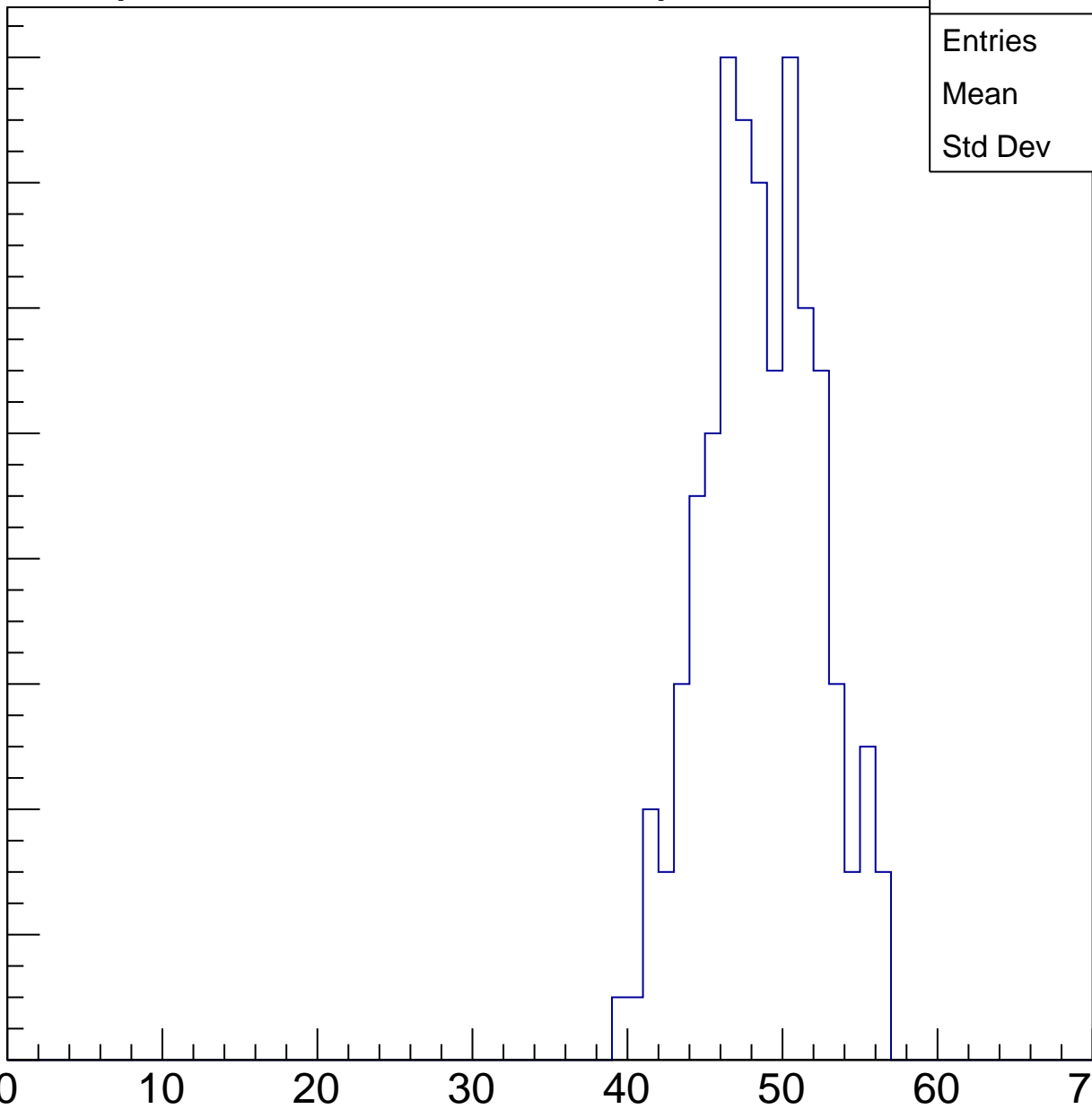
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	146
Mean	48.16
Std Dev	3.666

ampl



B0L101S, U15-ch64, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

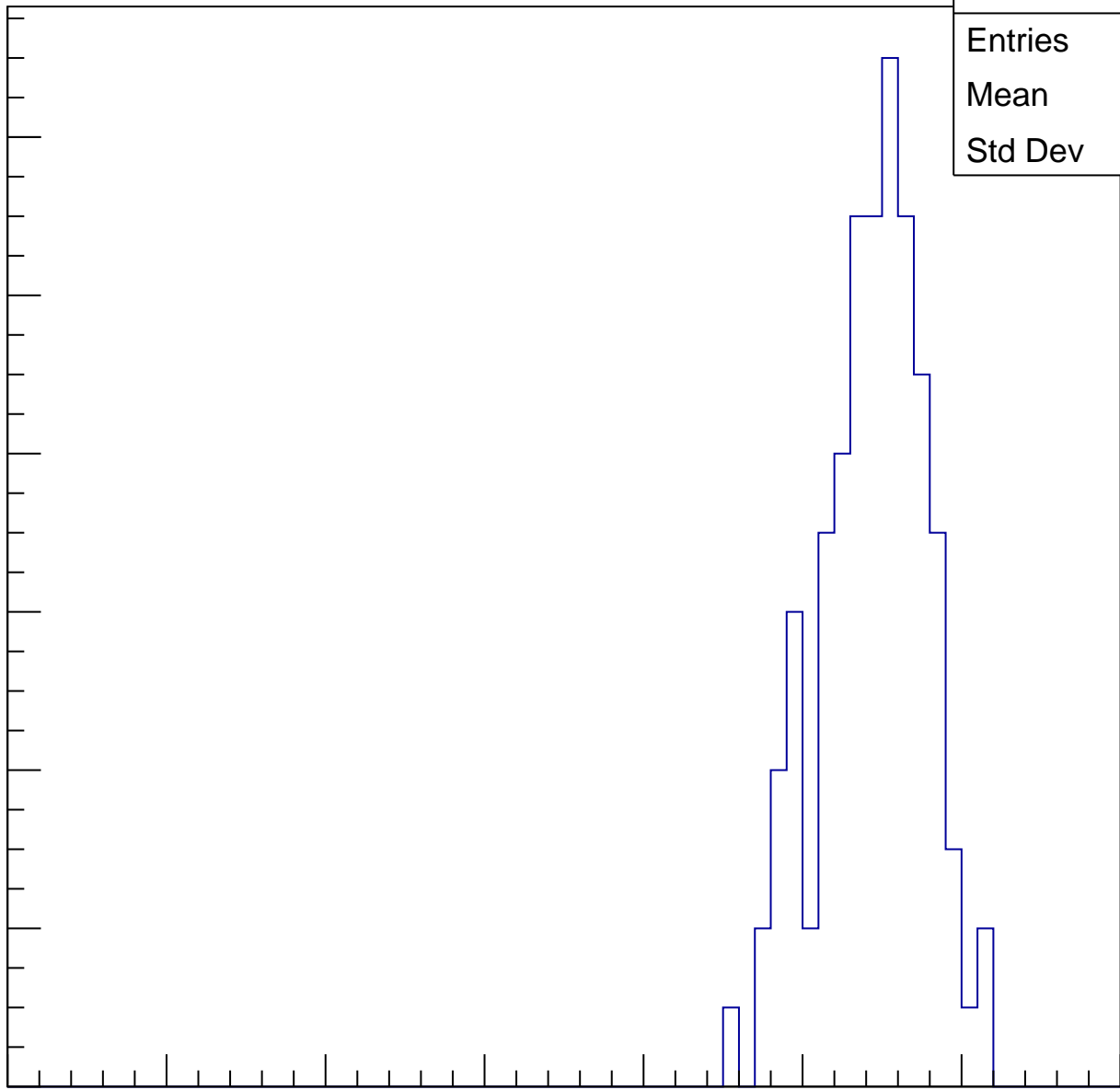
2

0

Entries	98
Mean	53.92
Std Dev	3.303

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch64, adc5

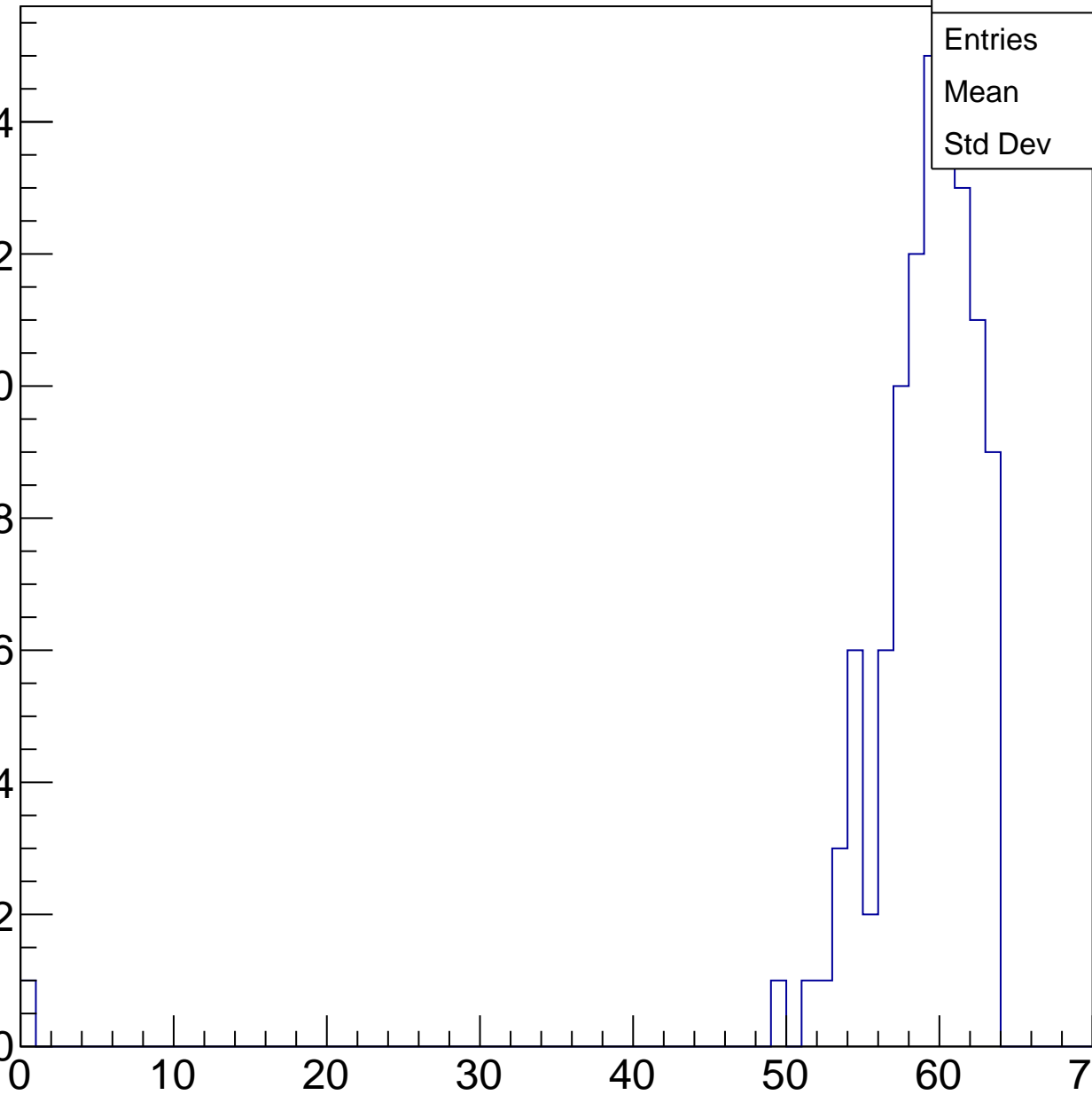
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	105
Mean	58.23
Std Dev	6.427

ampl



B0L101S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	35
Mean	59.69
Std Dev	10.35



B0L101S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



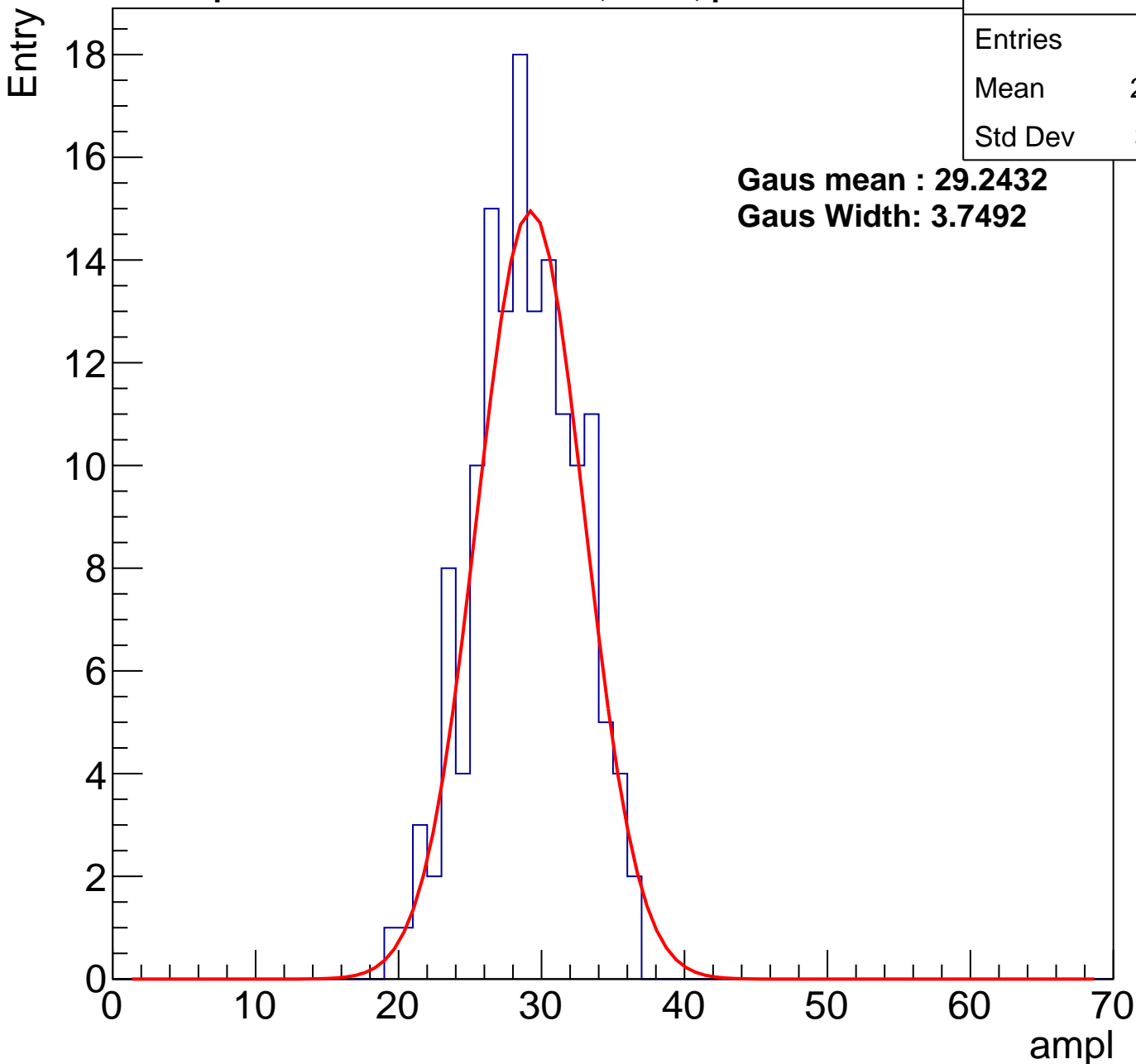
B0L101S, U15-ch65, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.44
Std Dev	3.591

Gaus mean : 29.2432

Gaus Width: 3.7492



B0L101S, U15-ch65, adc1

calib_packv5_042523_0143.root, FC#1, port C1

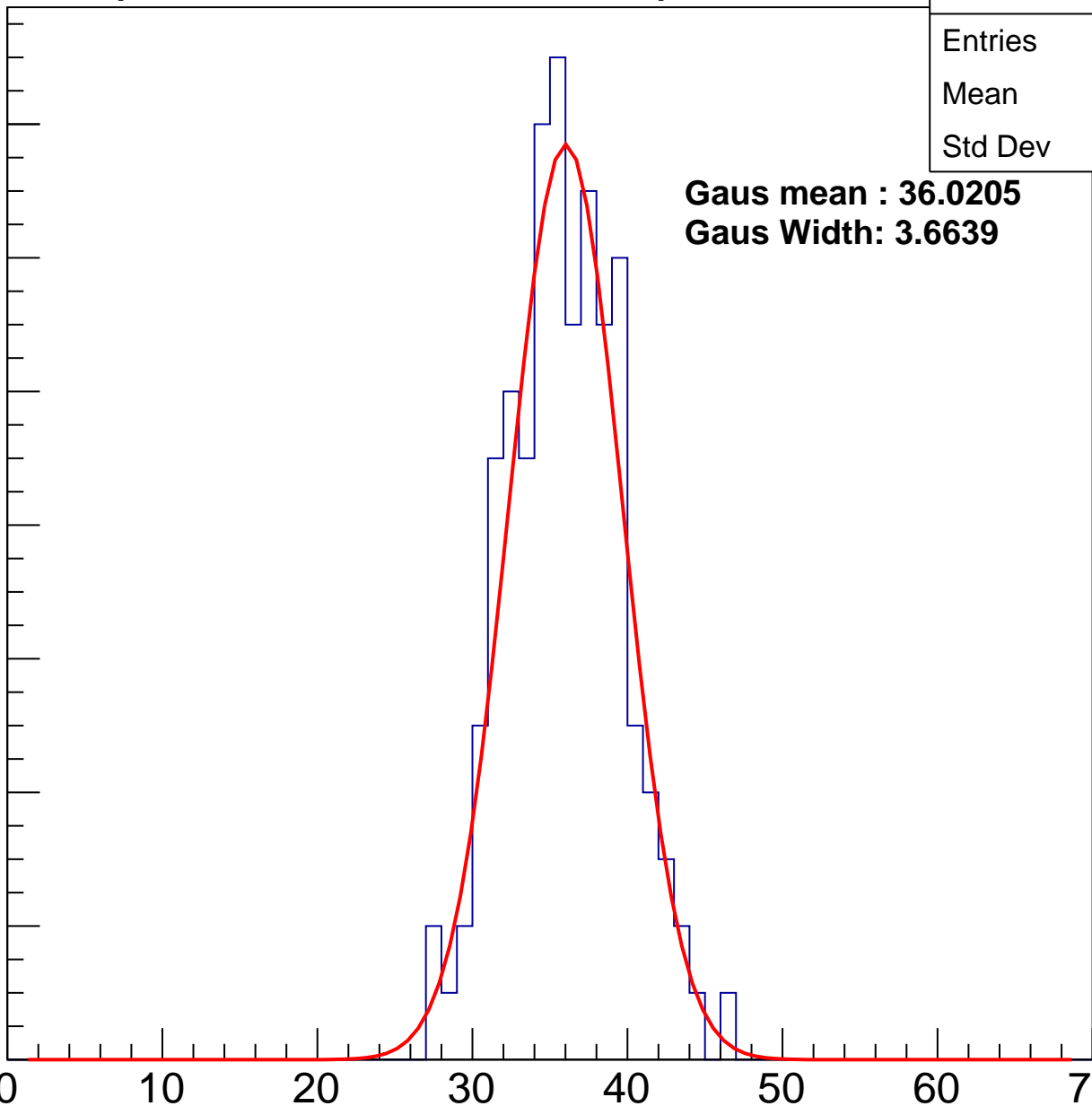
Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	35.51
Std Dev	3.642

Gaus mean : 36.0205
Gaus Width: 3.6639

ampl



B0L101S, U15-ch65, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

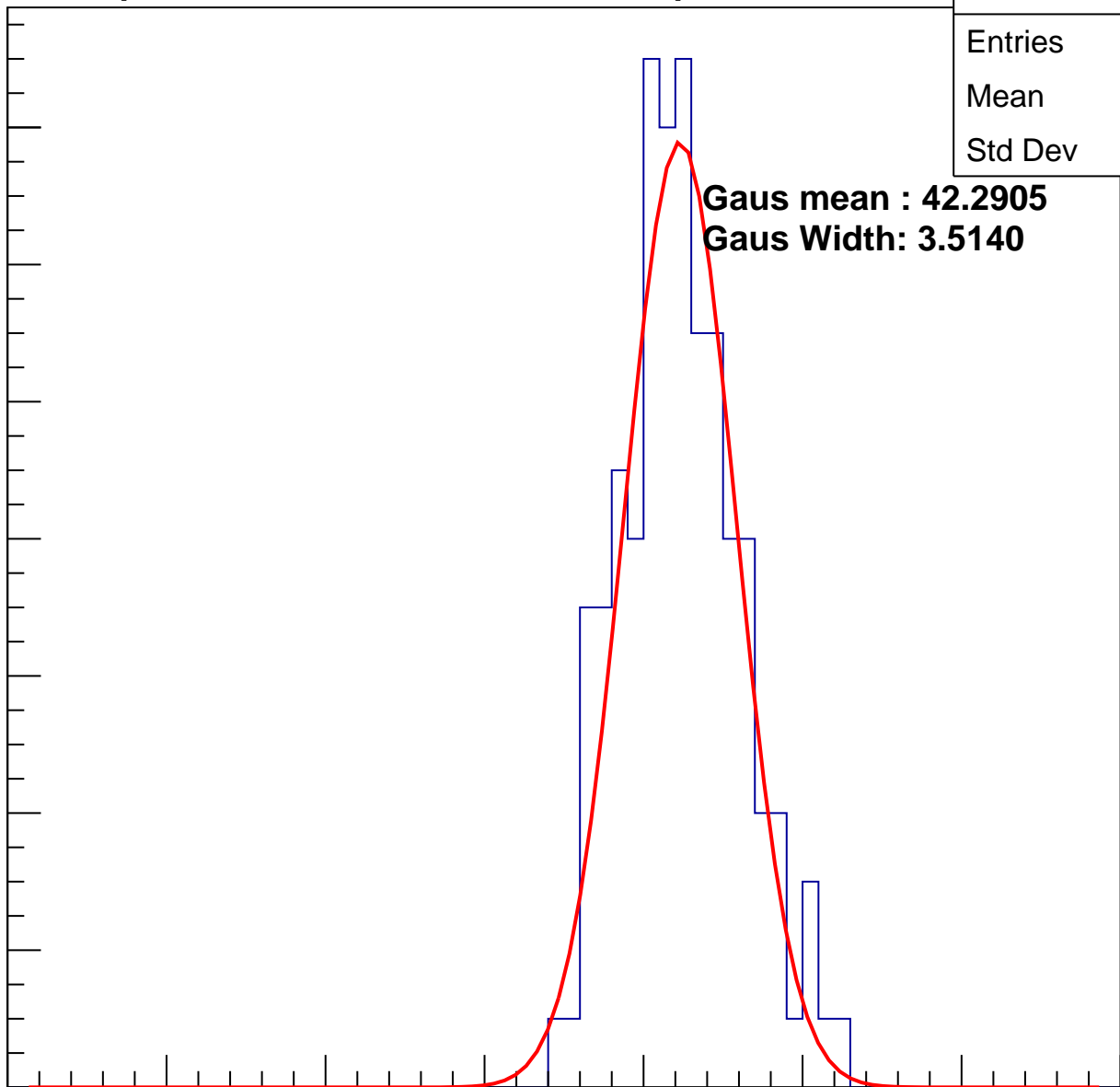
70

ampl

Entries	129
Mean	41.9
Std Dev	3.683

Gaus mean : 42.2905

Gaus Width: 3.5140

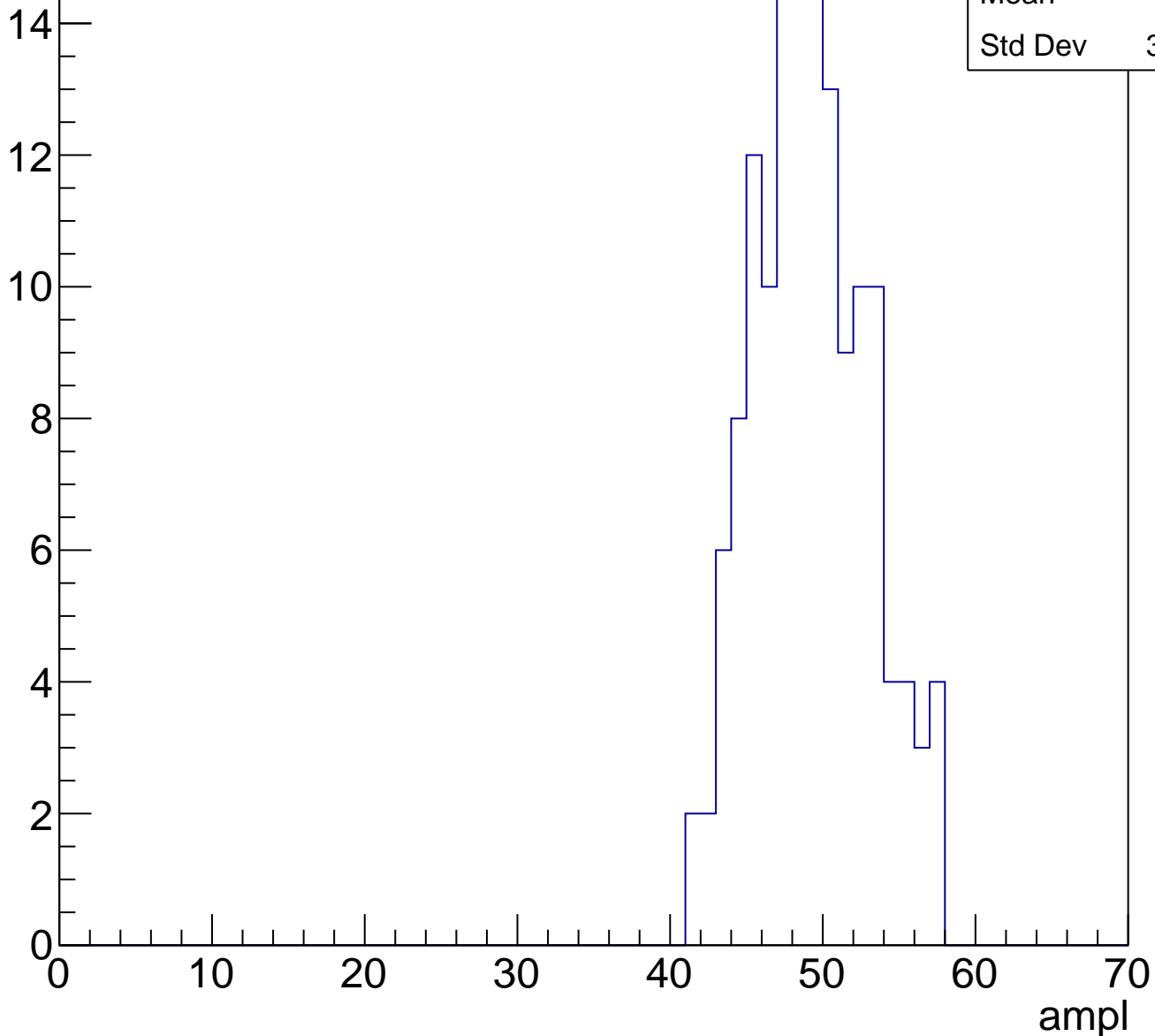


B0L101S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	48.78
Std Dev	3.695

Entry



B0L101S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#1, port C1

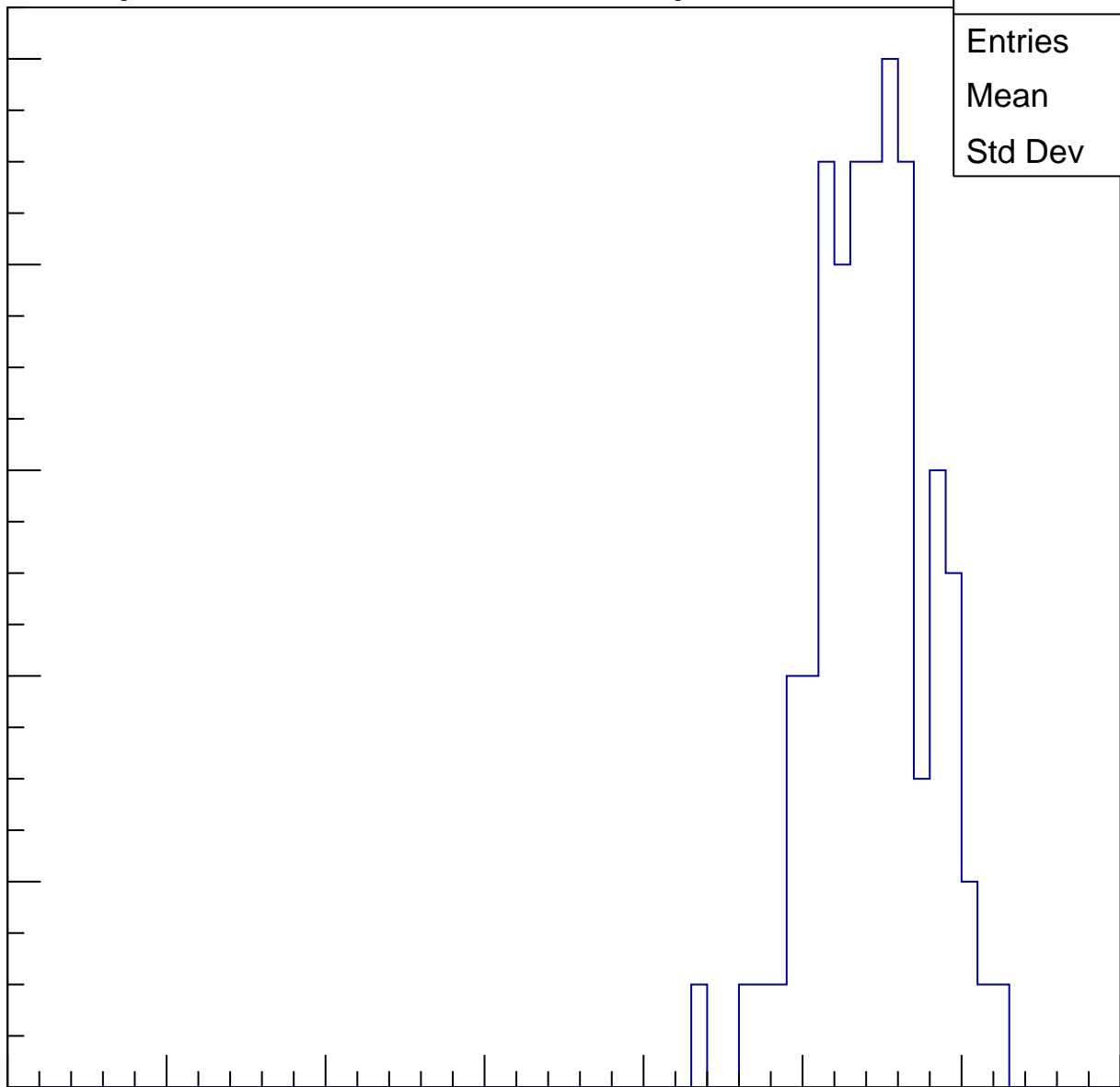
Entries	84
Mean	53.92
Std Dev	3.509

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch65, adc5

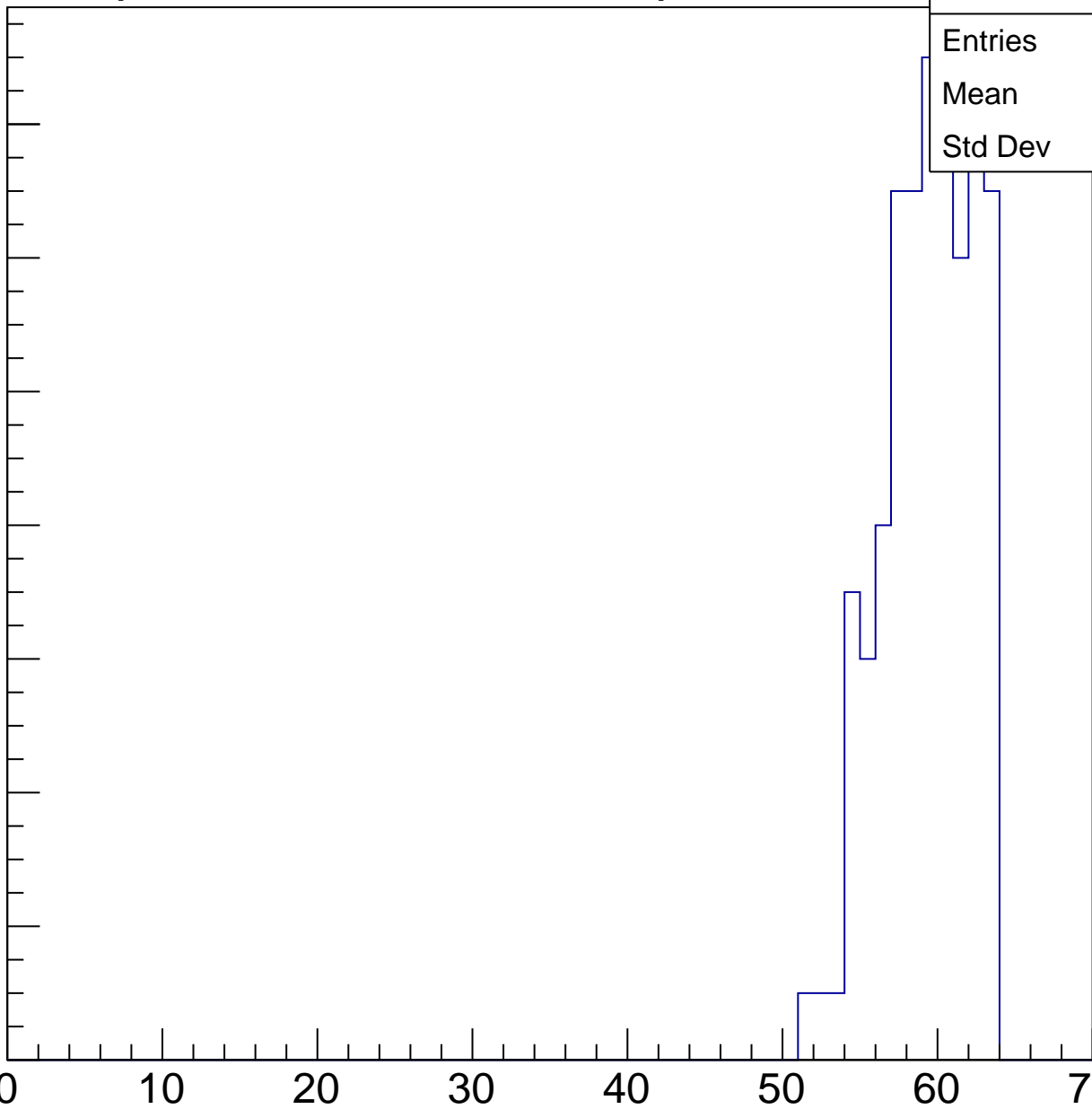
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	119
Mean	58.92
Std Dev	2.83

ampl

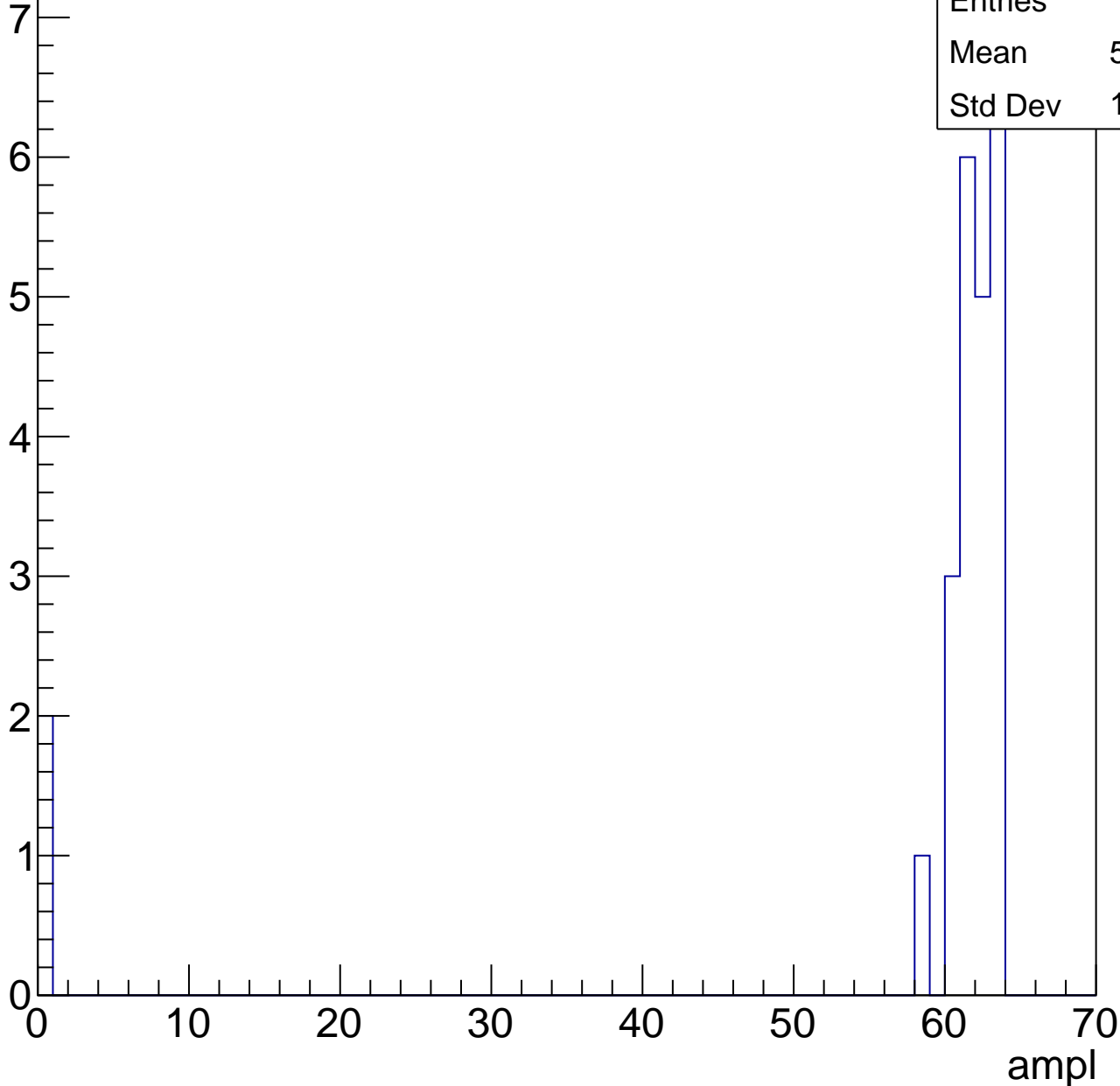


B0L101S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	56.46
Std Dev	17.07



B0L101S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch66, adc0

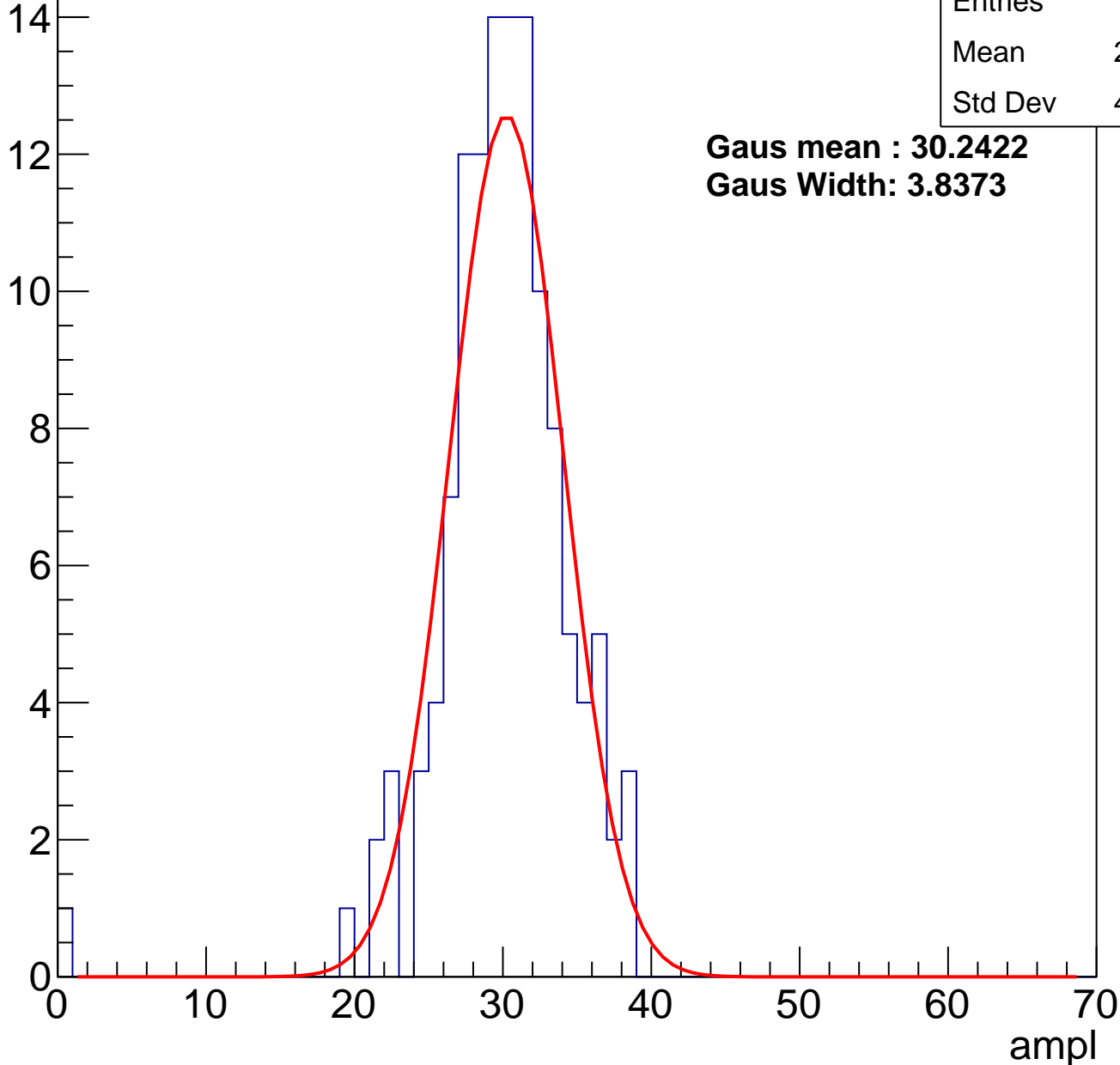
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	29.54
Std Dev	4.592

Gaus mean : 30.2422

Gaus Width: 3.8373

Entry

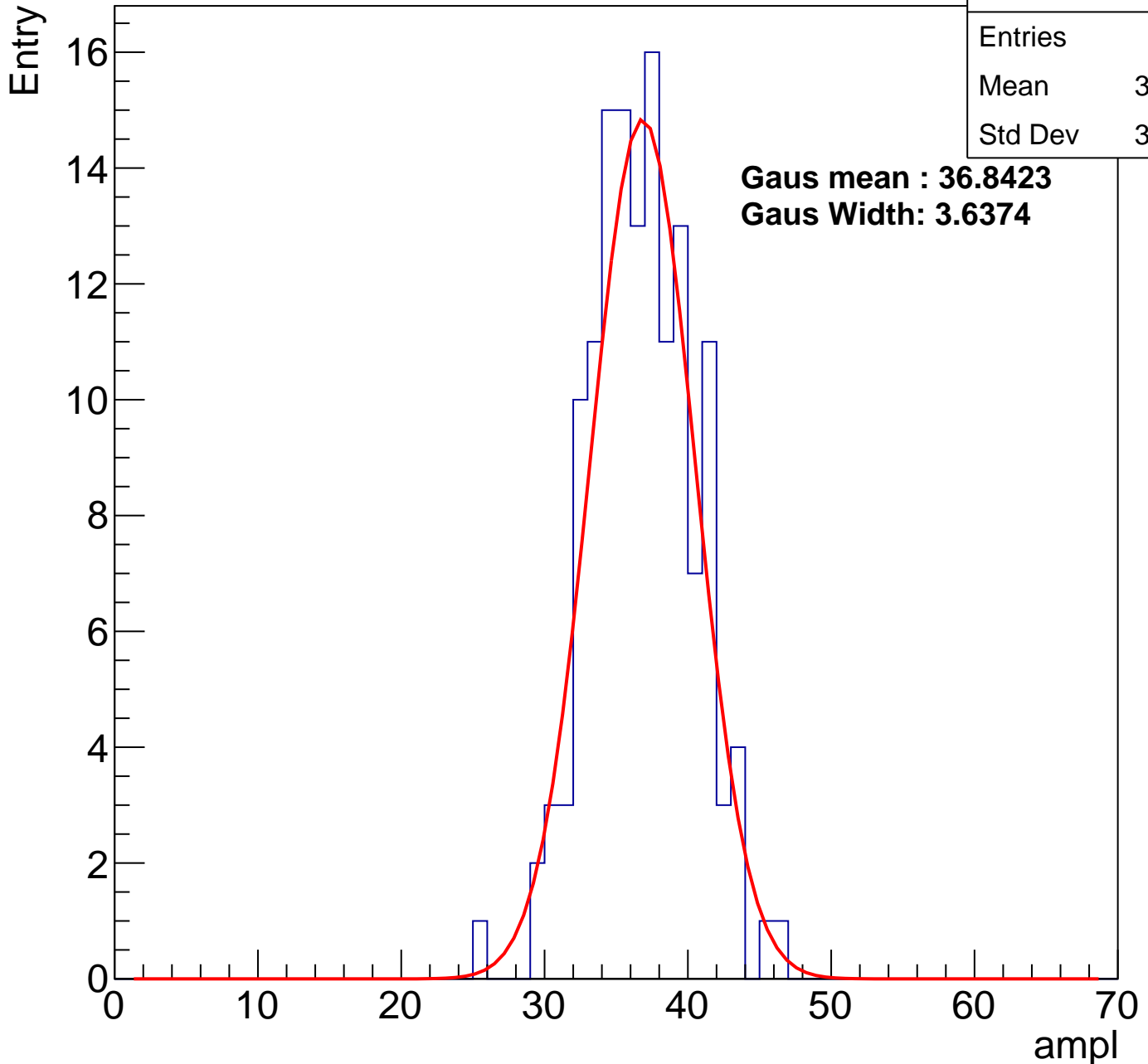


B0L101S, U15-ch66, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	36.35
Std Dev	3.554

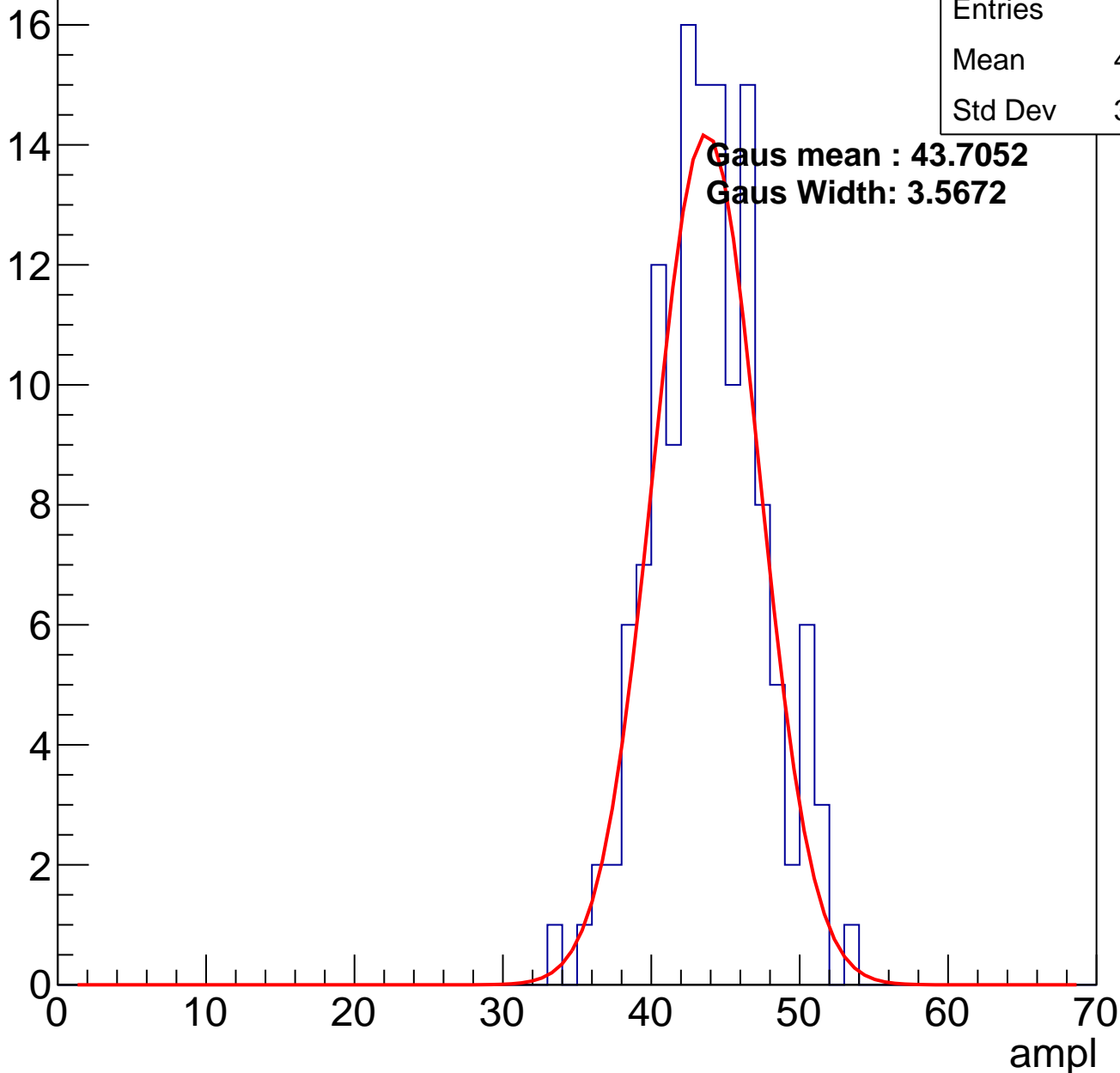
Gaus mean : 36.8423
Gaus Width: 3.6374



B0L101S, U15-ch66, adc2

calib_packv5_042523_0143.root, FC#1, port C1

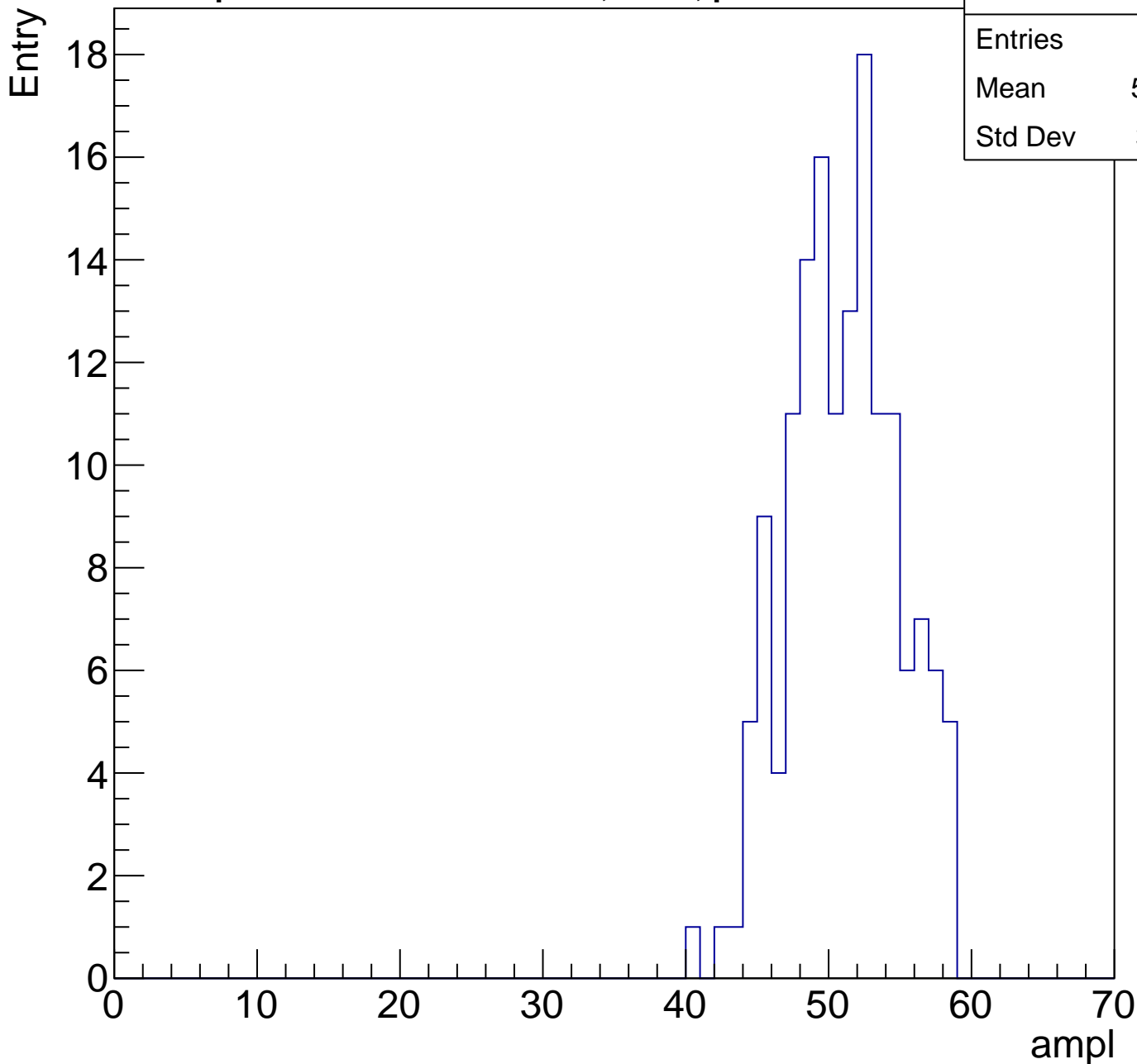
Entry



B0L101S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	50.58
Std Dev	3.811



B0L101S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	98
Mean	56.14
Std Dev	3.479

Entry

12

10

8

6

4

2

0

0

10

20

30

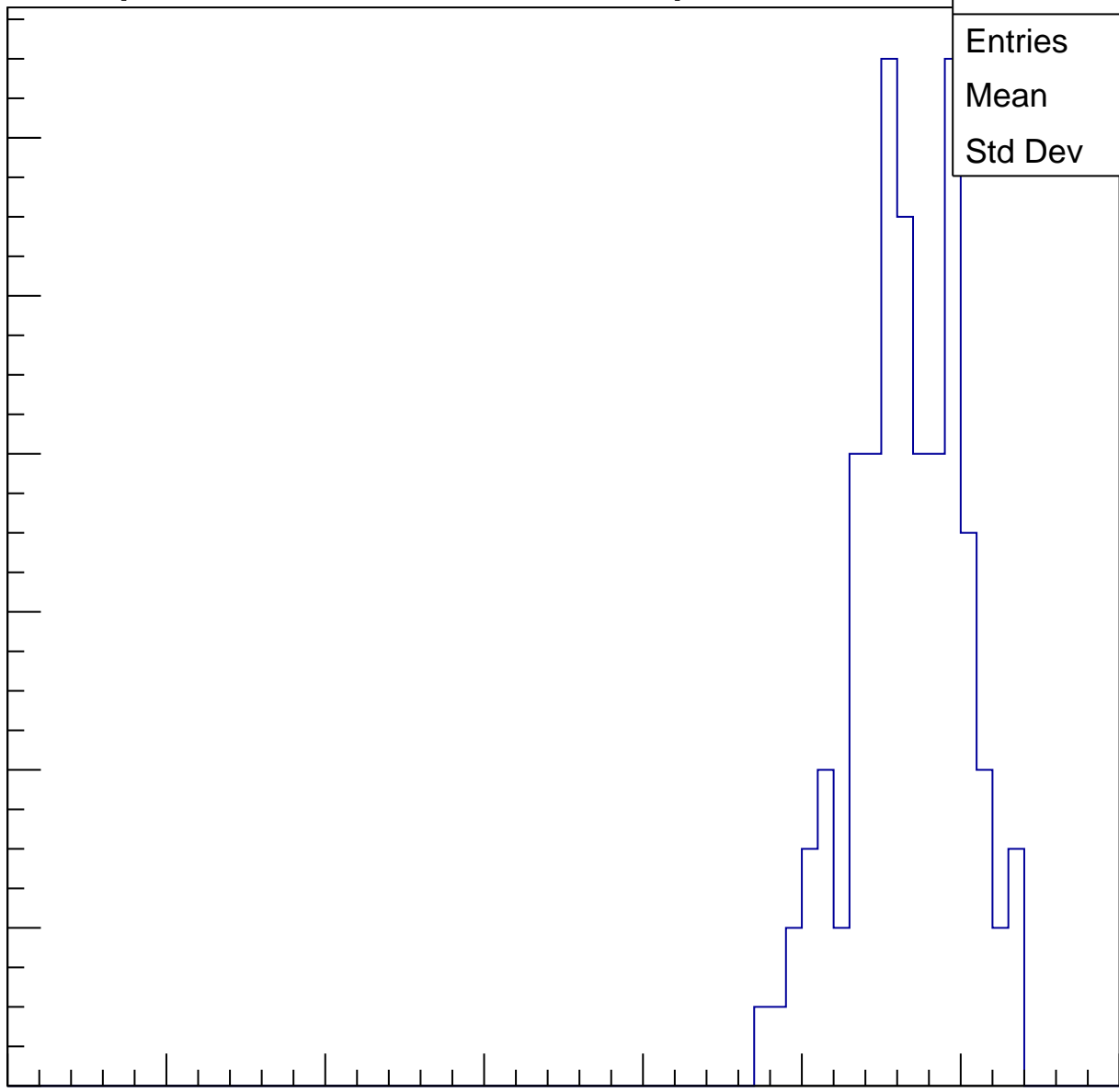
40

50

60

70

ampl



B0L101S, U15-ch66, adc5

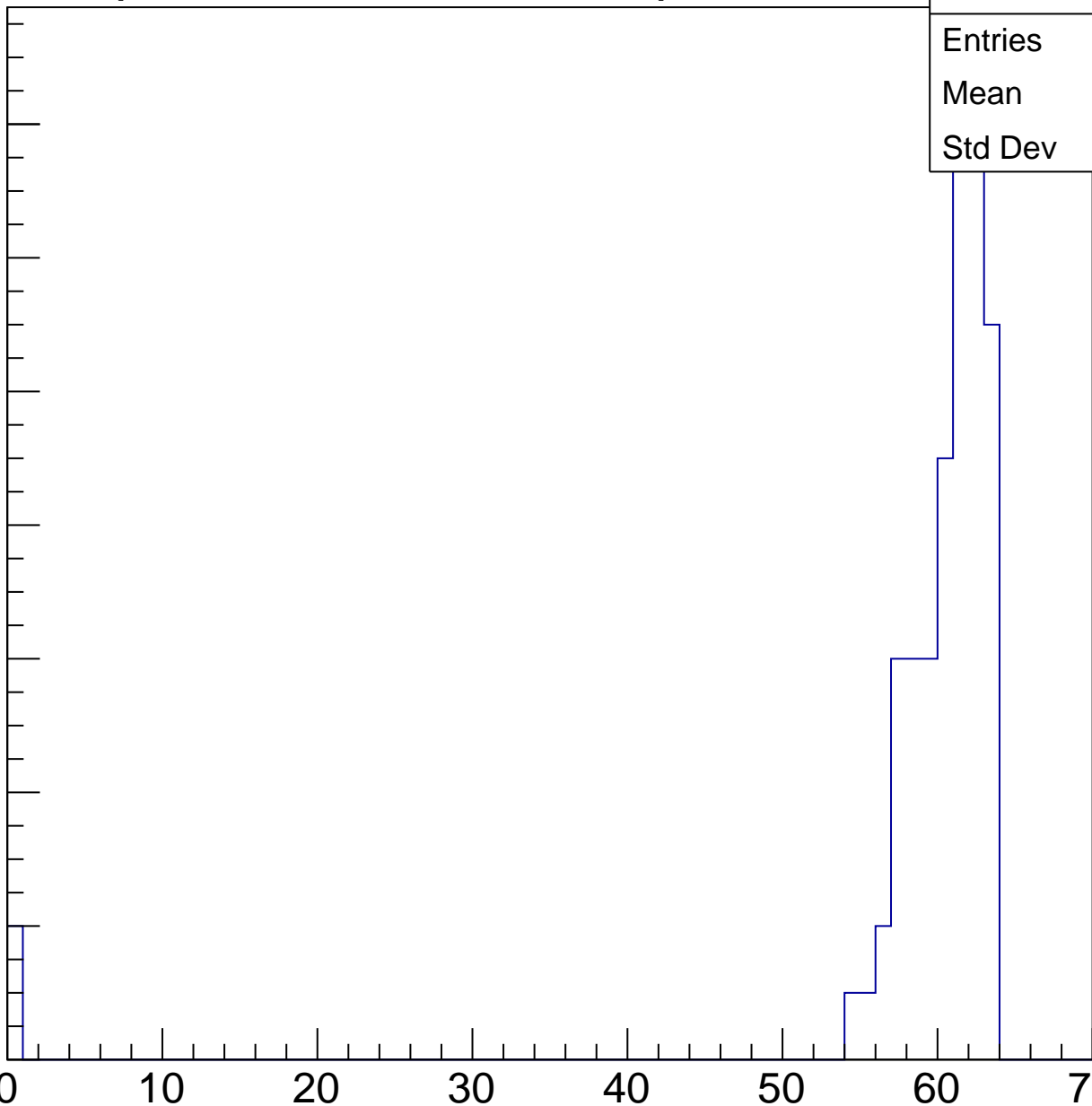
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	73
Mean	58.64
Std Dev	10.08

ampl

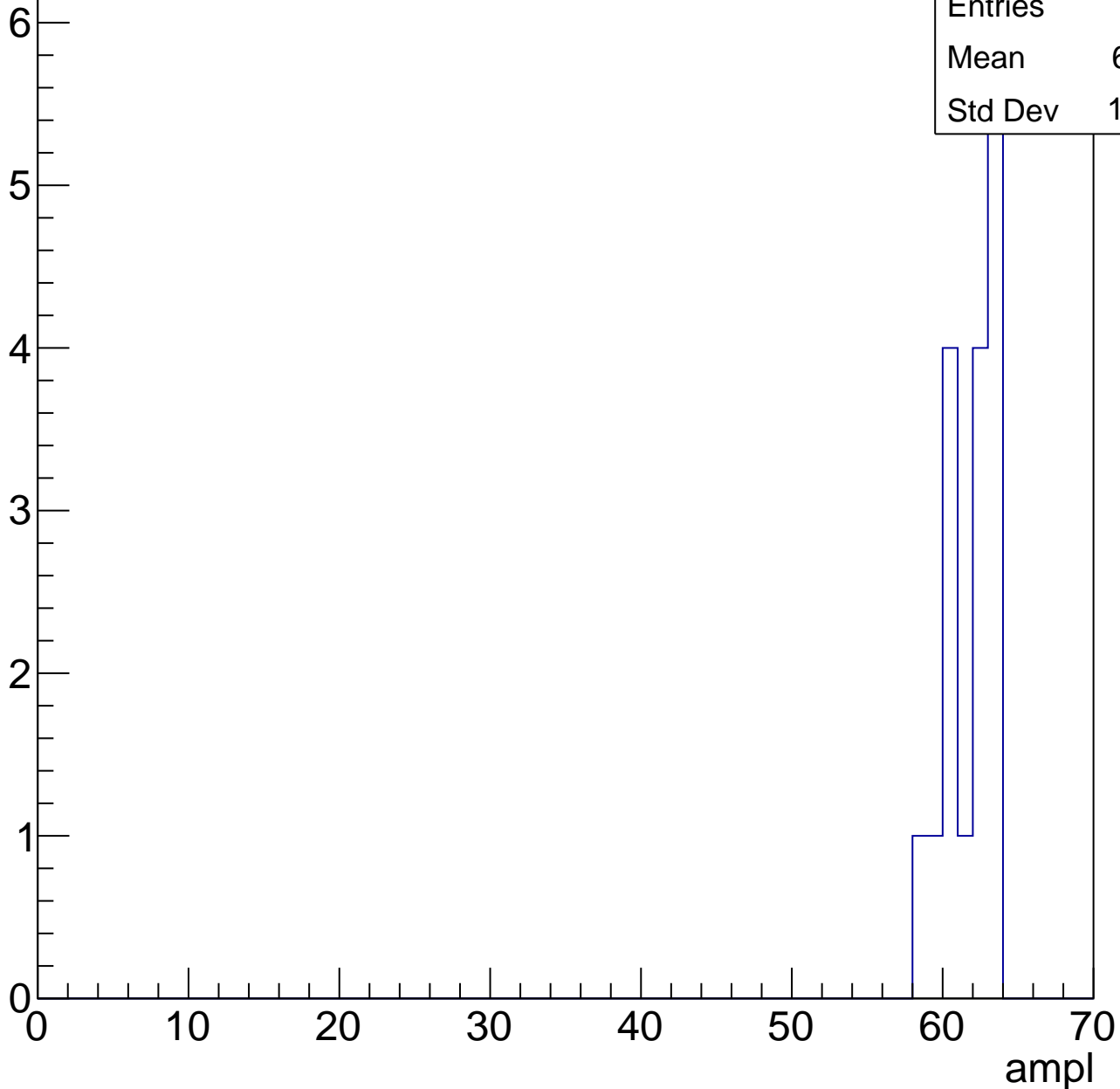


B0L101S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	61.41
Std Dev	1.574



B0L101S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch67, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.37
Std Dev	4.201

Gaus mean : 30.2170

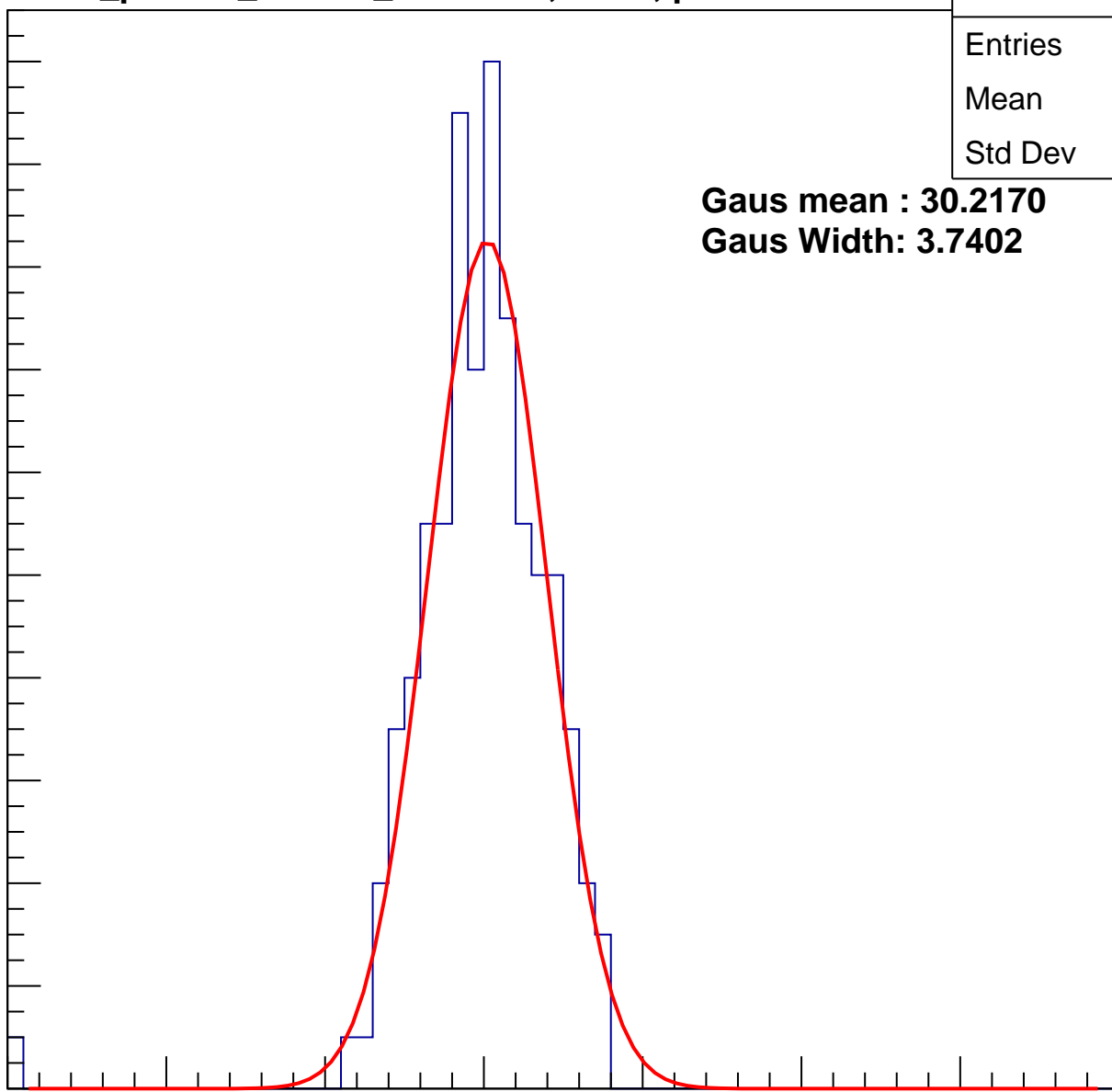
Gaus Width: 3.7402

Entry

20
18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



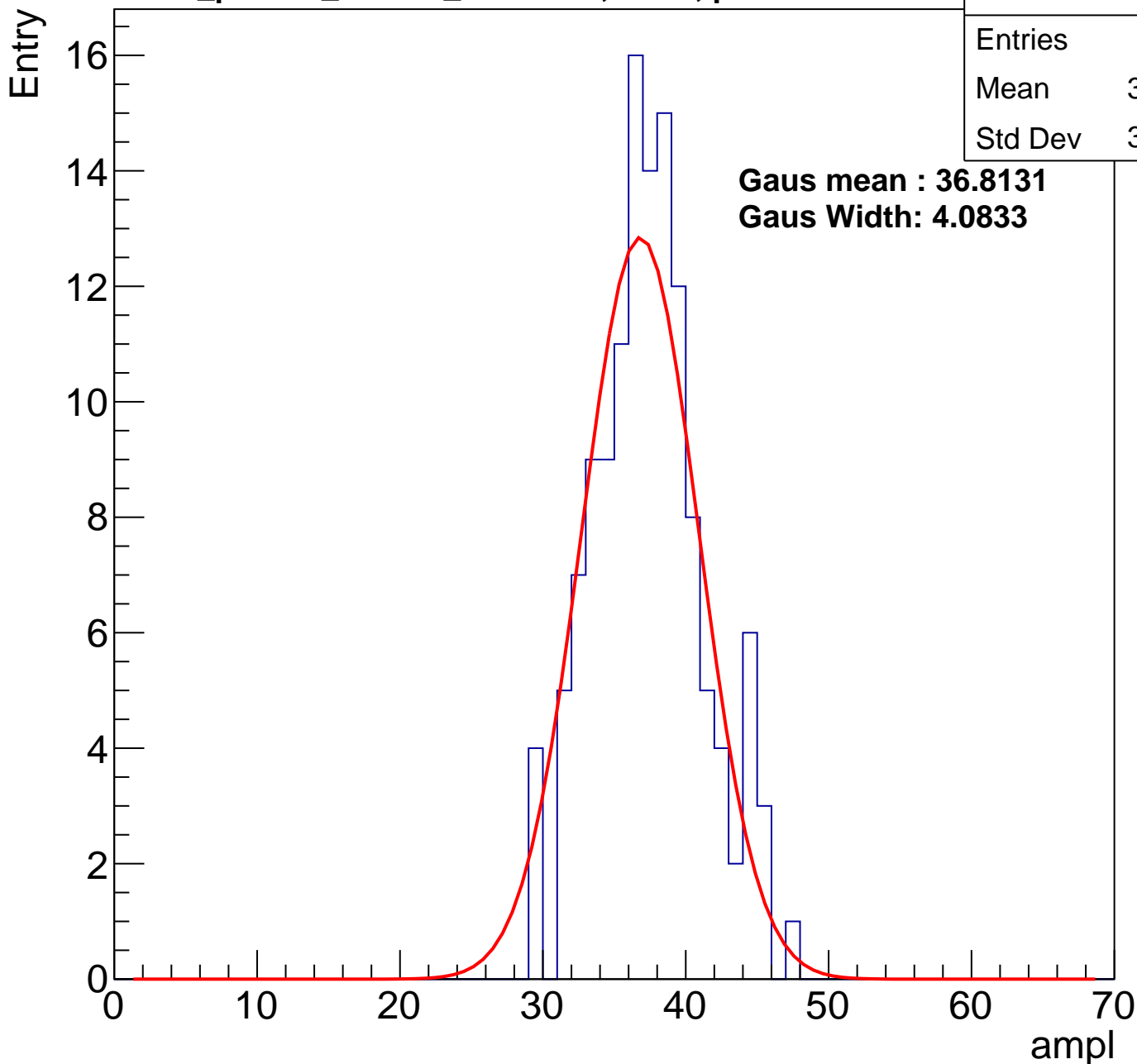
B0L101S, U15-ch67, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	36.95
Std Dev	3.777

Gaus mean : 36.8131

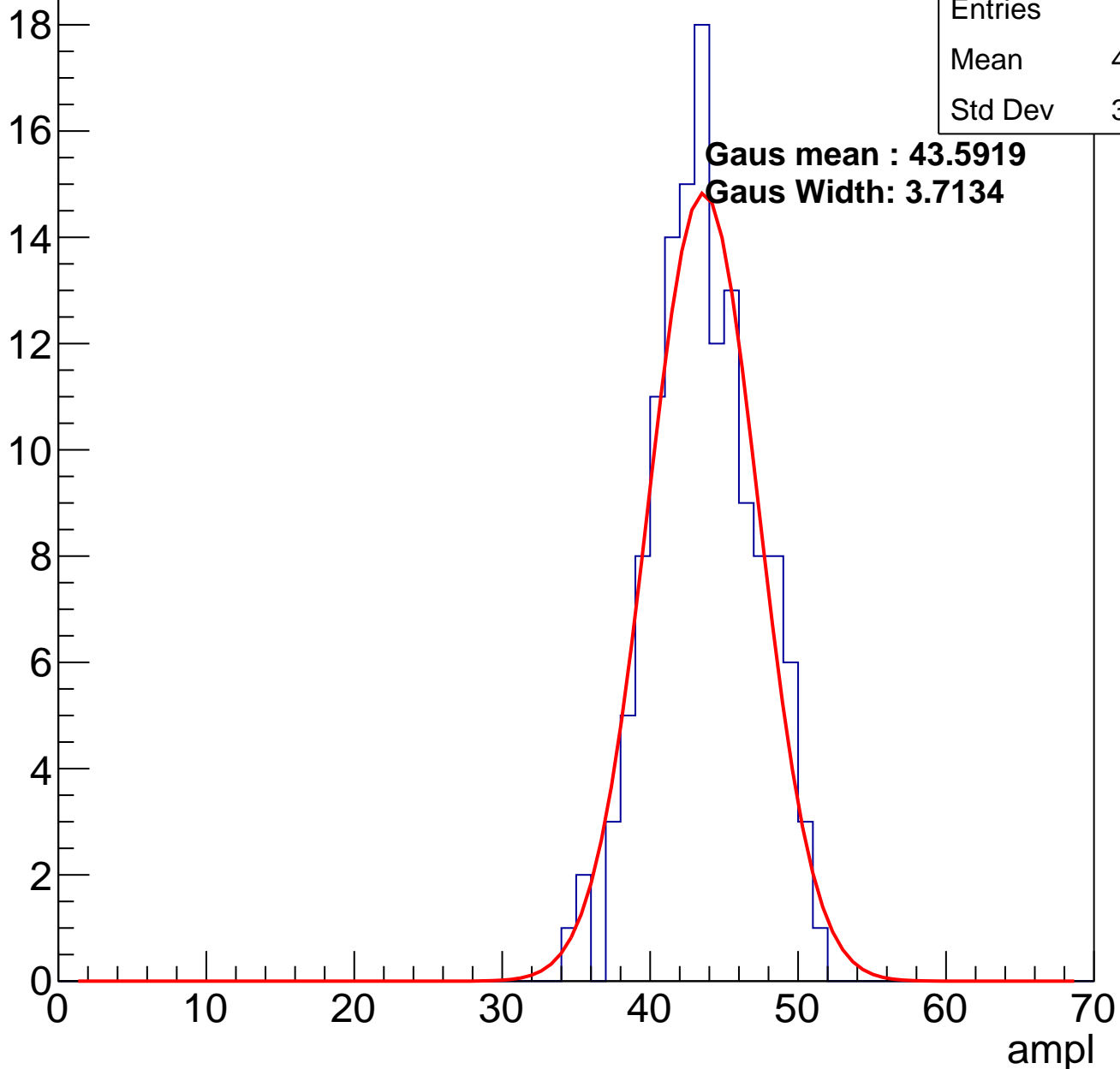
Gaus Width: 4.0833



B0L101S, U15-ch67, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	49.51
Std Dev	3.353

Entry

12

10

8

6

4

2

0

0

10

20

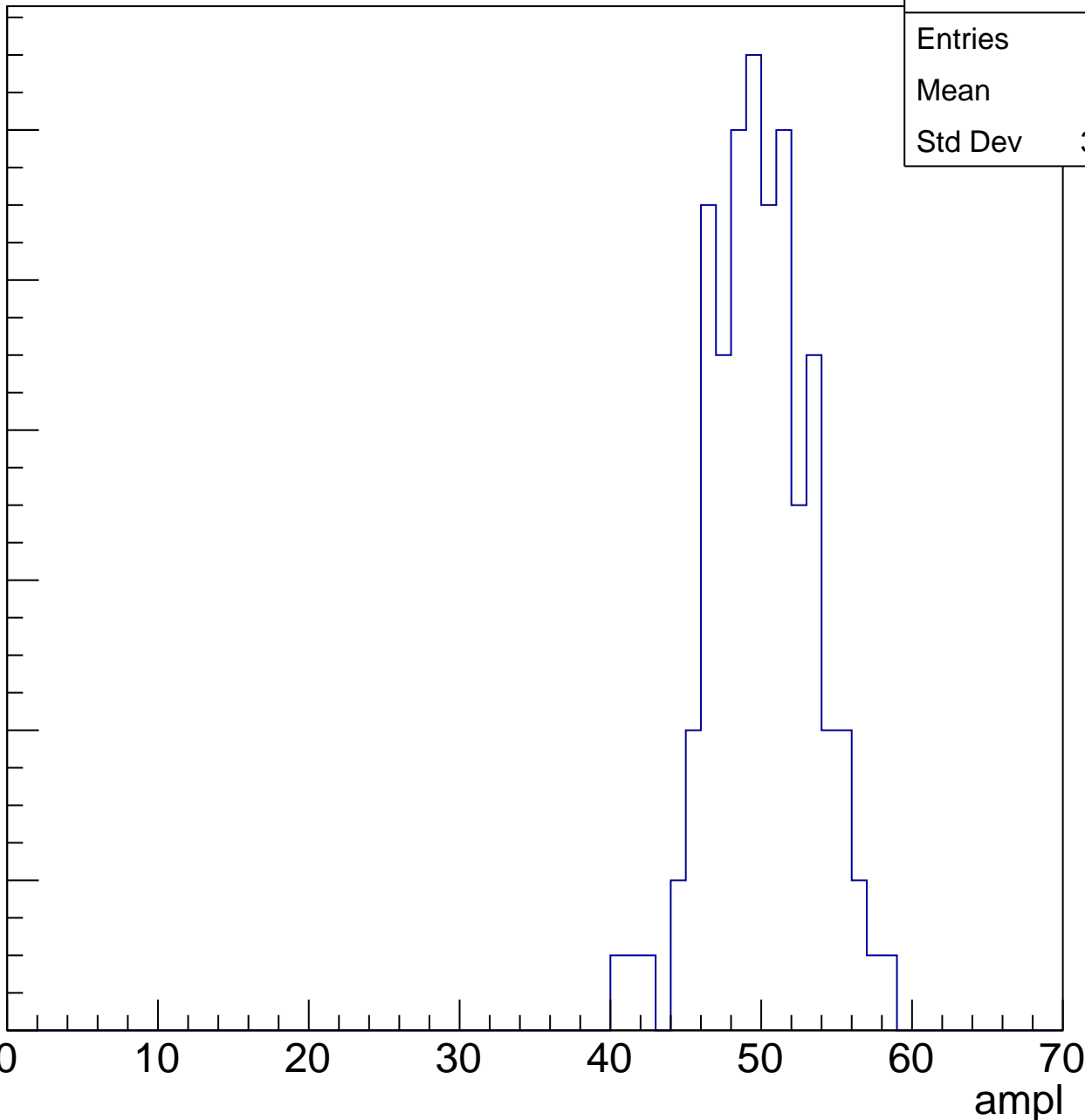
30

40

50

60

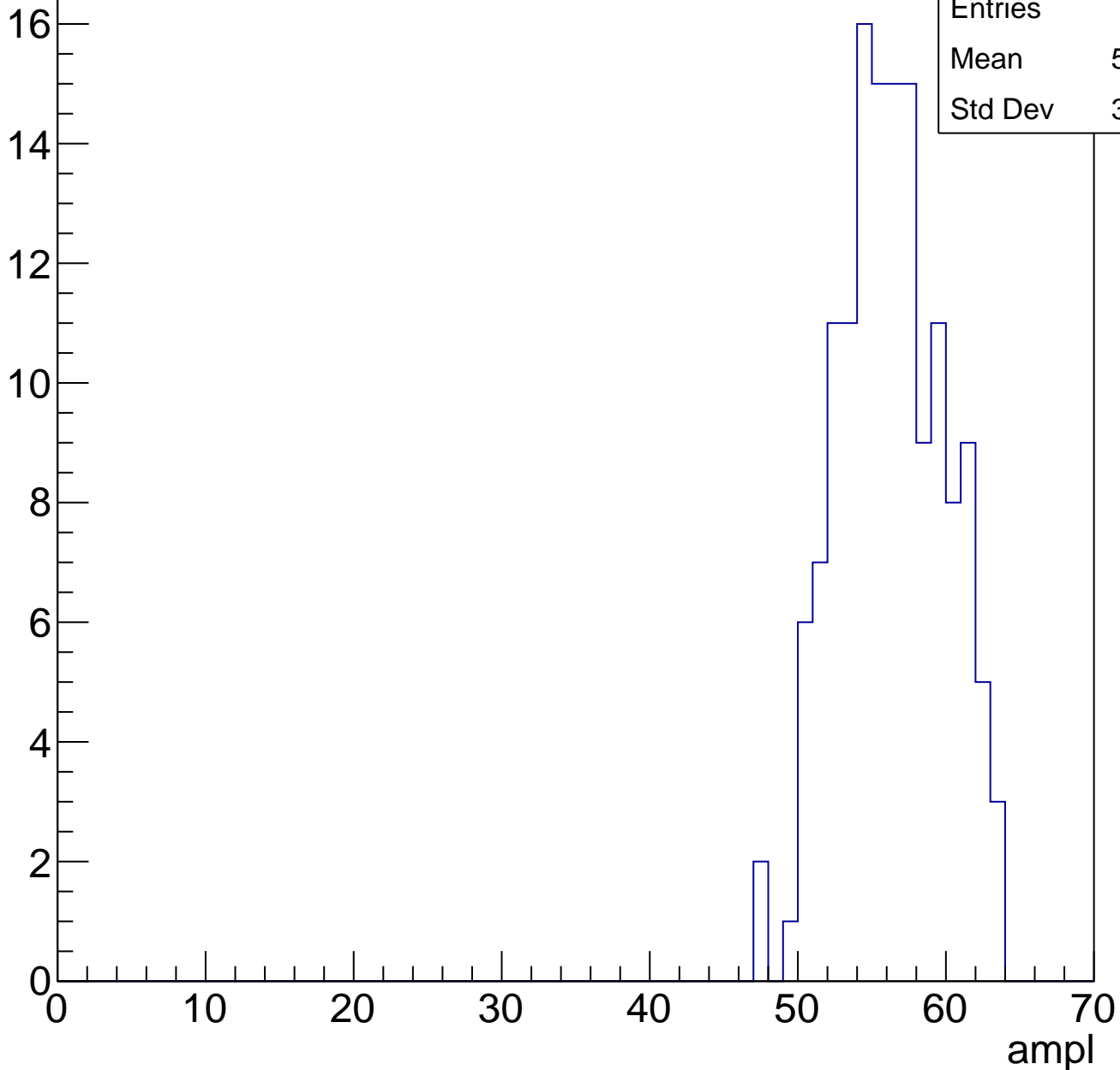
ampl



B0L101S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	144
Mean	55.82
Std Dev	3.517

B0L101S, U15-ch67, adc5

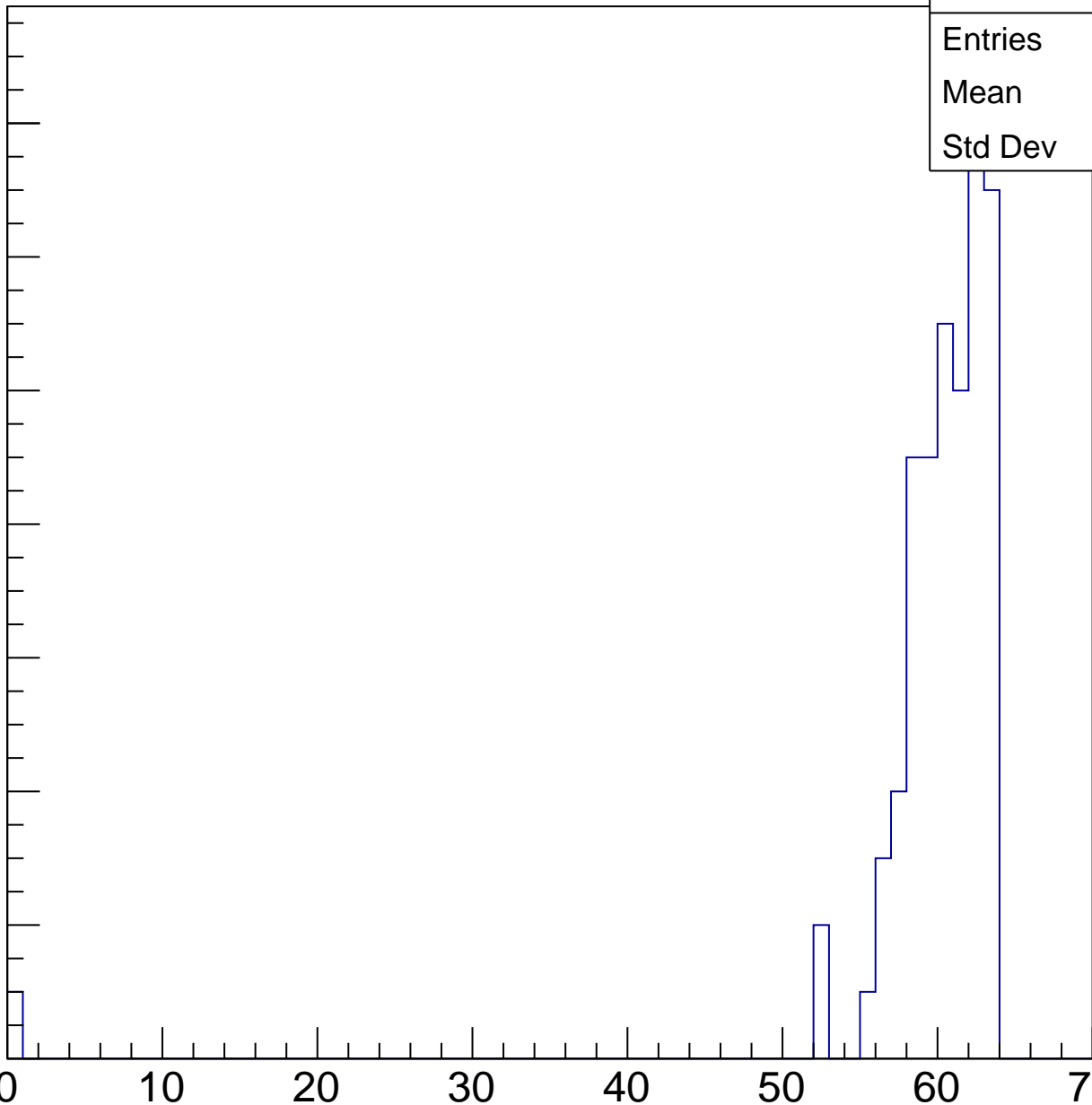
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	78
Mean	59.32
Std Dev	7.191

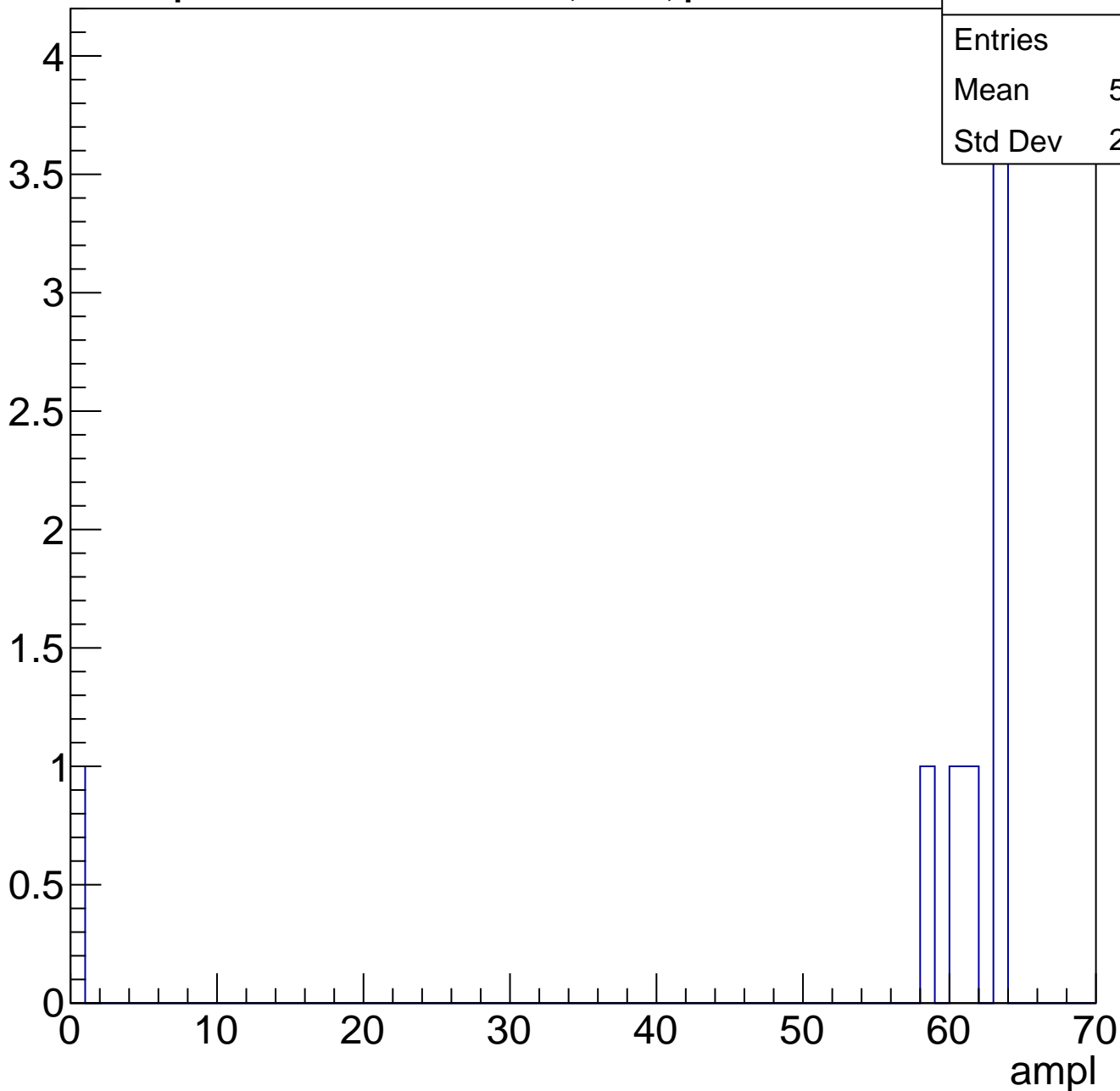
ampl



B0L101S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#1, port C1

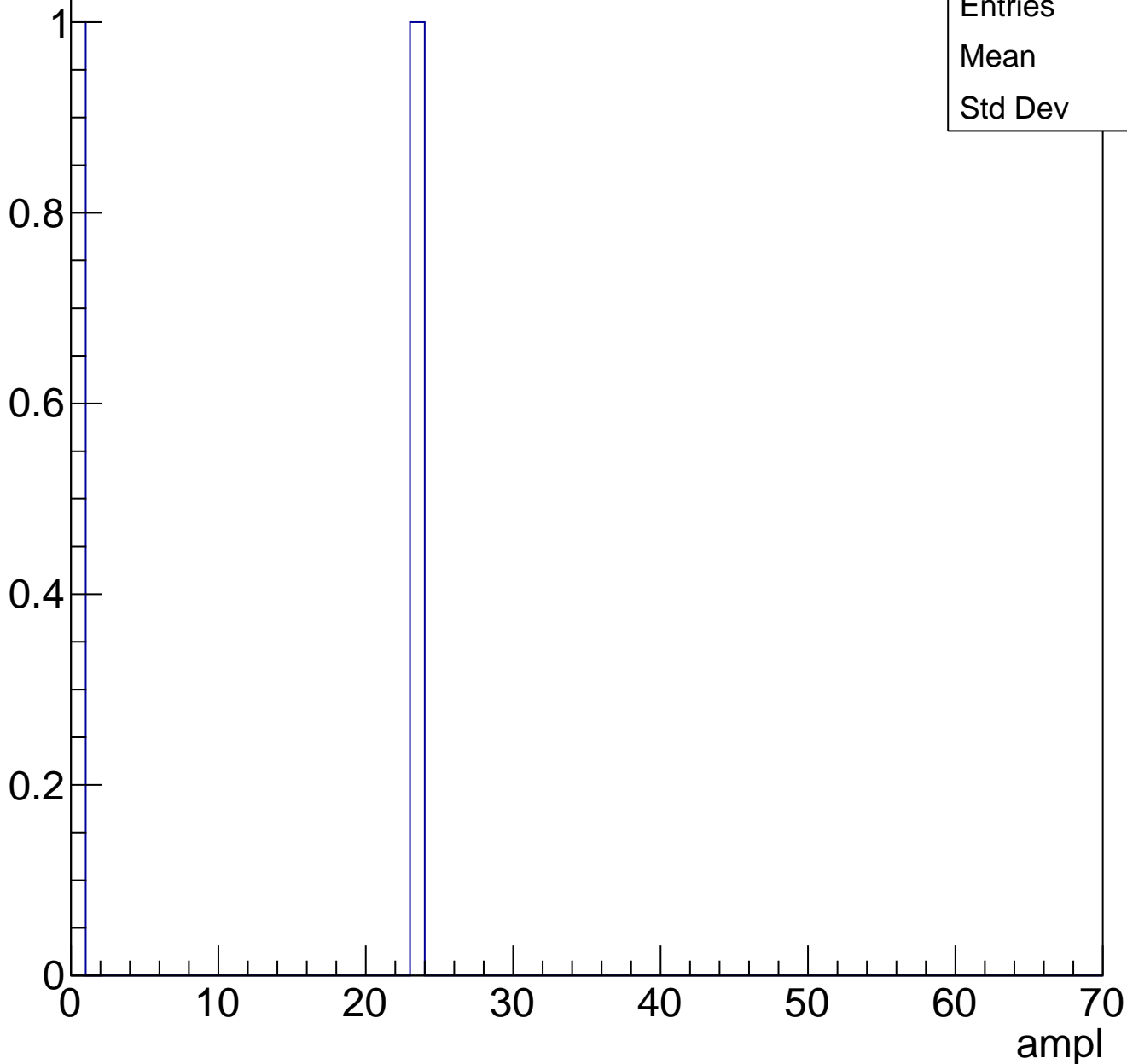
Entry



B0L101S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch68, adc0

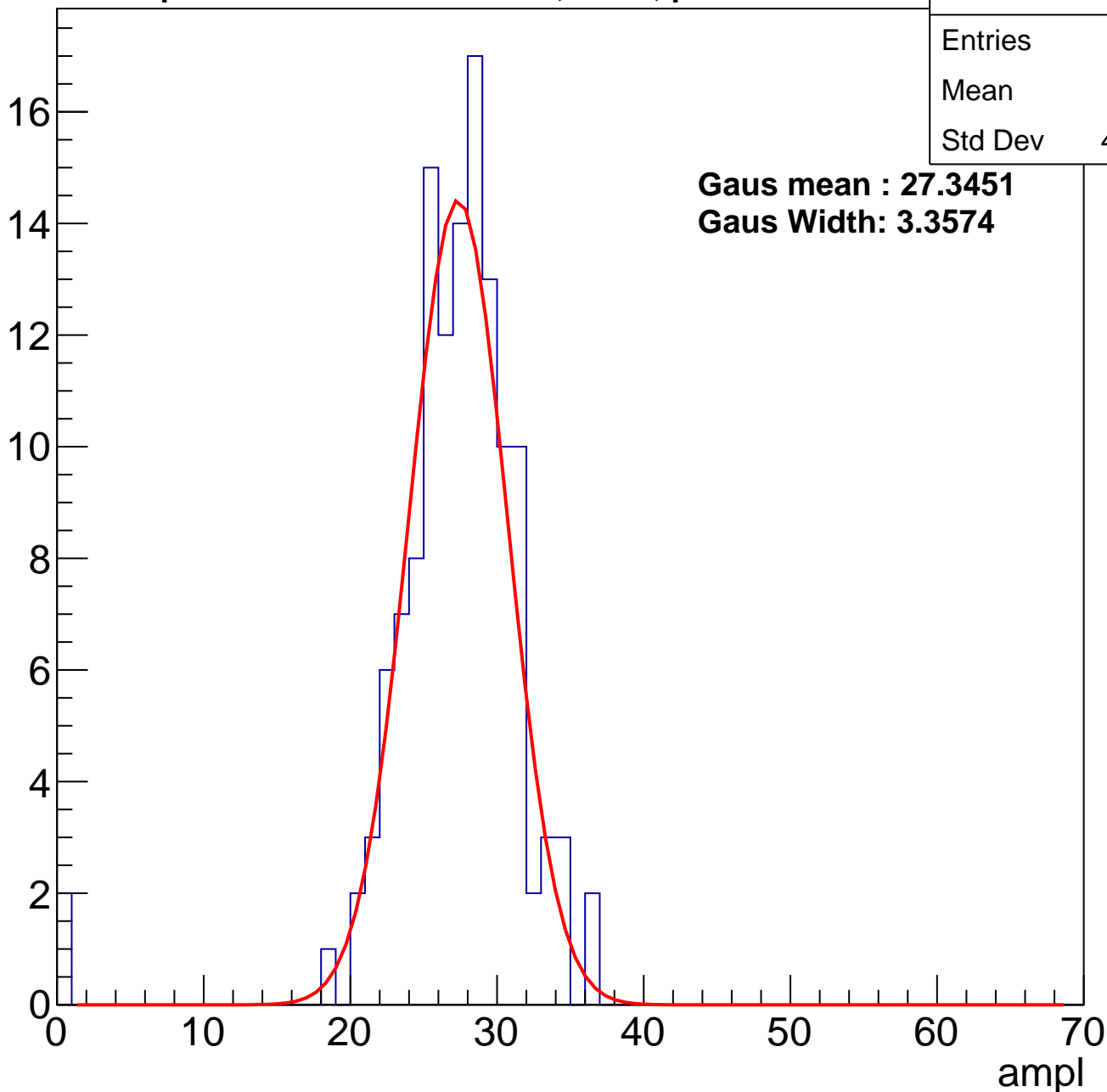
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	26.7
Std Dev	4.744

Gaus mean : 27.3451

Gaus Width: 3.3574

Entry



B0L101S, U15-ch68, adc1

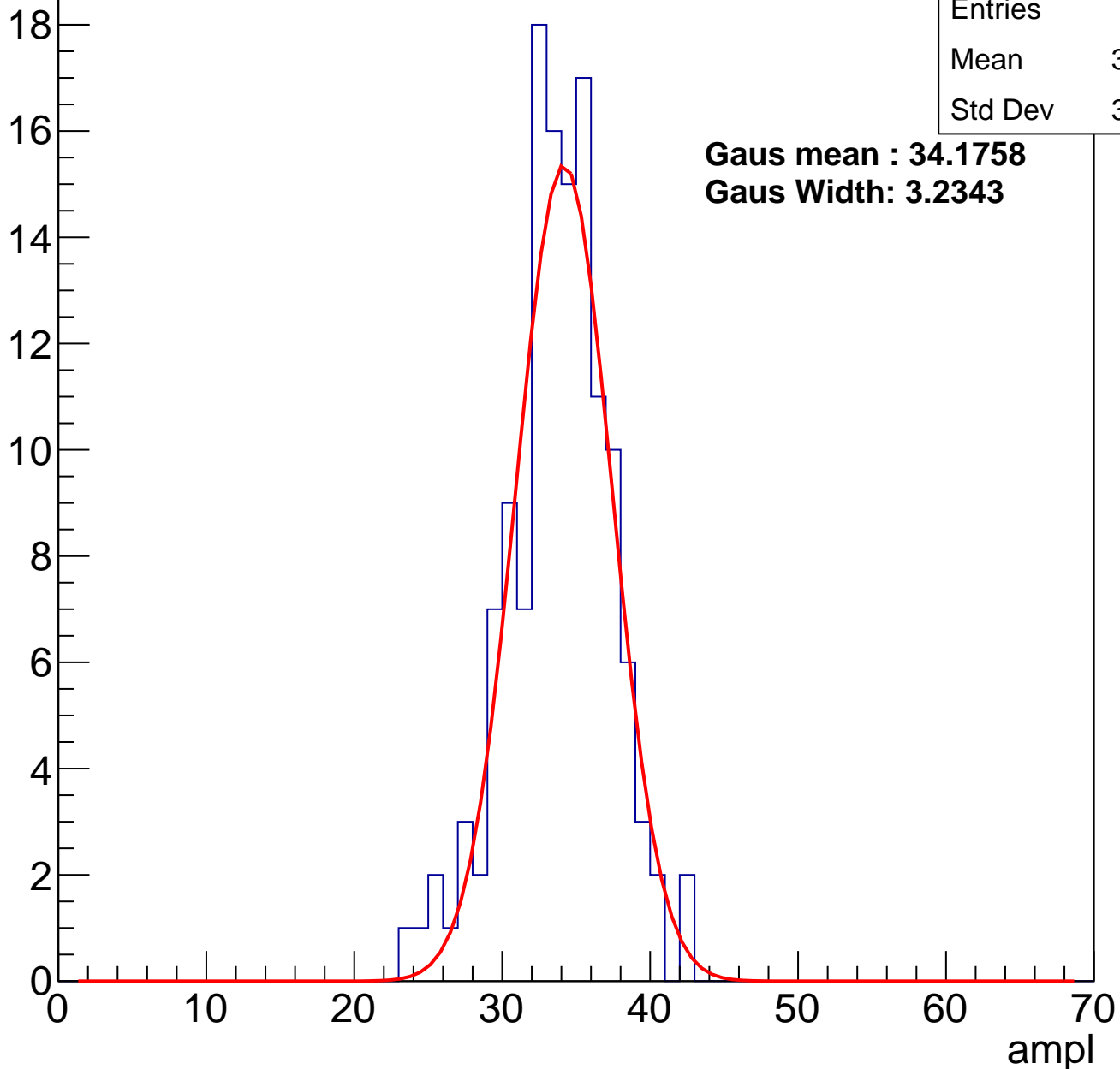
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	33.34
Std Dev	3.468

Gaus mean : 34.1758

Gaus Width: 3.2343

Entry



B0L101S, U15-ch68, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	40.93
Std Dev	3.55

Gaus mean : 41.2195

Gaus Width: 3.4316

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

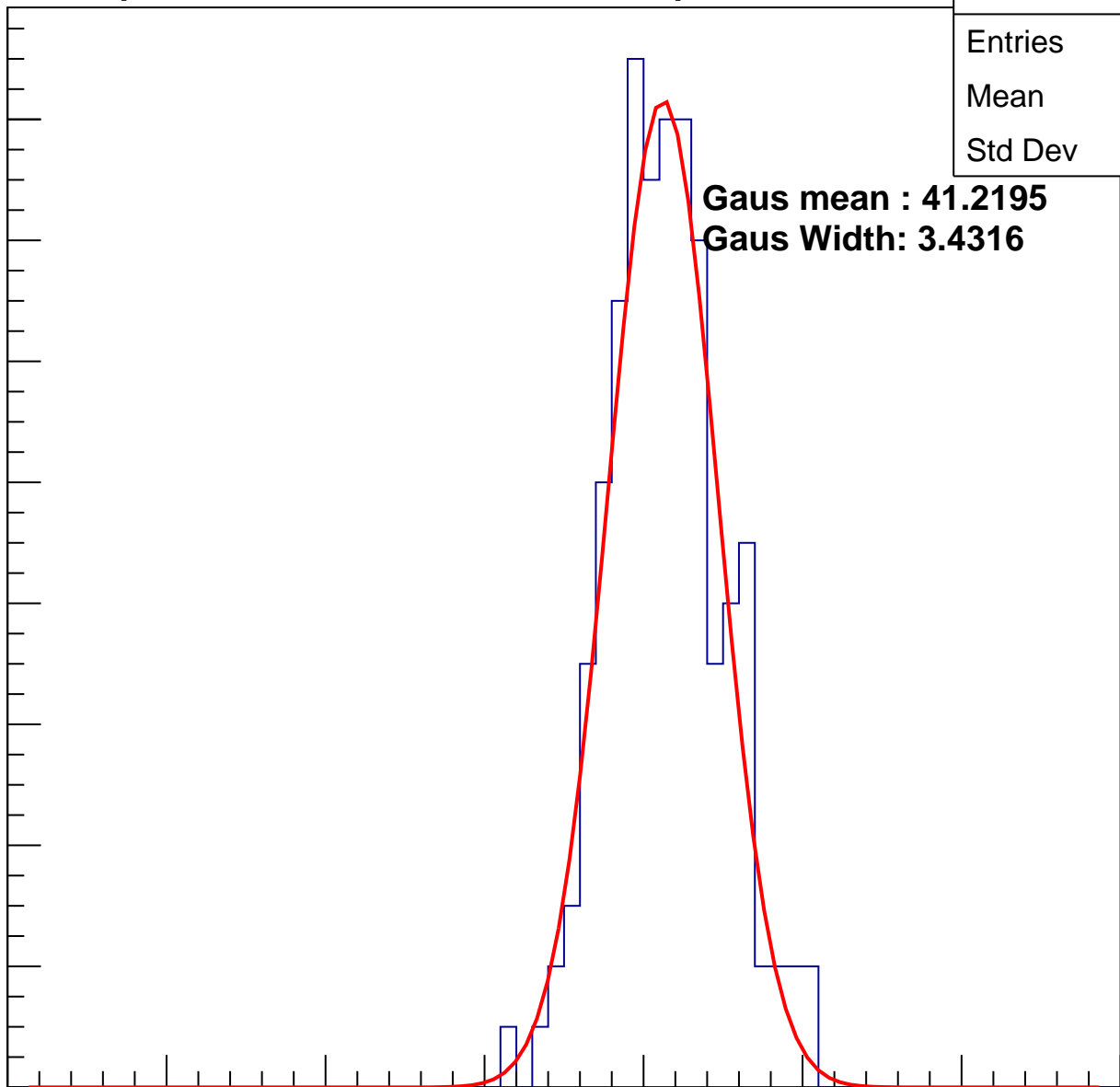
40

50

60

70

ampl

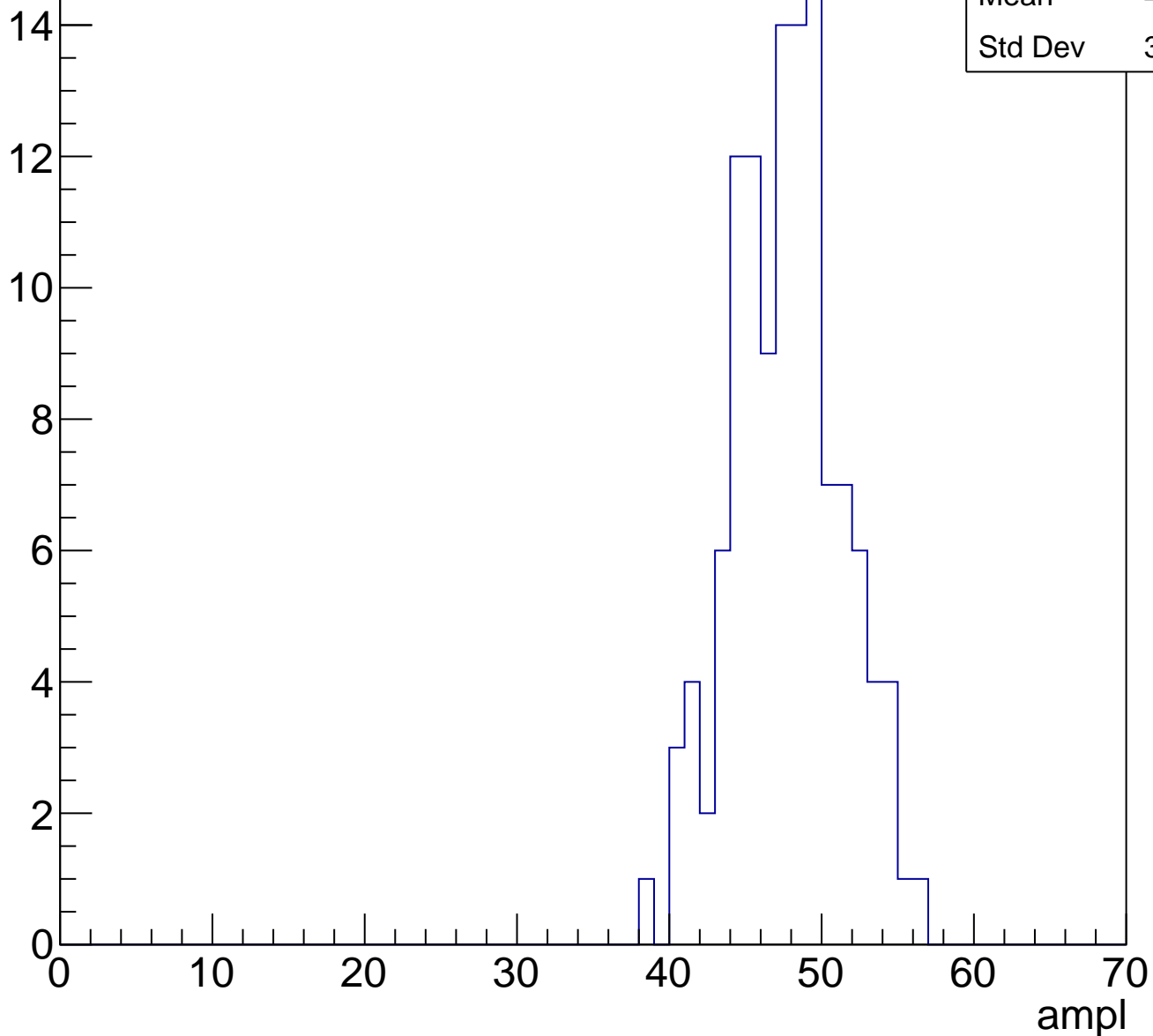


B0L101S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	47.29
Std Dev	3.572

Entry

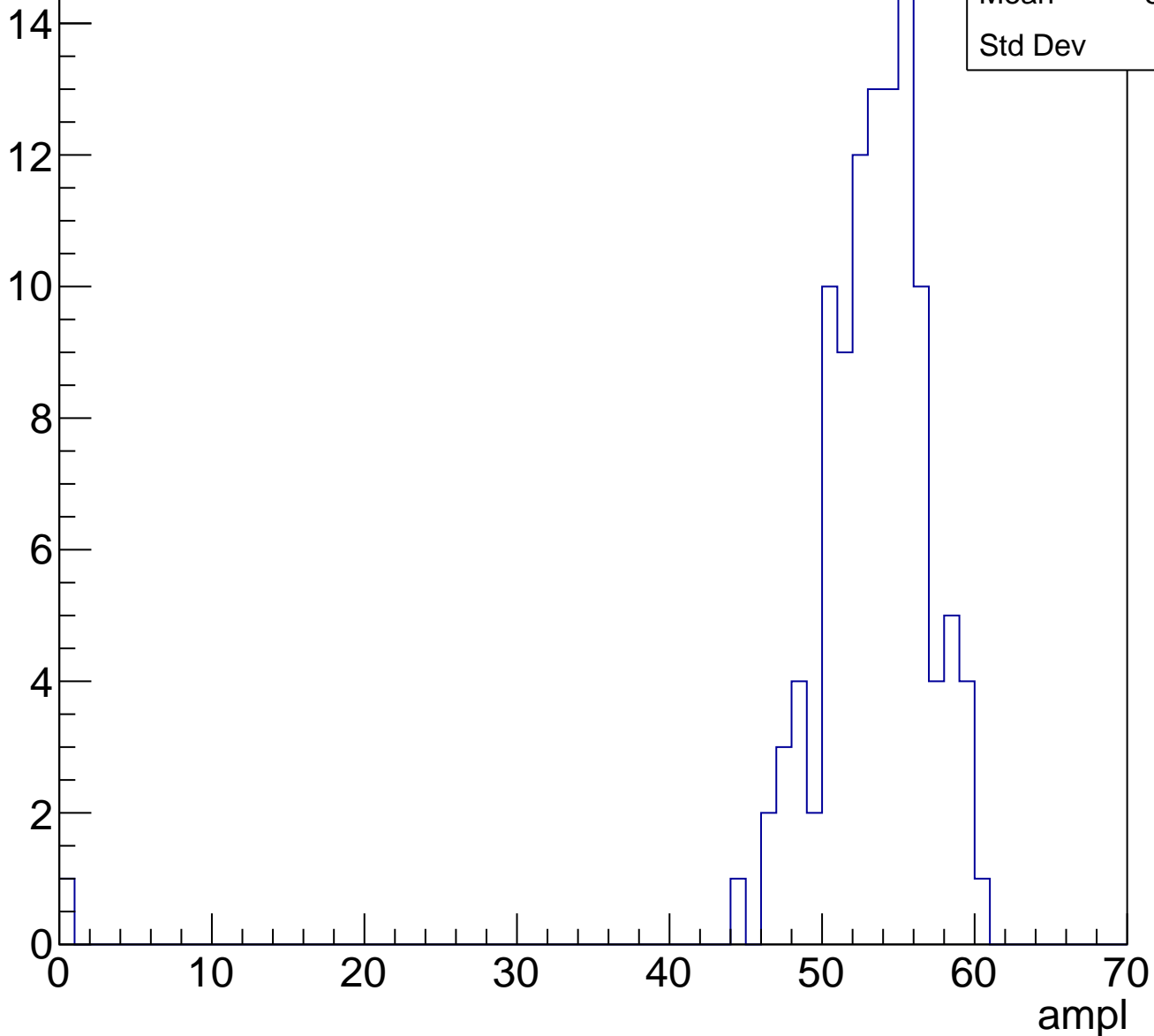


B0L101S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	52.66
Std Dev	5.98

Entry



B0L101S, U15-ch68, adc5

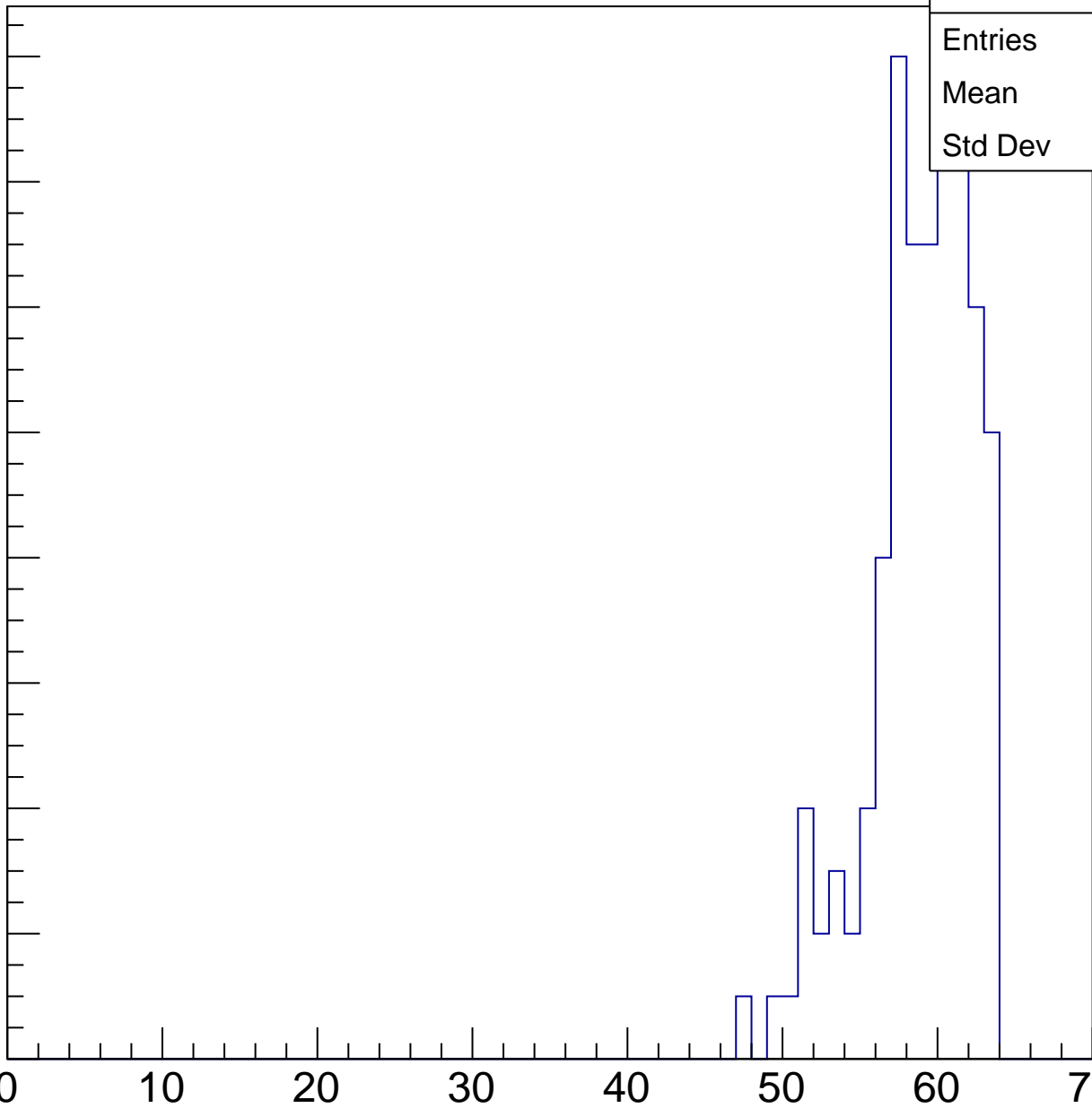
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	120
Mean	58.42
Std Dev	3.358

ampl

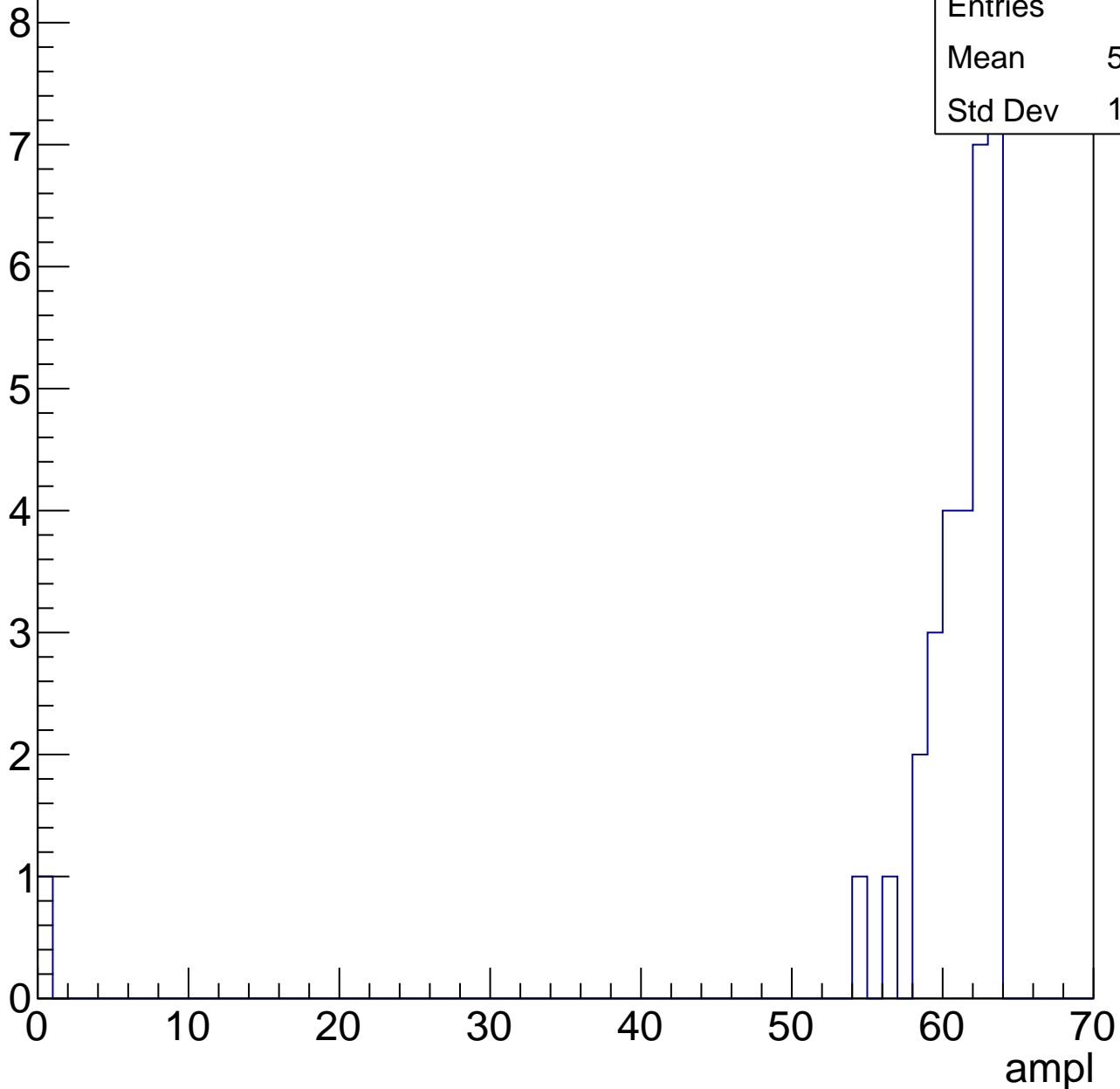


B0L101S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

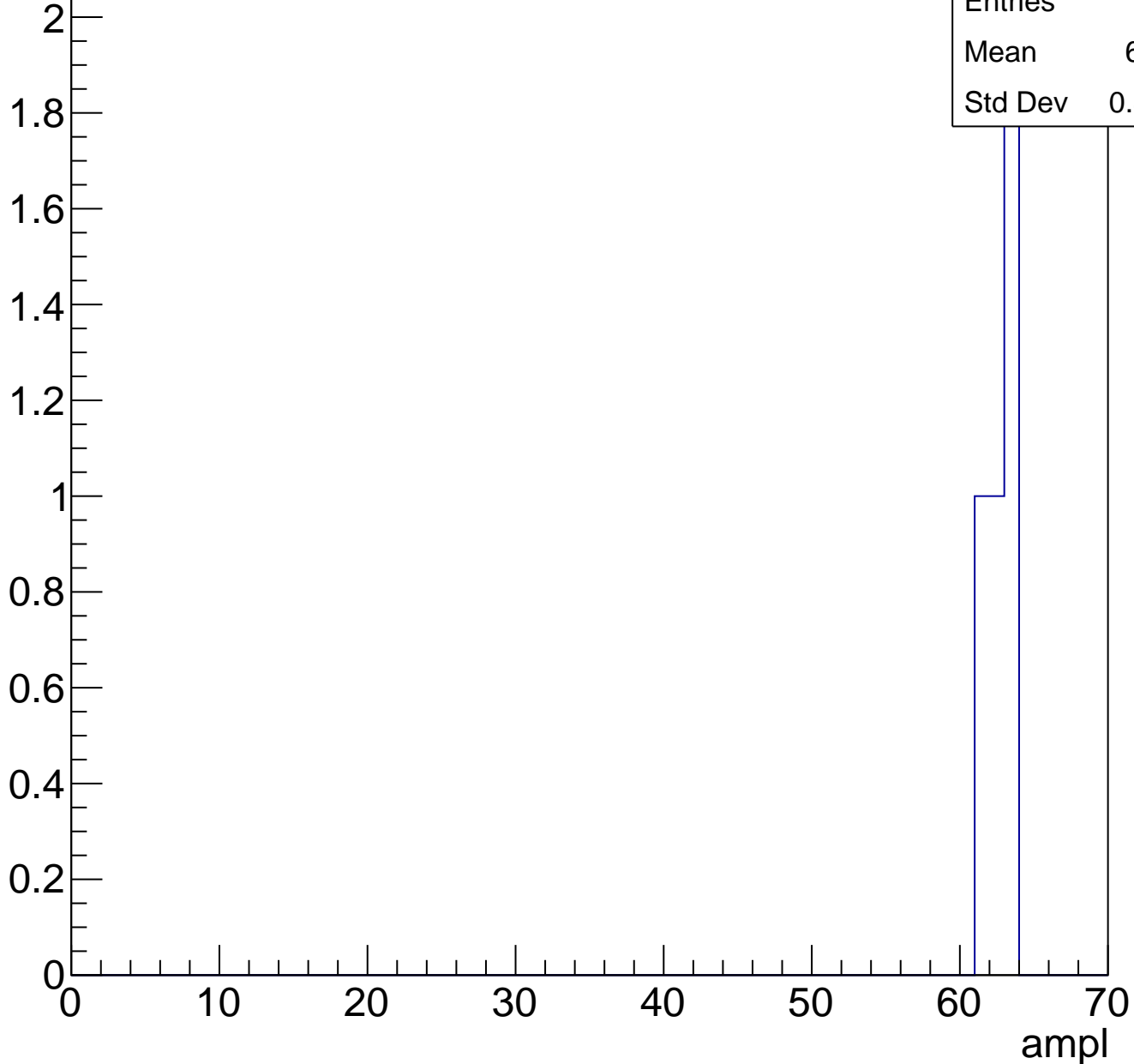
Entries	31
Mean	58.87
Std Dev	10.97



B0L101S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch69, adc0

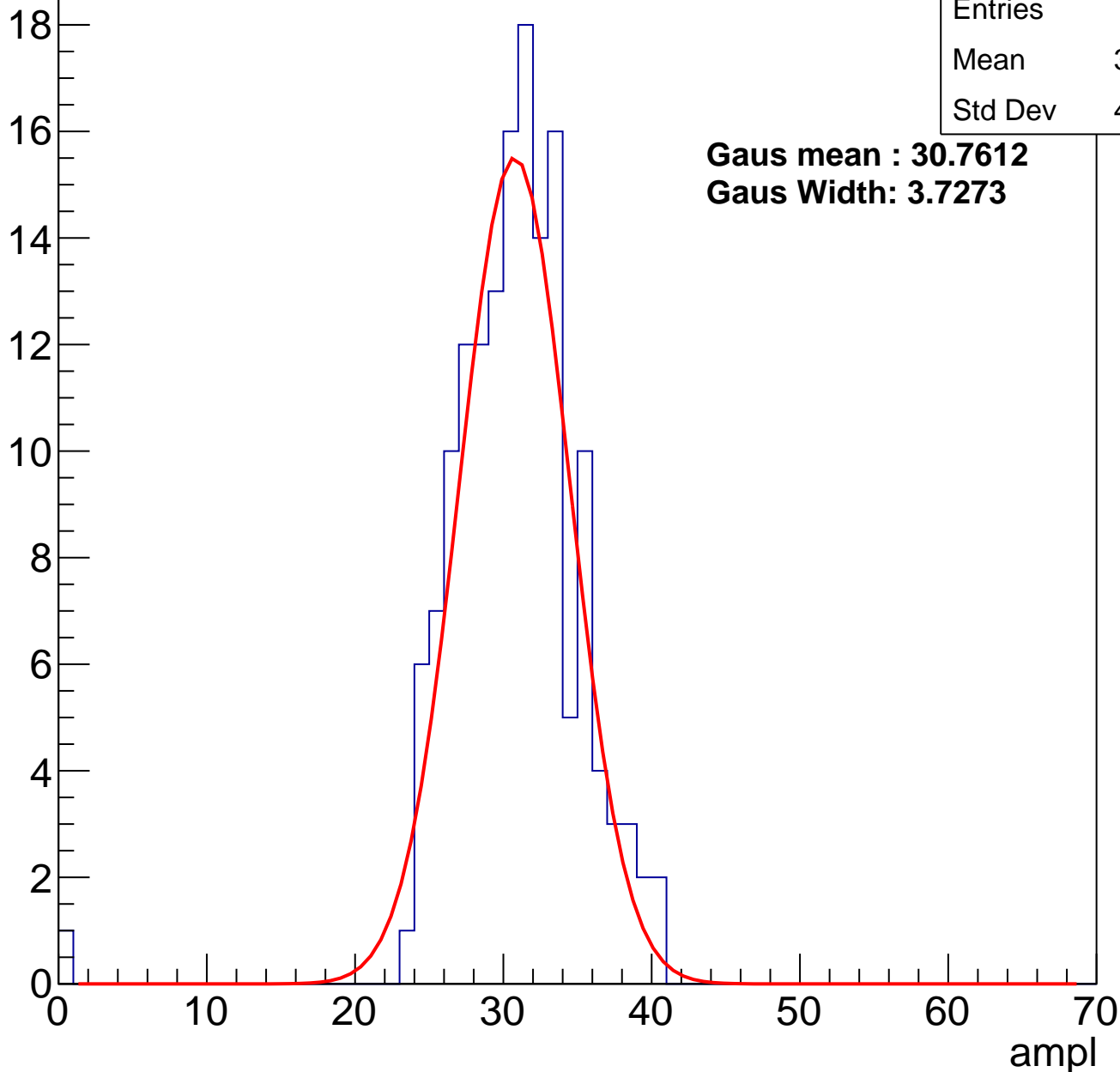
calib_packv5_042523_0143.root, FC#1, port C1

Entries	155
Mean	30.32
Std Dev	4.433

Gaus mean : 30.7612

Gaus Width: 3.7273

Entry



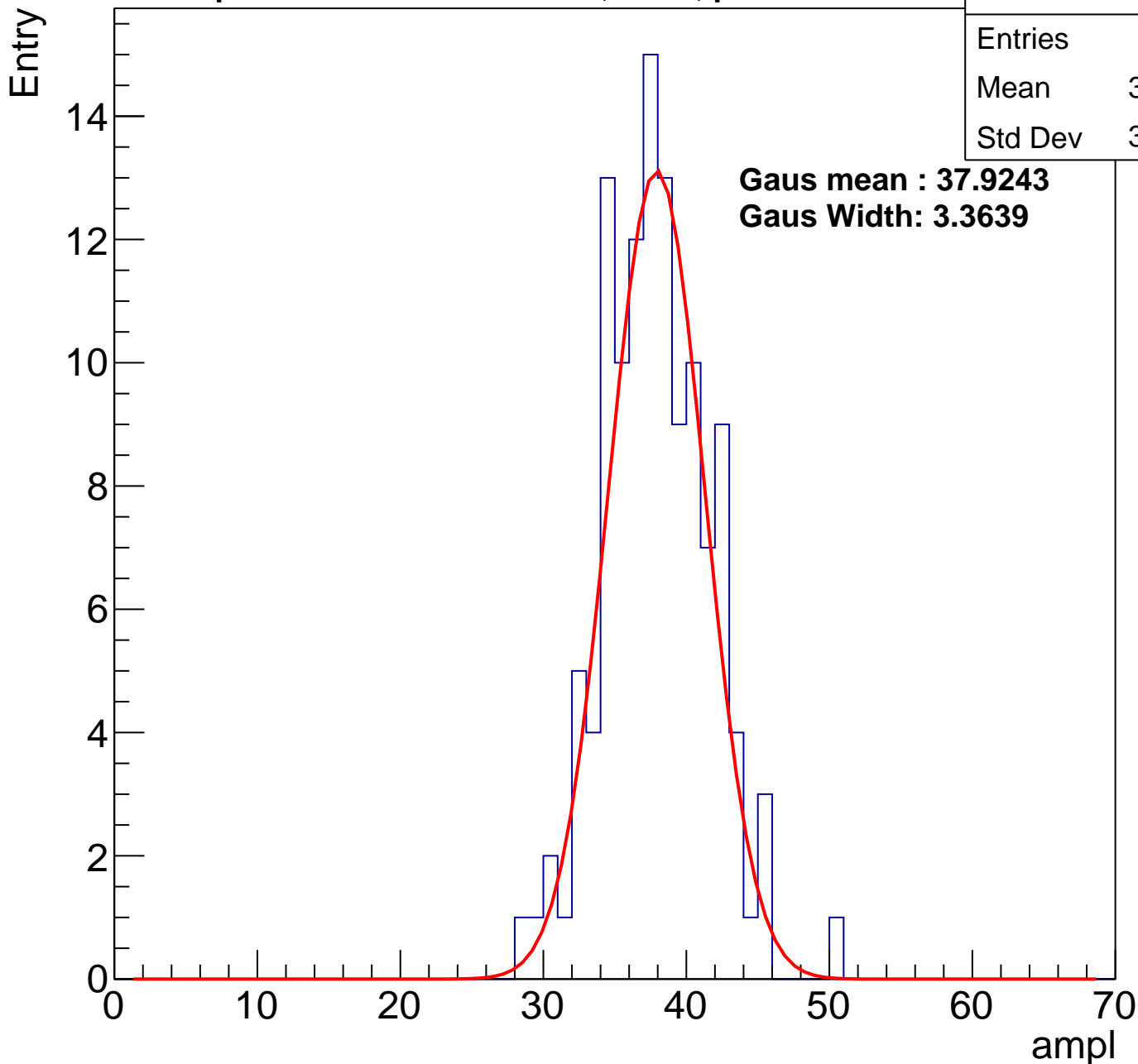
B0L101S, U15-ch69, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	37.44
Std Dev	3.686

Gaus mean : 37.9243

Gaus Width: 3.3639



B0L101S, U15-ch69, adc2

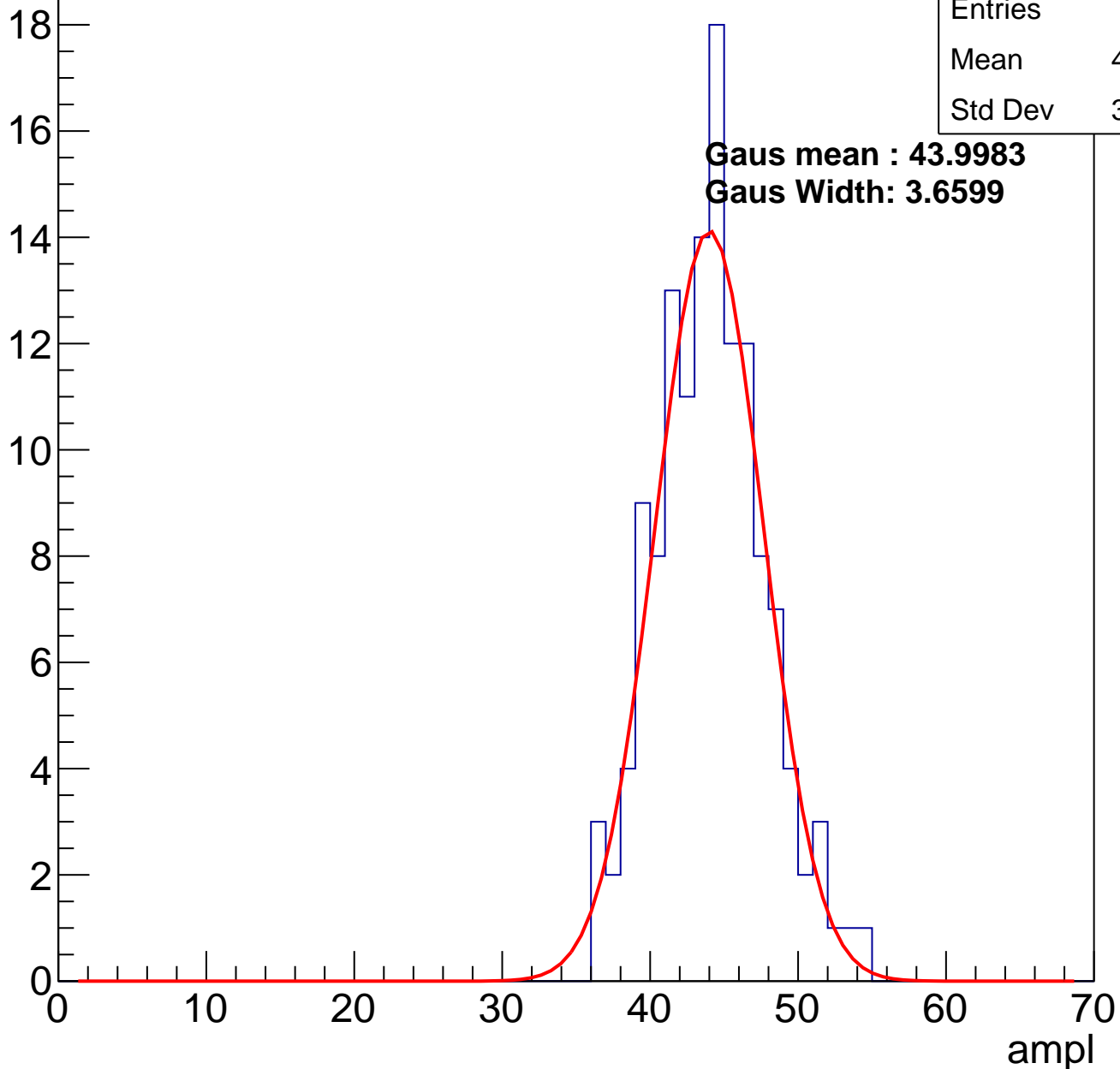
calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	43.65
Std Dev	3.632

Gaus mean : 43.9983

Gaus Width: 3.6599

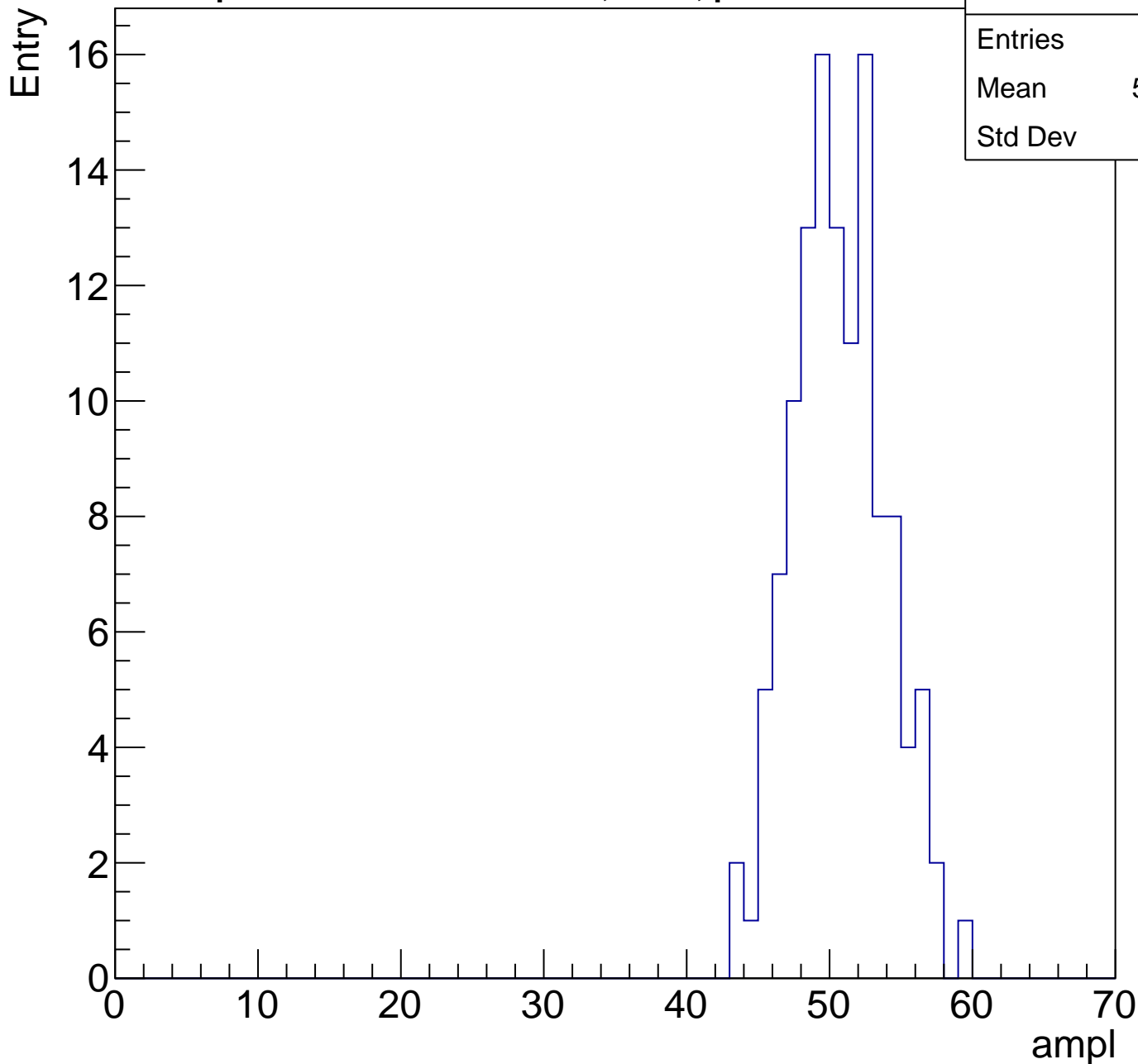
Entry



B0L101S, U15-ch69, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	50.22
Std Dev	3.21



B0L101S, U15-ch69, adc4

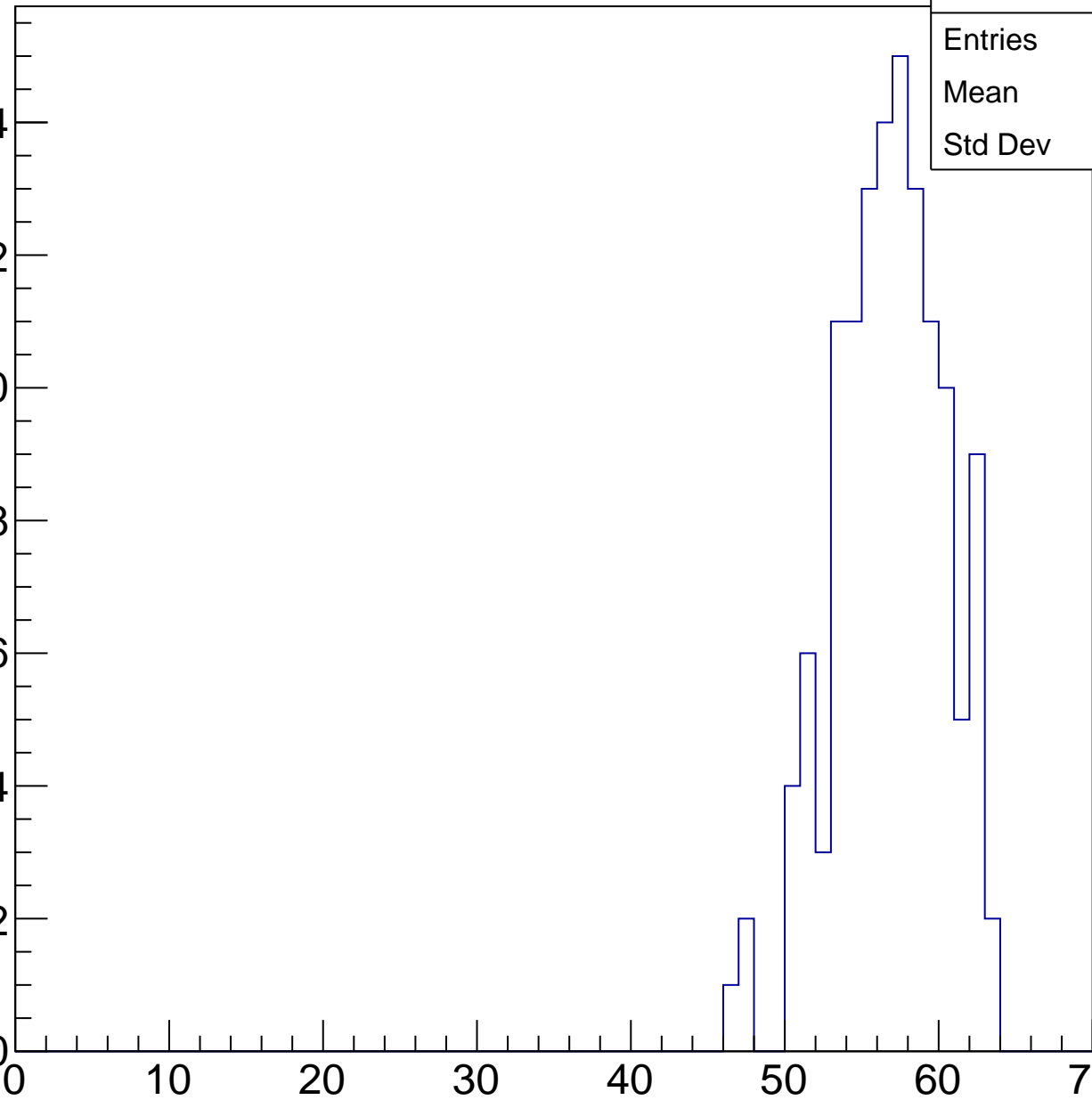
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	56.35
Std Dev	3.527

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch69, adc5

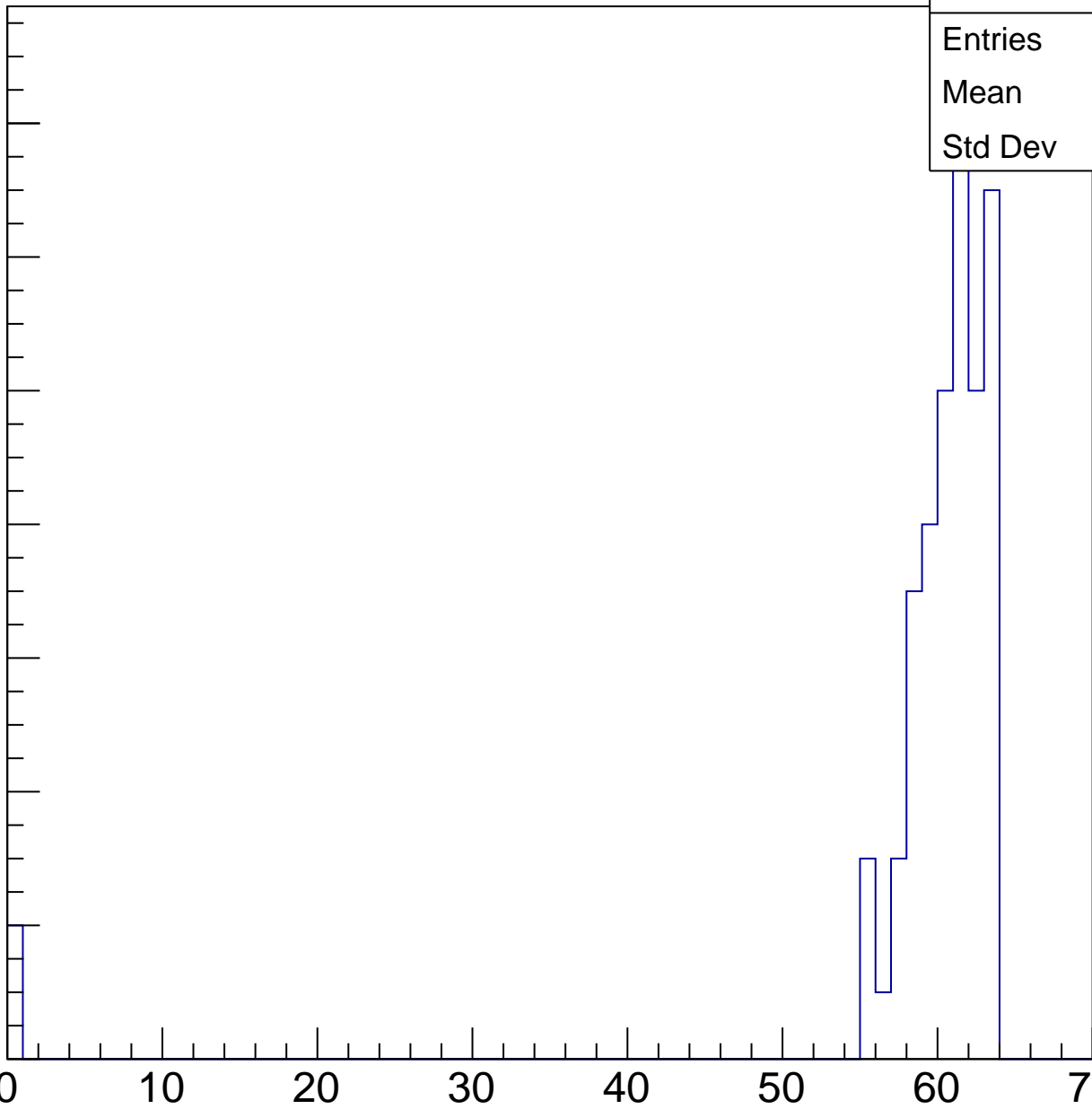
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.67
Std Dev	10.14

ampl

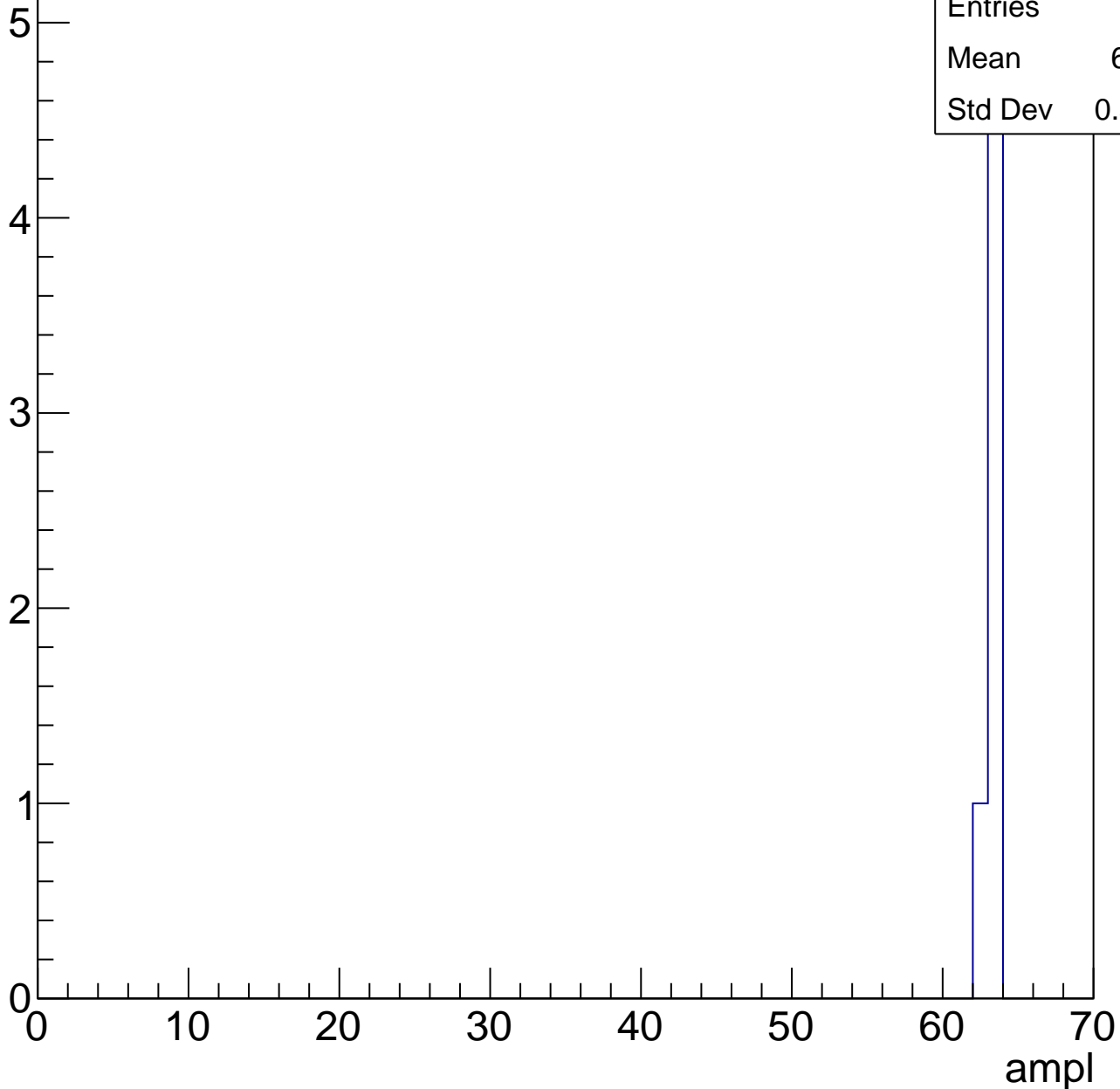


B0L101S, U15-ch69, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L101S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch70, adc0

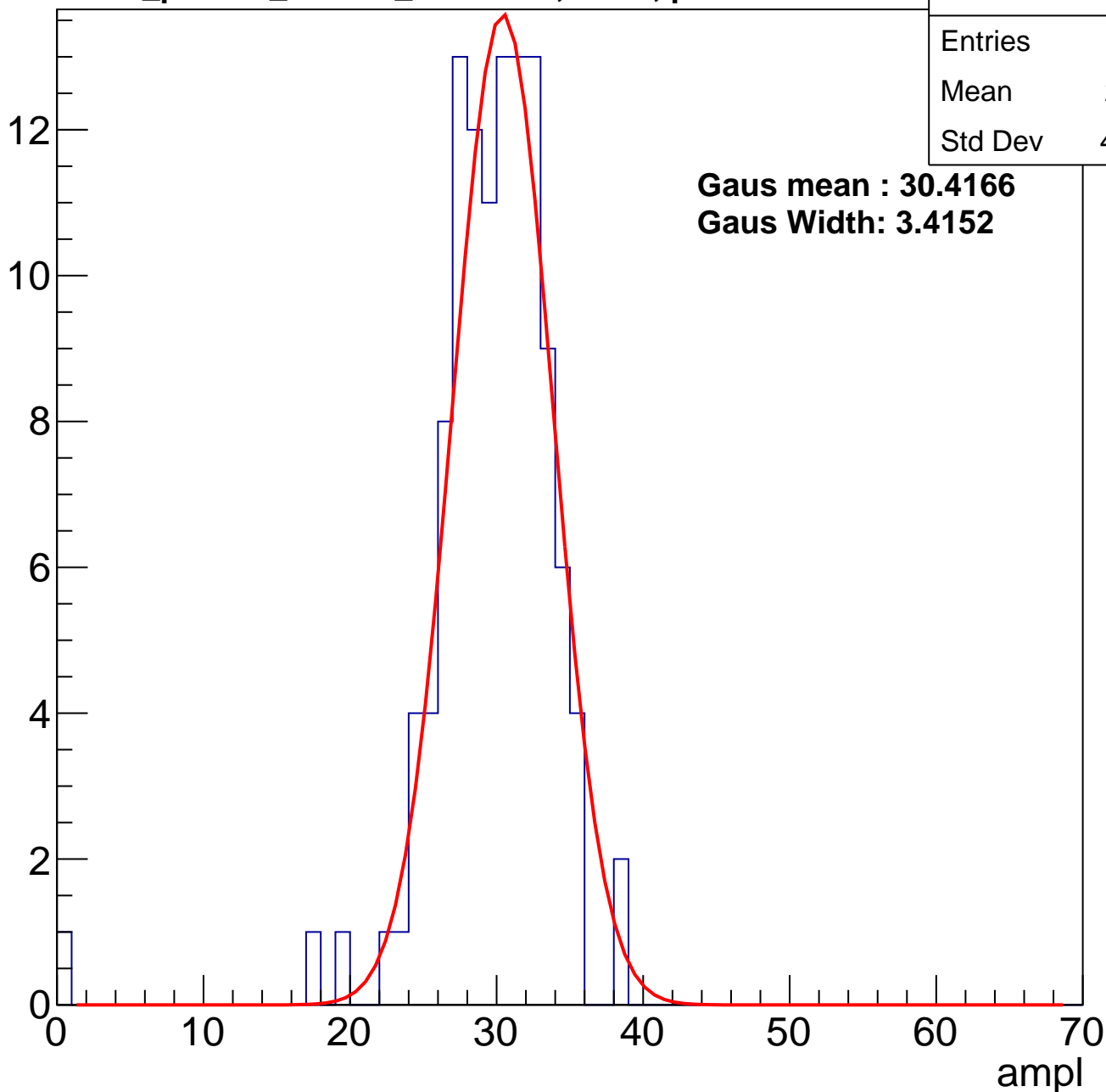
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	29.21
Std Dev	4.382

Gaus mean : 30.4166

Gaus Width: 3.4152

Entry



B0L101S, U15-ch70, adc1

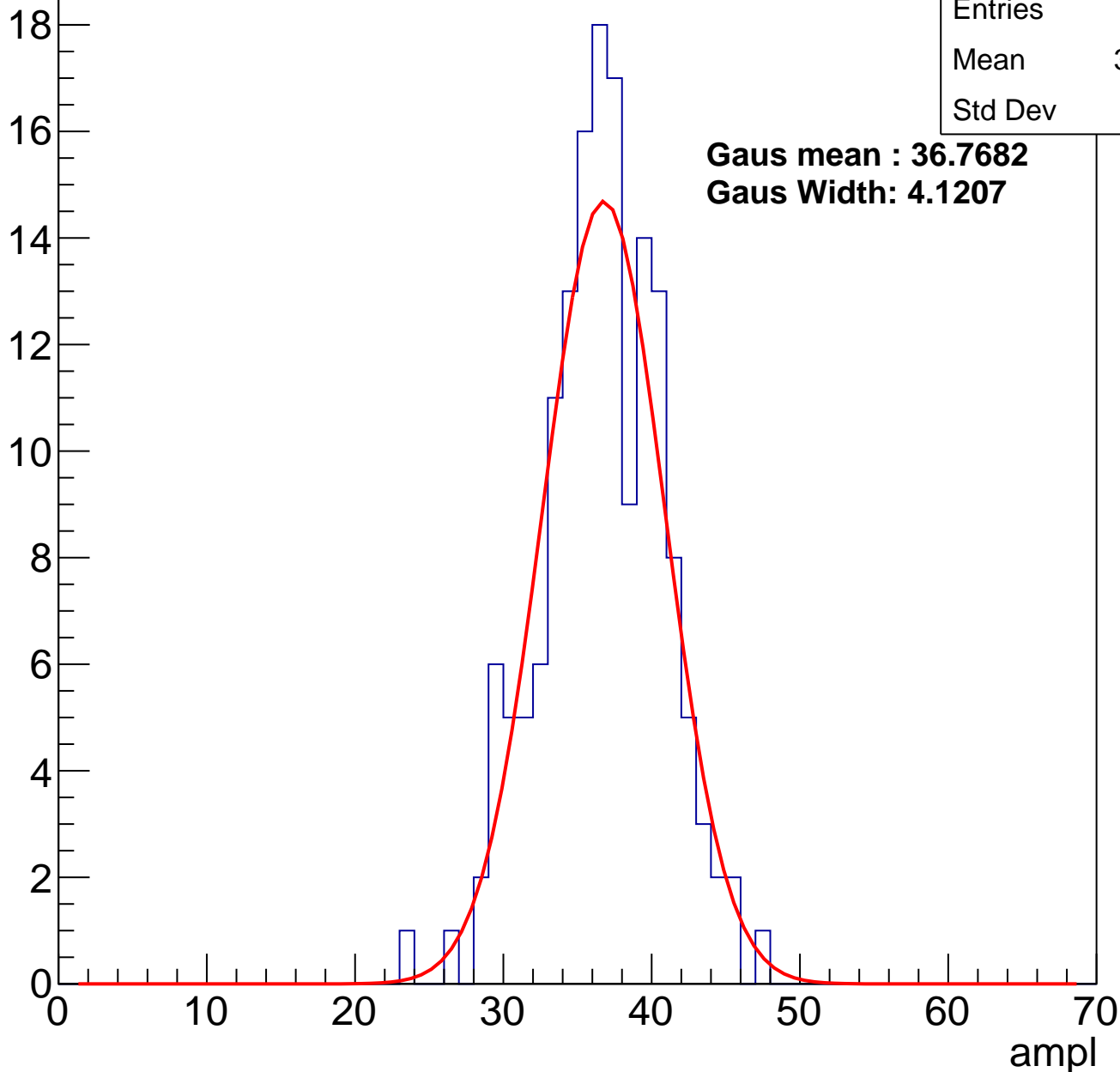
calib_packv5_042523_0143.root, FC#1, port C1

Entries	158
Mean	36.19
Std Dev	4.04

Gaus mean : 36.7682

Gaus Width: 4.1207

Entry



B0L101S, U15-ch70, adc2

calib_packv5_042523_0143.root, FC#1, port C1

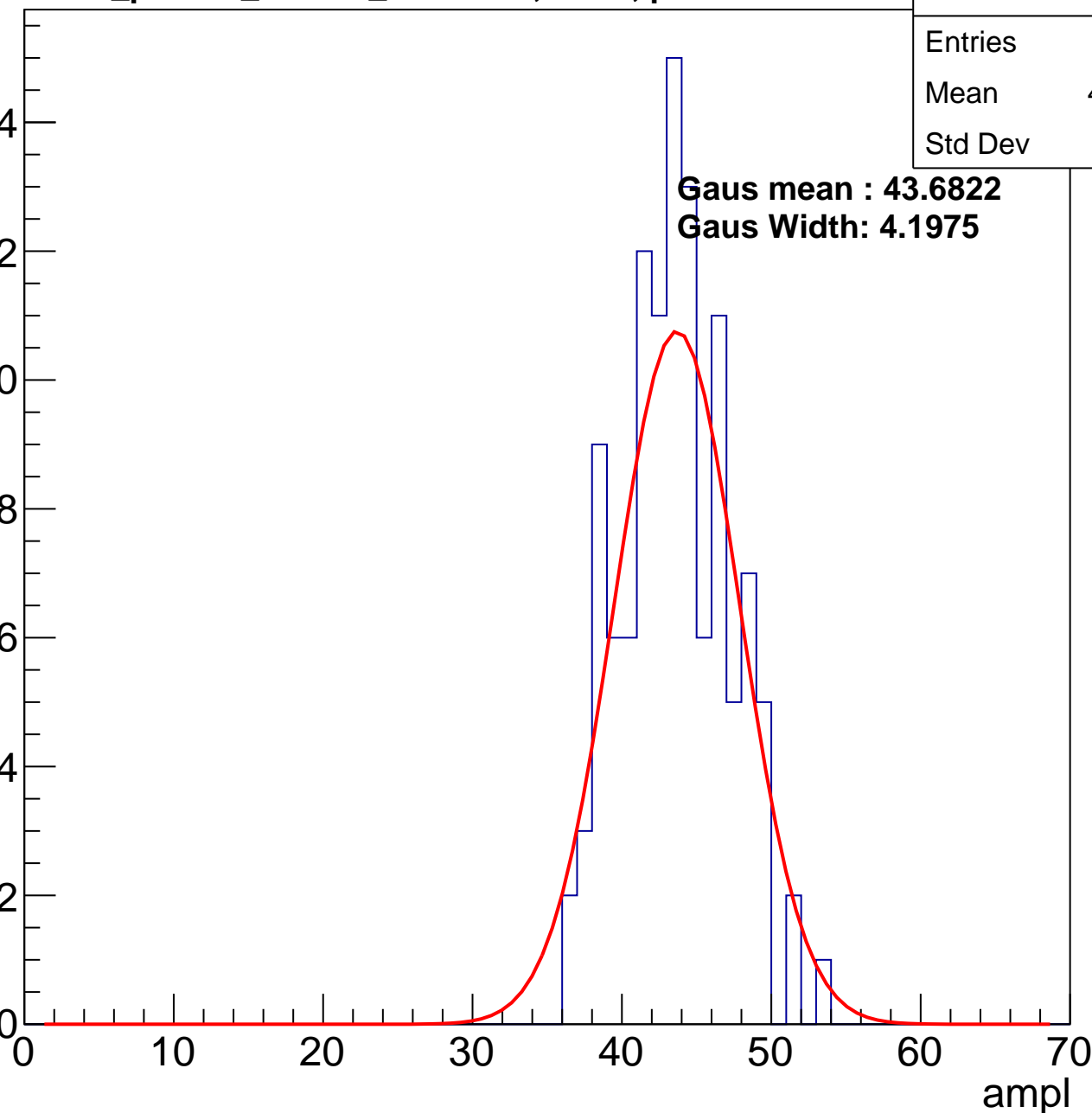
Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	43.13
Std Dev	3.55

Gaus mean : 43.6822

Gaus Width: 4.1975

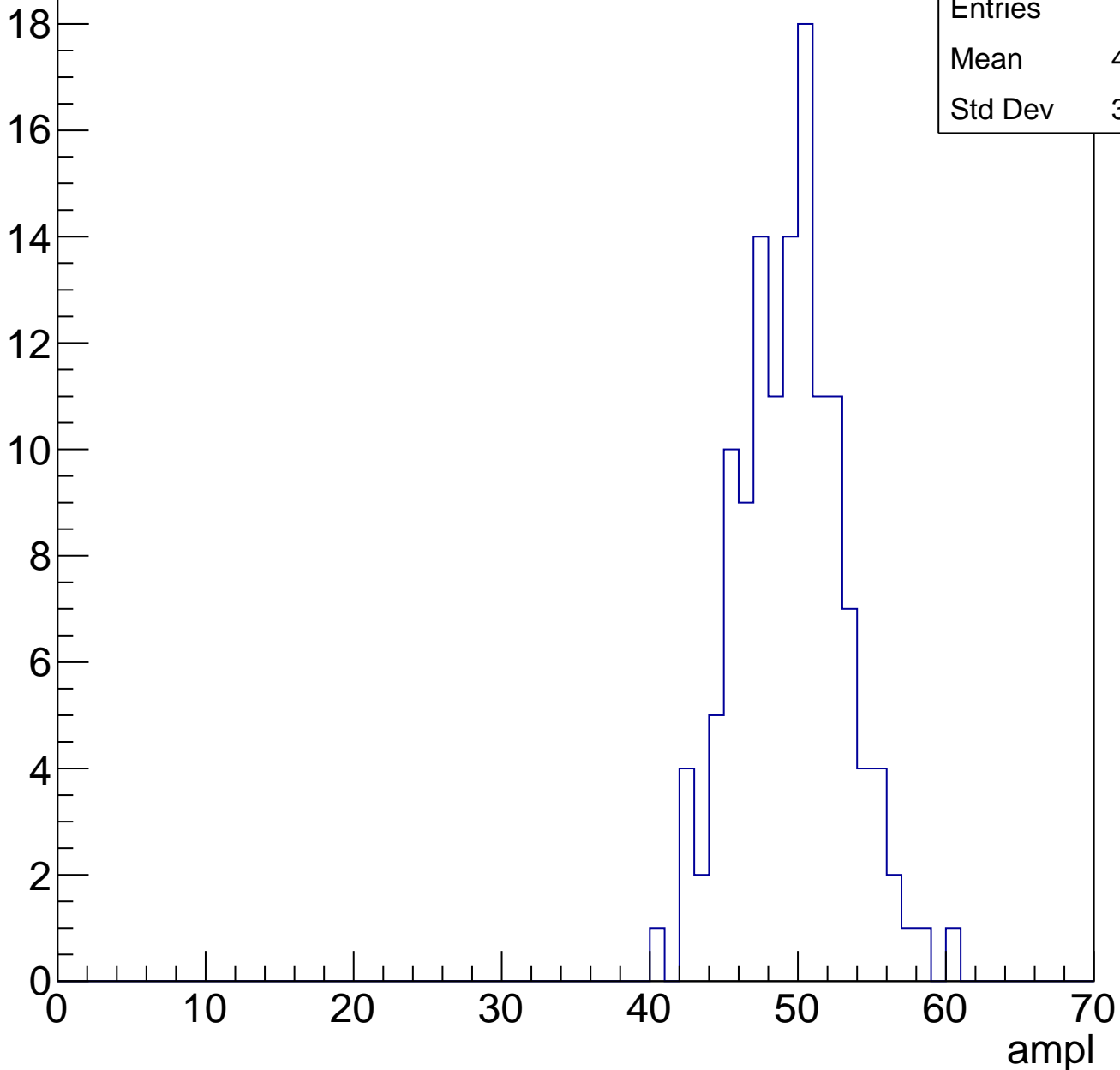


B0L101S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	49.05
Std Dev	3.576

Entry



B0L101S, U15-ch70, adc4

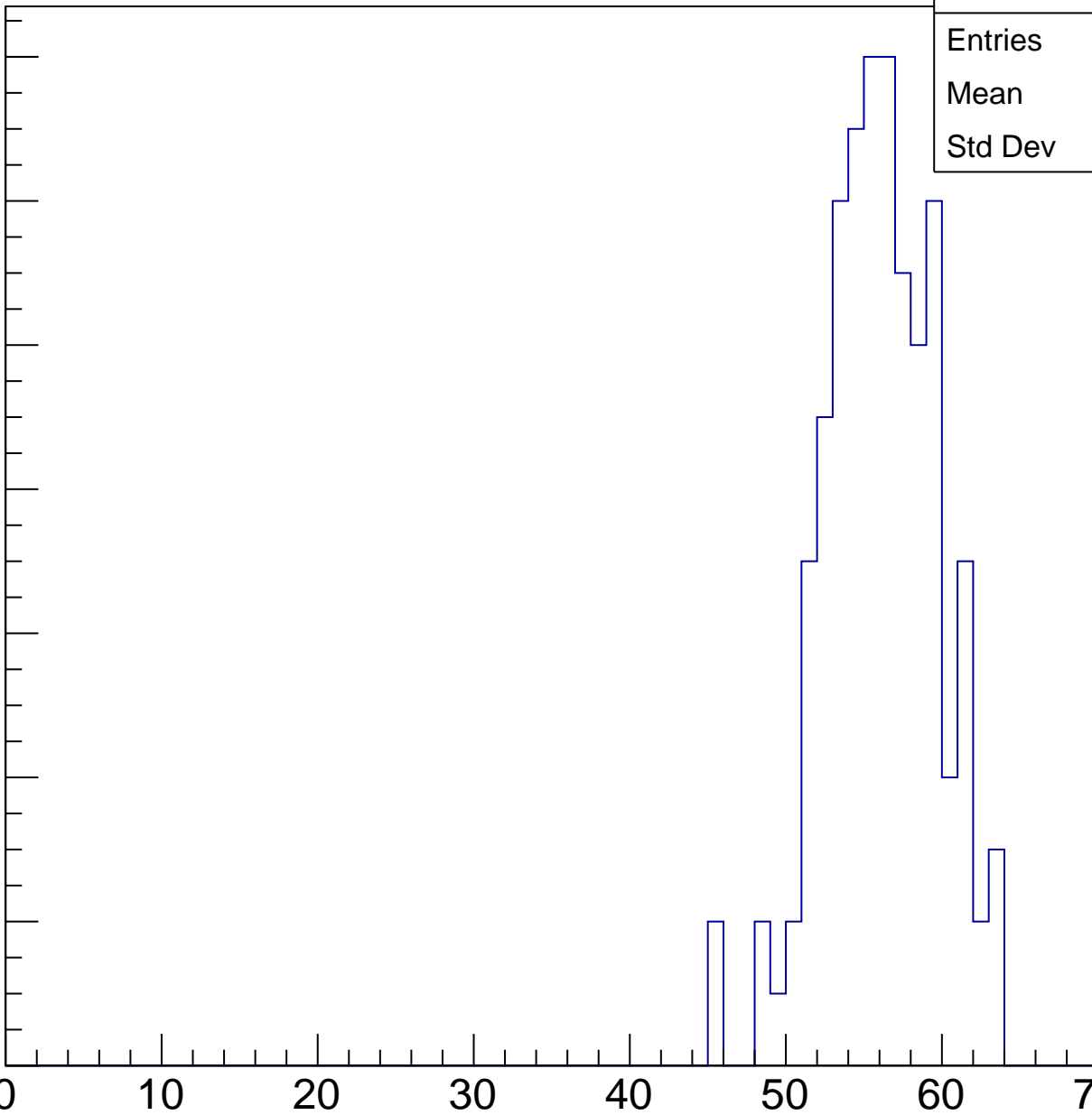
calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	55.58
Std Dev	3.554

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch70, adc5

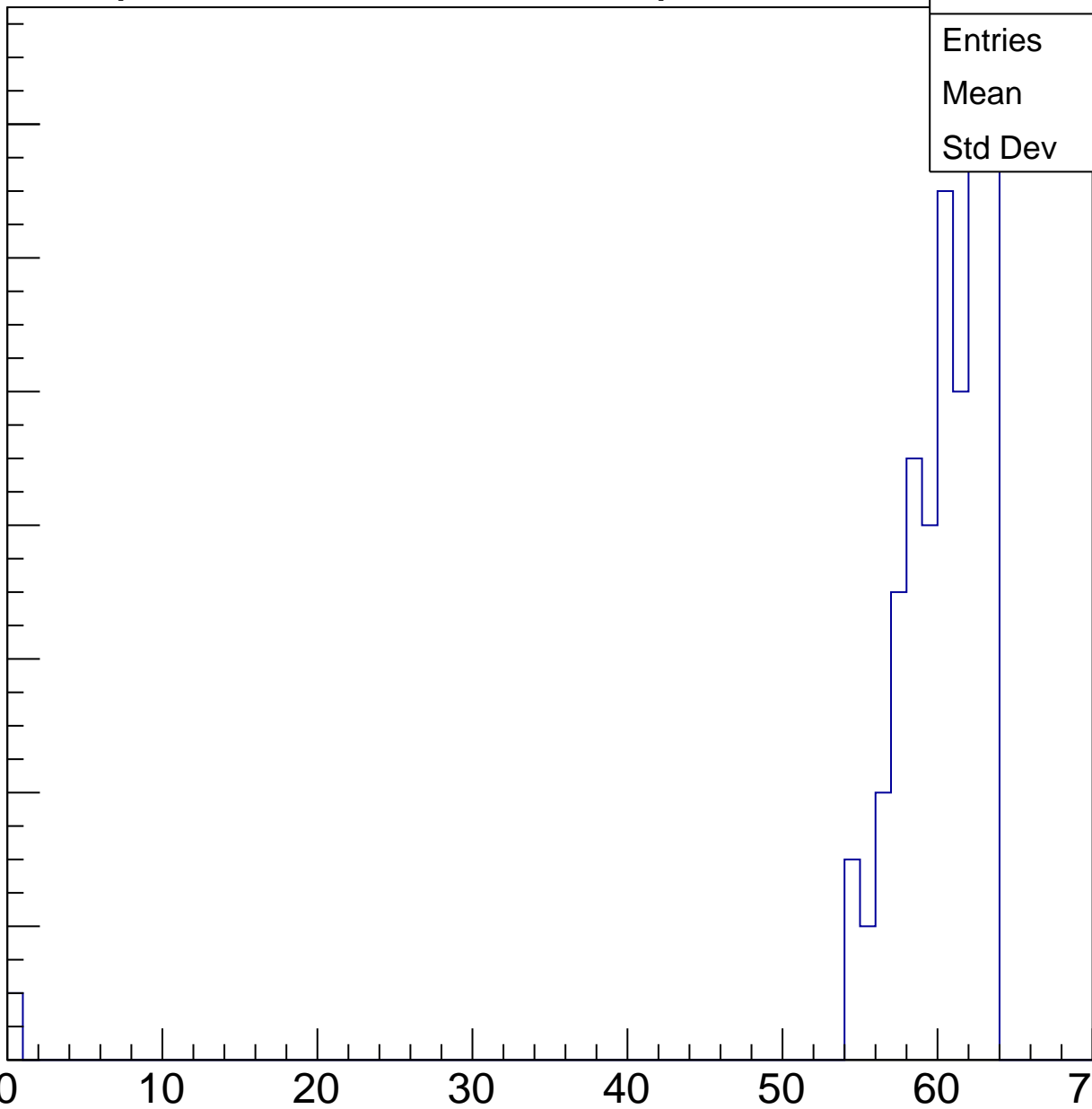
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	87
Mean	59.24
Std Dev	6.851

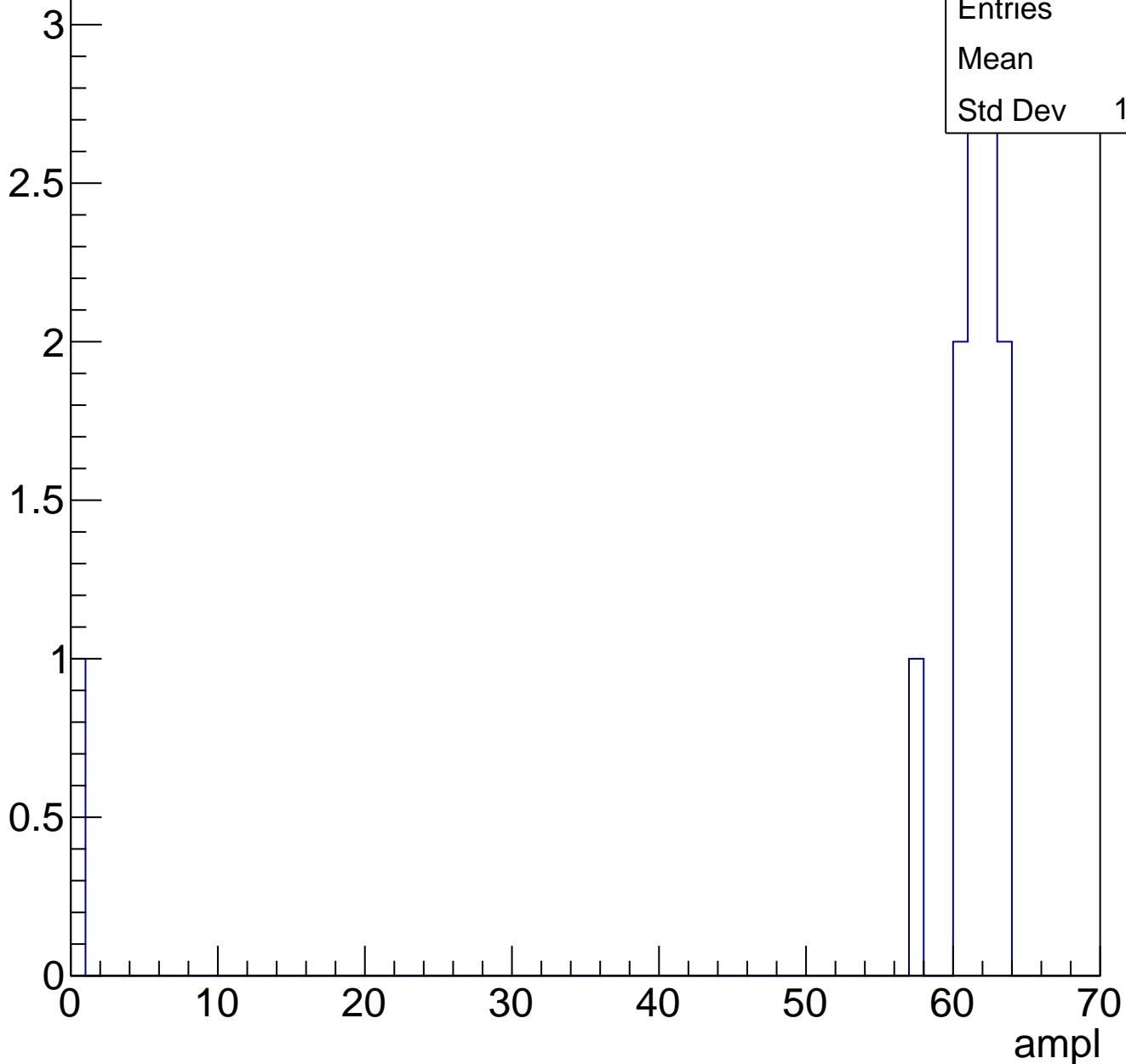
ampl



B0L101S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch71, adc0

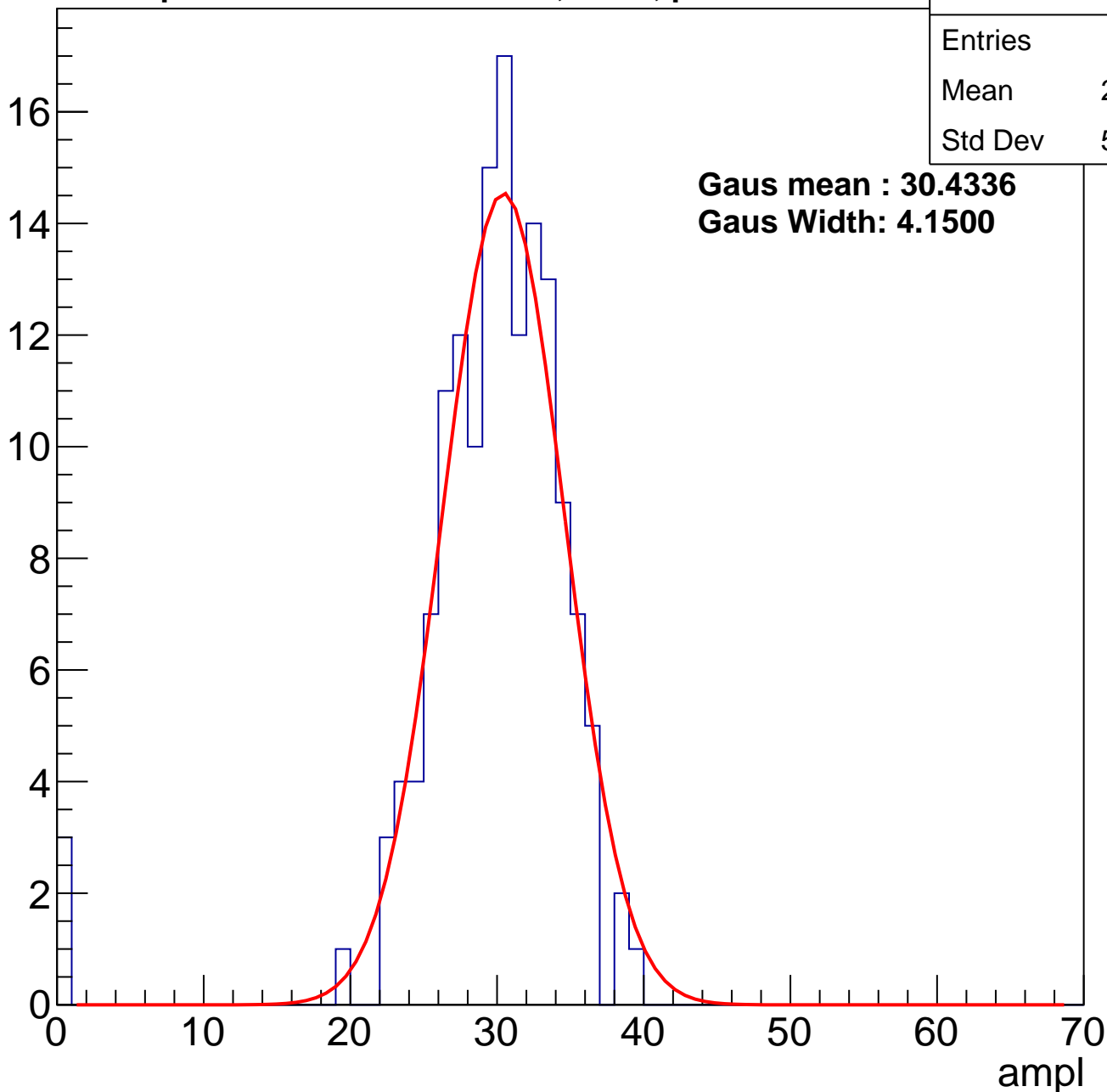
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	29.19
Std Dev	5.574

Gaus mean : 30.4336

Gaus Width: 4.1500

Entry



B0L101S, U15-ch71, adc1

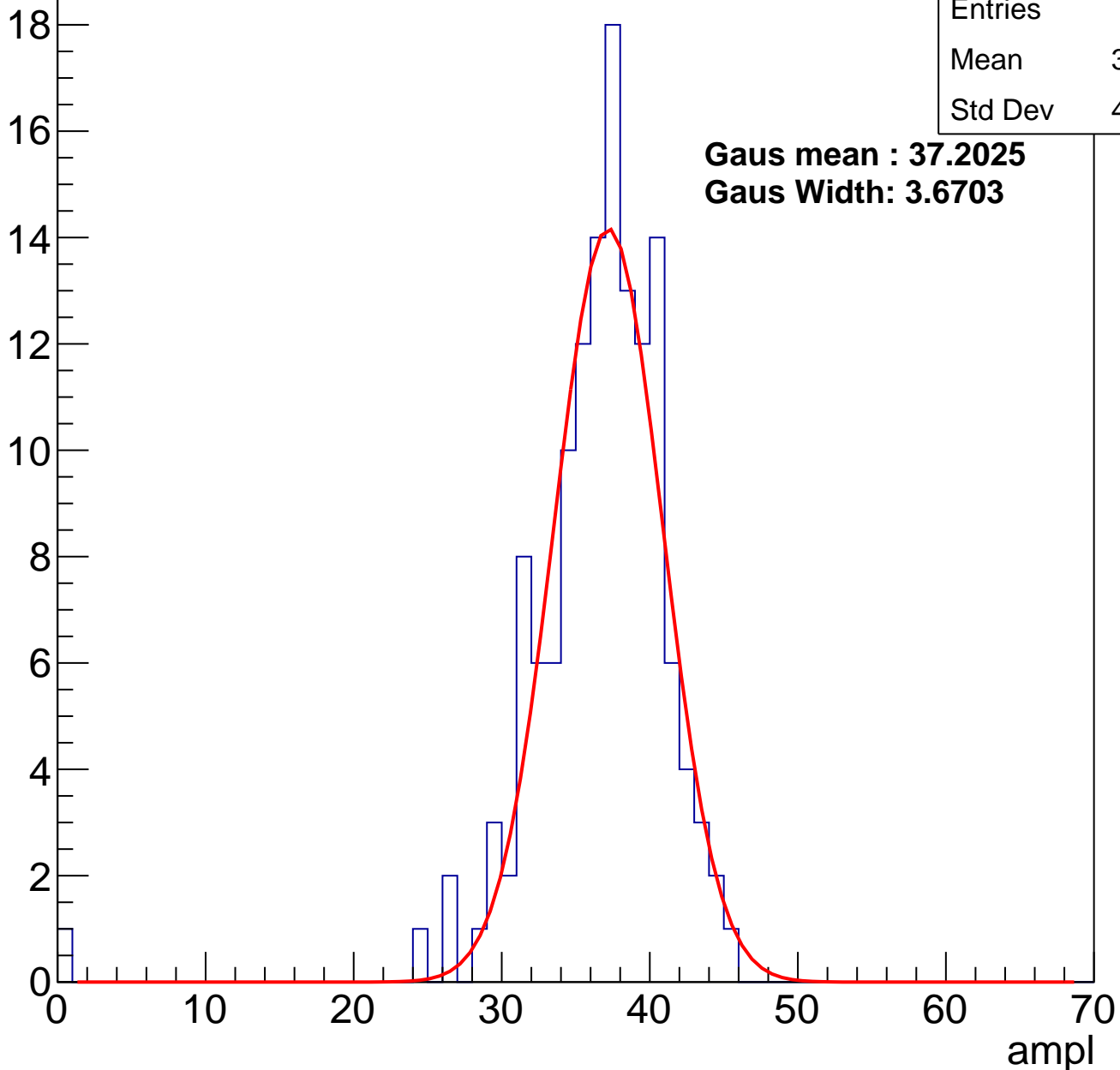
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	36.09
Std Dev	4.928

Gaus mean : 37.2025

Gaus Width: 3.6703

Entry



B0L101S, U15-ch71, adc2

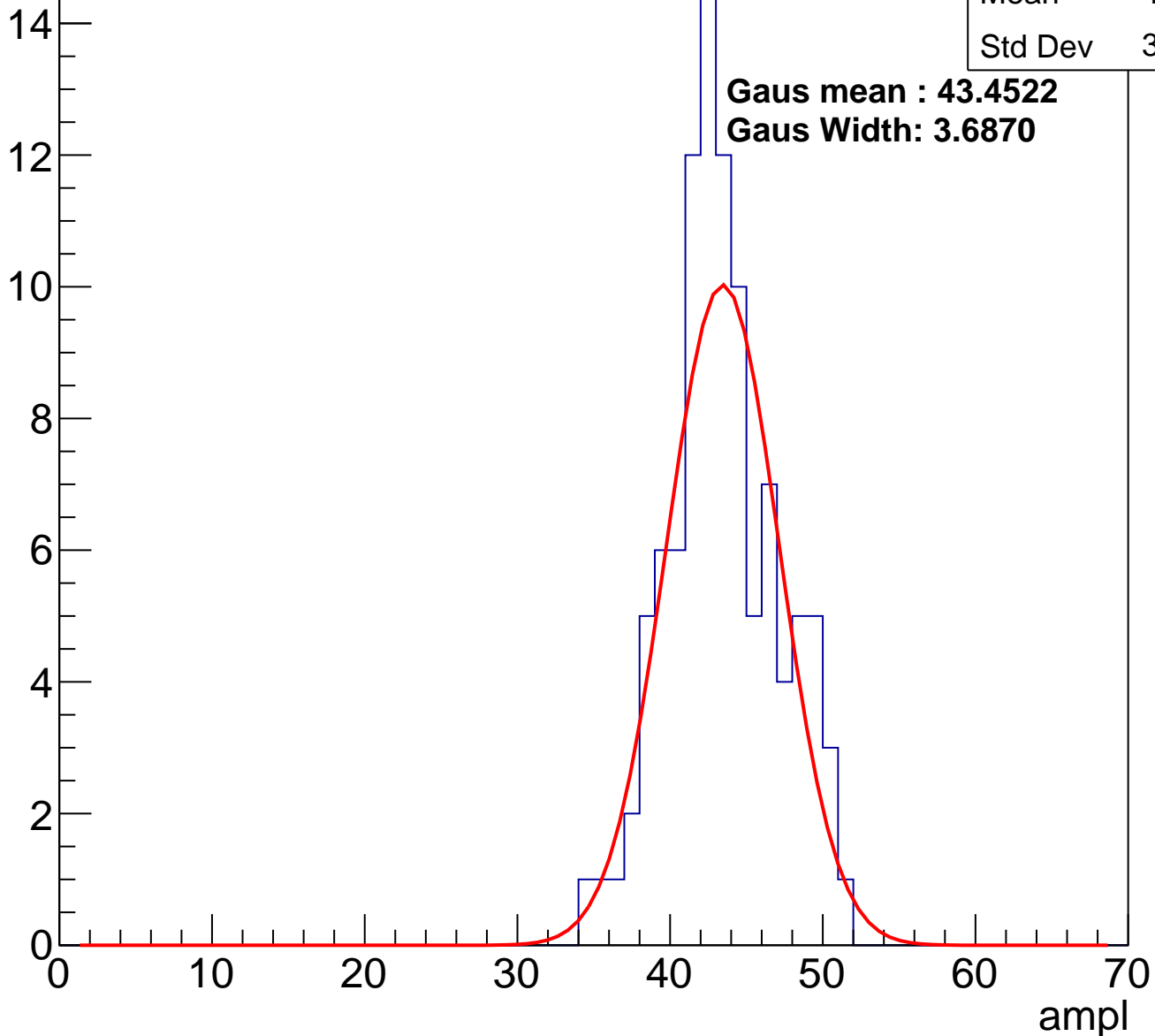
calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	42.99
Std Dev	3.556

Gaus mean : 43.4522

Gaus Width: 3.6870

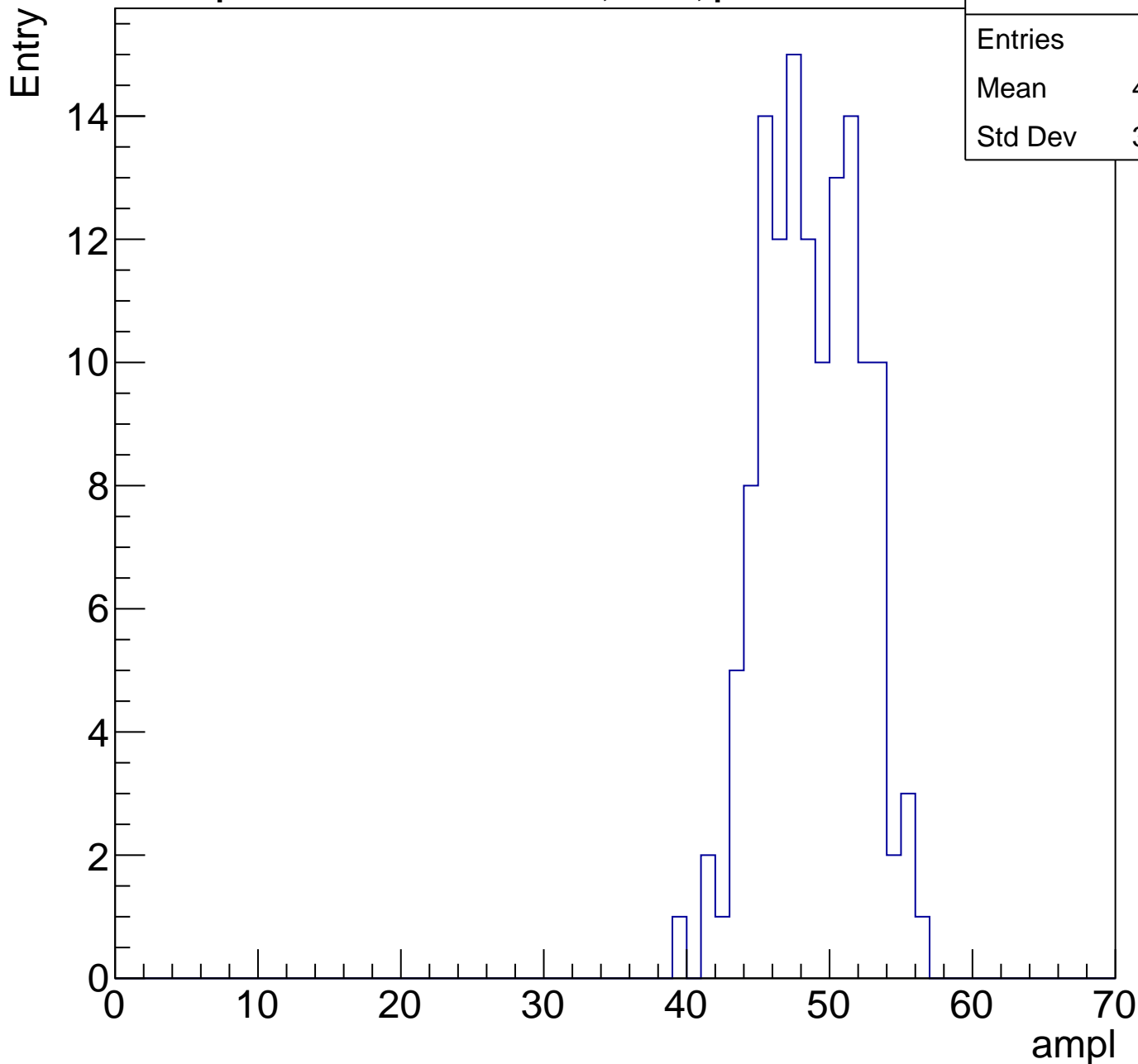
Entry



B0L101S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#1, port C1

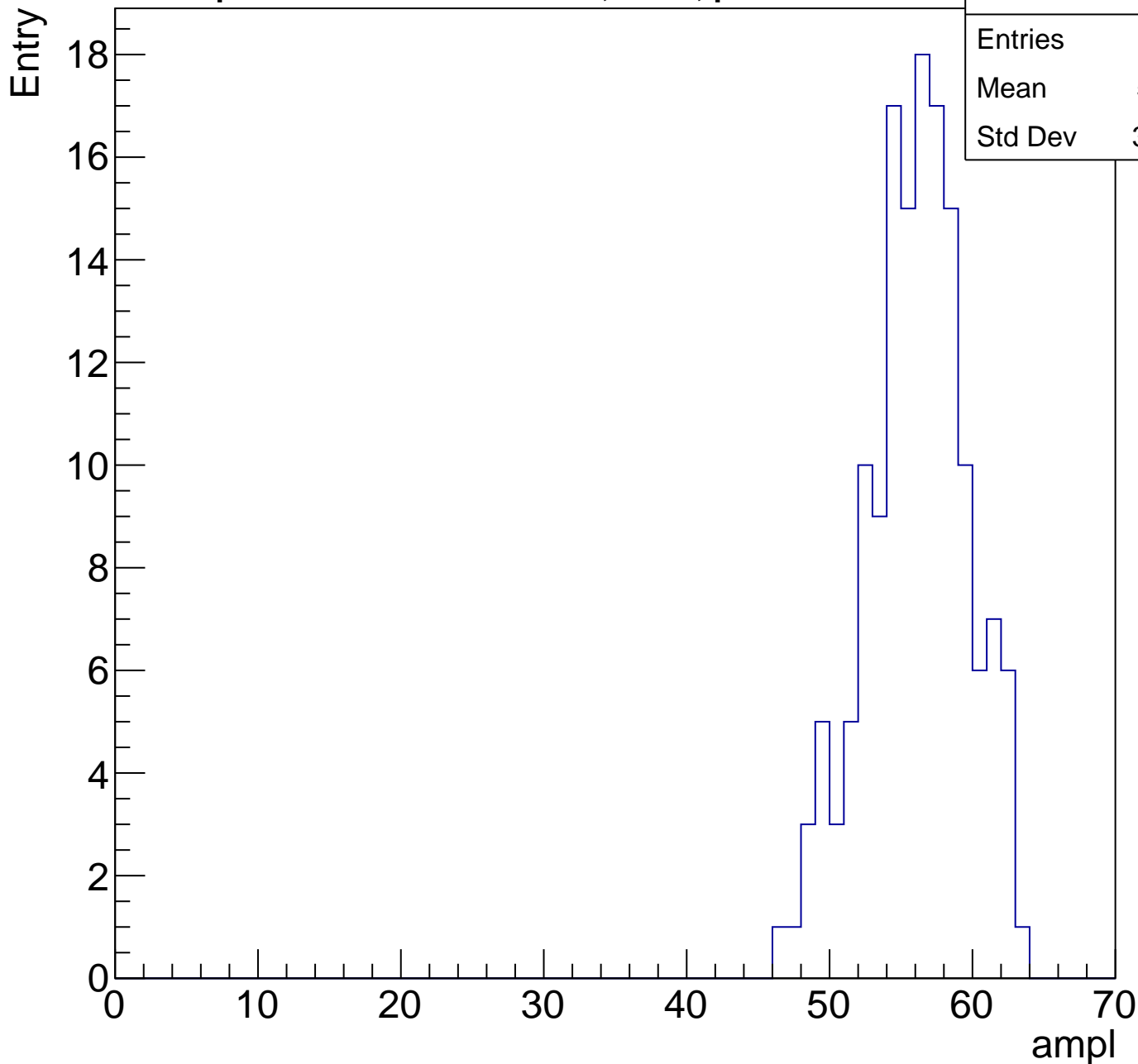
Entries	133
Mean	48.32
Std Dev	3.386



B0L101S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	55.61
Std Dev	3.529



B0L101S, U15-ch71, adc5

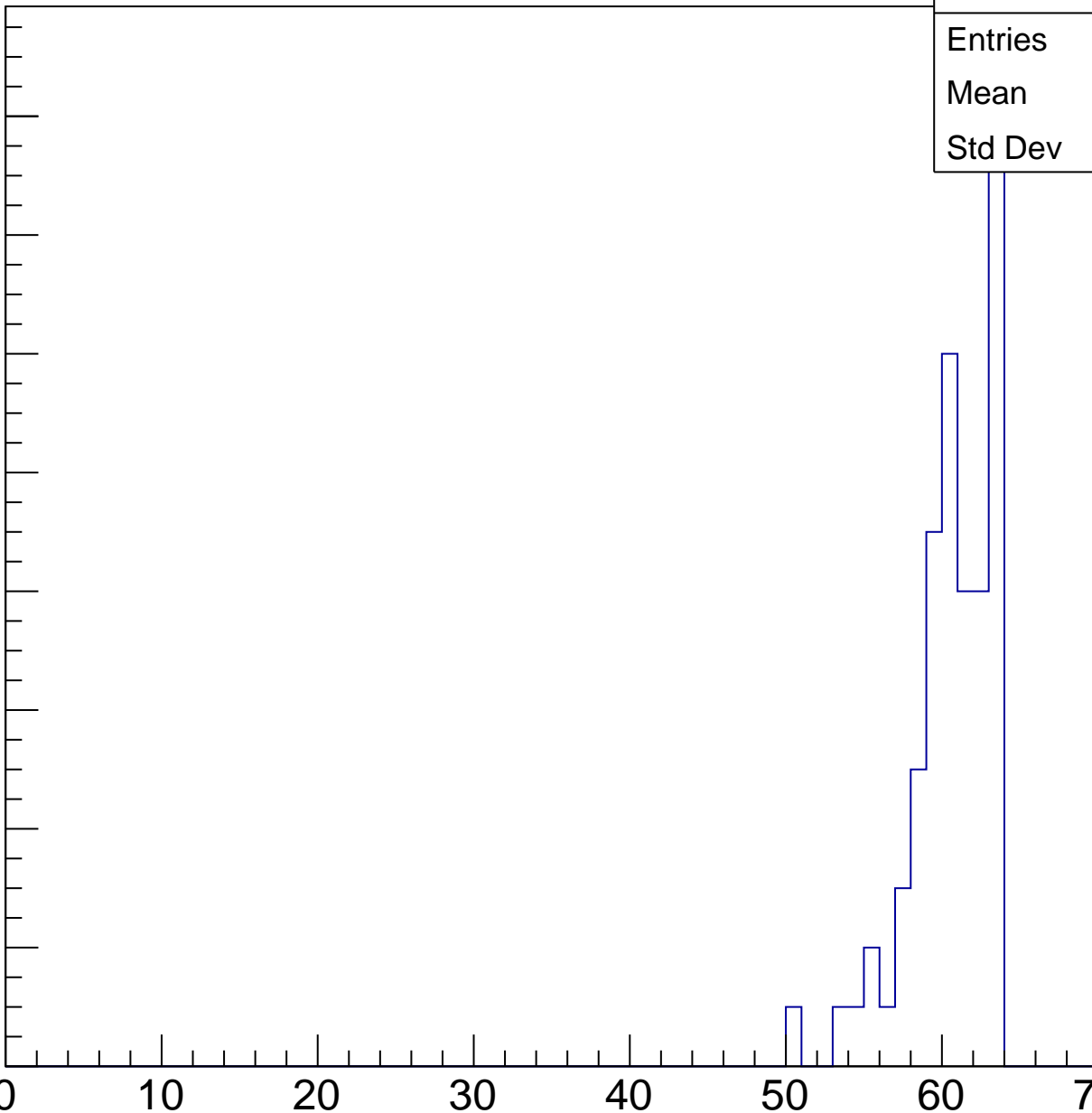
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	68
Mean	60.15
Std Dev	2.708

ampl

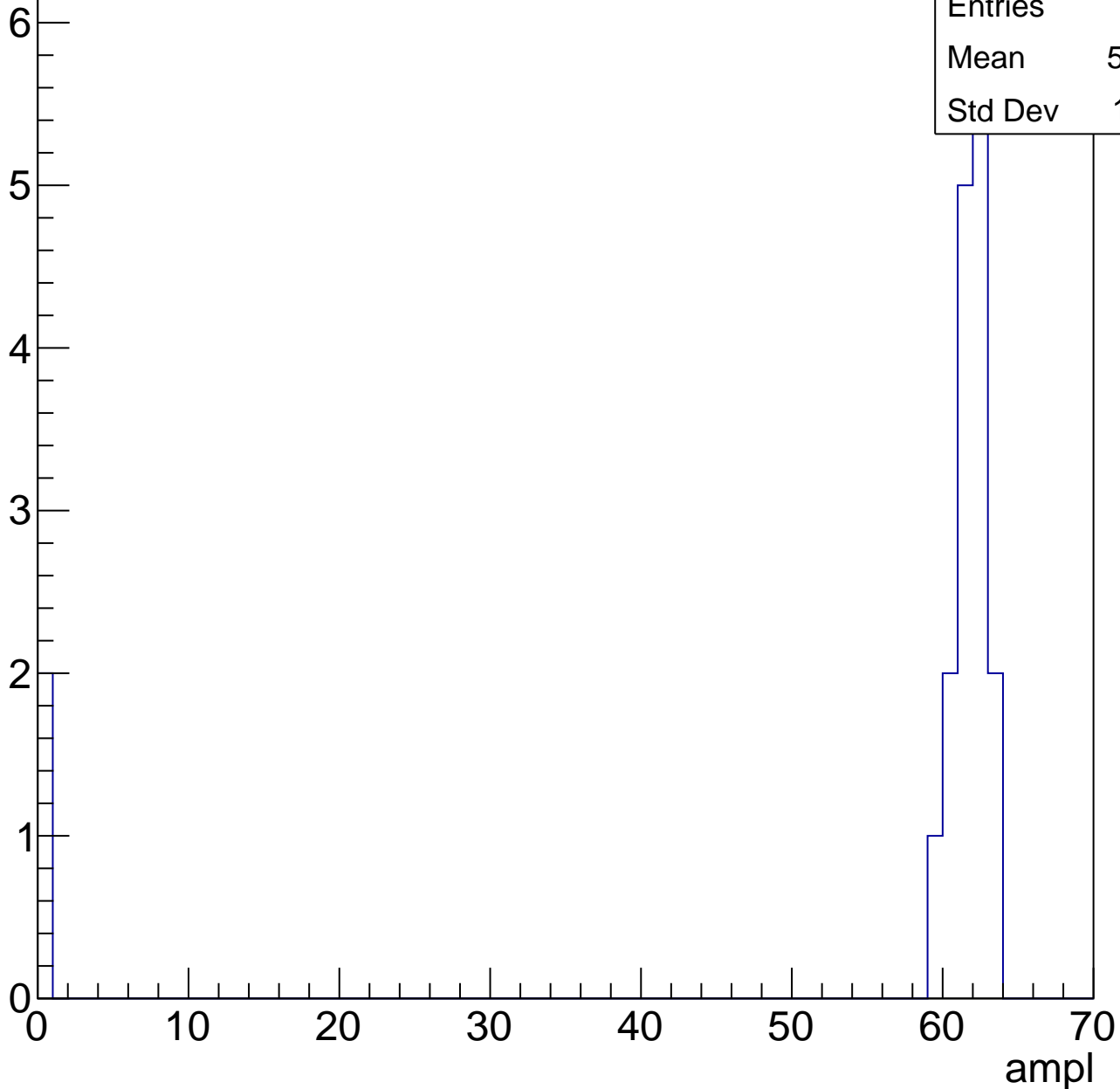


B0L101S, U15-ch71, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	18
Mean	54.56
Std Dev	19.31



B0L101S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

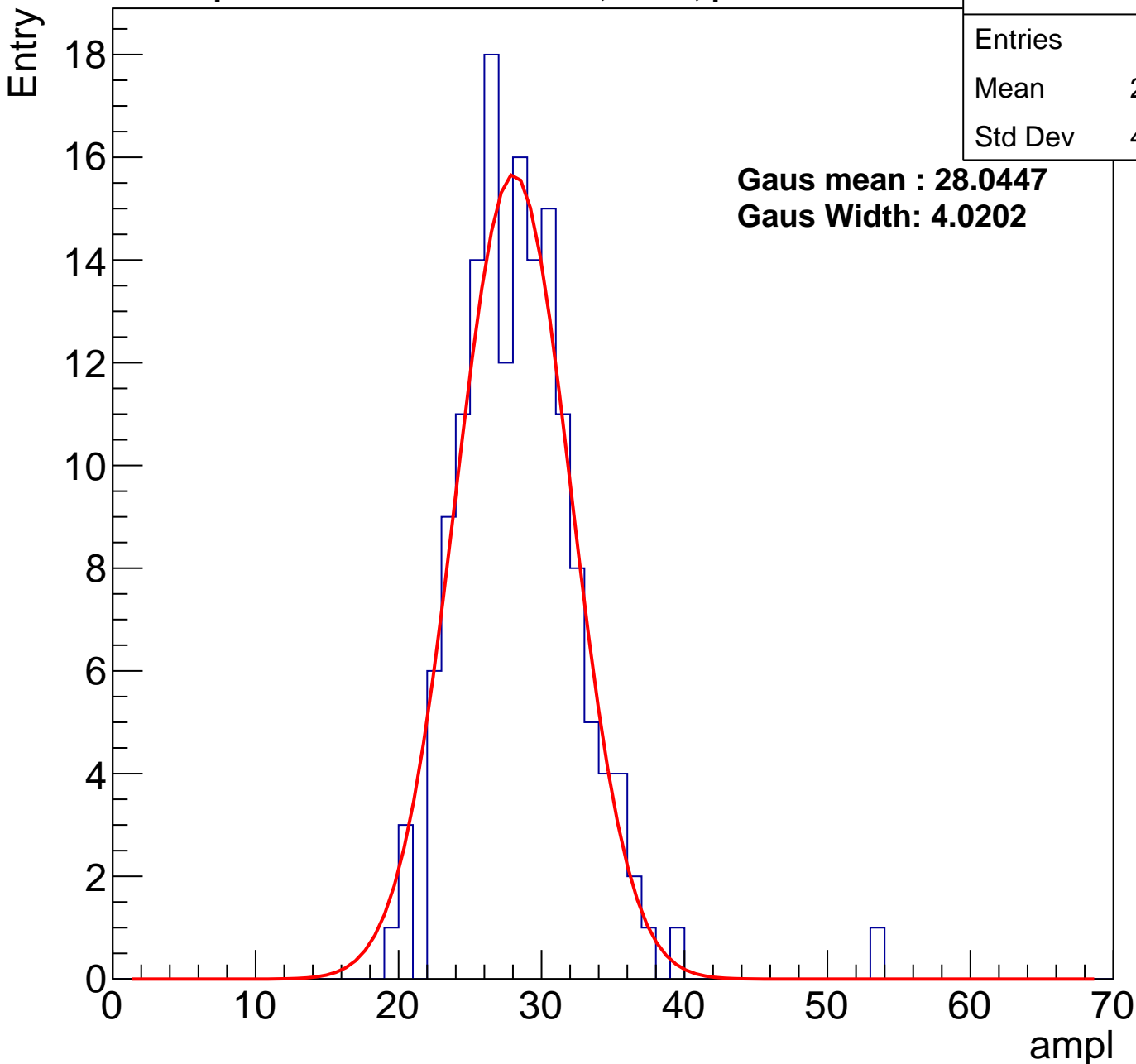
B0L101S, U15-ch72, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	27.99
Std Dev	4.252

Gaus mean : 28.0447

Gaus Width: 4.0202



B0L101S, U15-ch72, adc1

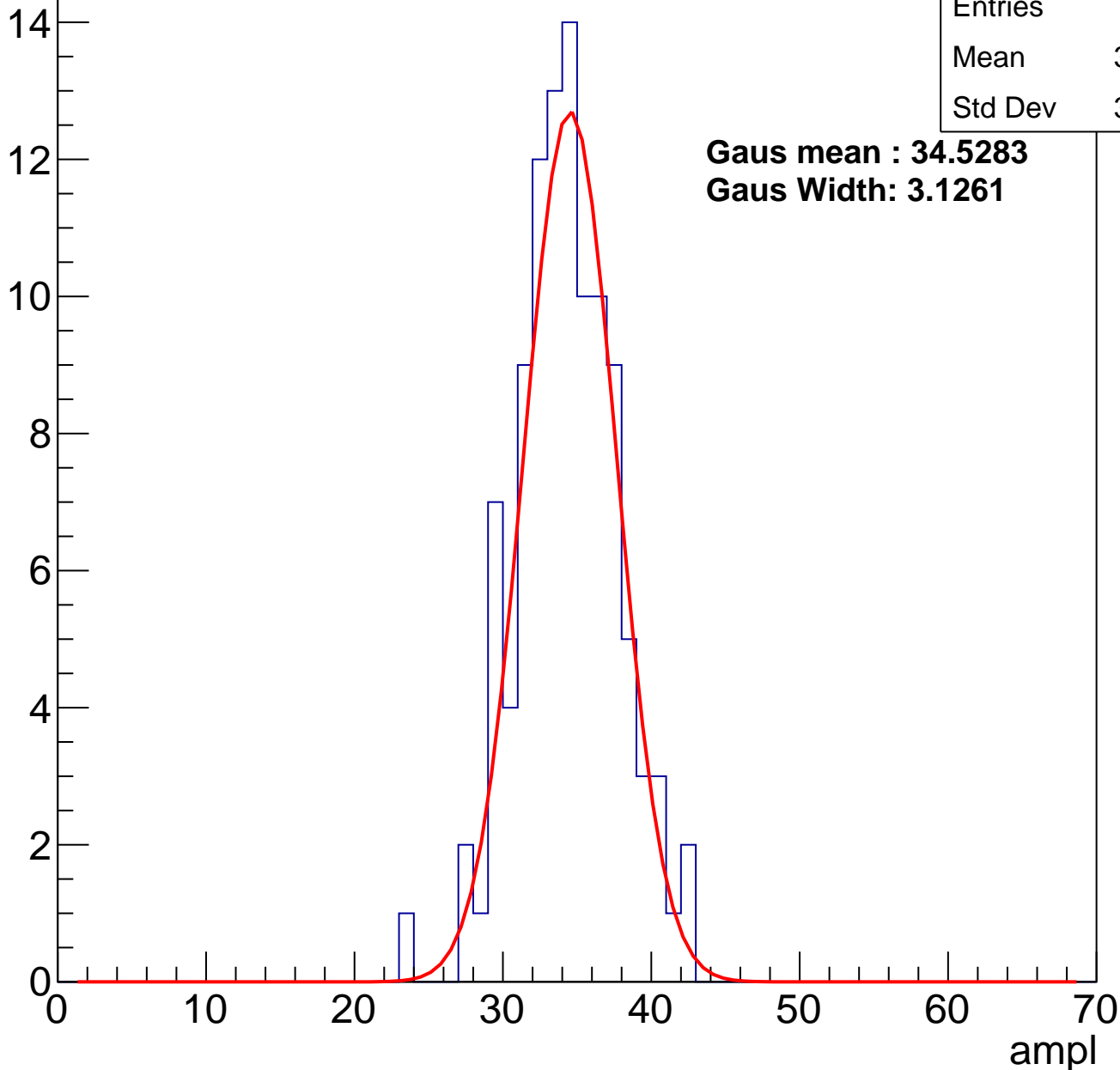
calib_packv5_042523_0143.root, FC#1, port C1

Entries	106
Mean	33.88
Std Dev	3.372

Gaus mean : 34.5283

Gaus Width: 3.1261

Entry



B0L101S, U15-ch72, adc2

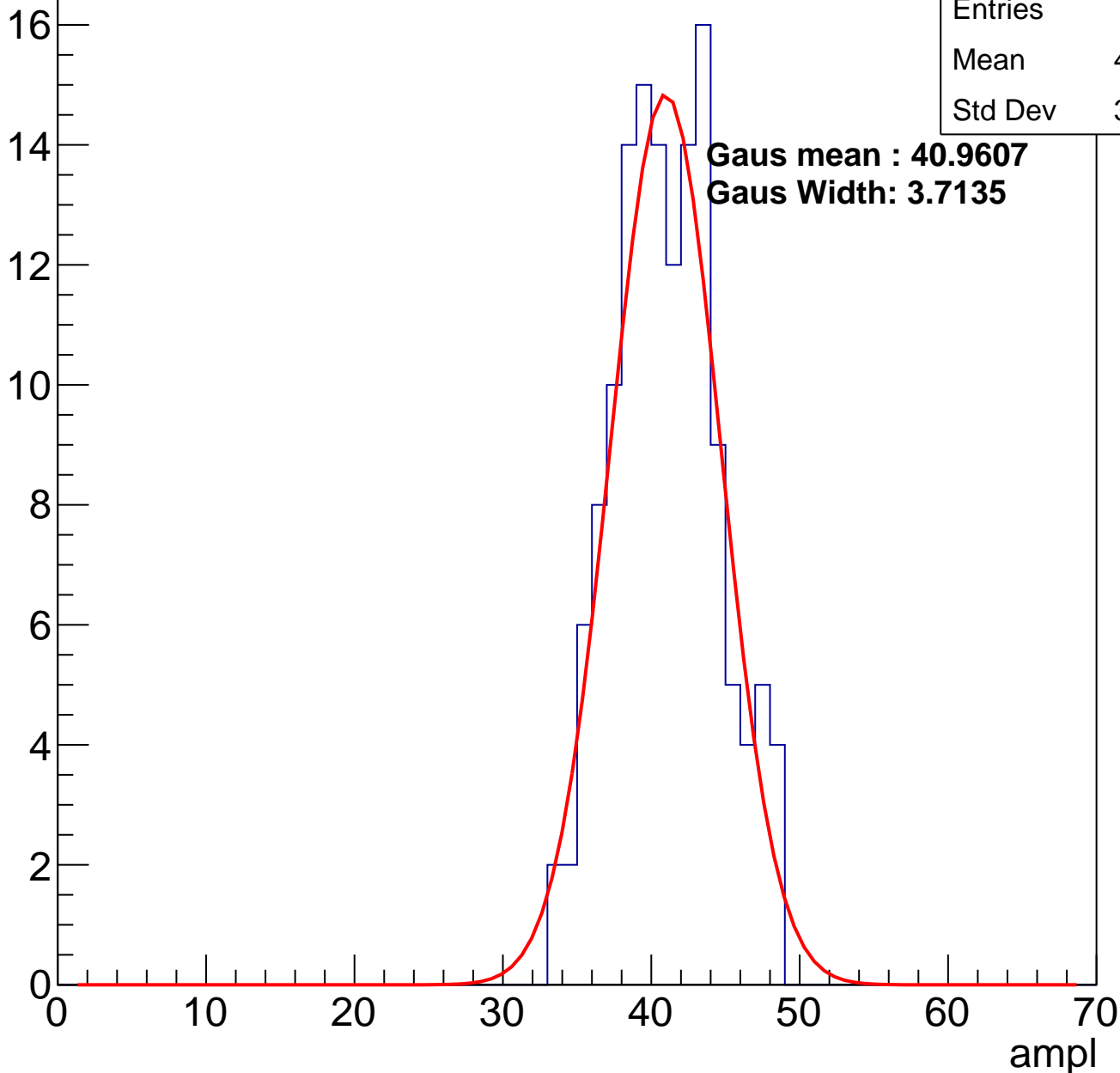
calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	40.56
Std Dev	3.475

Gaus mean : 40.9607

Gaus Width: 3.7135

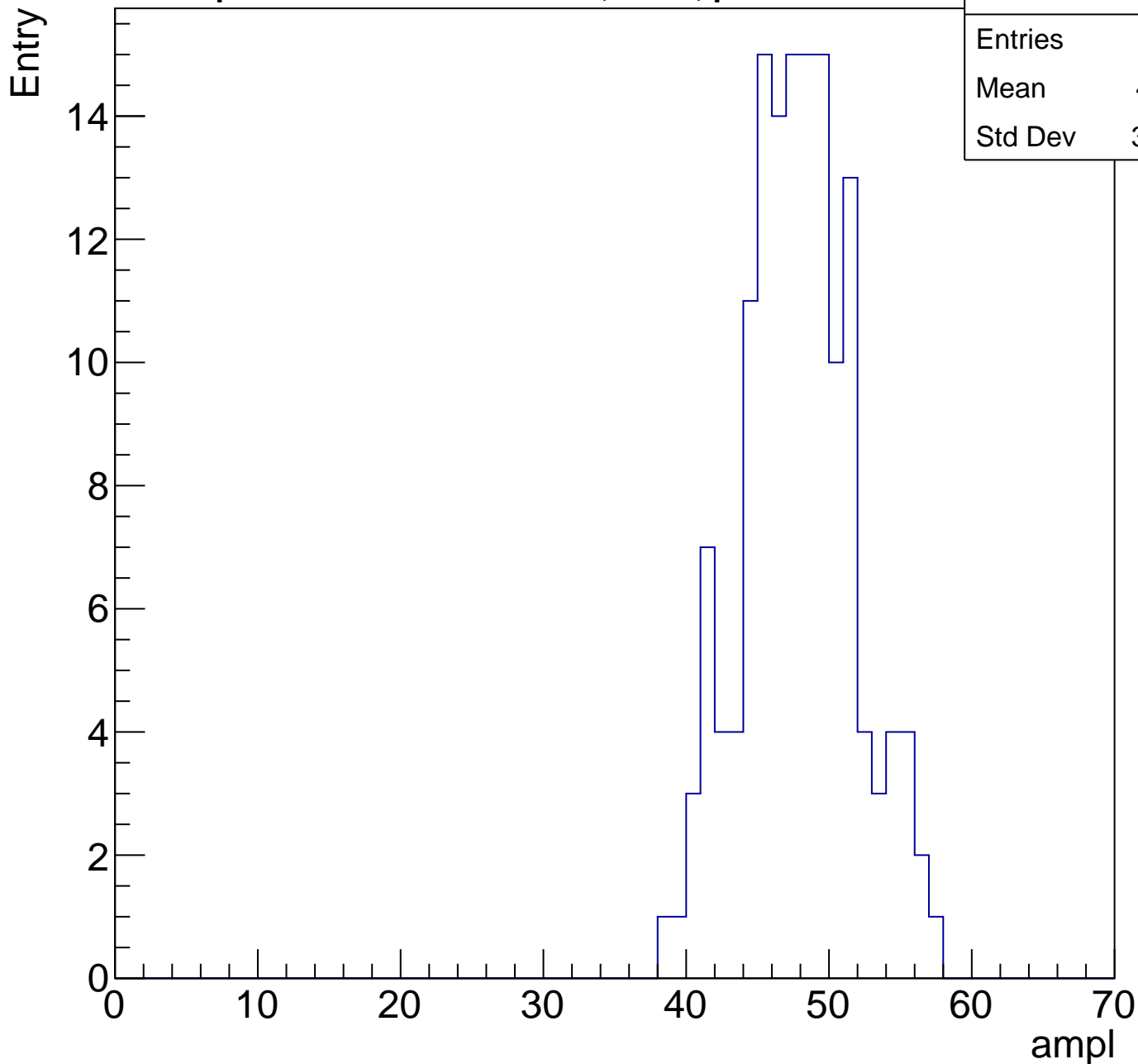
Entry



B0L101S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#1, port C1

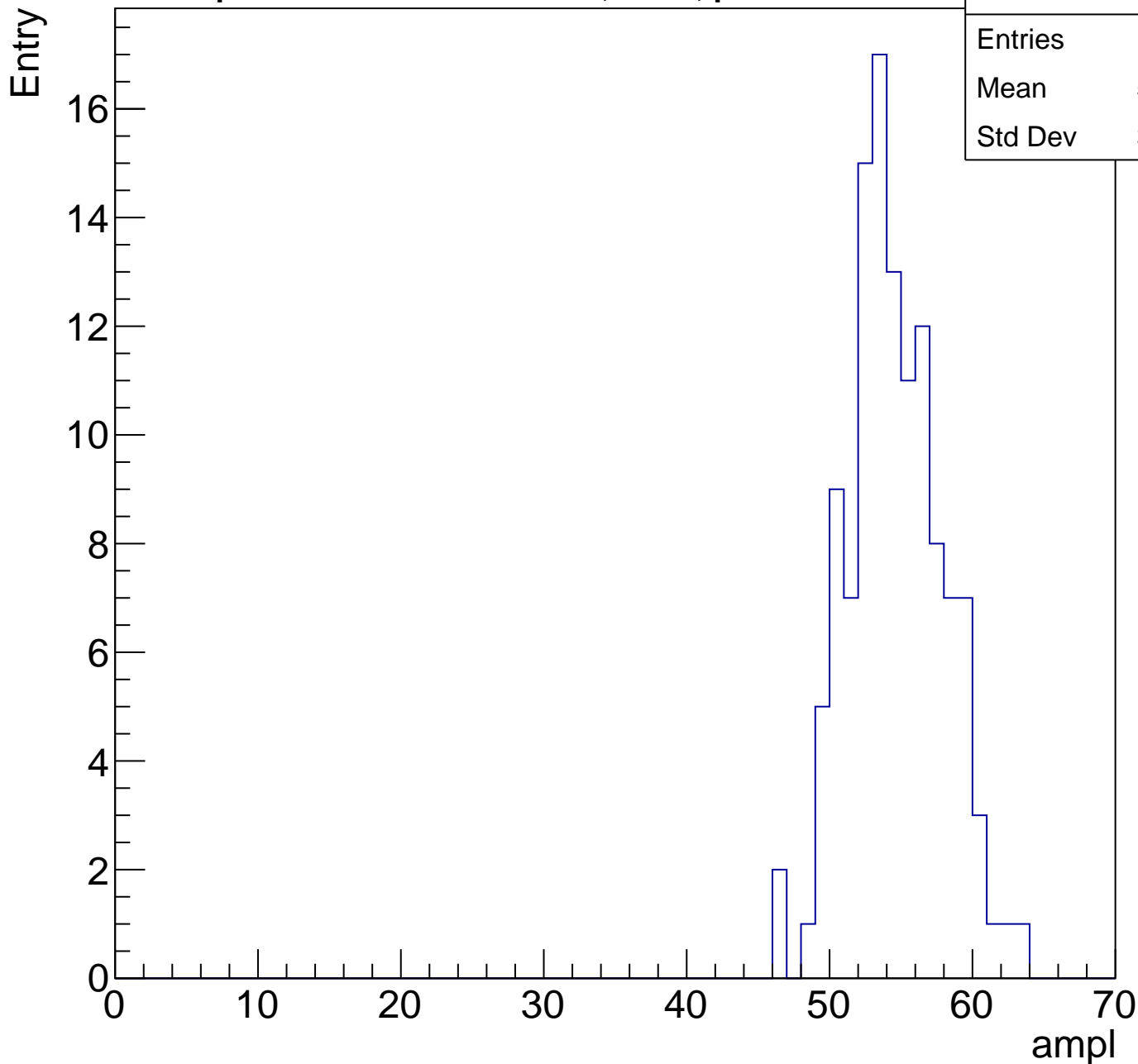
Entries	146
Mean	47.41
Std Dev	3.839



B0L101S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	54.11
Std Dev	3.281



B0L101S, U15-ch72, adc5

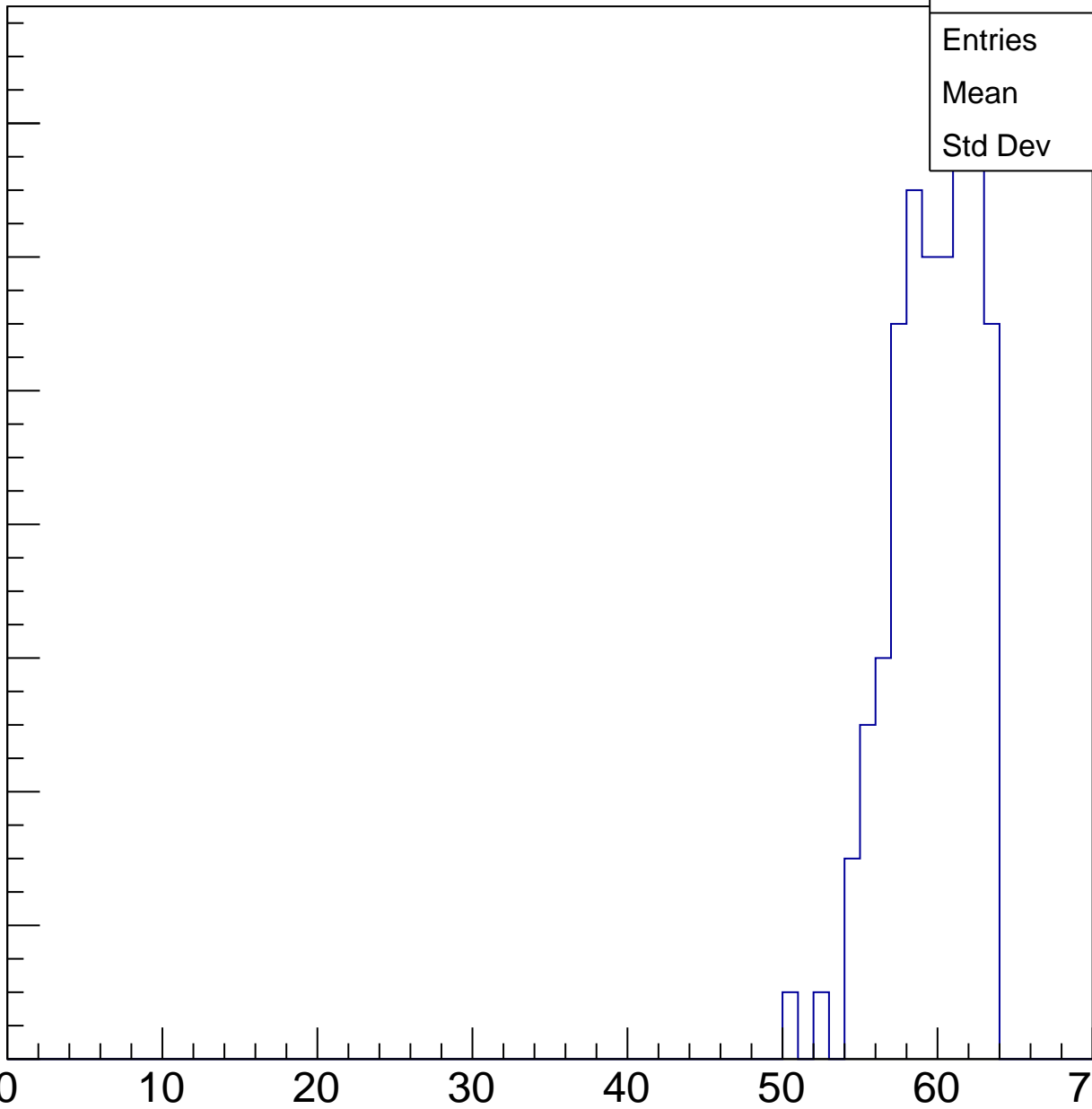
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	59.23
Std Dev	2.711

ampl

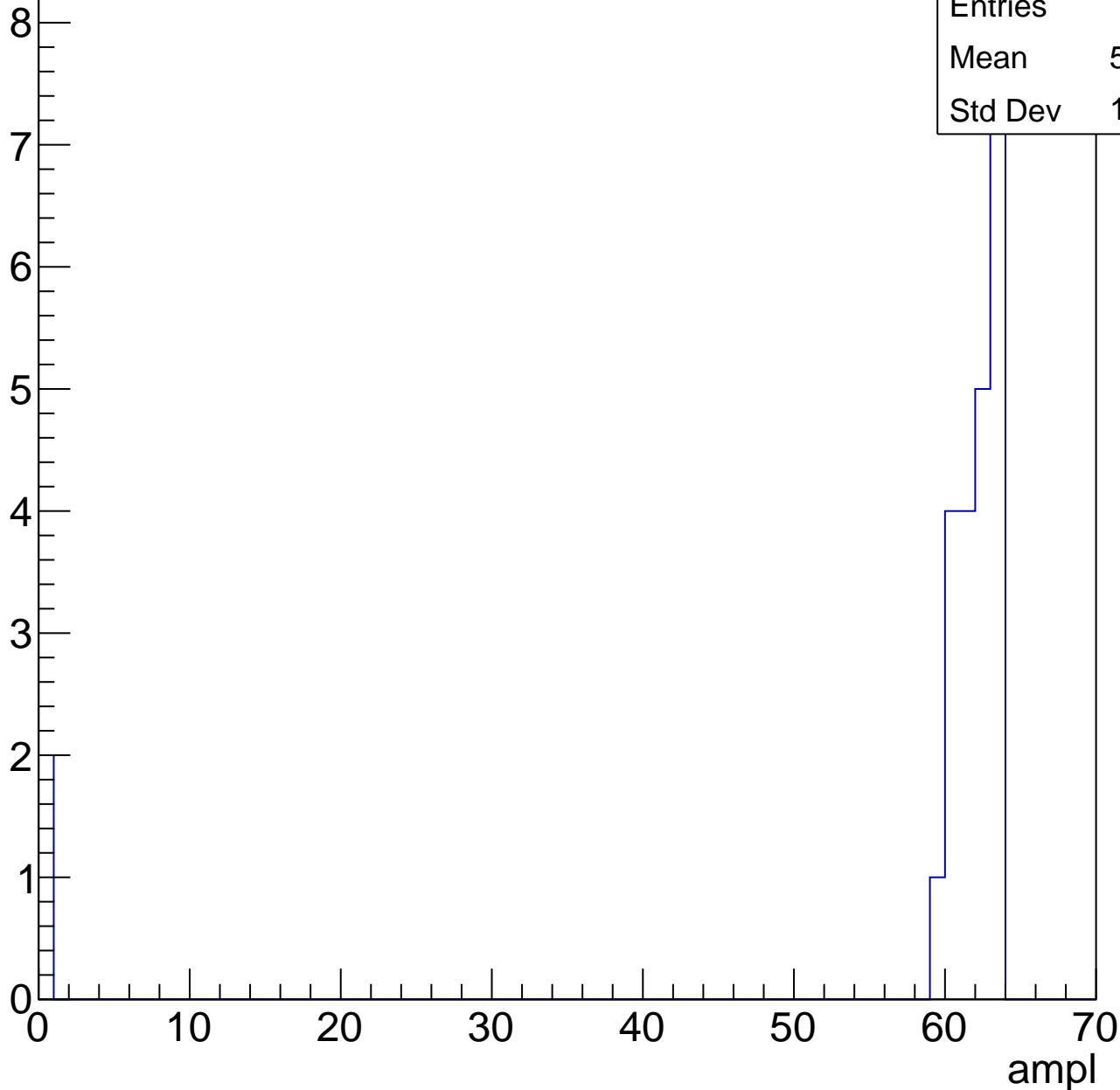


B0L101S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	56.54
Std Dev	17.09



B0L101S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



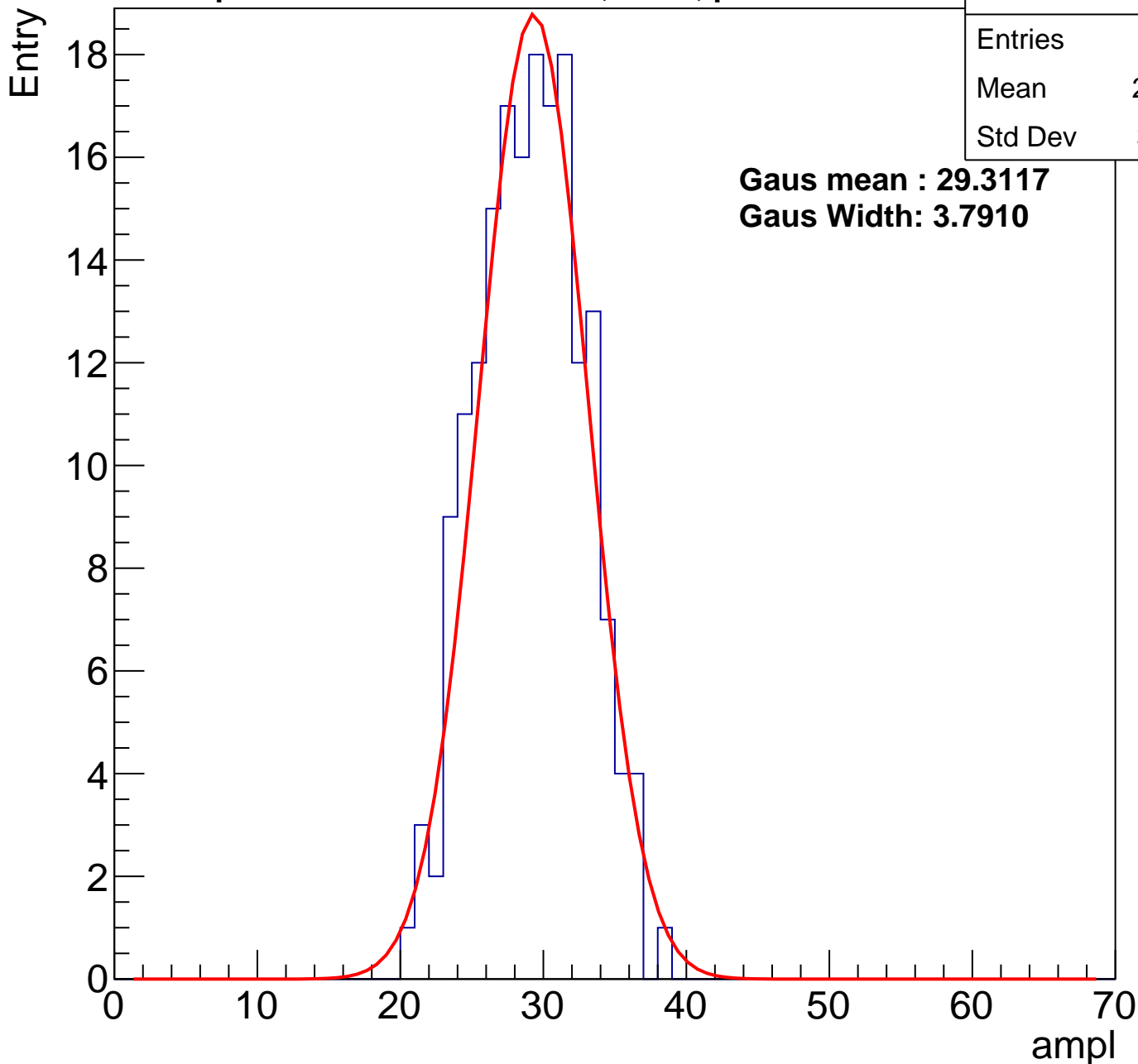
B0L101S, U15-ch73, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	28.66
Std Dev	3.621

Gaus mean : 29.3117

Gaus Width: 3.7910



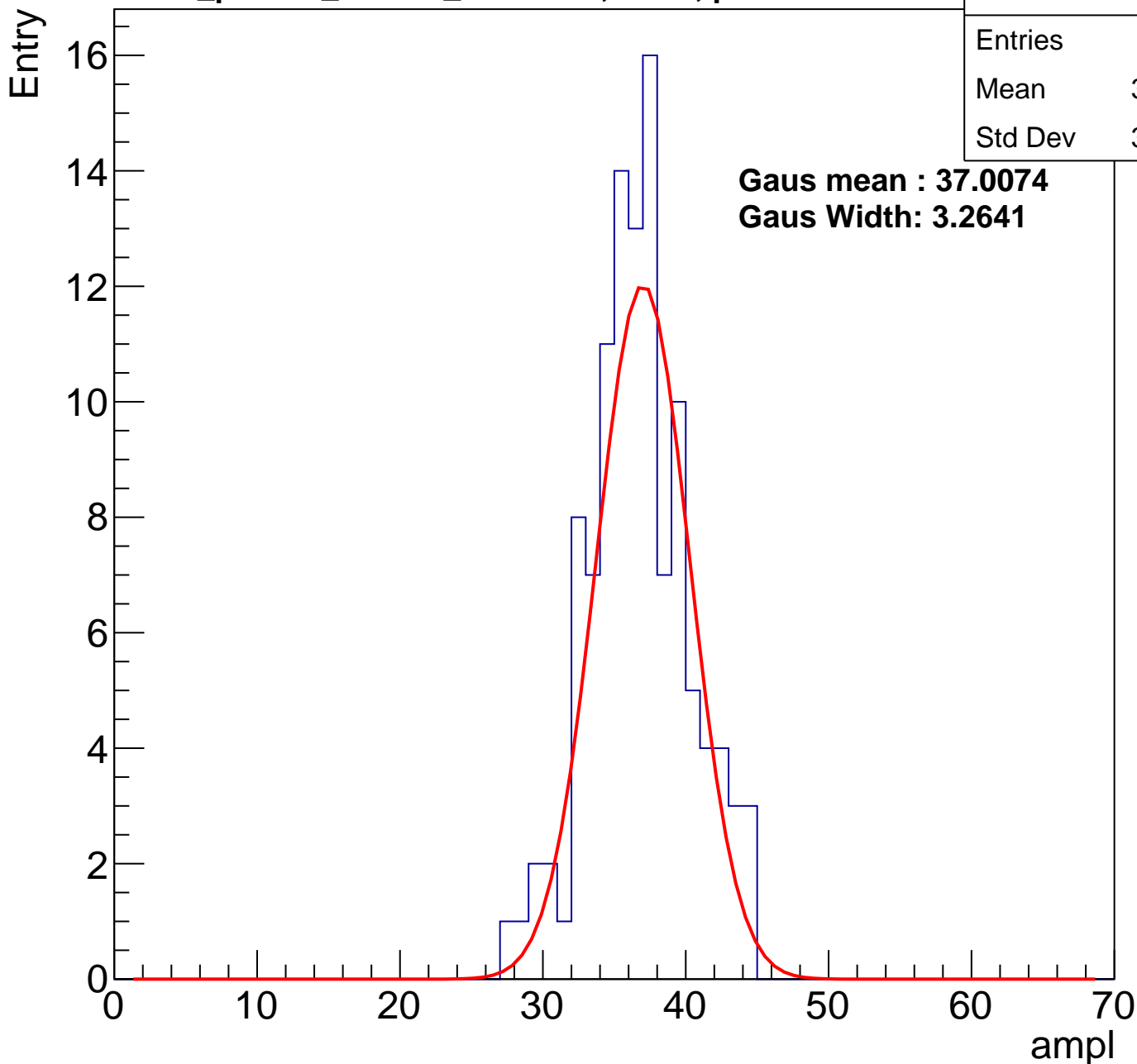
B0L101S, U15-ch73, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	36.29
Std Dev	3.506

Gaus mean : 37.0074

Gaus Width: 3.2641



B0L101S, U15-ch73, adc2

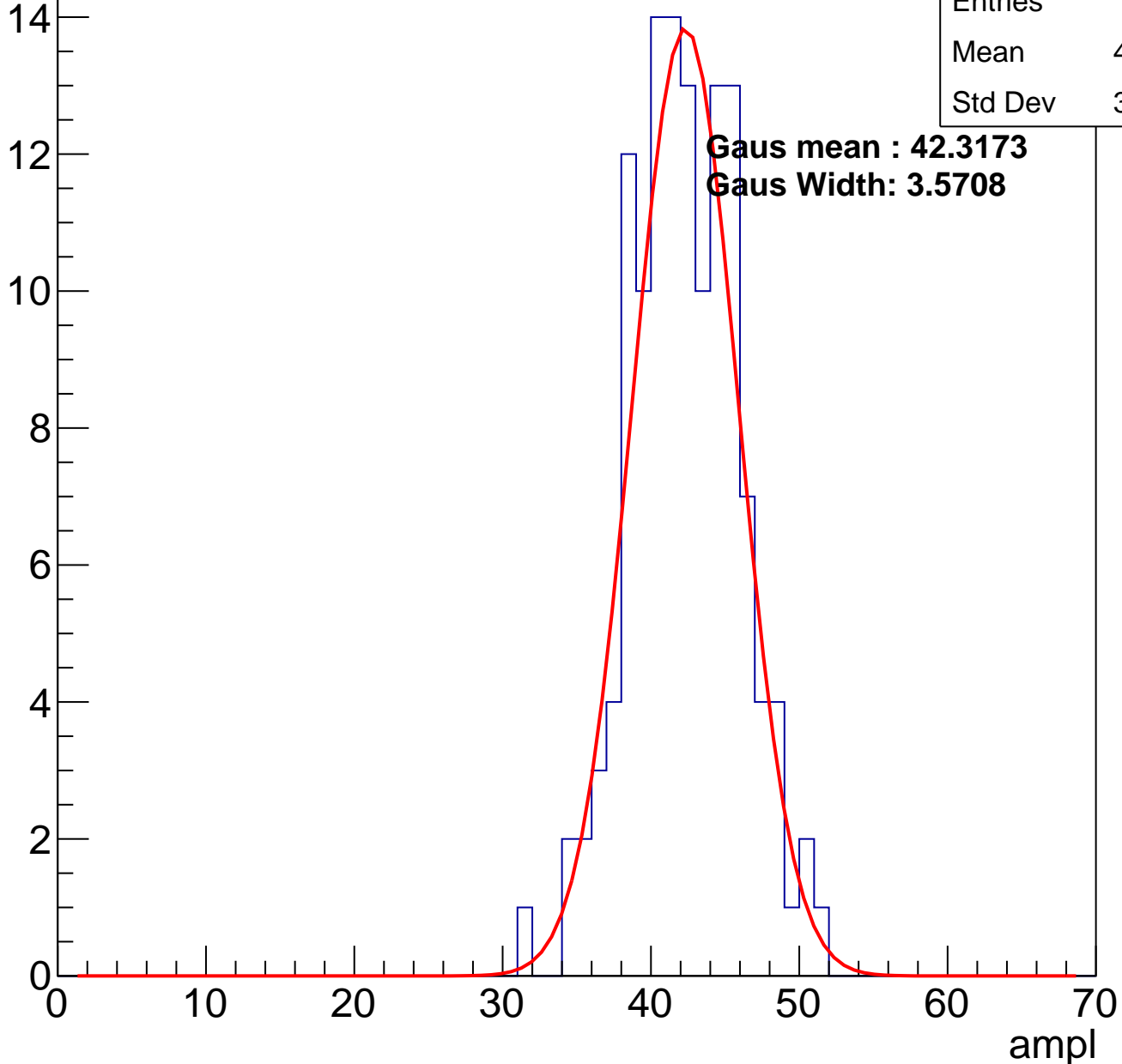
calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	41.85
Std Dev	3.613

Gaus mean : 42.3173

Gaus Width: 3.5708

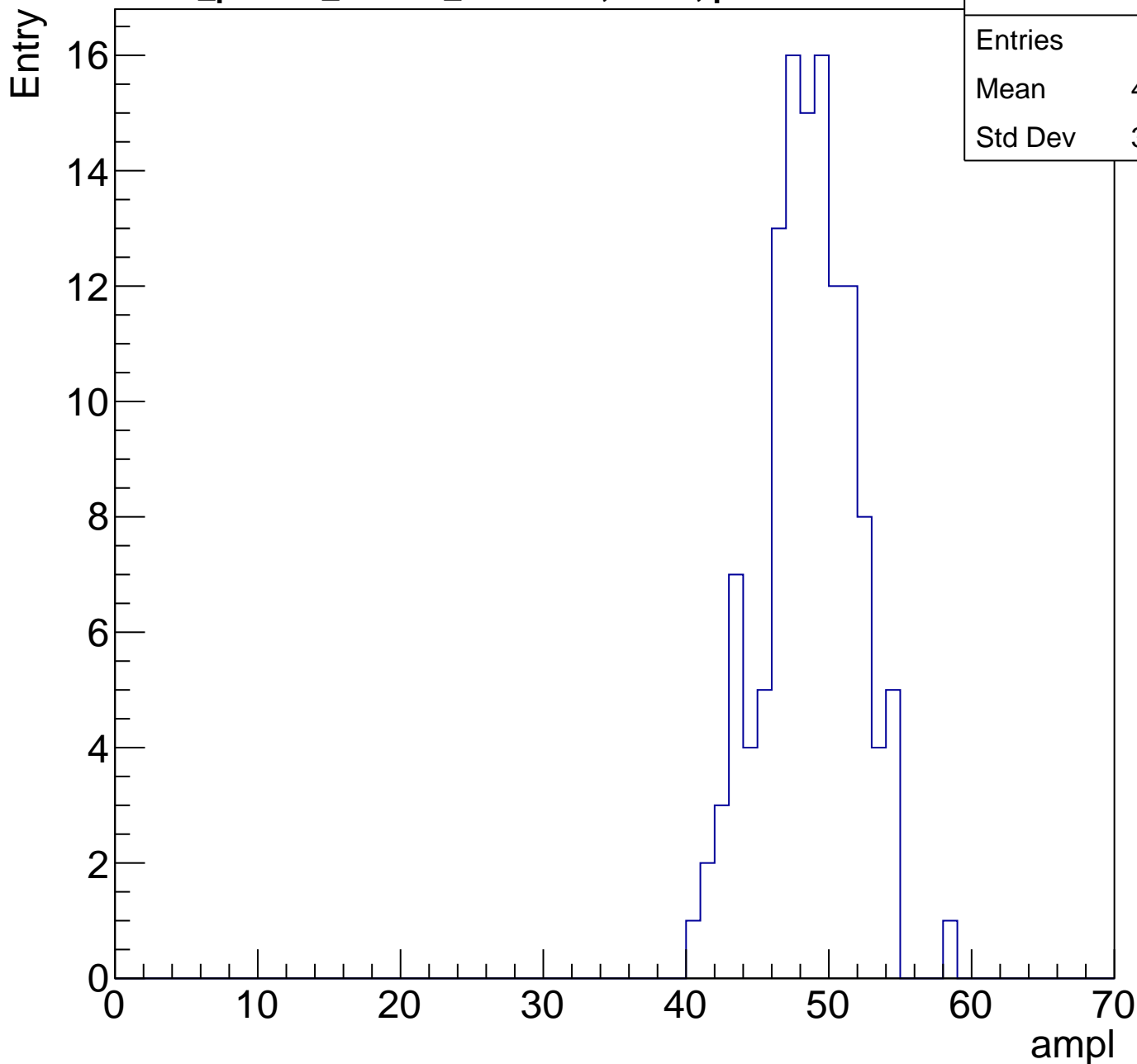
Entry



B0L101S, U15-ch73, adc3

calib_packv5_042523_0143.root, FC#1, port C1

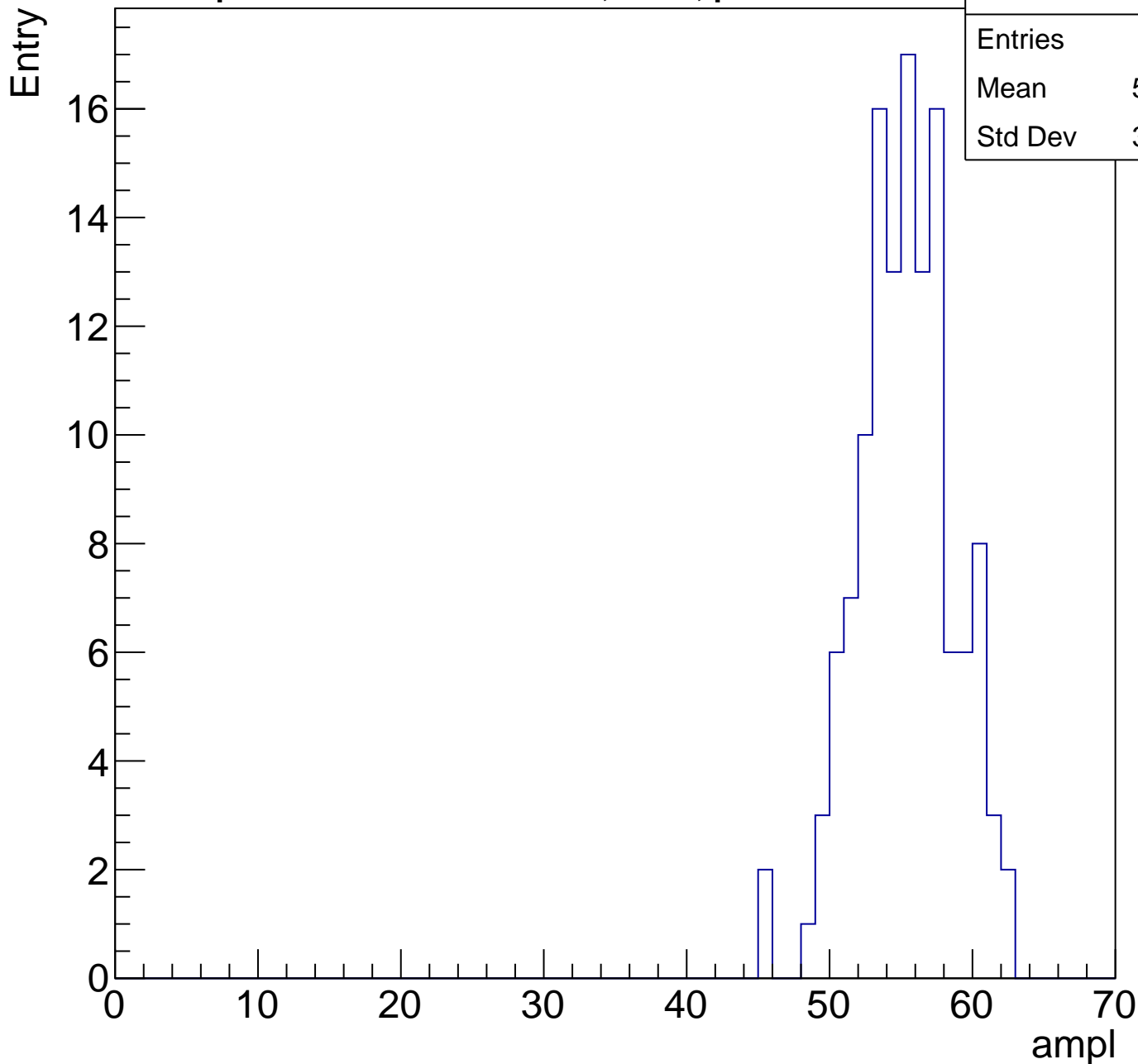
Entries	124
Mean	48.16
Std Dev	3.236



B0L101S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	54.85
Std Dev	3.313



B0L101S, U15-ch73, adc5

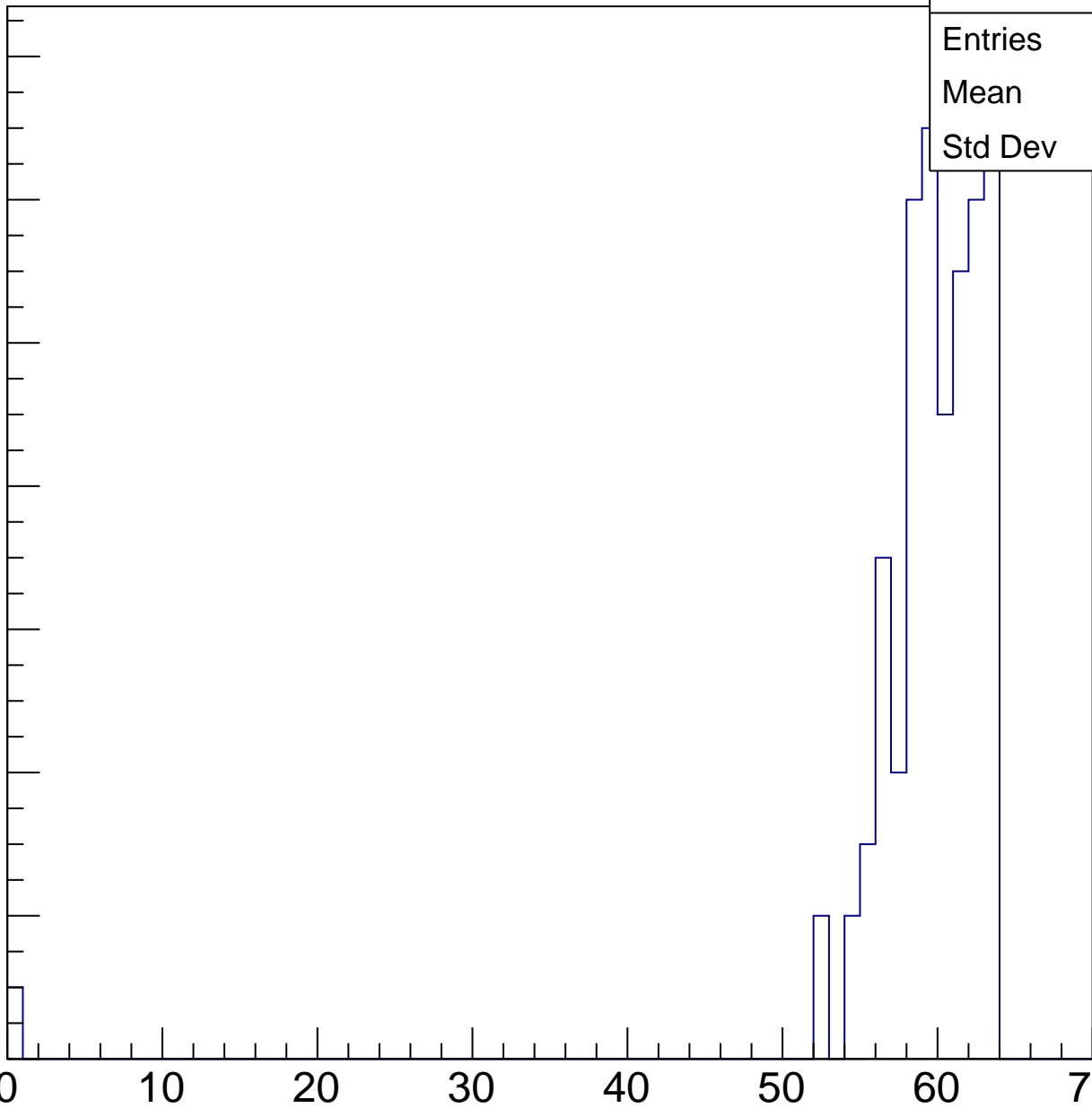
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	58.86
Std Dev	6.791

ampl

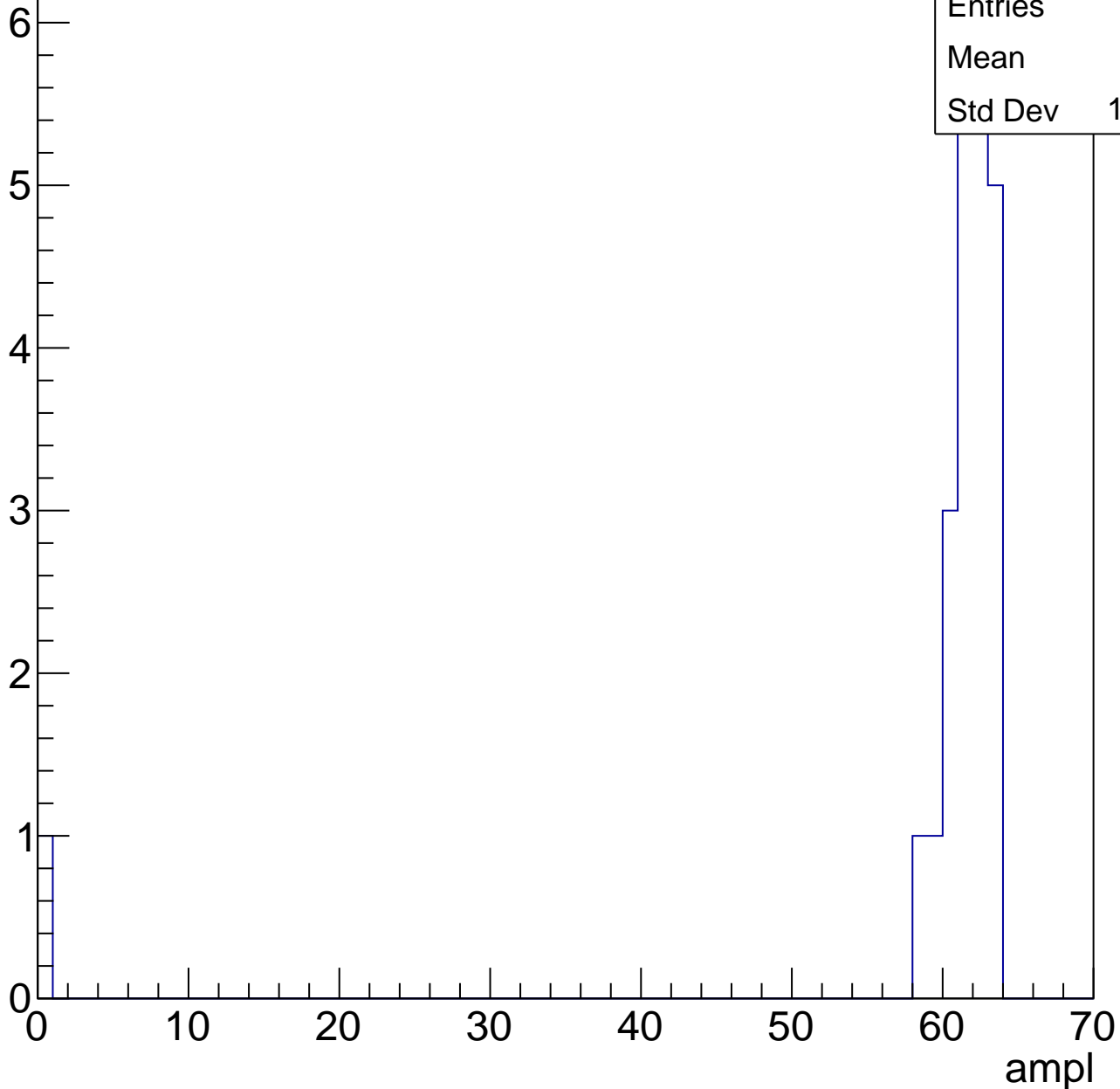


B0L101S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	23
Mean	58.7
Std Dev	12.58



B0L101S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



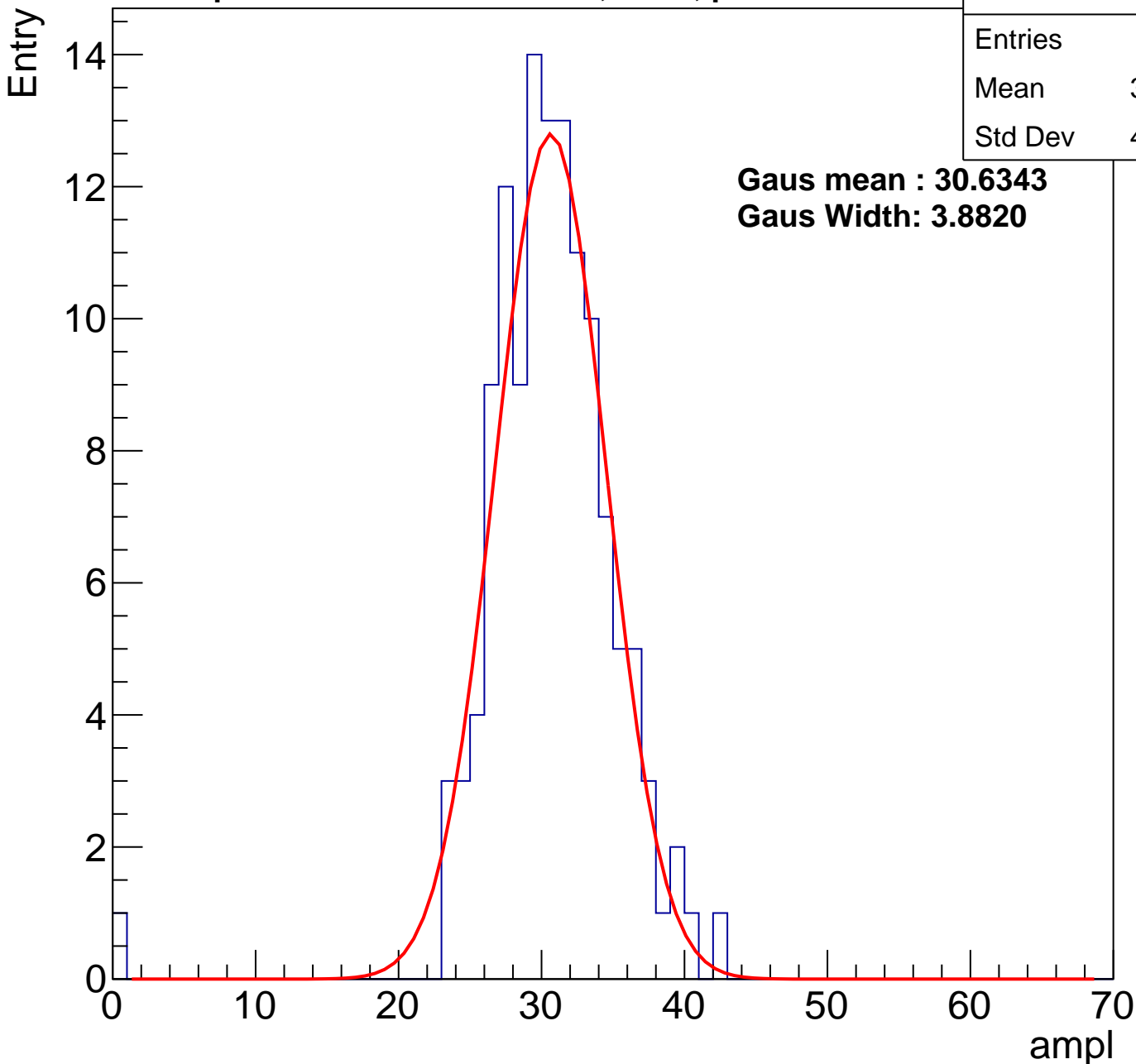
B0L101S, U15-ch74, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	30.19
Std Dev	4.639

Gaus mean : 30.6343

Gaus Width: 3.8820



B0L101S, U15-ch74, adc1

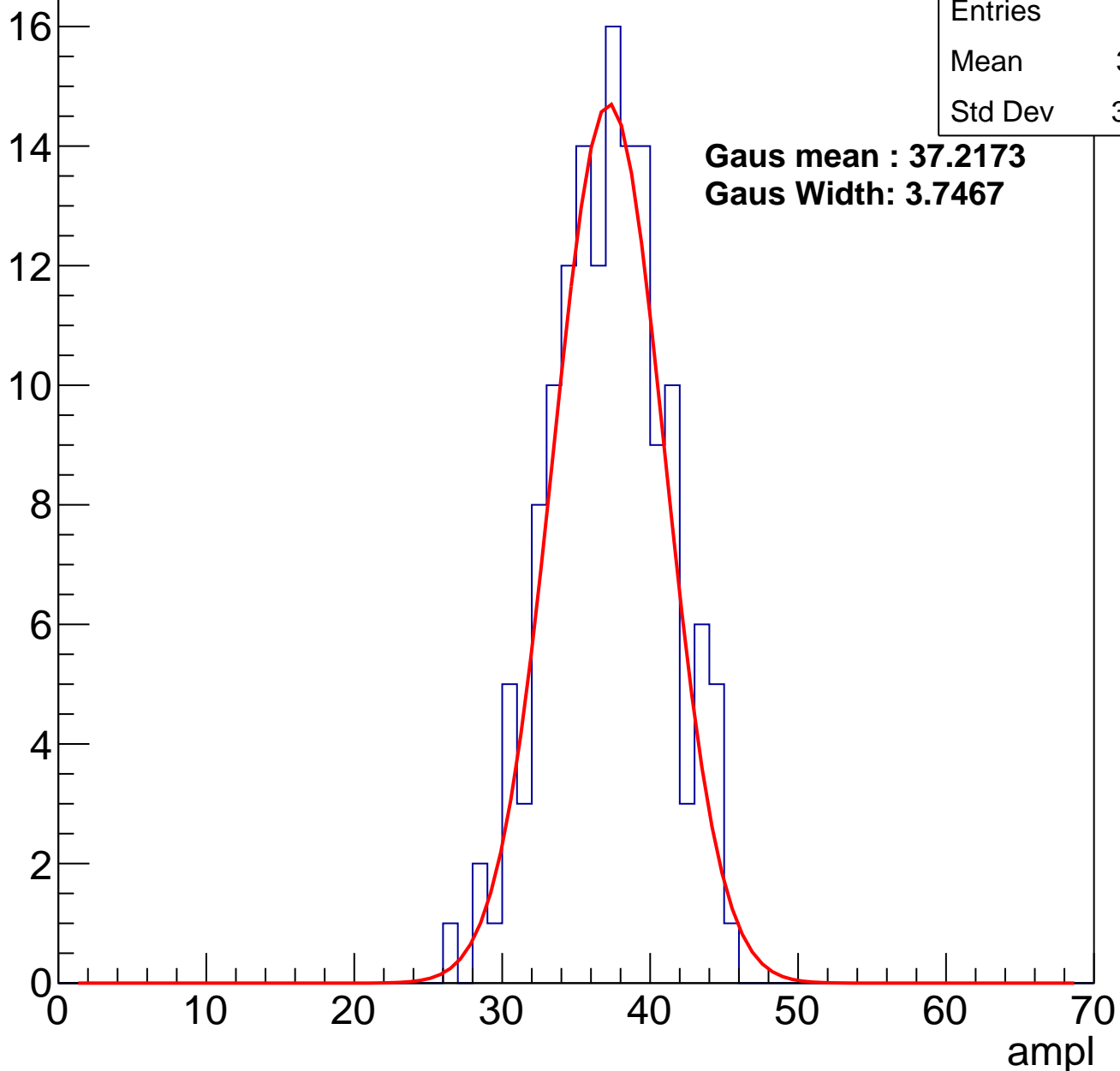
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.71
Std Dev	3.823

Gaus mean : 37.2173

Gaus Width: 3.7467

Entry



B0L101S, U15-ch74, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

137

Mean

43.96

Std Dev

3.792

Gaus mean : 44.6750

Gaus Width: 3.7511

0

10

20

30

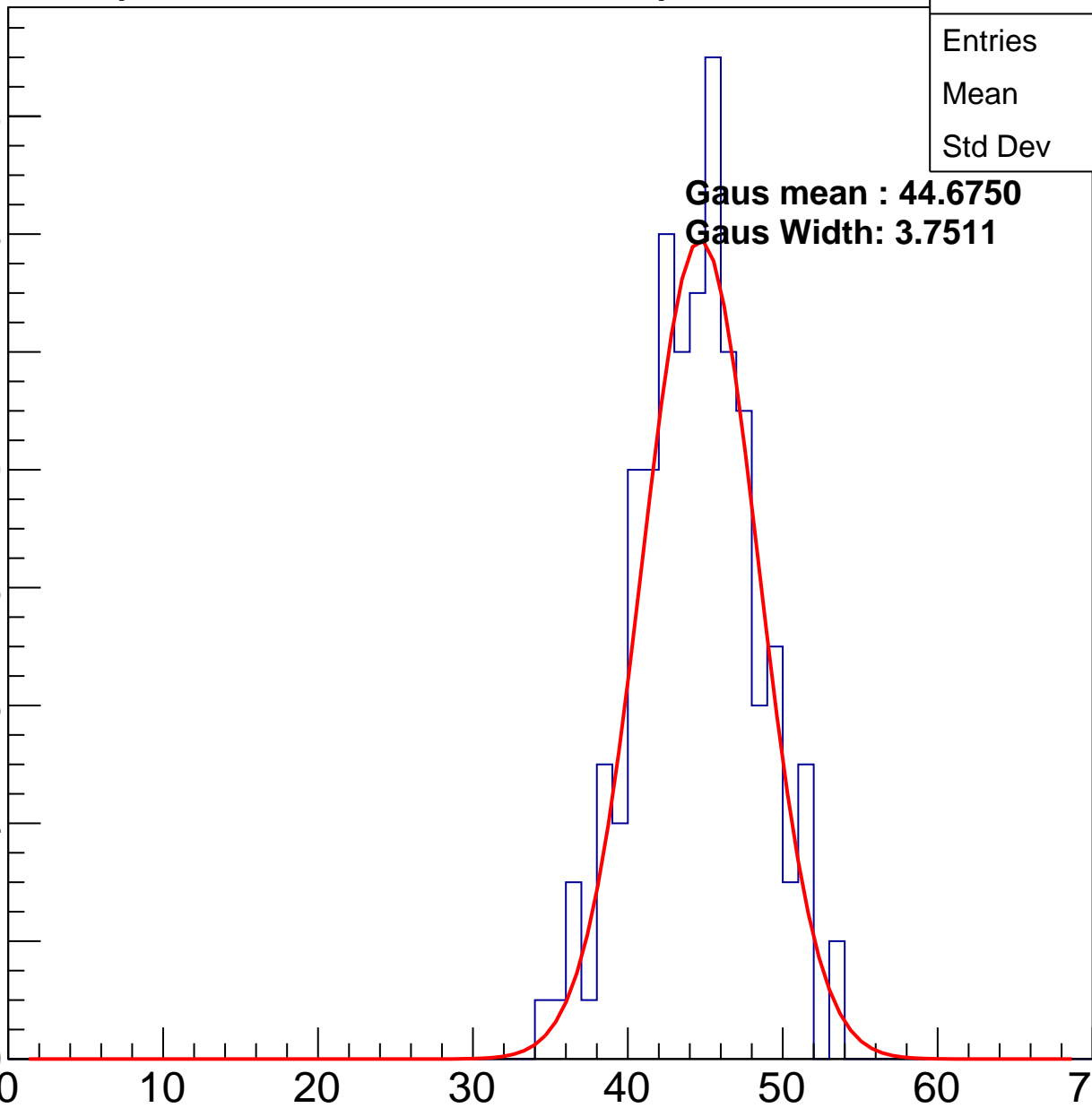
40

50

60

70

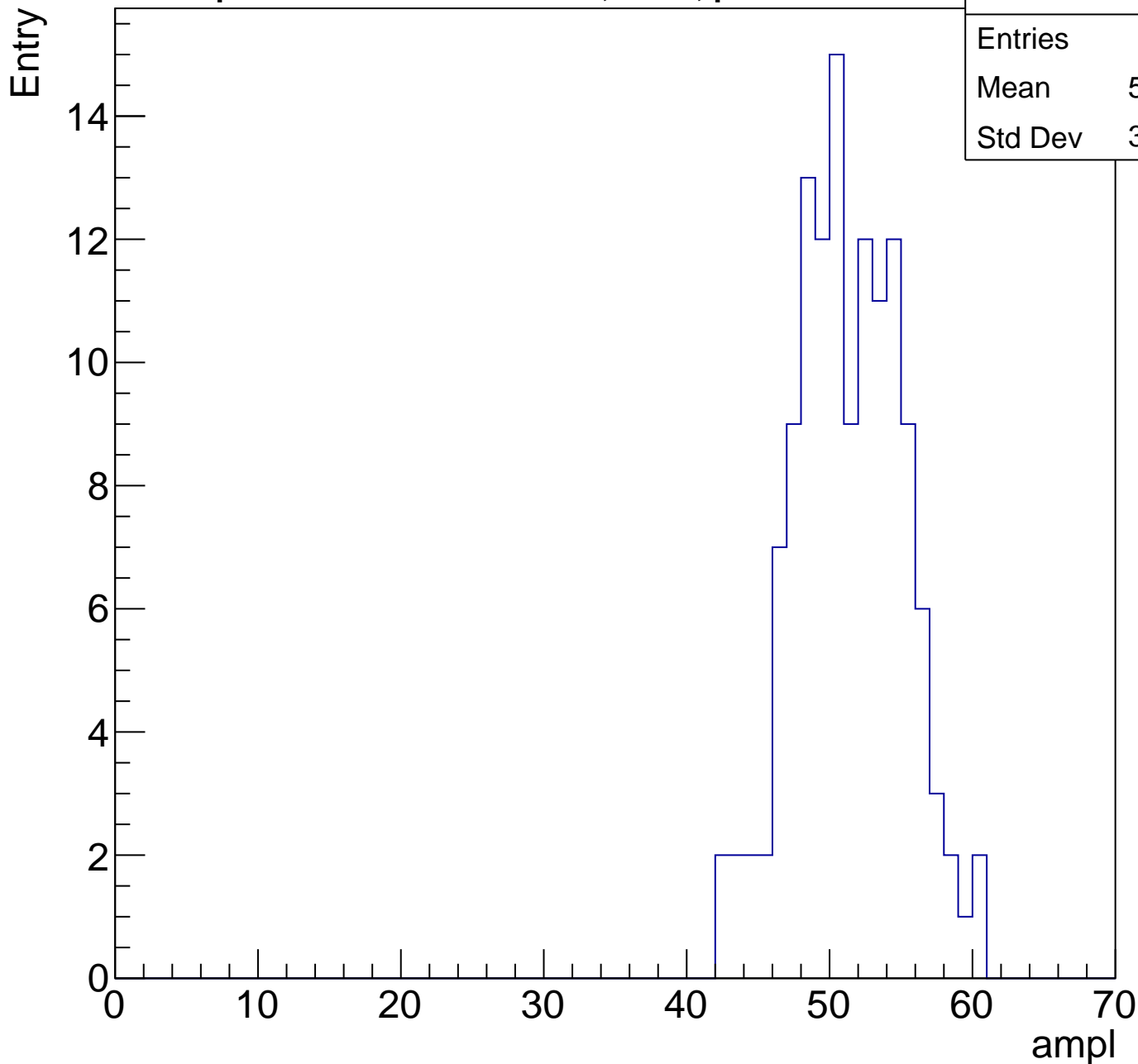
ampl



B0L101S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#1, port C1

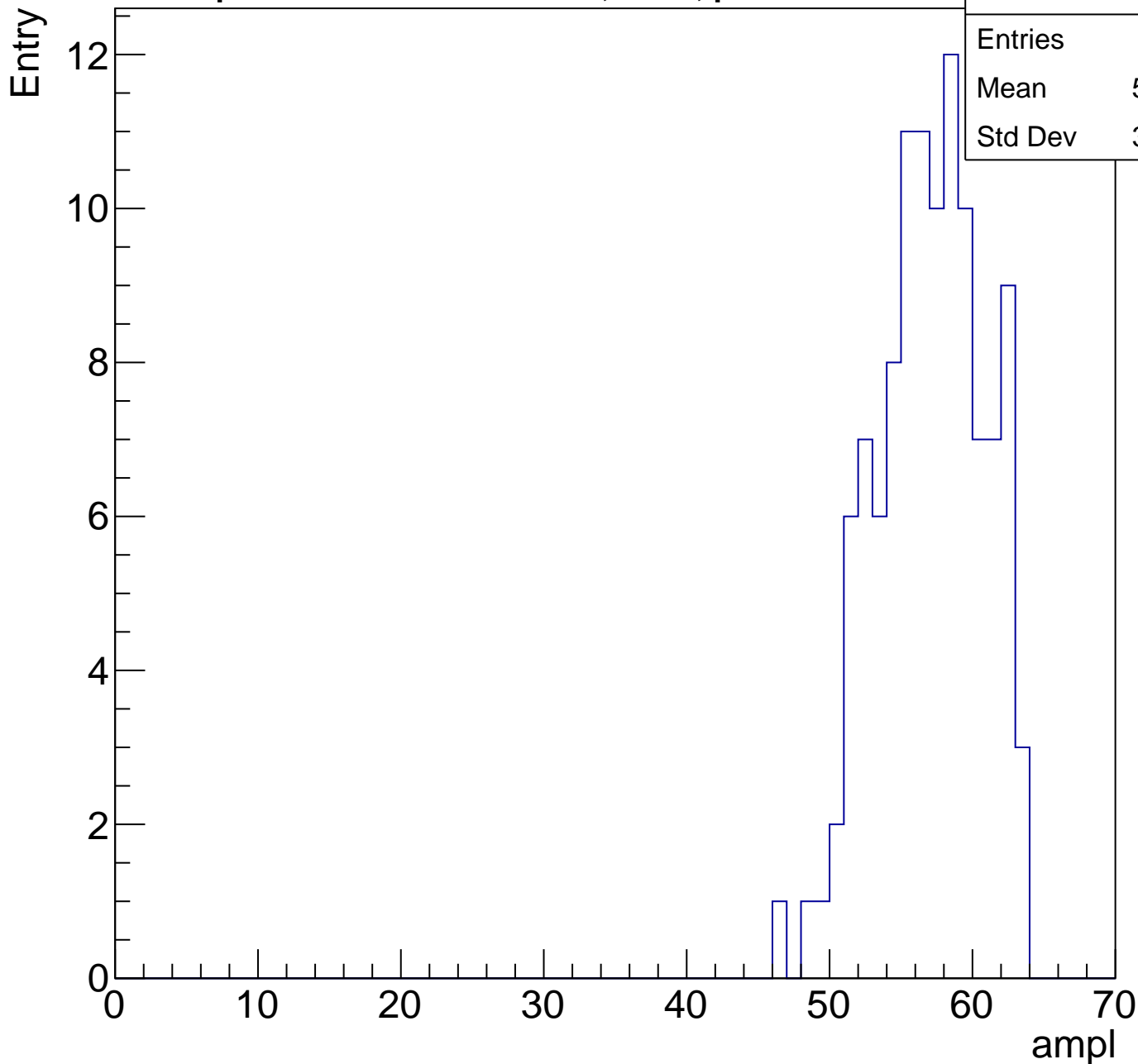
Entries	131
Mean	50.89
Std Dev	3.755



B0L101S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	56.55
Std Dev	3.679



B0L101S, U15-ch74, adc5

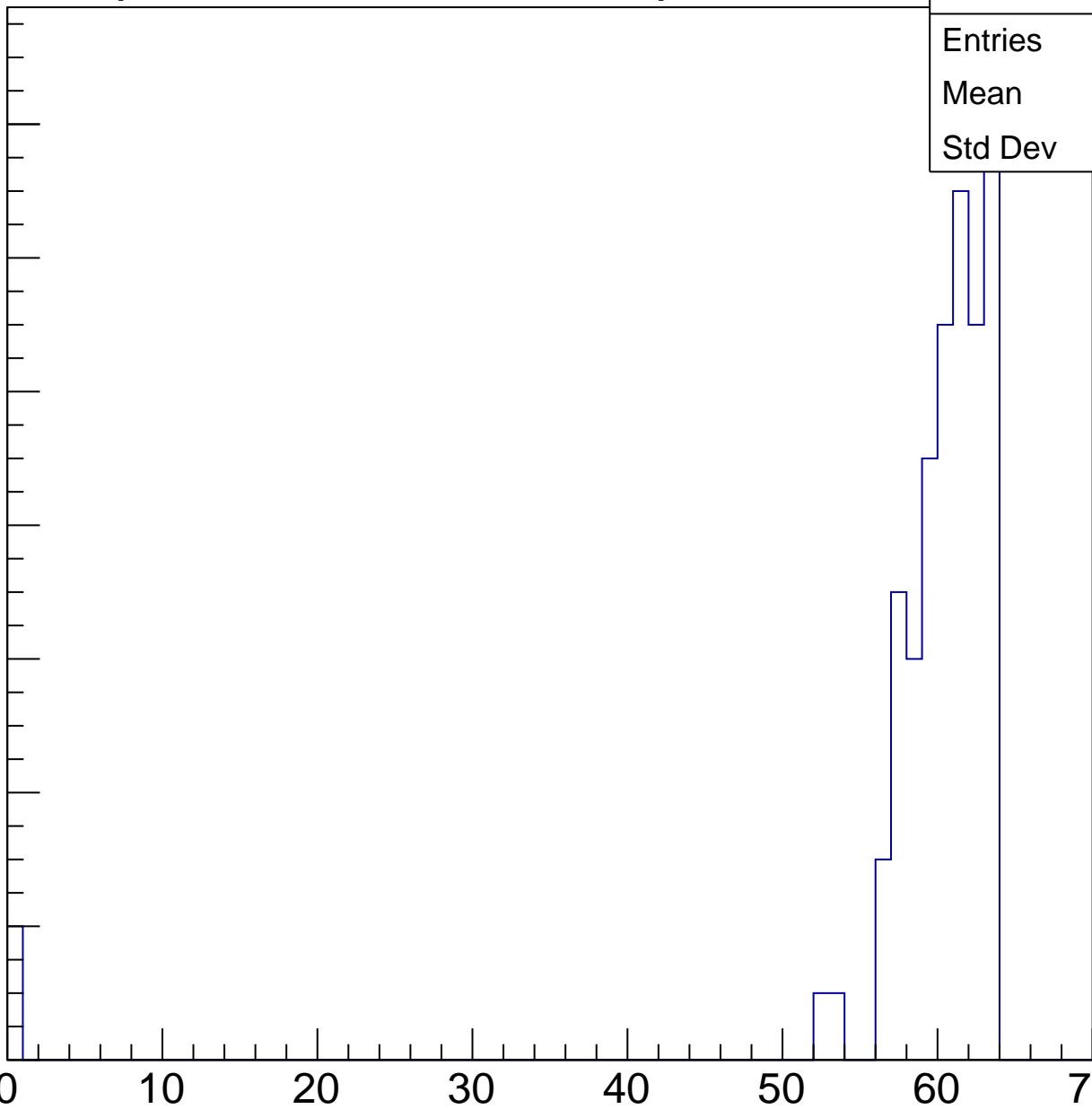
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	79
Mean	58.62
Std Dev	9.743

ampl



B0L101S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch75, adc0

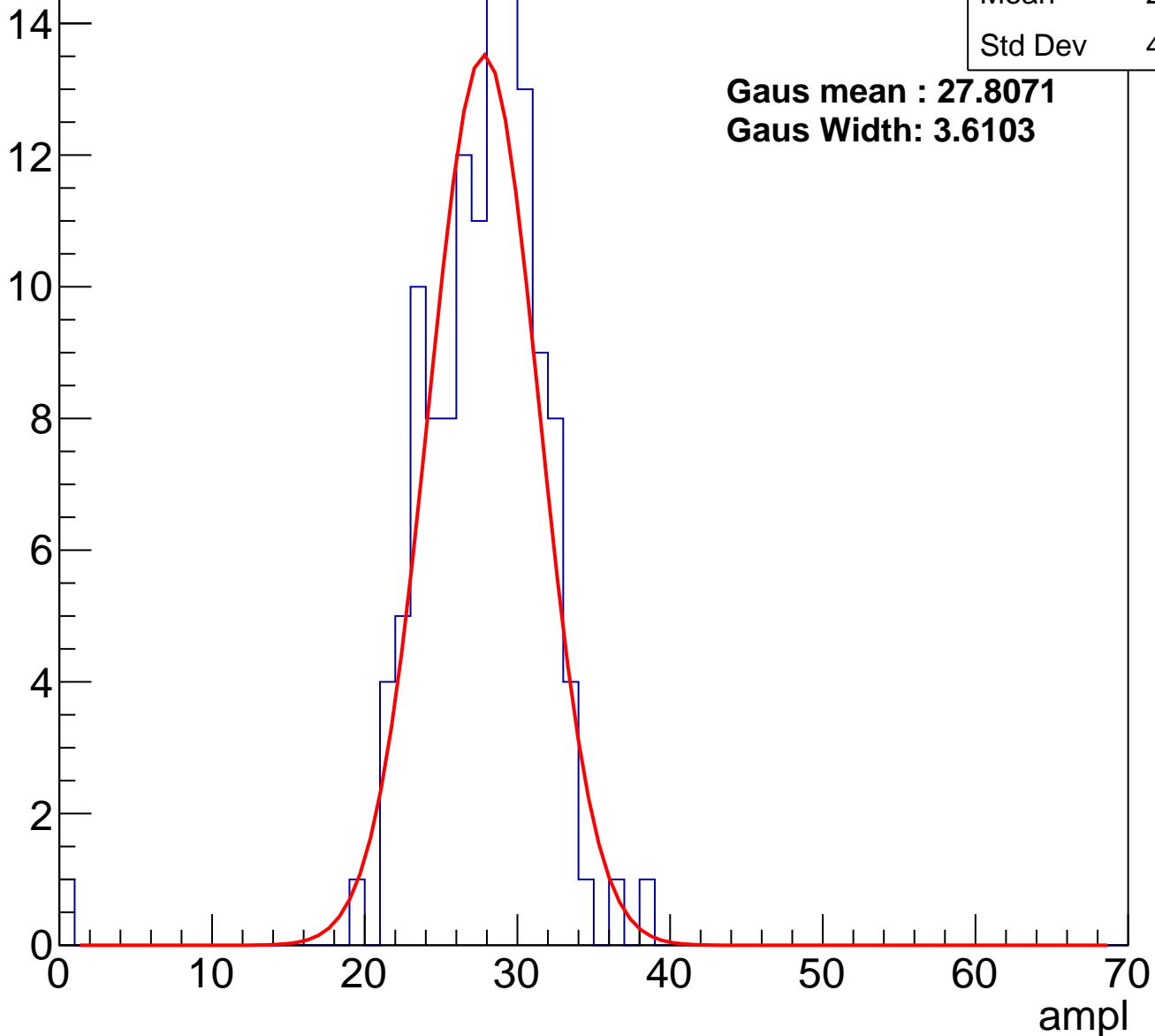
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	27.28
Std Dev	4.218

Gaus mean : 27.8071

Gaus Width: 3.6103

Entry



B0L101S, U15-ch75, adc1

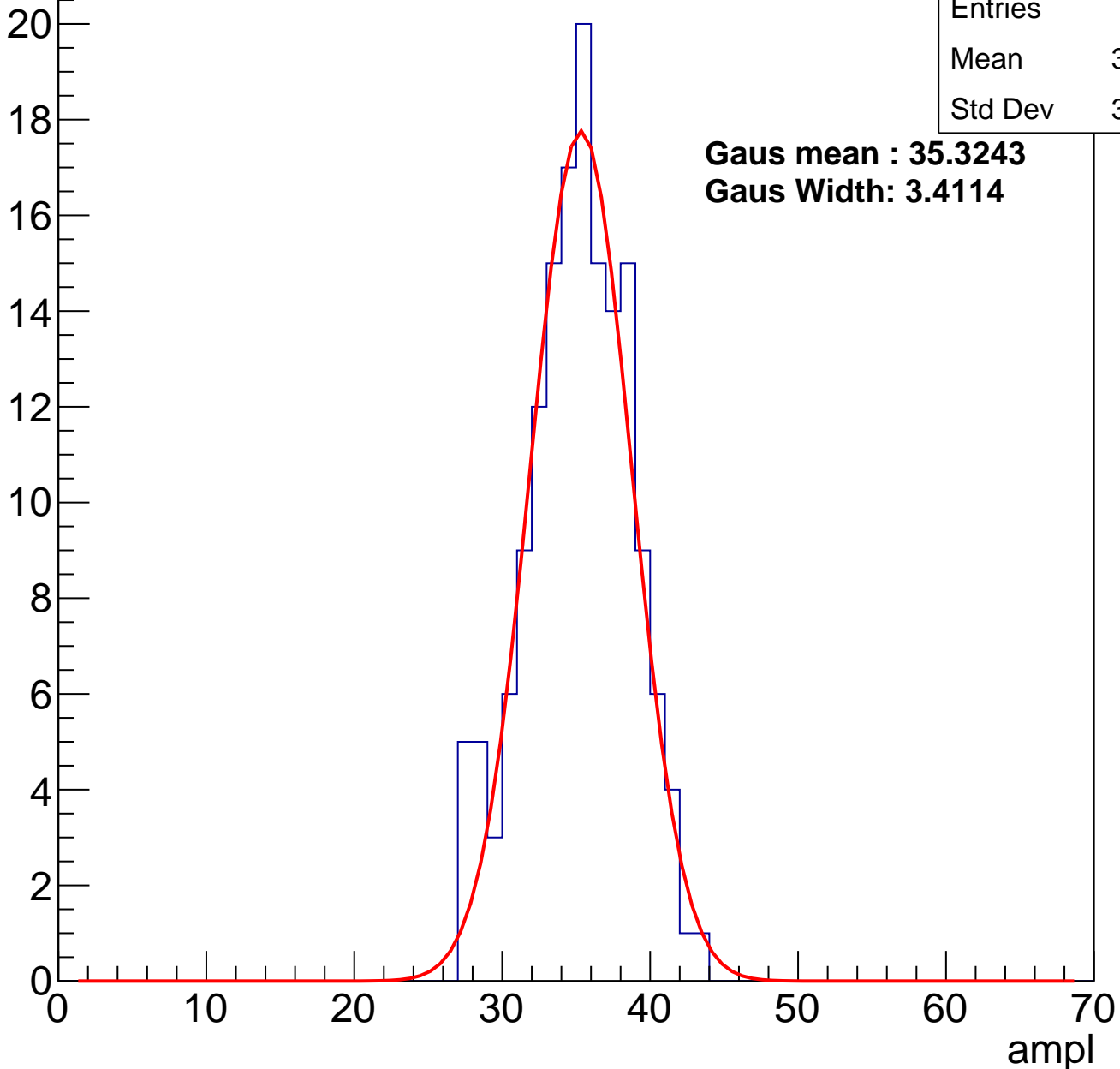
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	34.69
Std Dev	3.473

Gaus mean : 35.3243

Gaus Width: 3.4114

Entry



B0L101S, U15-ch75, adc2

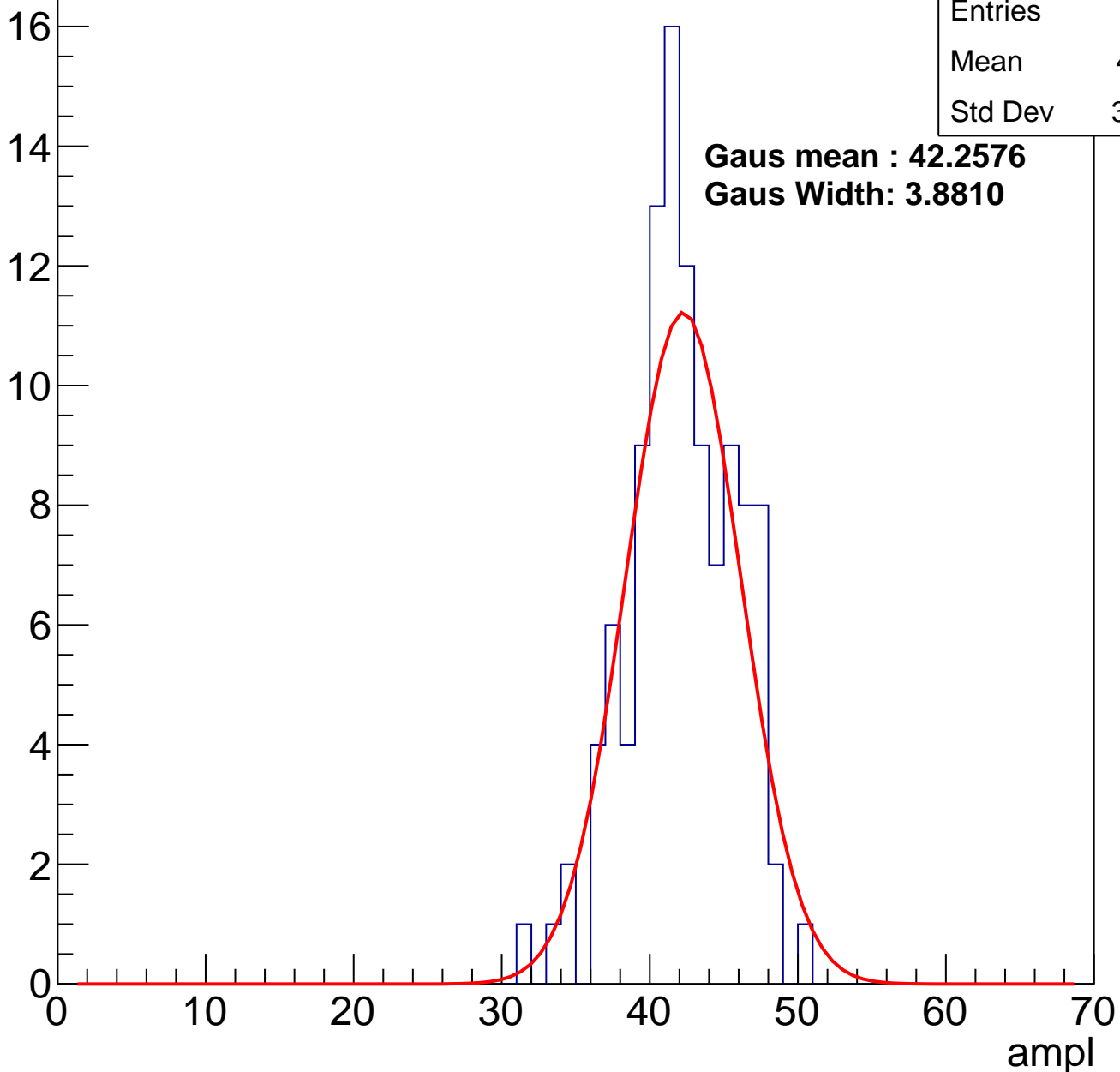
calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	41.71
Std Dev	3.547

Gaus mean : 42.2576

Gaus Width: 3.8810

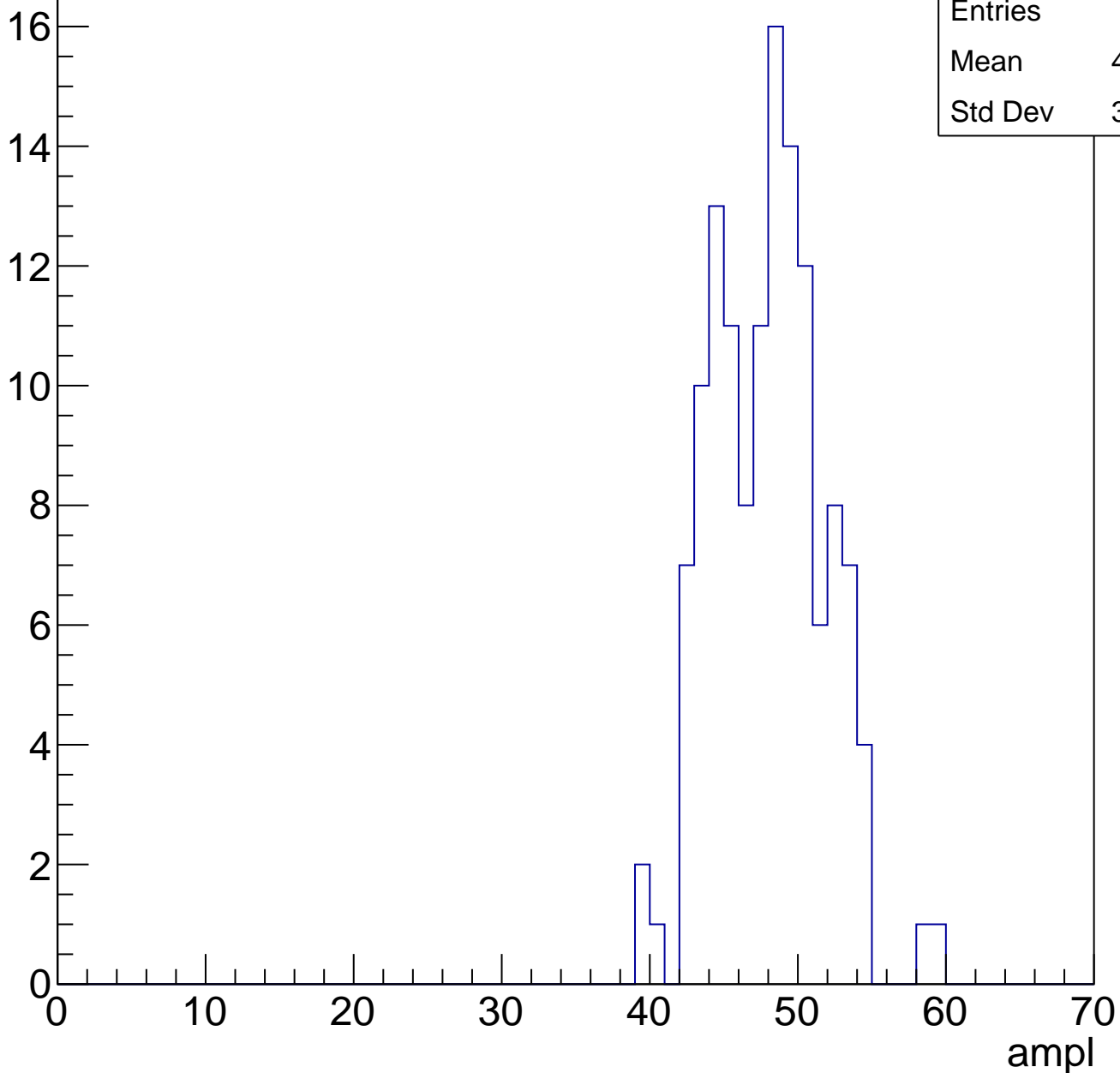
Entry



B0L101S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

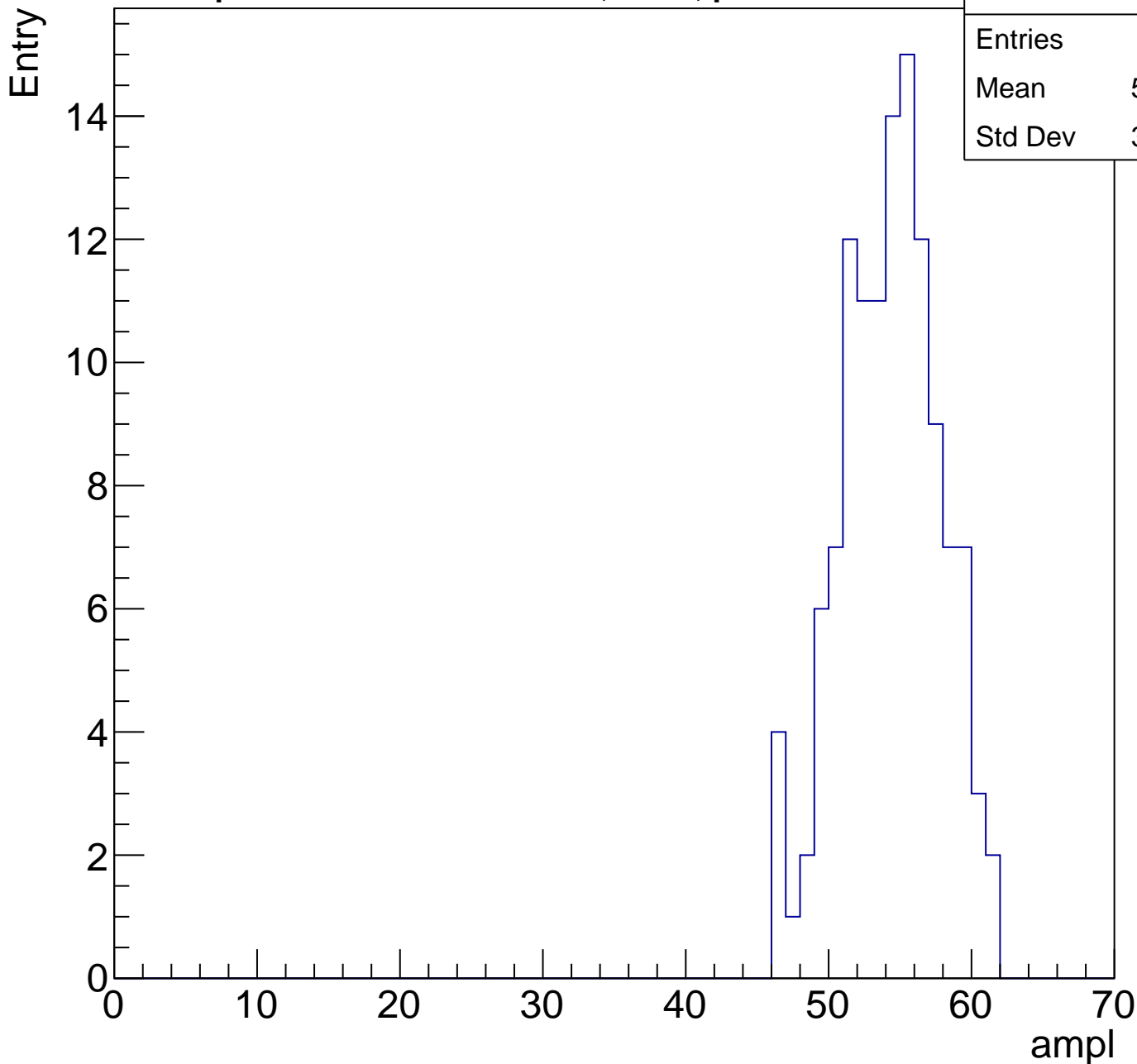


Entries	132
Mean	47.53
Std Dev	3.726

B0L101S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	53.86
Std Dev	3.418



B0L101S, U15-ch75, adc5

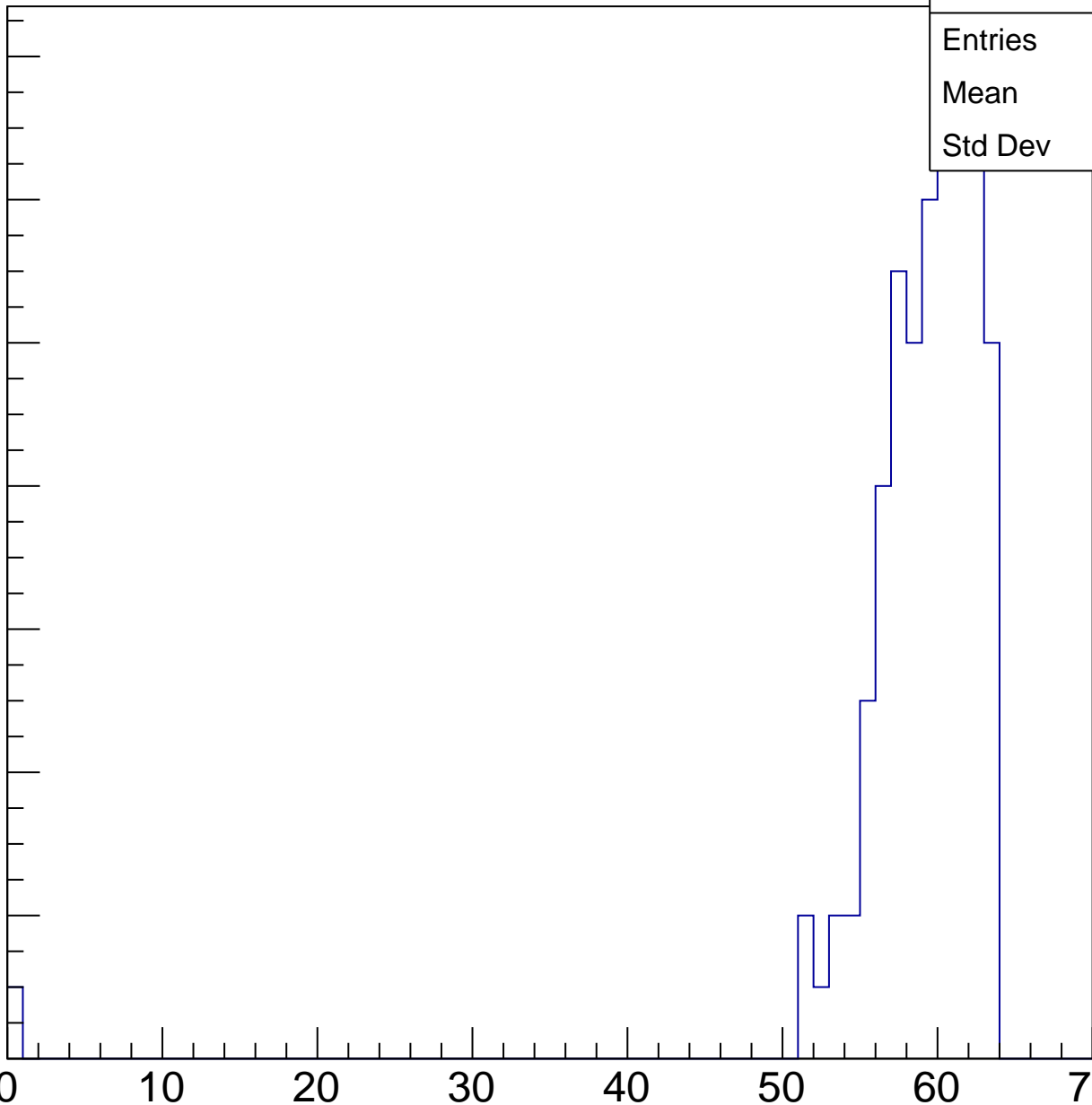
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	58.42
Std Dev	6.428

ampl

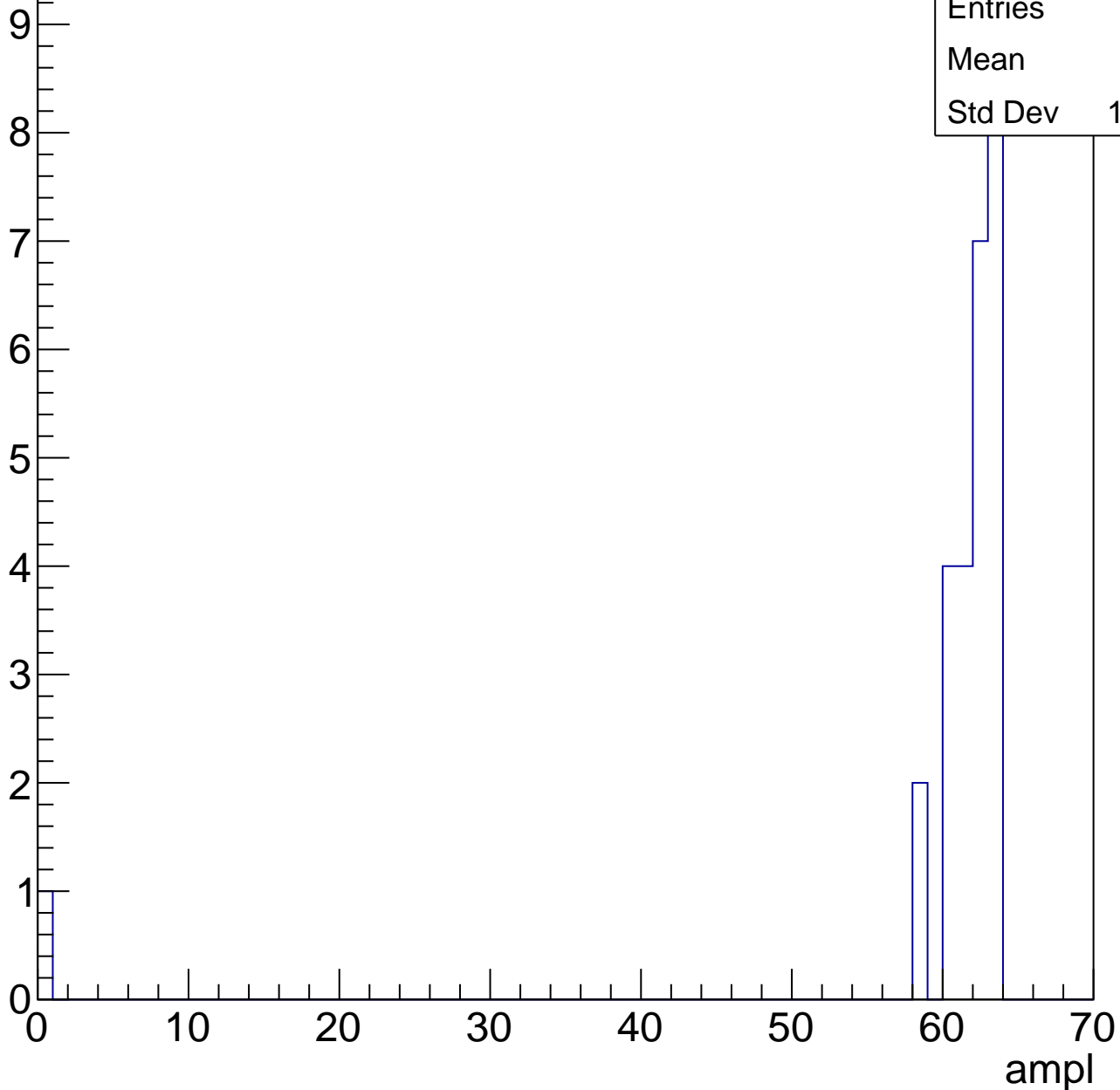


B0L101S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	27
Mean	59.3
Std Dev	11.72



B0L101S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch76, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	28.49
Std Dev	5.813

Gaus mean : 29.8112

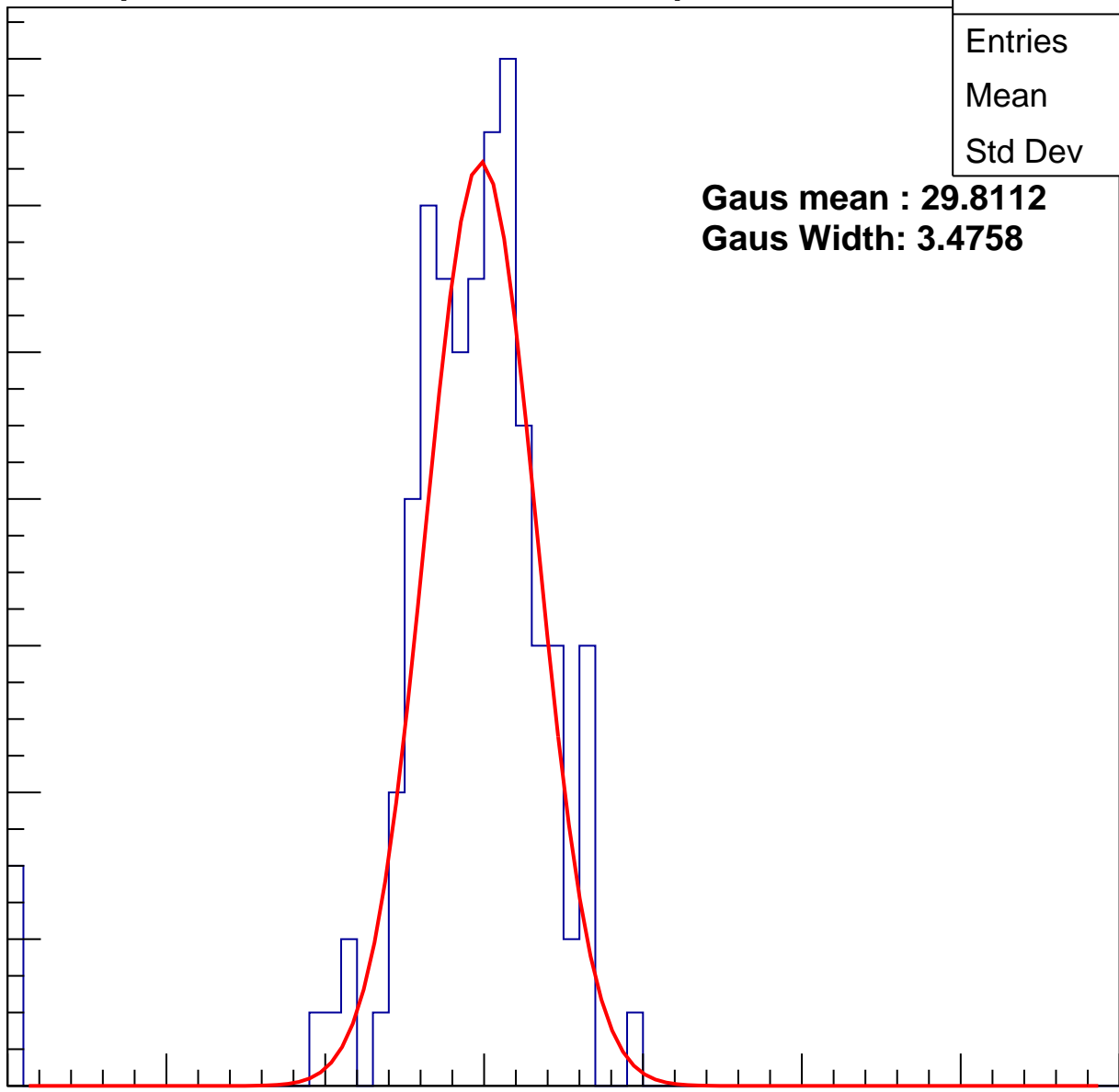
Gaus Width: 3.4758

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch76, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

105

Mean

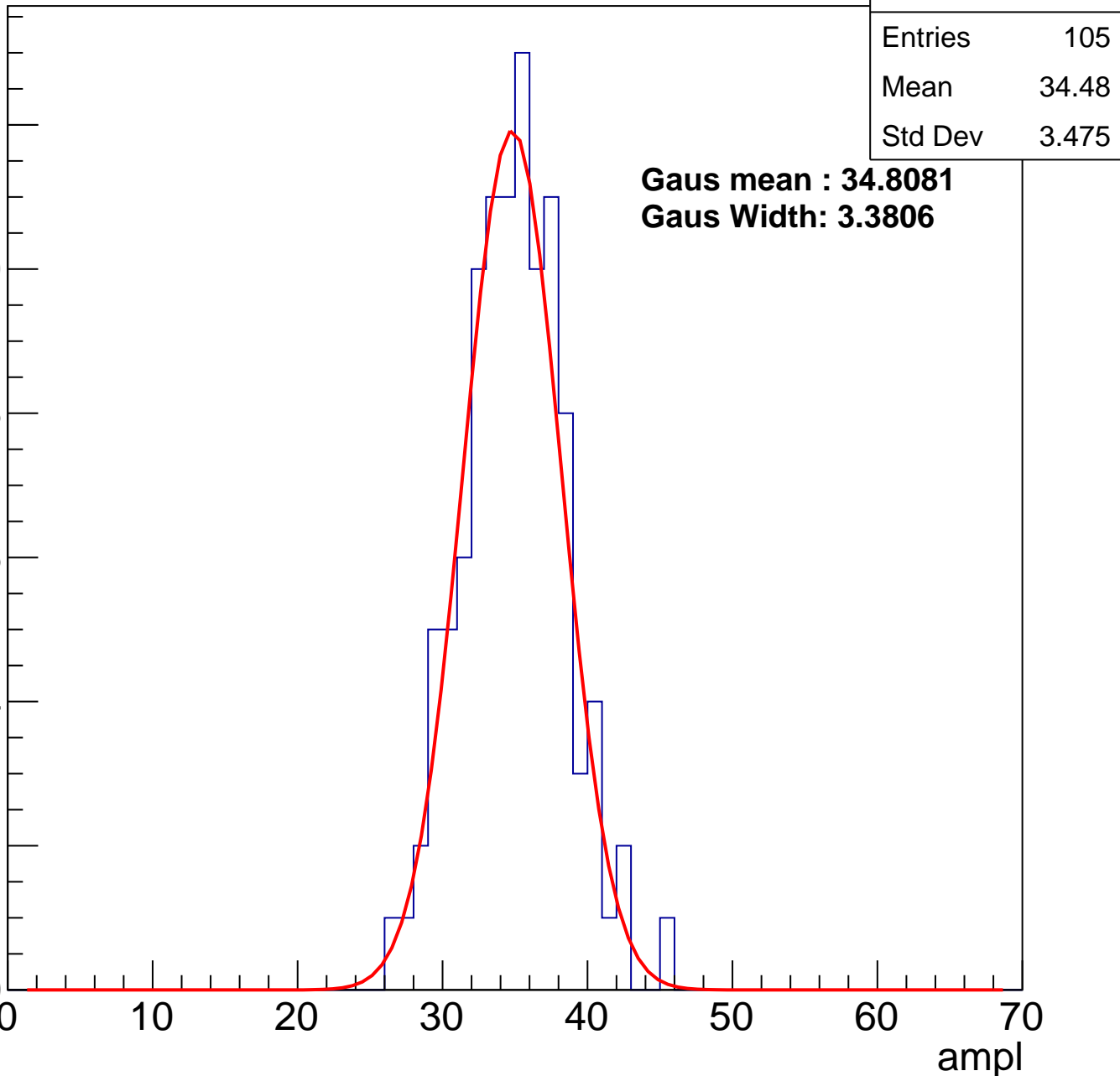
34.48

Std Dev

3.475

Gaus mean : 34.8081

Gaus Width: 3.3806



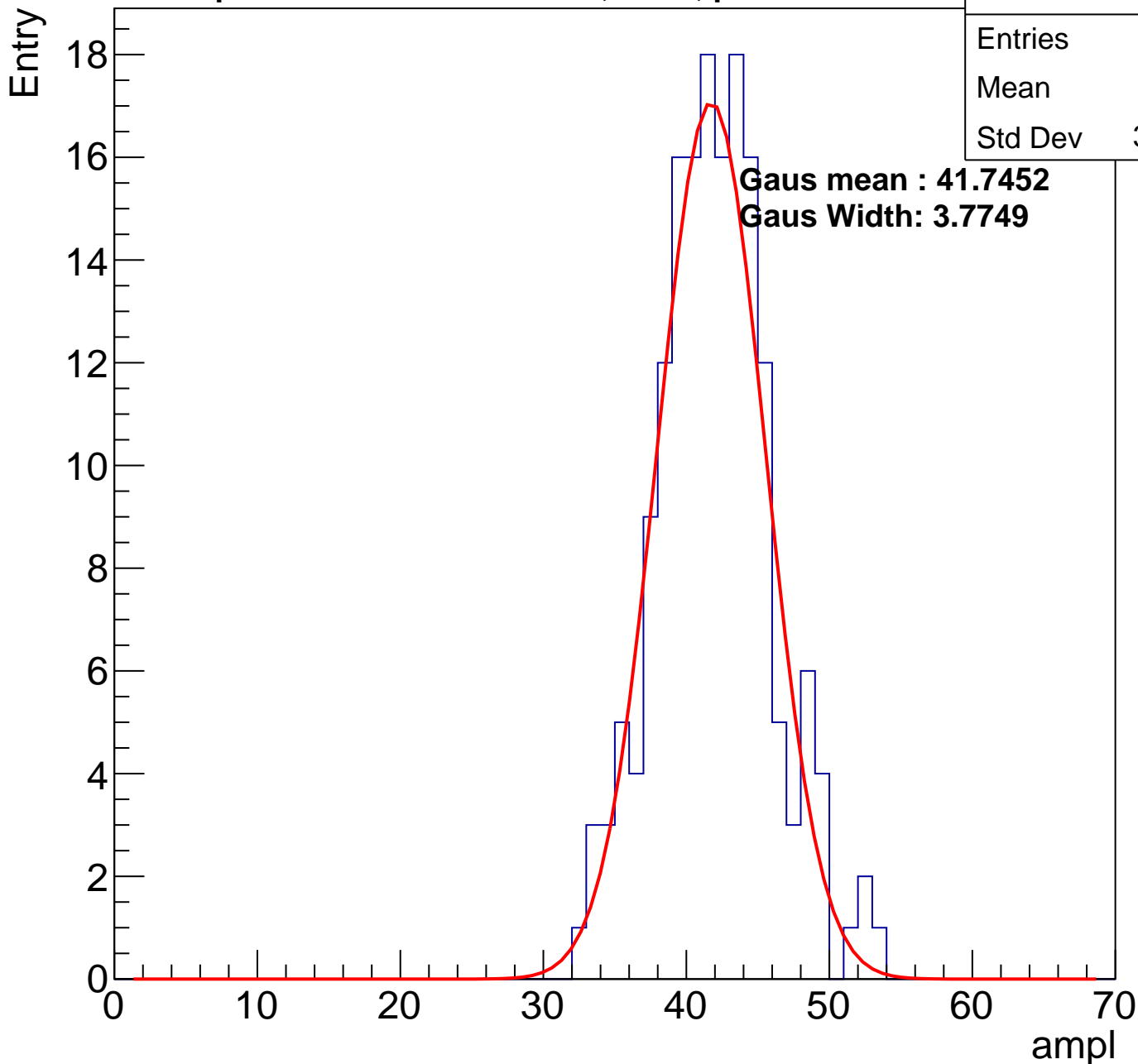
B0L101S, U15-ch76, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	171
Mean	41.5
Std Dev	3.961

Gaus mean : 41.7452

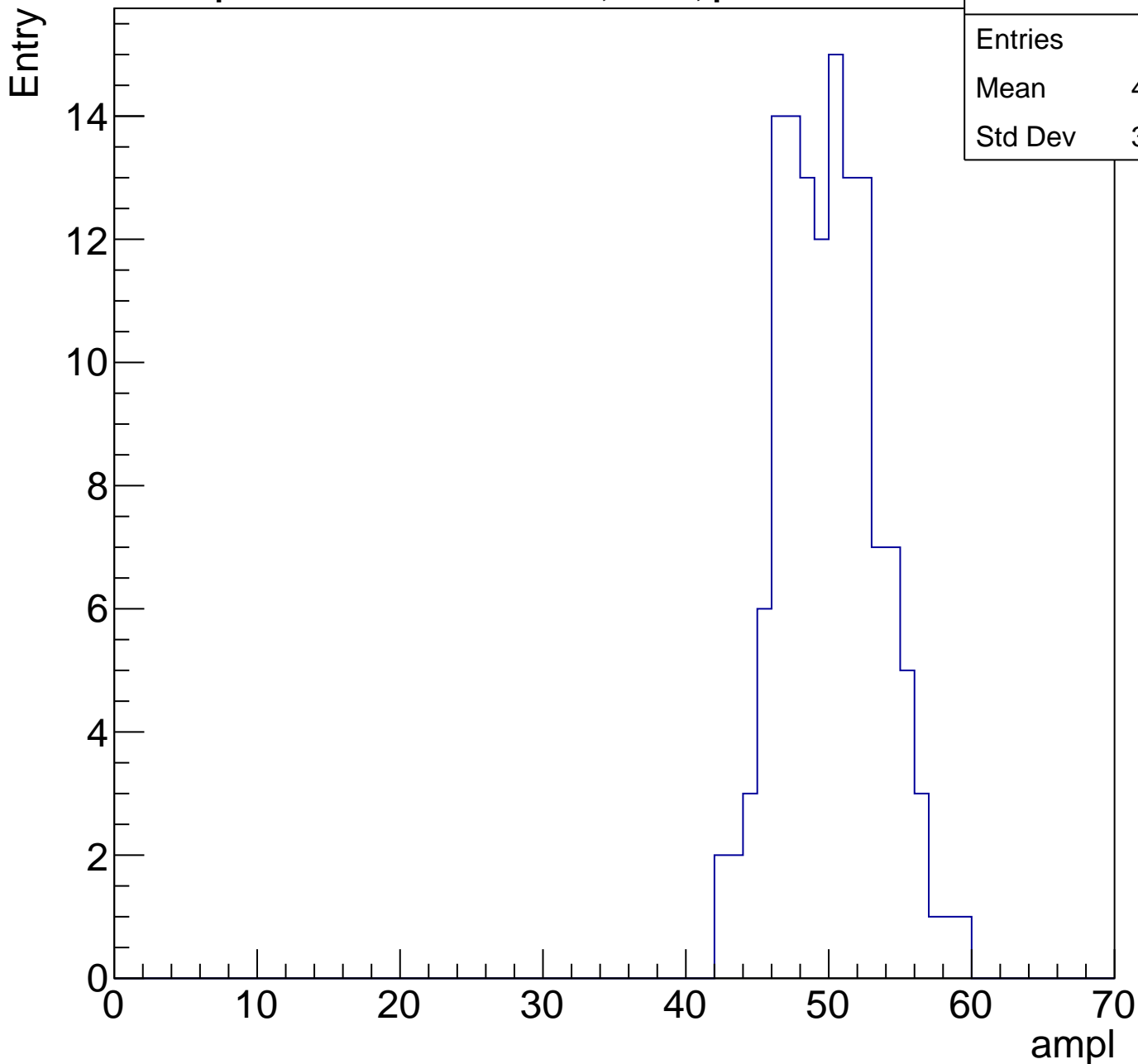
Gaus Width: 3.7749



B0L101S, U15-ch76, adc3

calib_packv5_042523_0143.root, FC#1, port C1

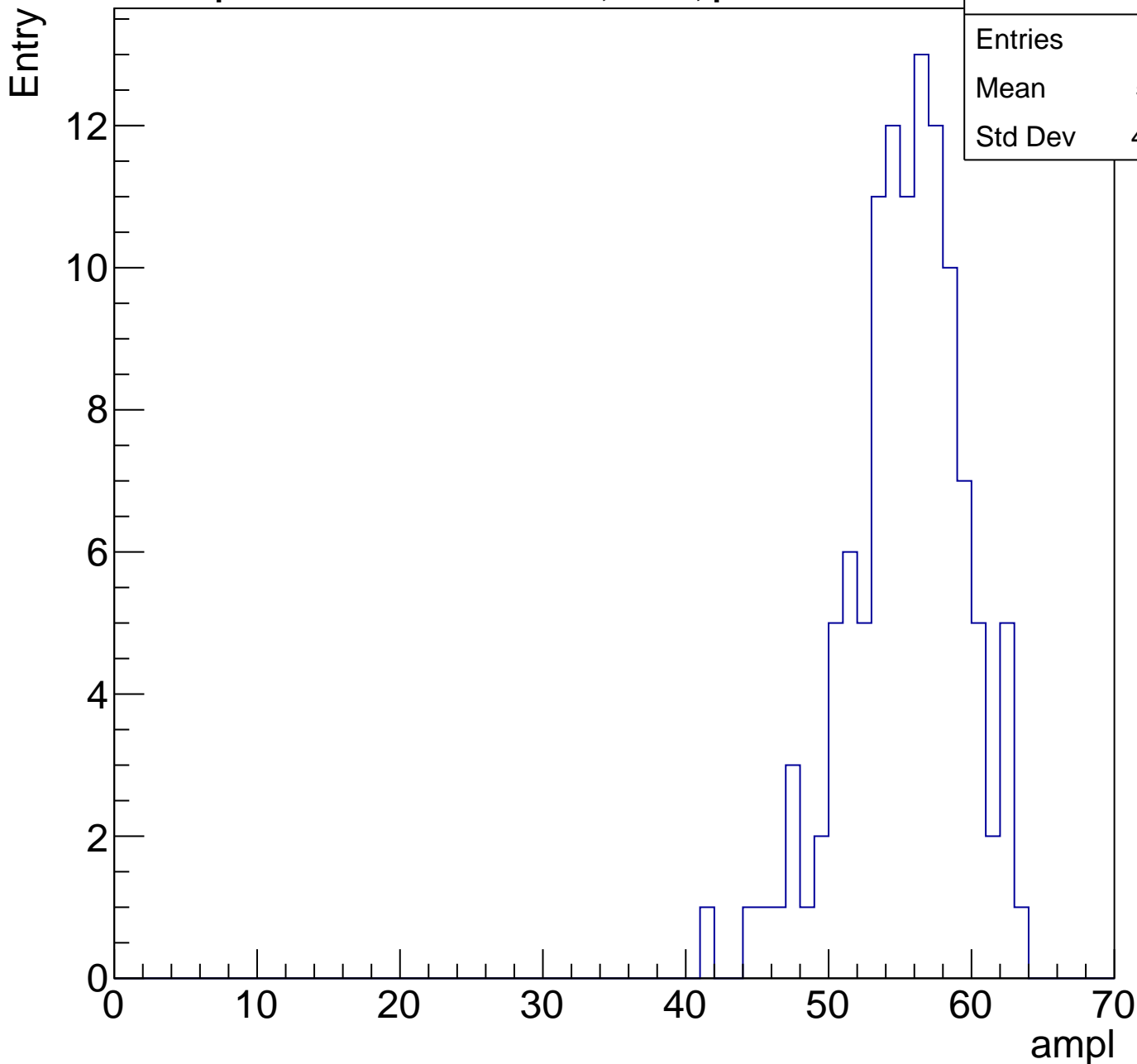
Entries	132
Mean	49.55
Std Dev	3.414



B0L101S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	54.91
Std Dev	4.066



B0L101S, U15-ch76, adc5

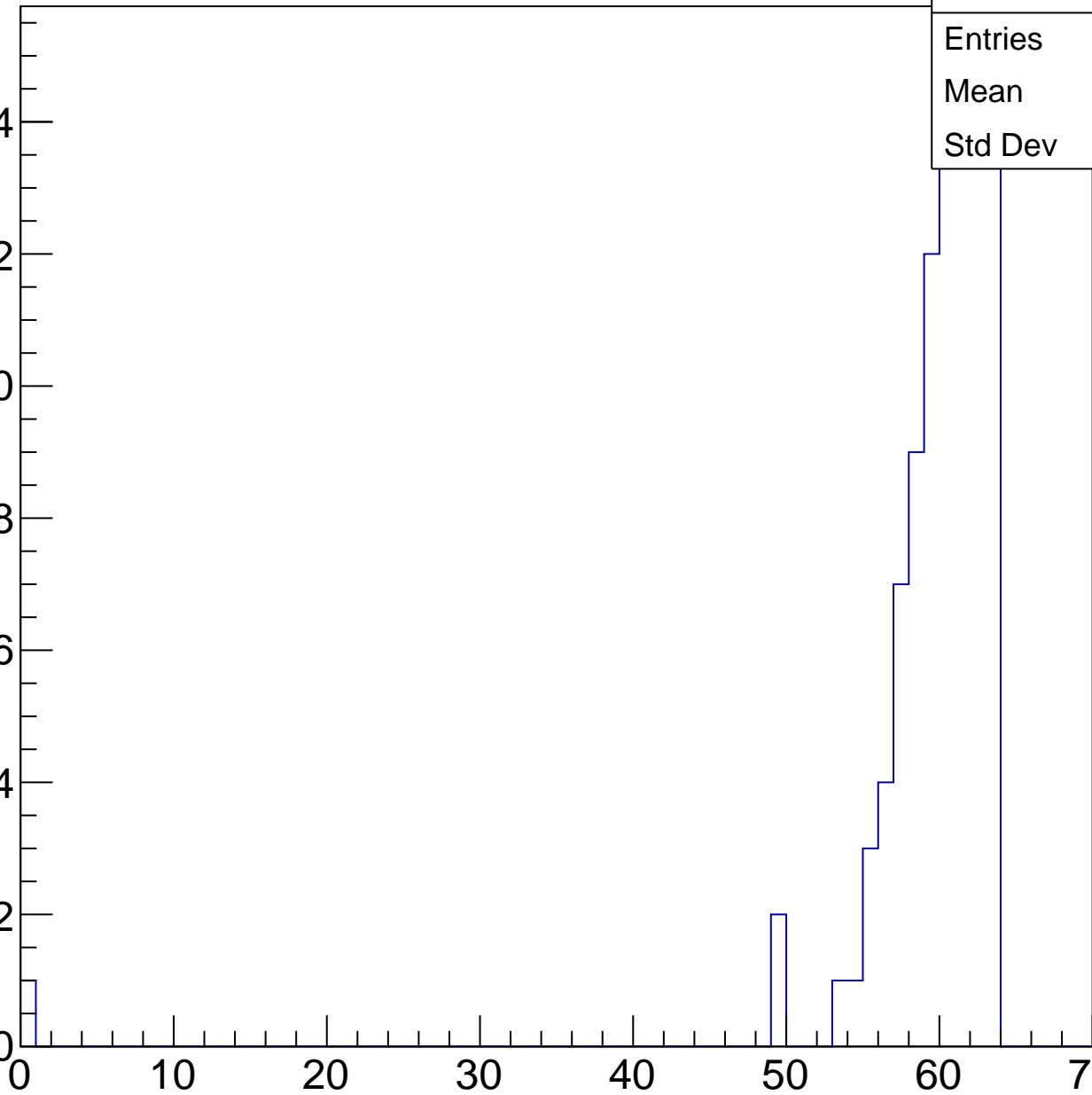
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	59.1
Std Dev	6.593

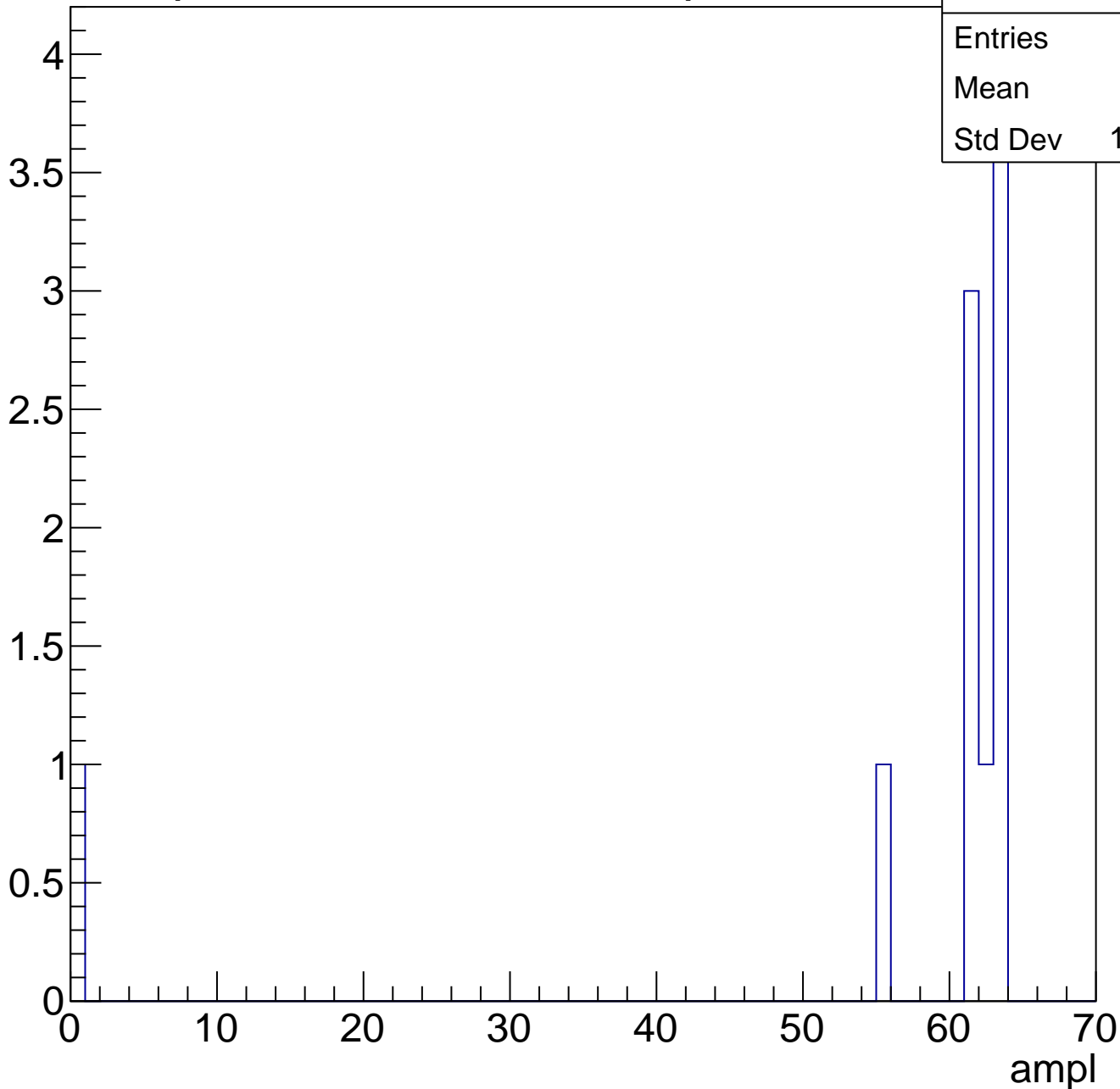
ampl



B0L101S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch77, adc0

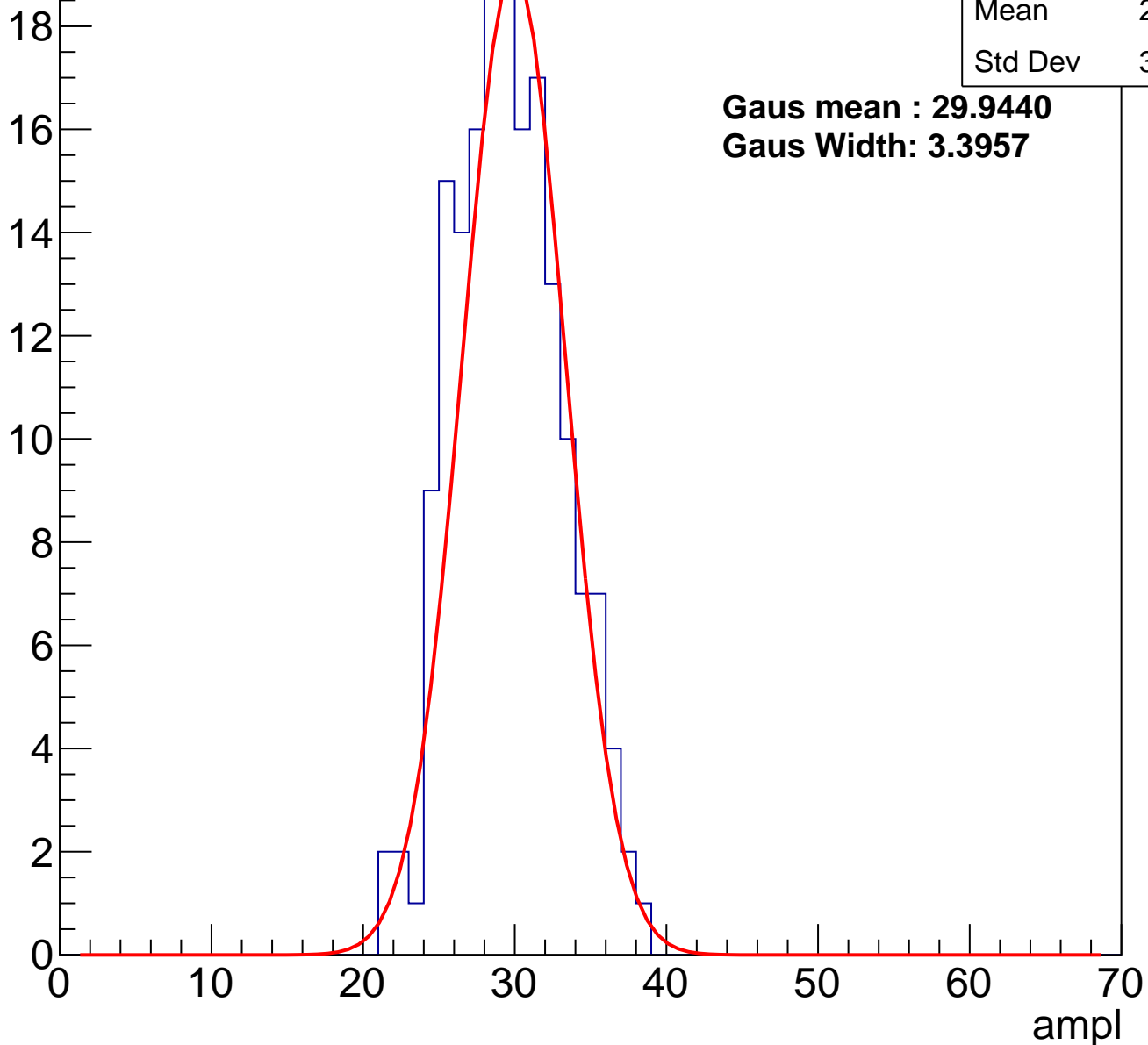
calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	29.14
Std Dev	3.495

Gaus mean : 29.9440

Gaus Width: 3.3957

Entry



B0L101S, U15-ch77, adc1

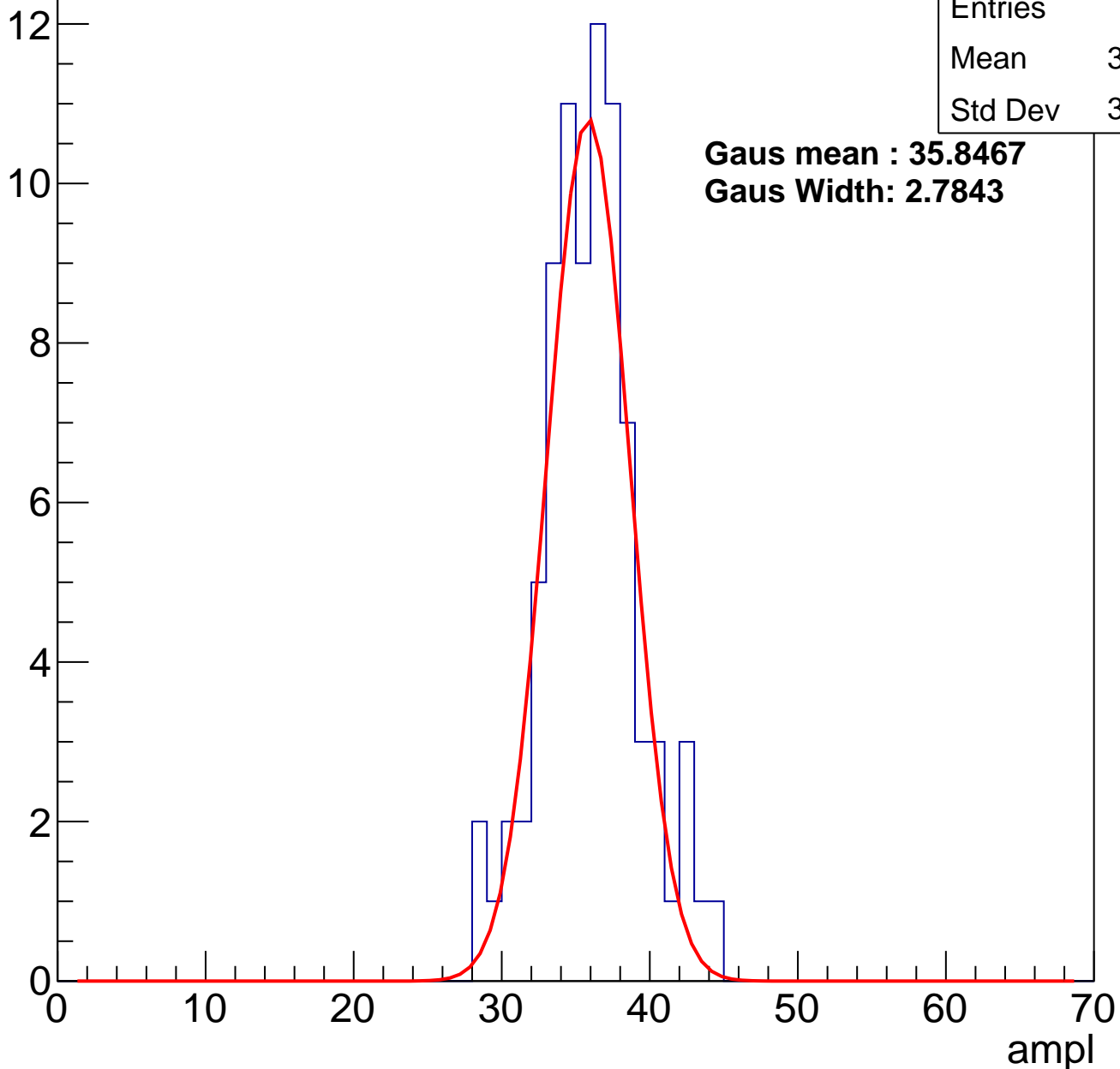
calib_packv5_042523_0143.root, FC#1, port C1

Entries	83
Mean	35.53
Std Dev	3.209

Gaus mean : 35.8467

Gaus Width: 2.7843

Entry



B0L101S, U15-ch77, adc2

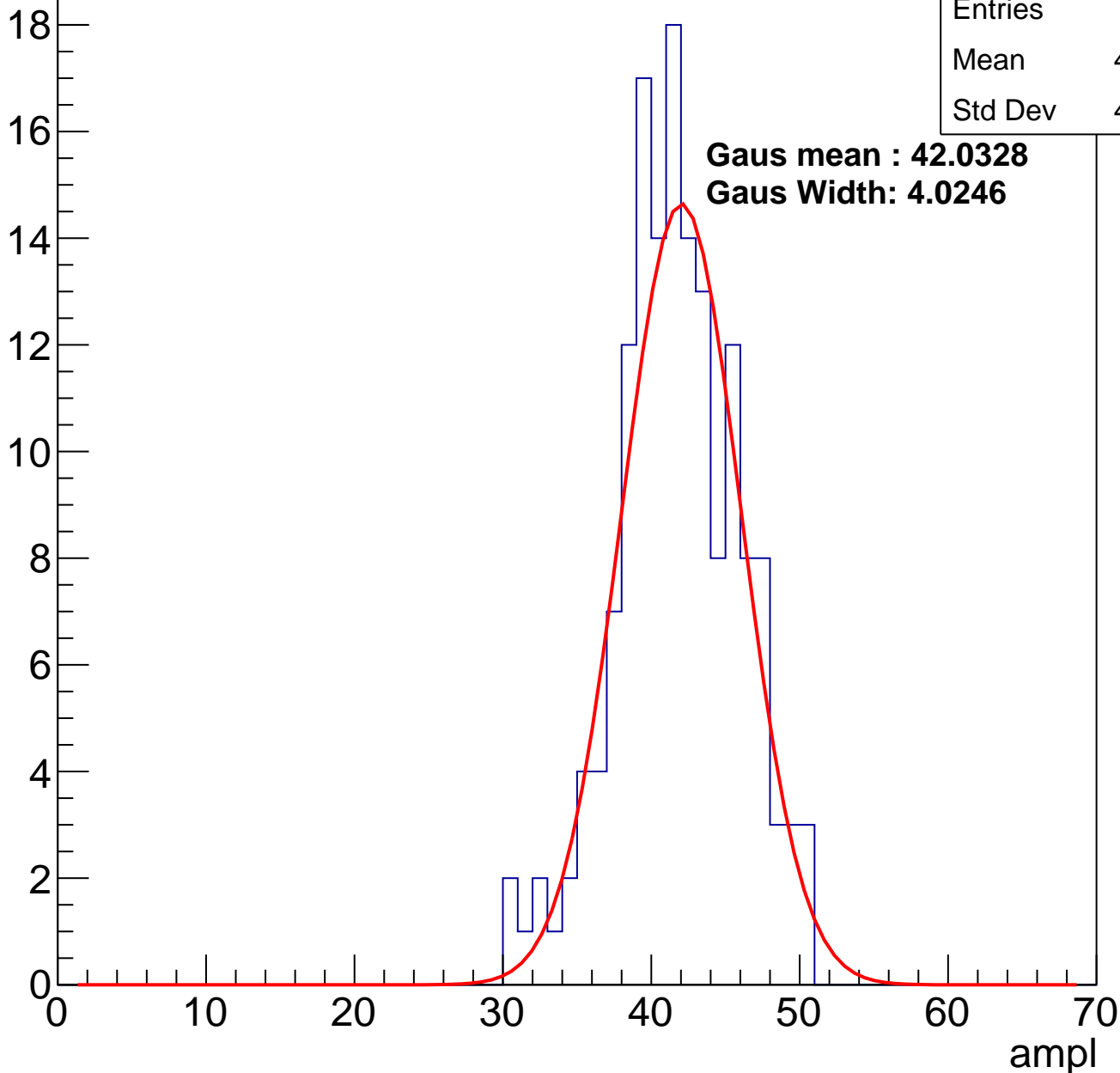
calib_packv5_042523_0143.root, FC#1, port C1

Entries	156
Mean	41.28
Std Dev	4.082

Gaus mean : 42.0328

Gaus Width: 4.0246

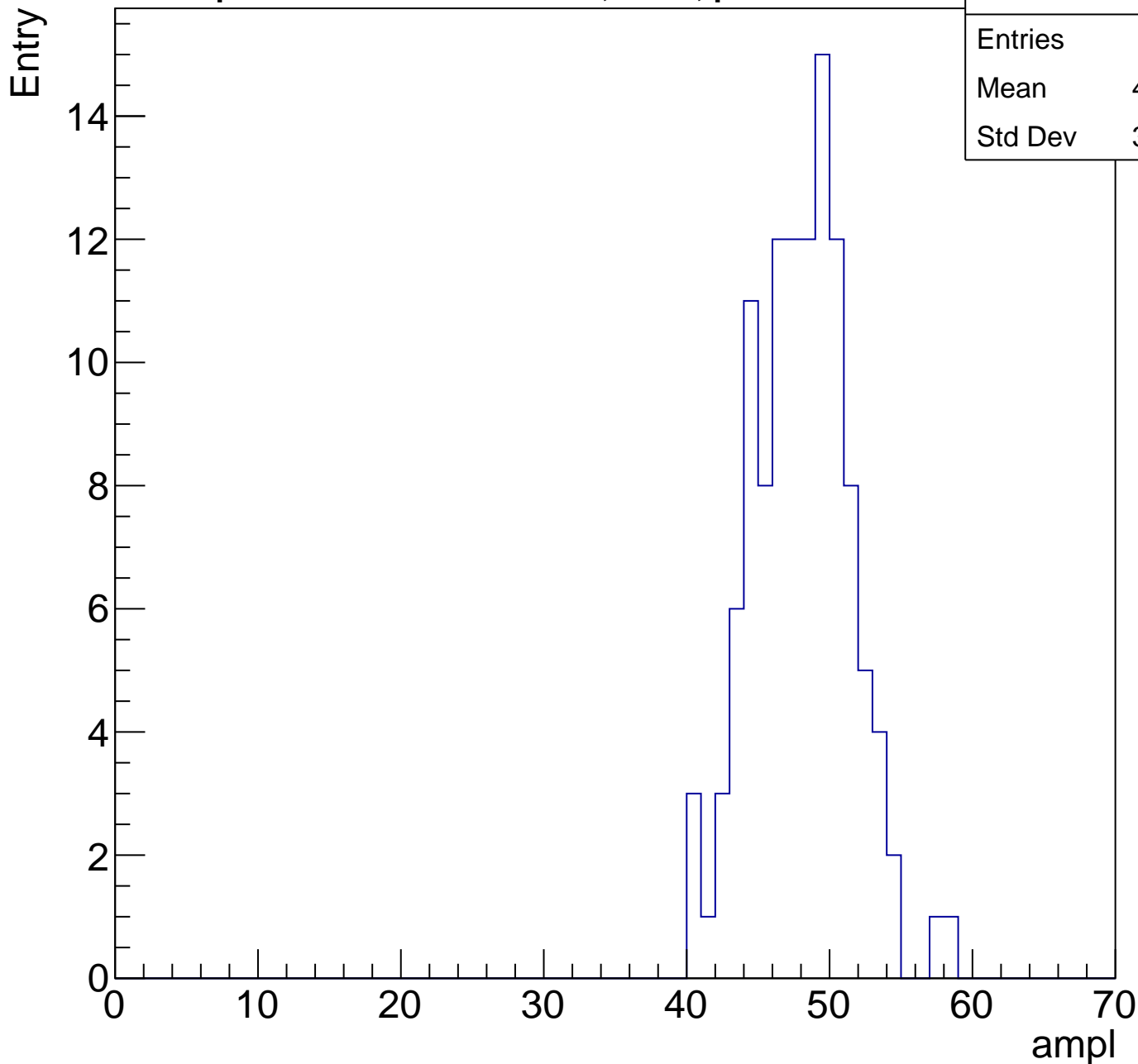
Entry



B0L101S, U15-ch77, adc3

calib_packv5_042523_0143.root, FC#1, port C1

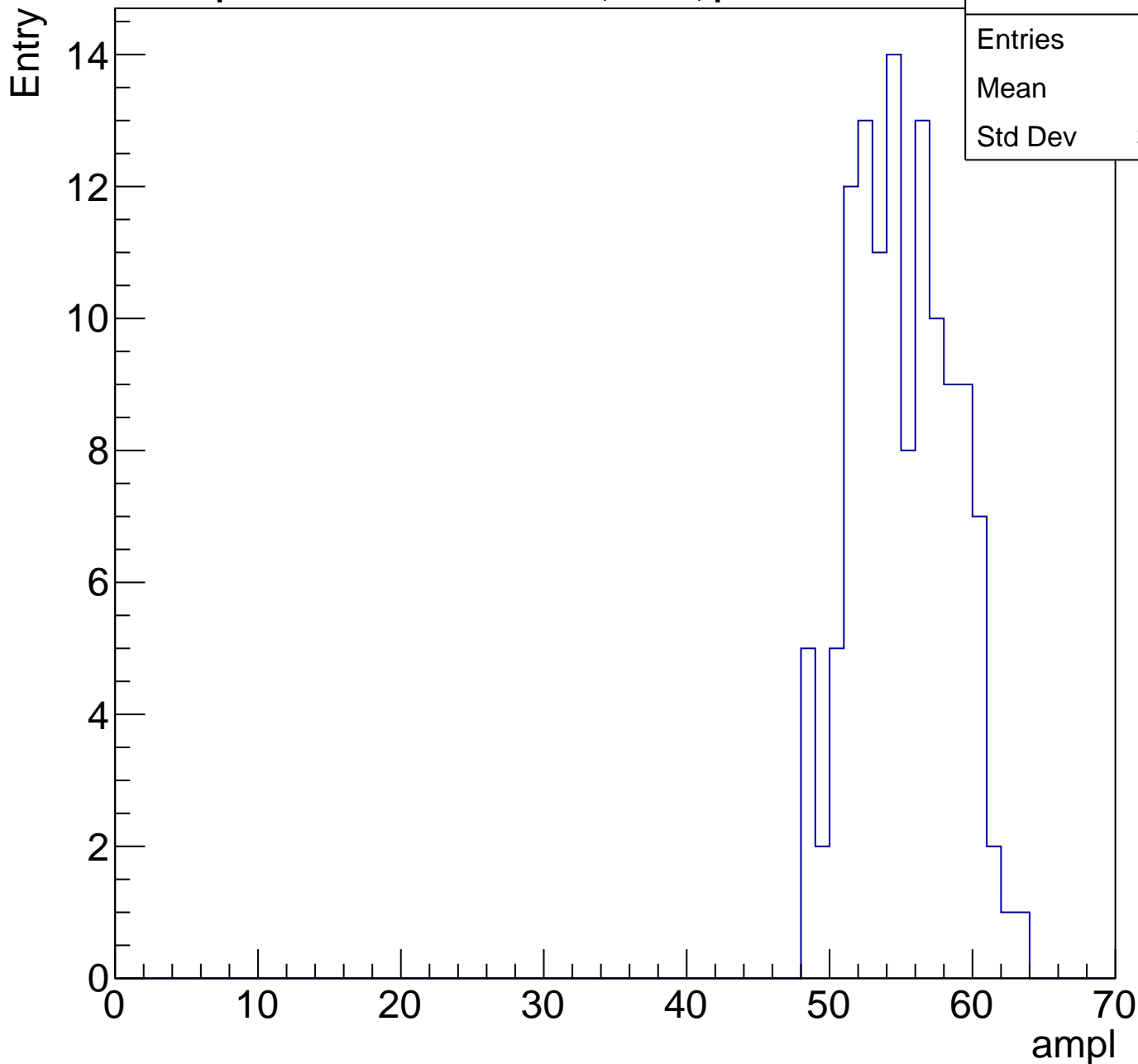
Entries	116
Mean	47.58
Std Dev	3.417



B0L101S, U15-ch77, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	54.7
Std Dev	3.431



B0L101S, U15-ch77, adc5

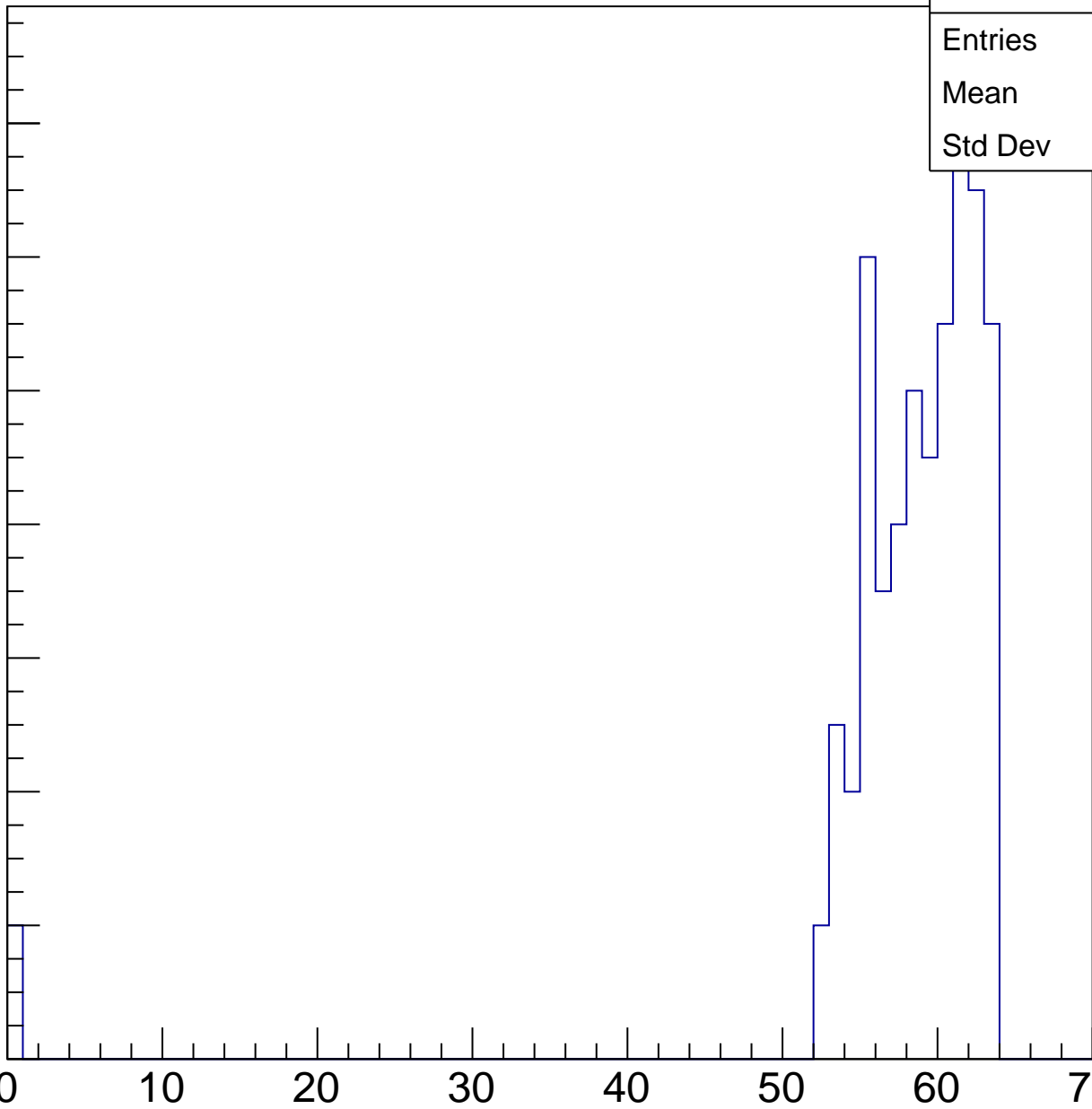
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	57.6
Std Dev	8.452

ampl

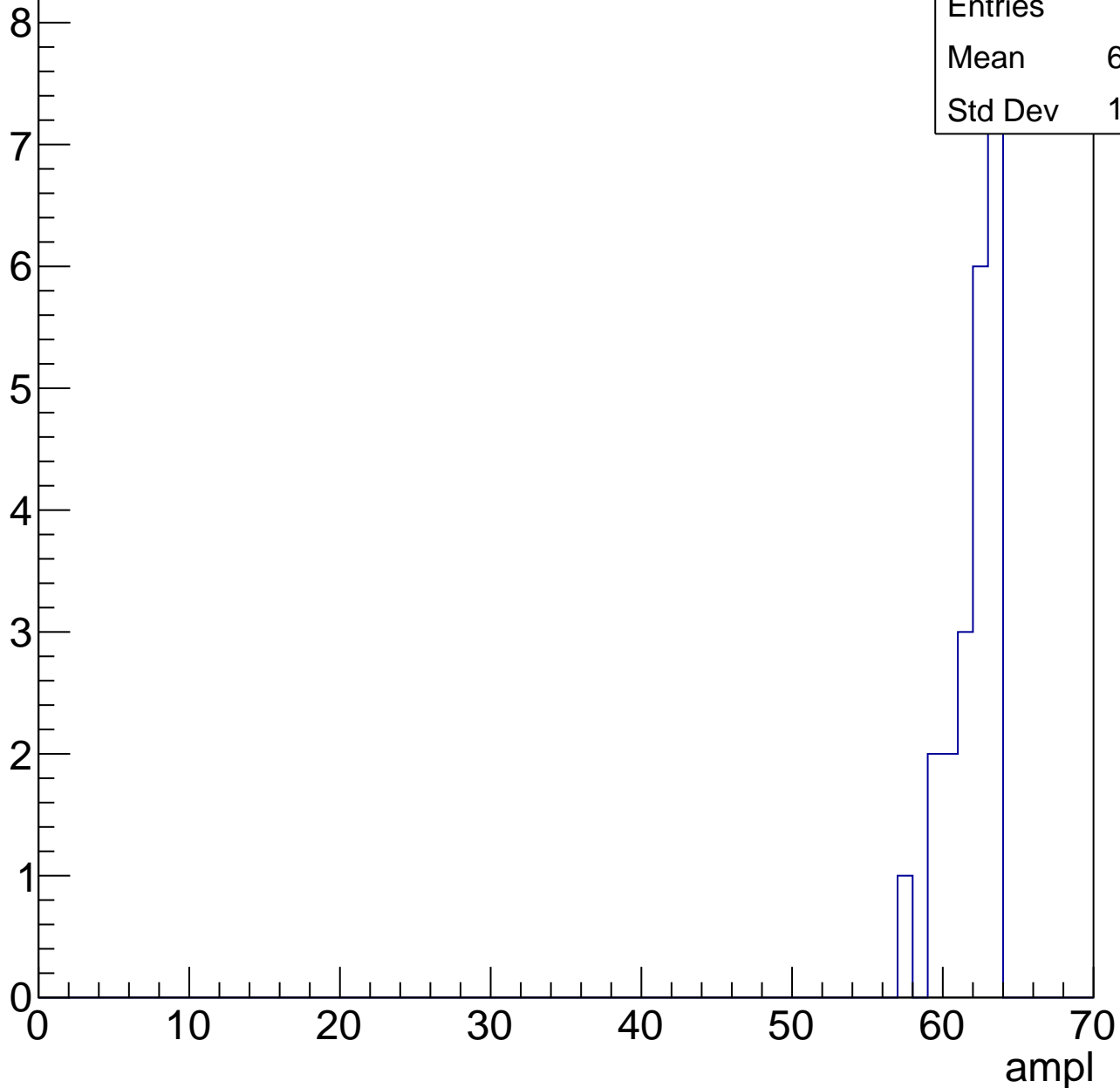


B0L101S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	22
Mean	61.55
Std Dev	1.616



B0L101S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch78, adc0

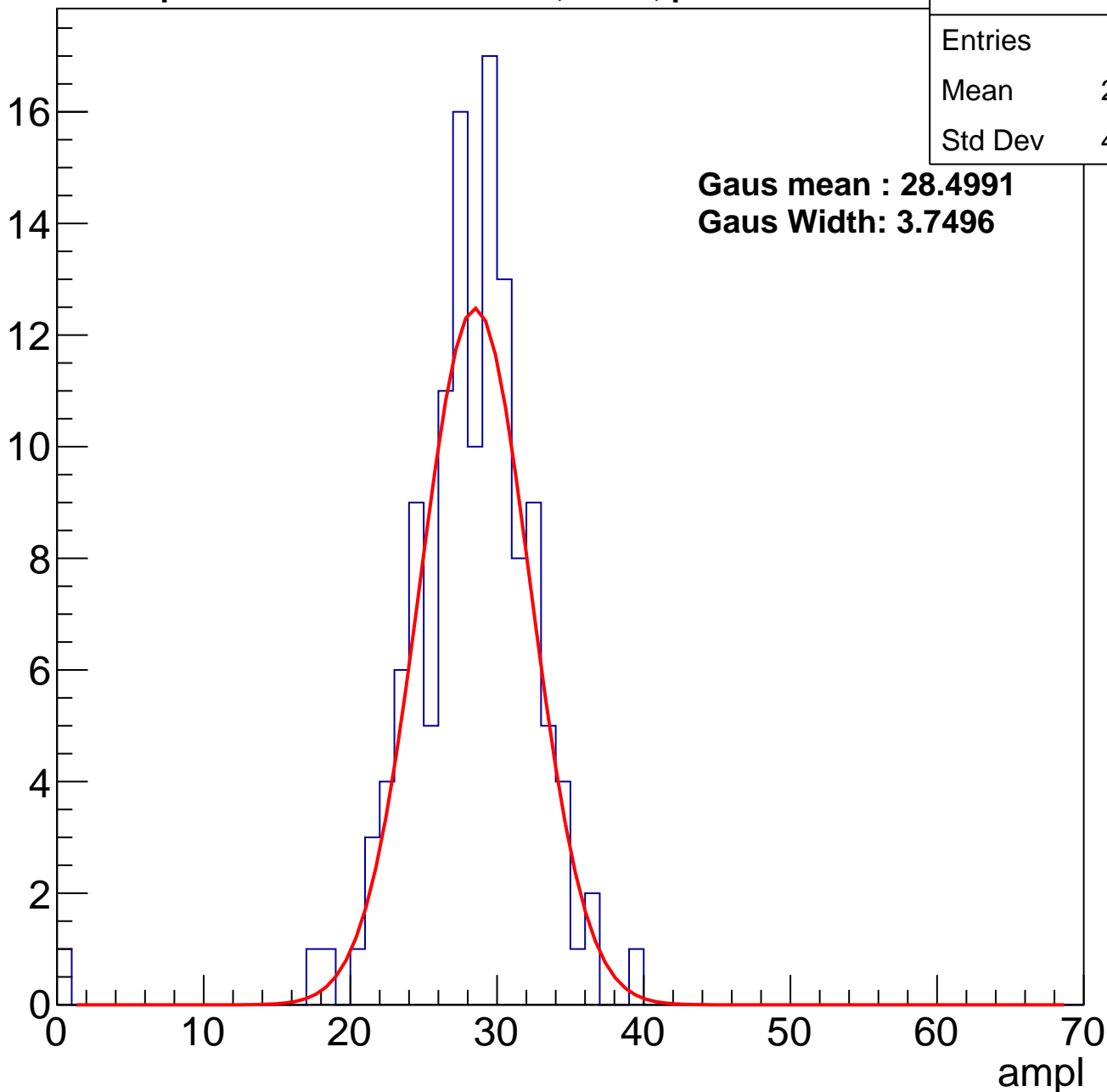
calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	27.73
Std Dev	4.508

Gaus mean : 28.4991

Gaus Width: 3.7496

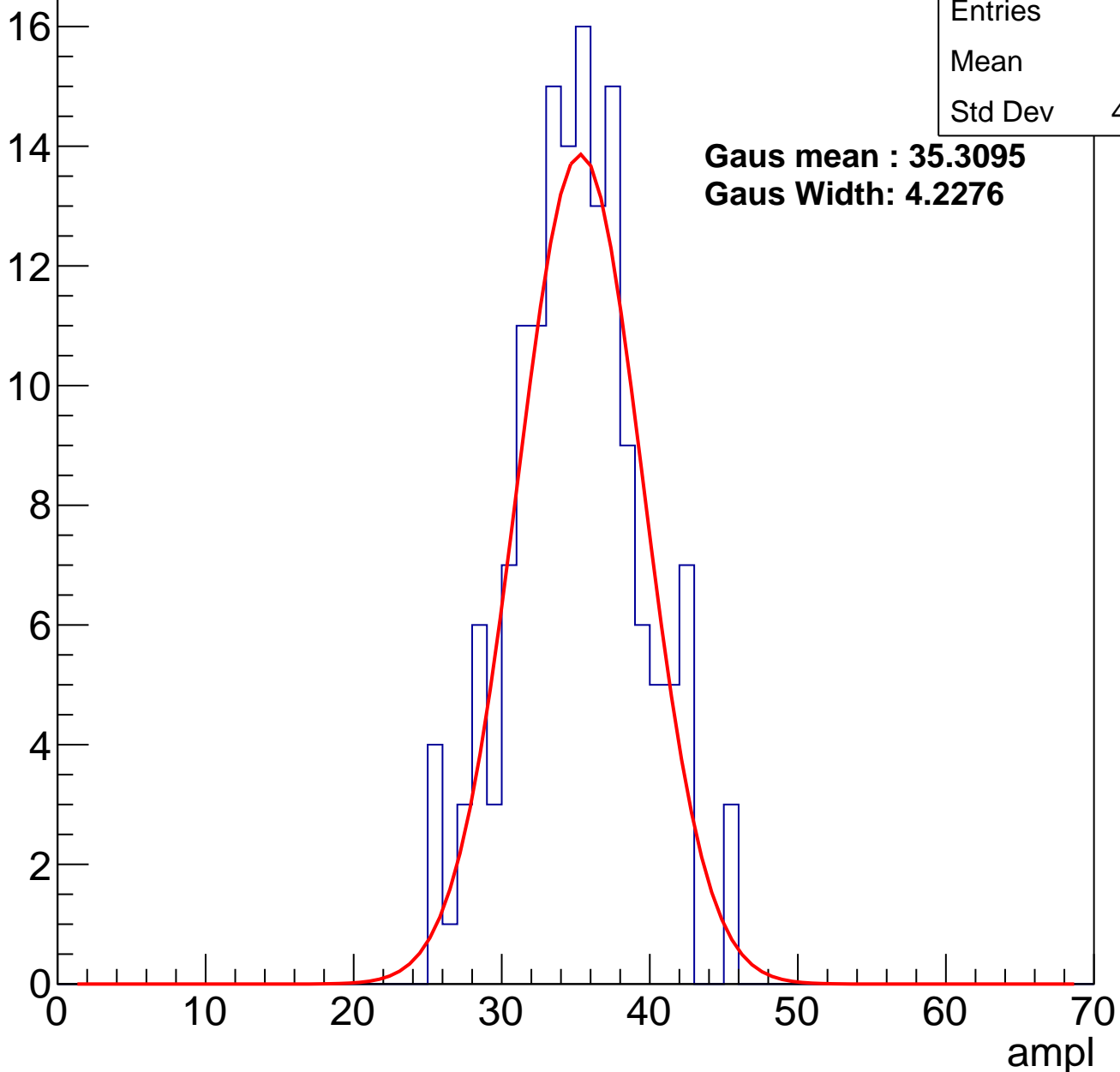
Entry



B0L101S, U15-ch78, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch78, adc2

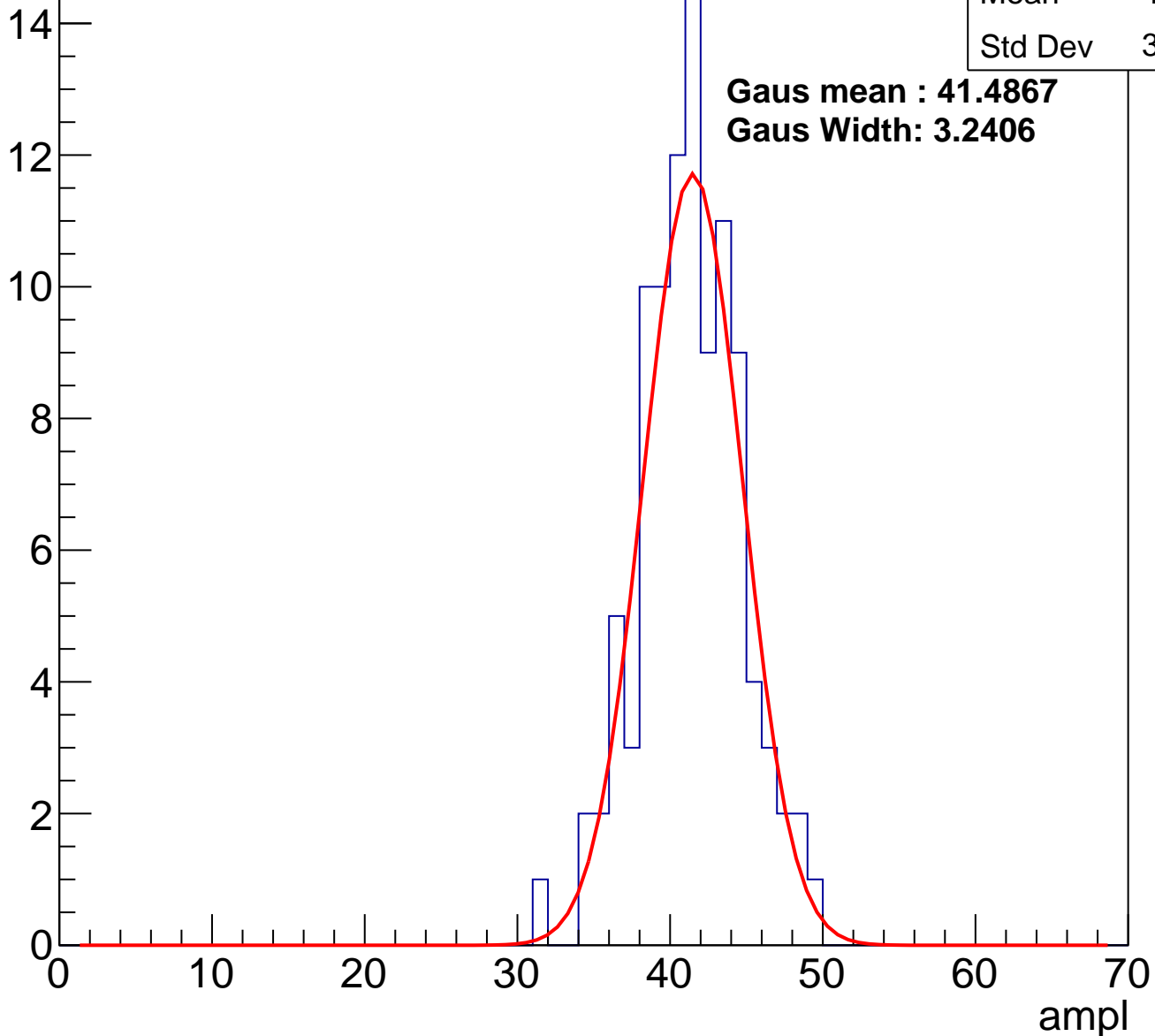
calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	40.88
Std Dev	3.286

Gaus mean : 41.4867

Gaus Width: 3.2406

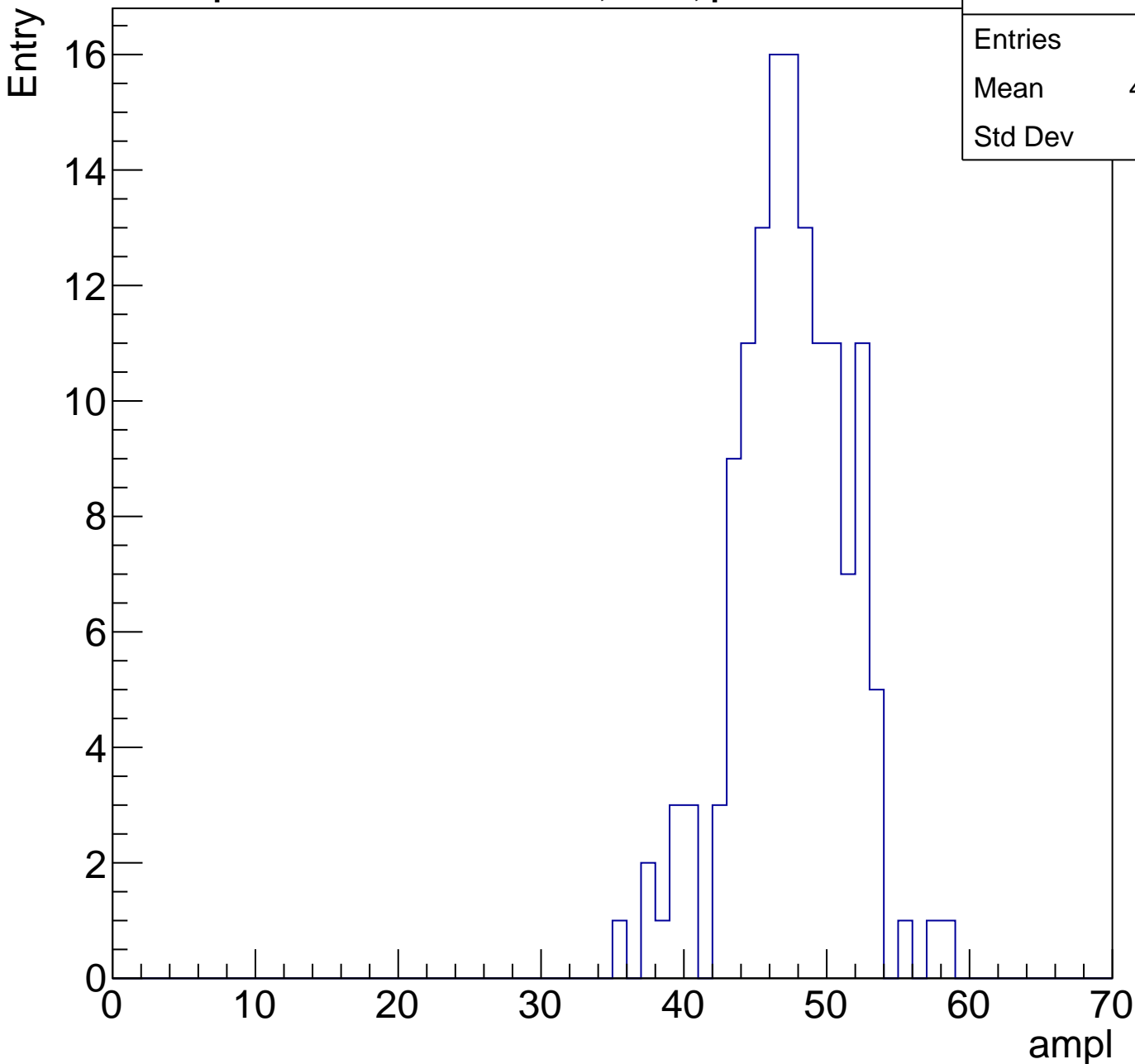
Entry



B0L101S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	46.99
Std Dev	3.94

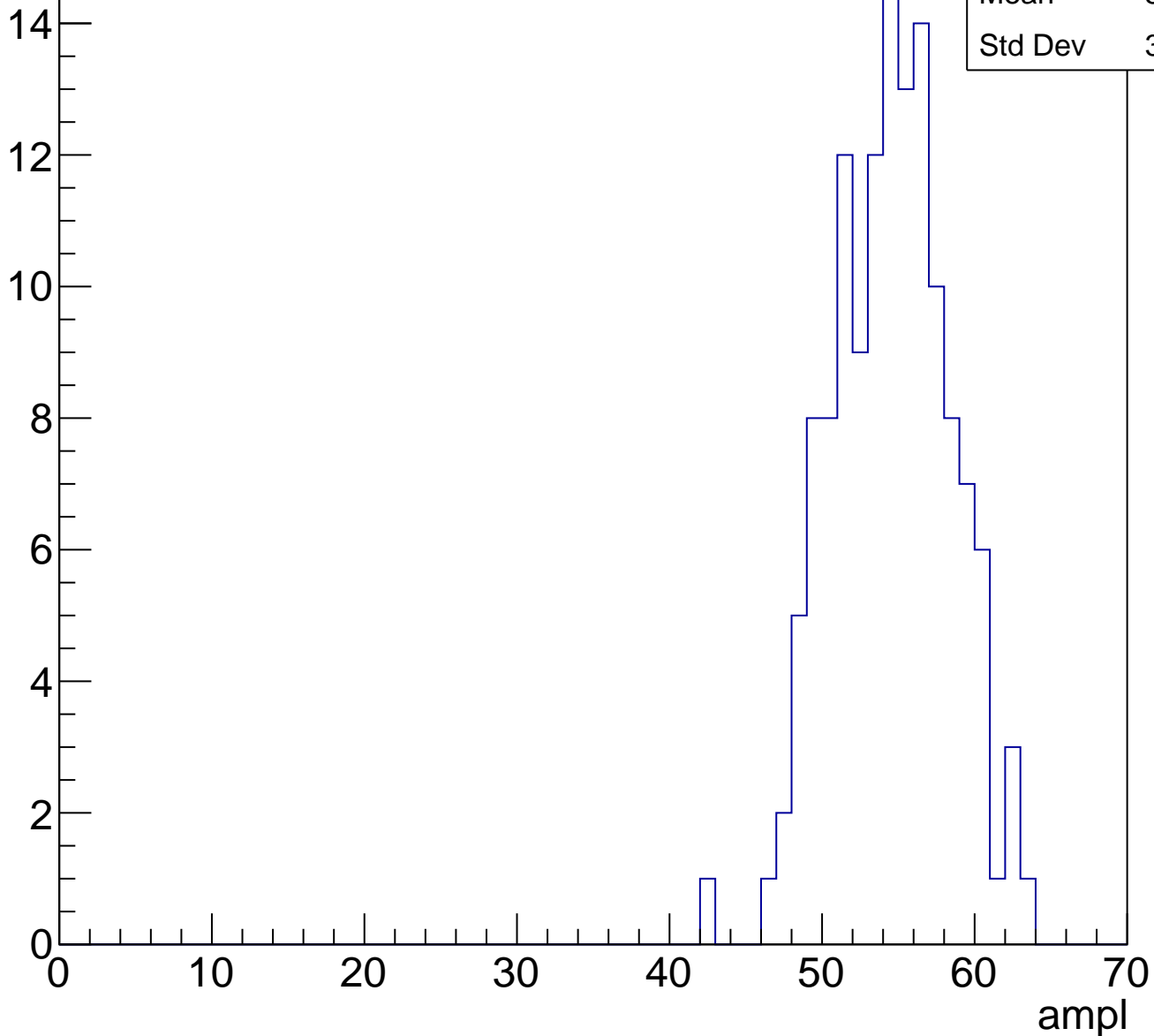


B0L101S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	54.09
Std Dev	3.805

Entry



B0L101S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#1, port C1

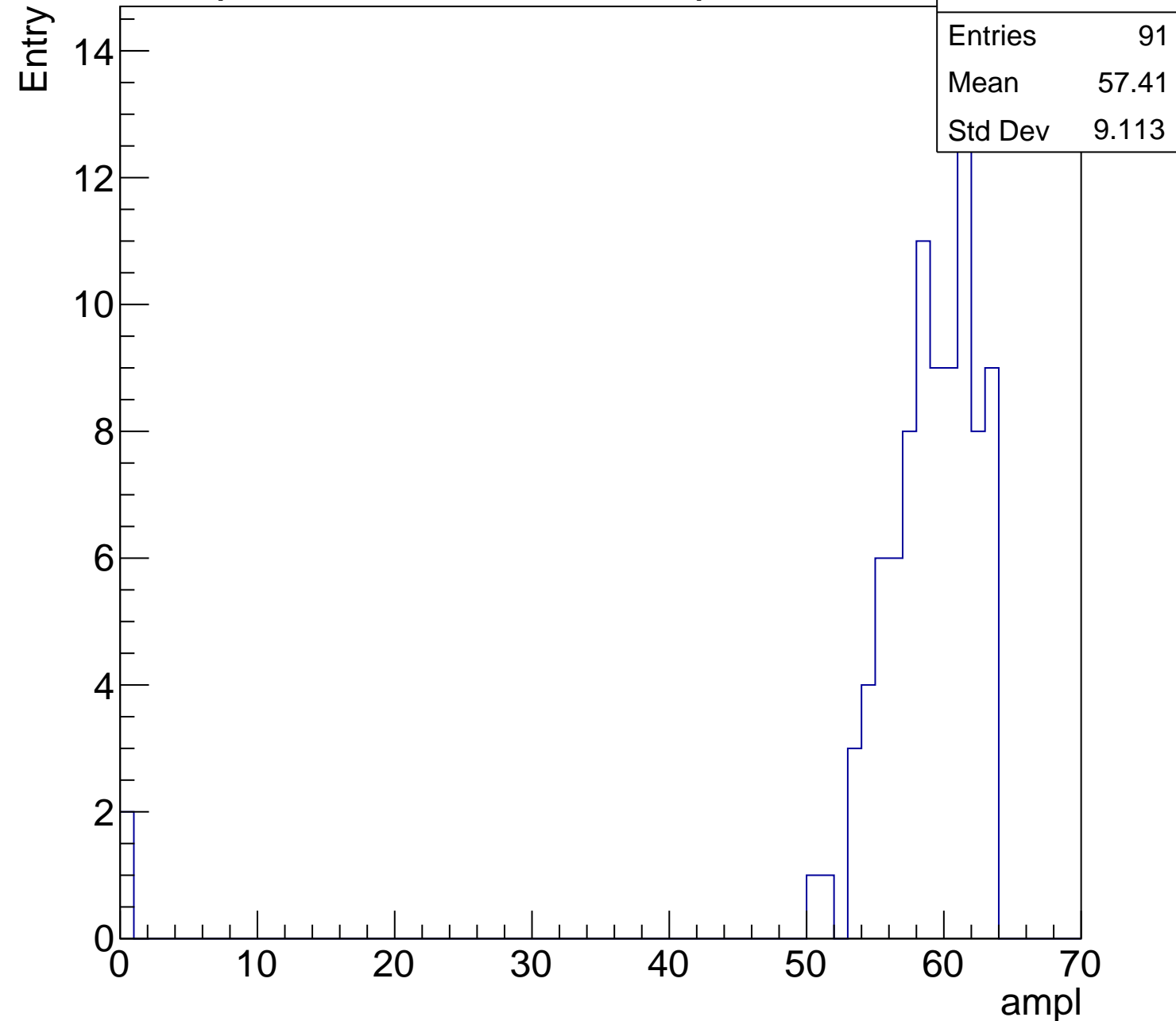
Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	57.41
Std Dev	9.113

ampl

0 10 20 30 40 50 60 70

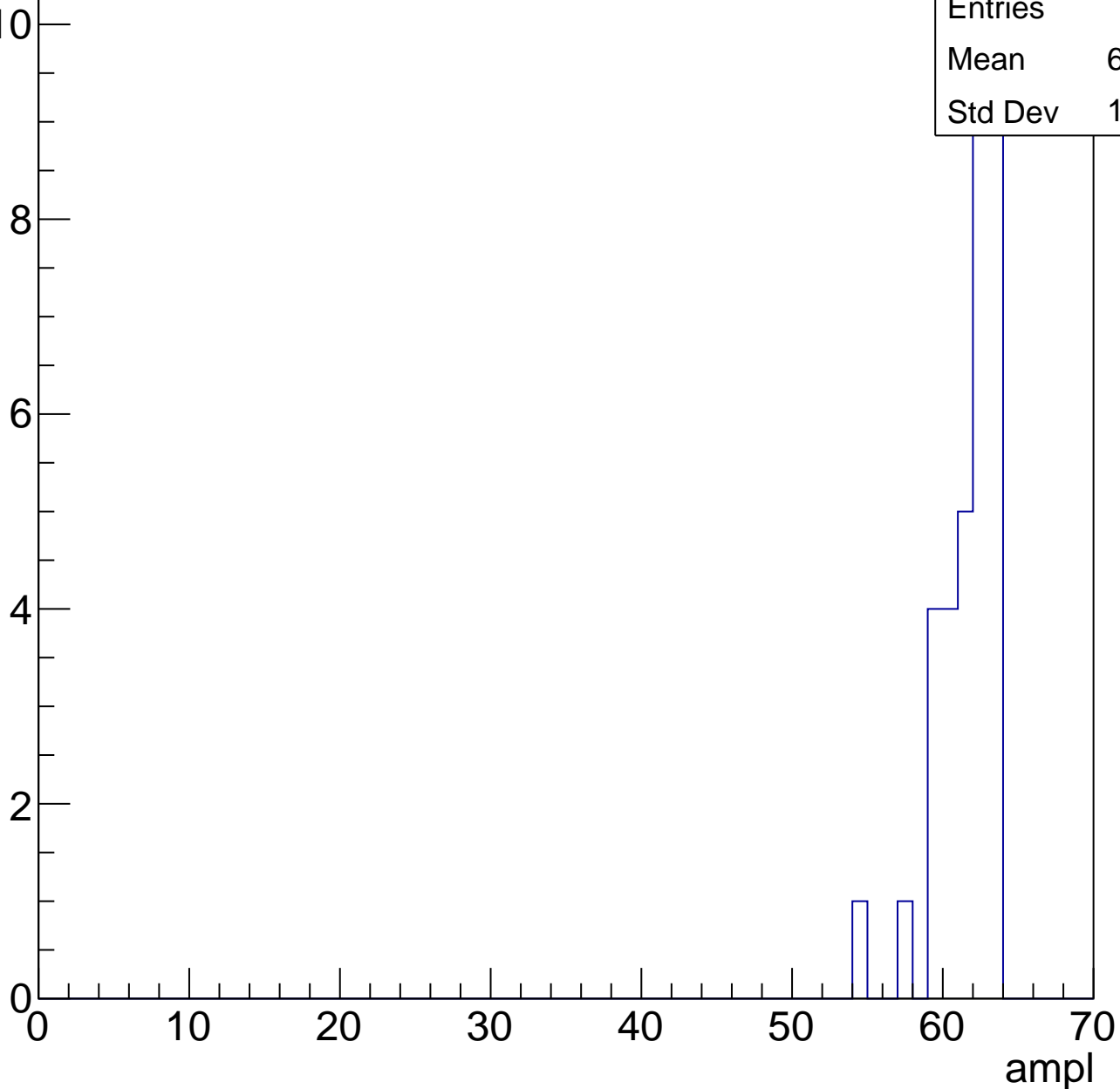


B0L101S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

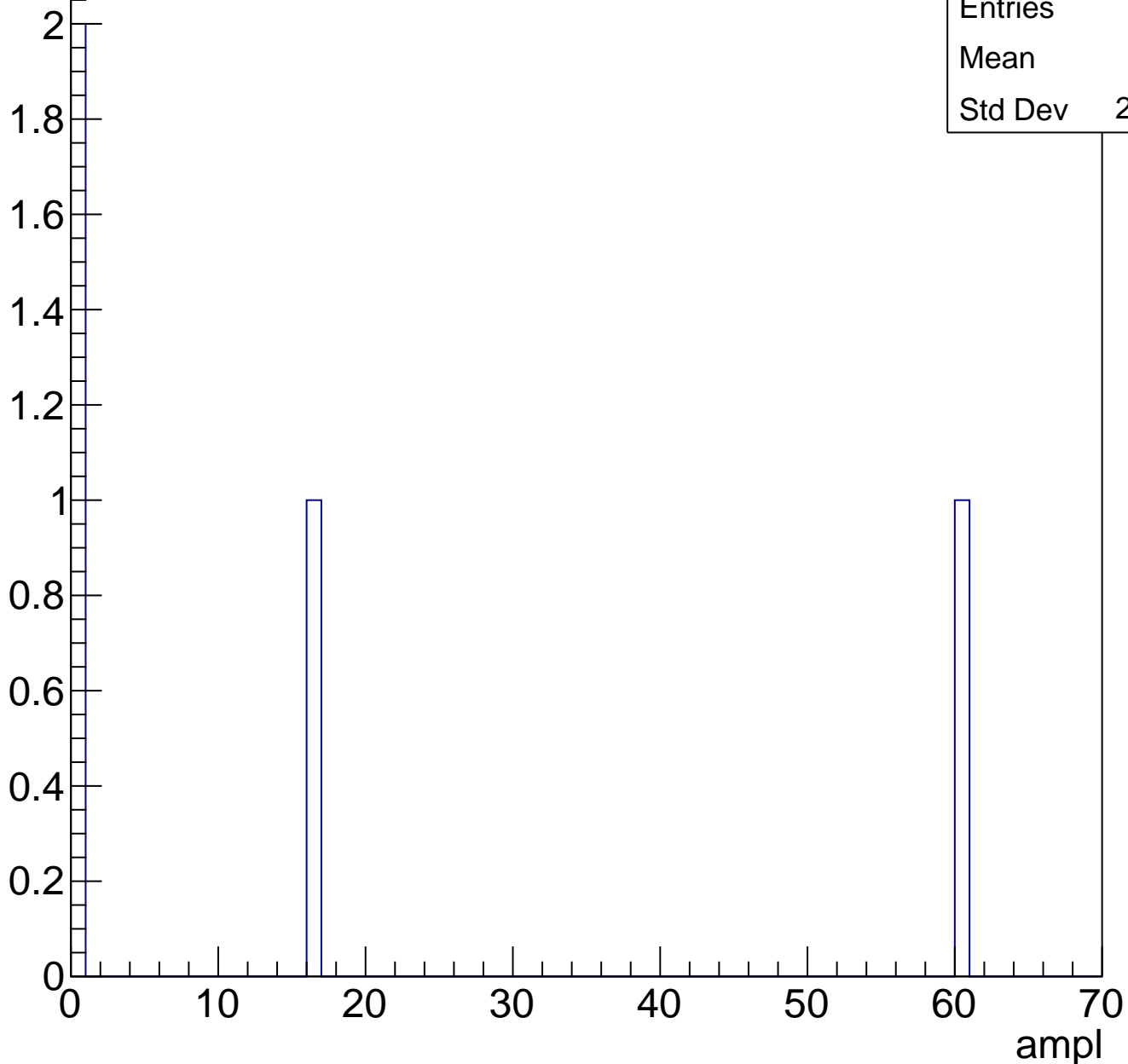
Entries	34
Mean	61.18
Std Dev	1.977



B0L101S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	19
Std Dev	24.56

B0L101S, U15-ch79, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	26.91
Std Dev	3.457

Gaus mean : 27.3196

Gaus Width: 3.5285

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch79, adc1

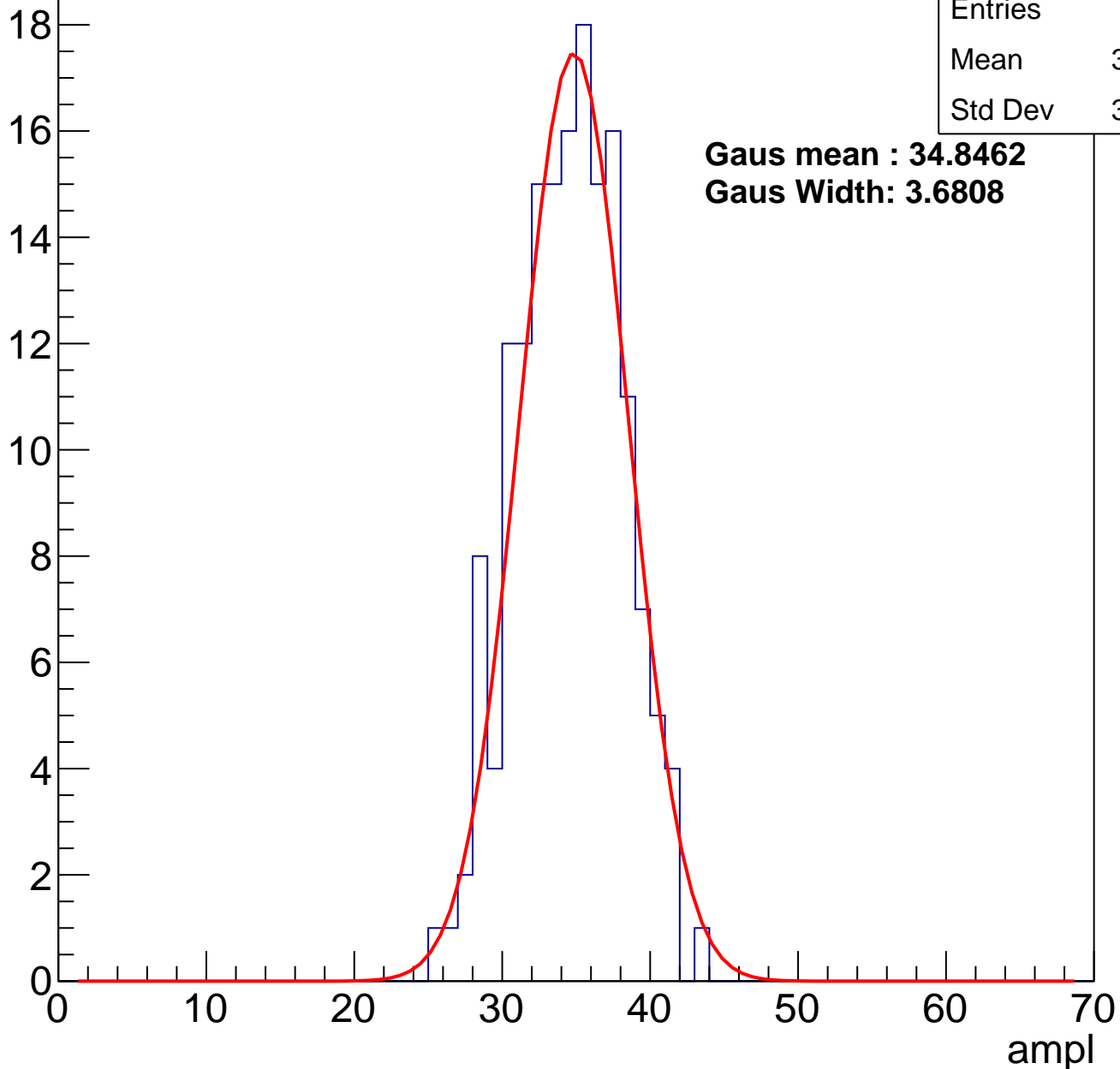
calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	34.09
Std Dev	3.533

Gaus mean : 34.8462

Gaus Width: 3.6808

Entry



B0L101S, U15-ch79, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

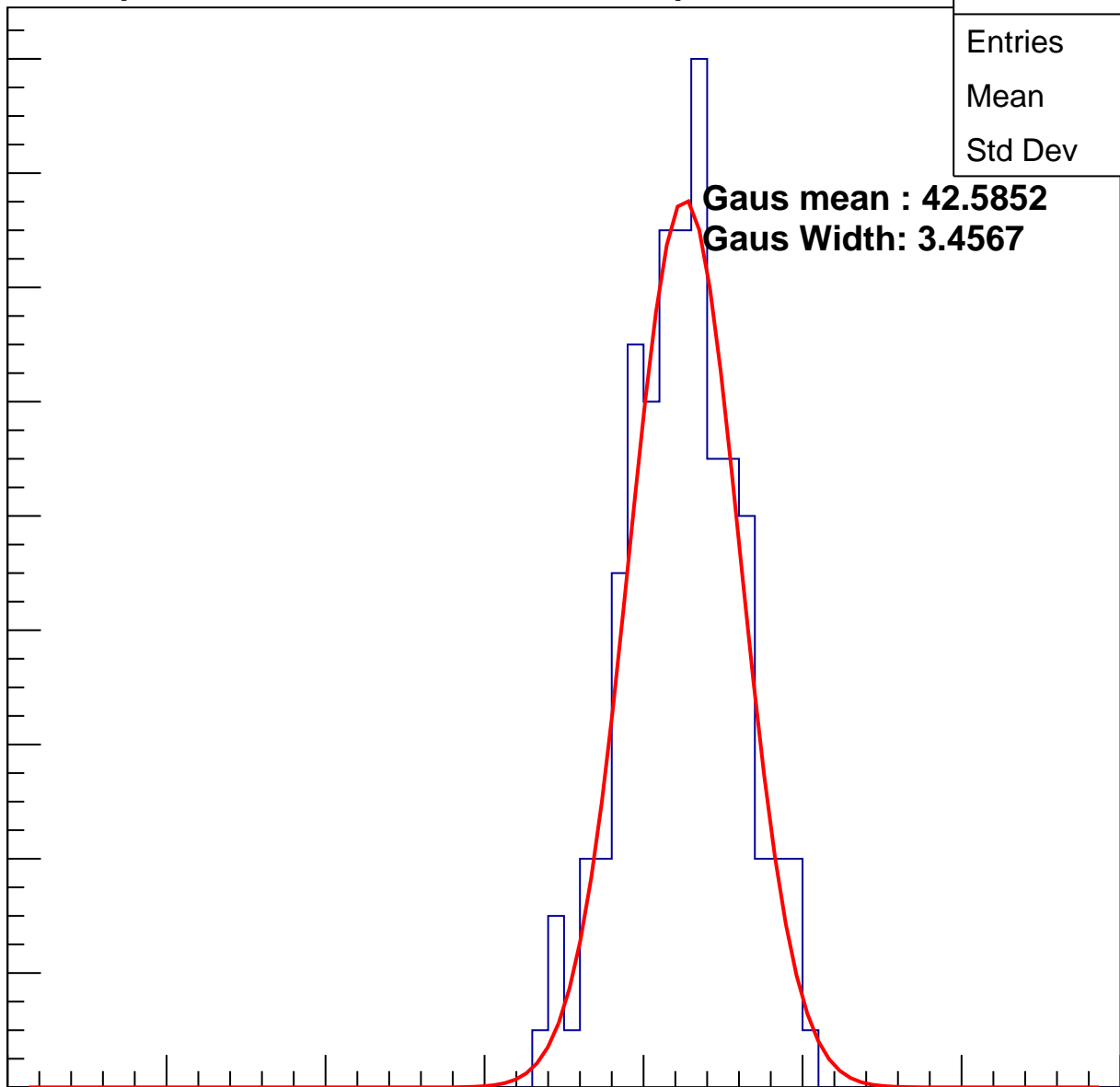
Entries	140
Mean	41.96
Std Dev	3.5

Gaus mean : 42.5852

Gaus Width: 3.4567

0 10 20 30 40 50 60 70

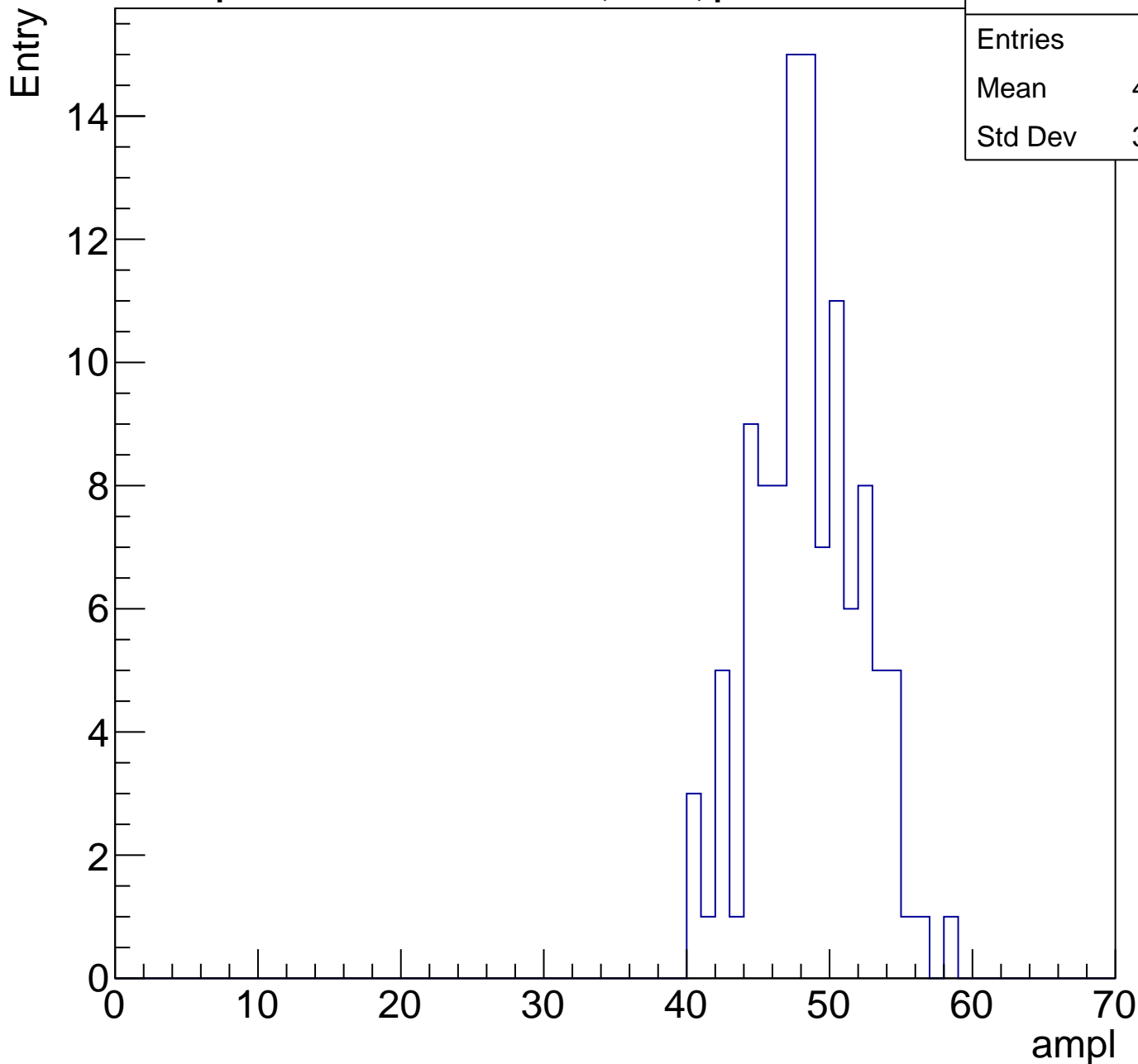
ampl



B0L101S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#1, port C1

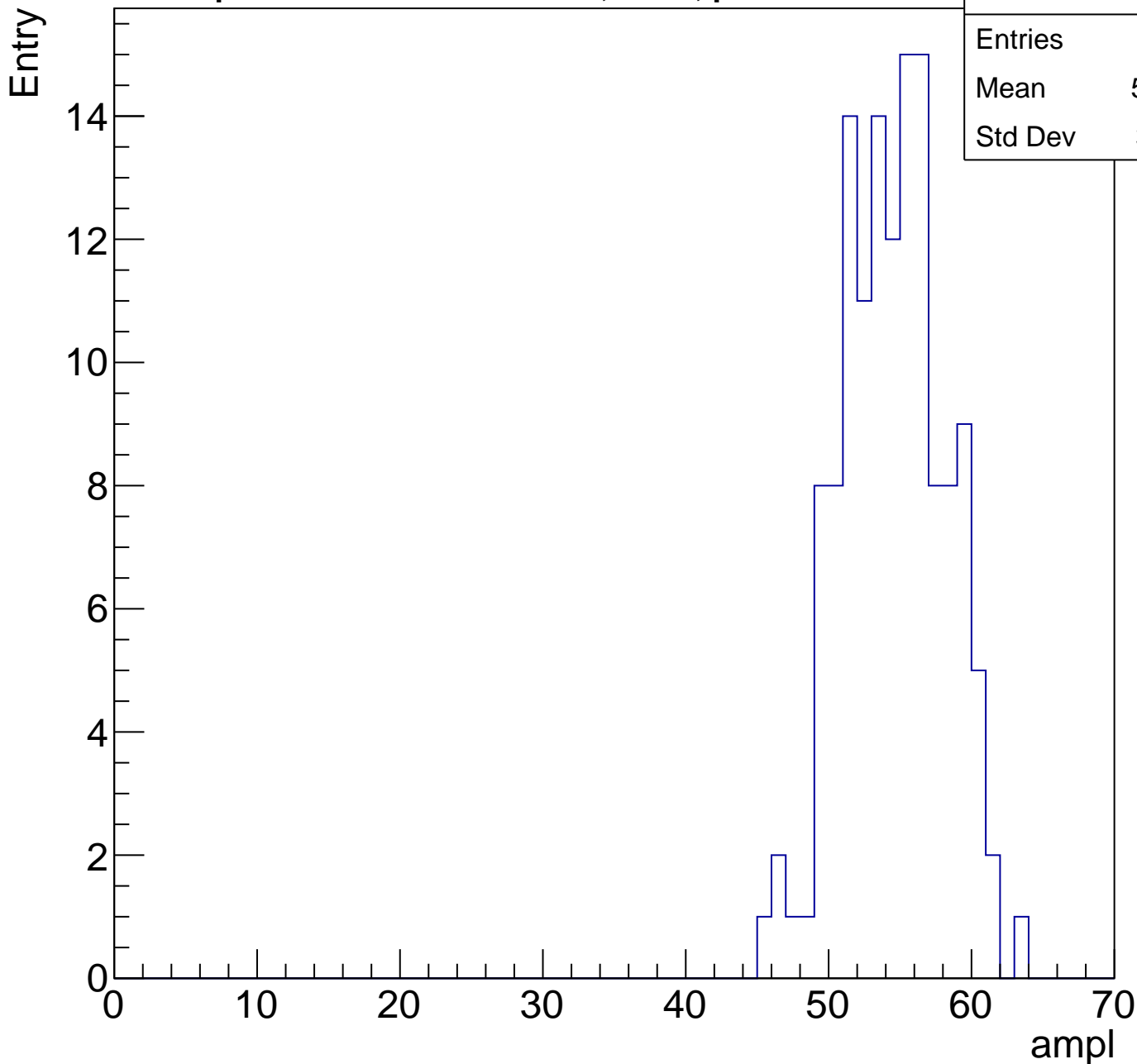
Entries	110
Mean	48.02
Std Dev	3.643



B0L101S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	54.08
Std Dev	3.511



B0L101S, U15-ch79, adc5

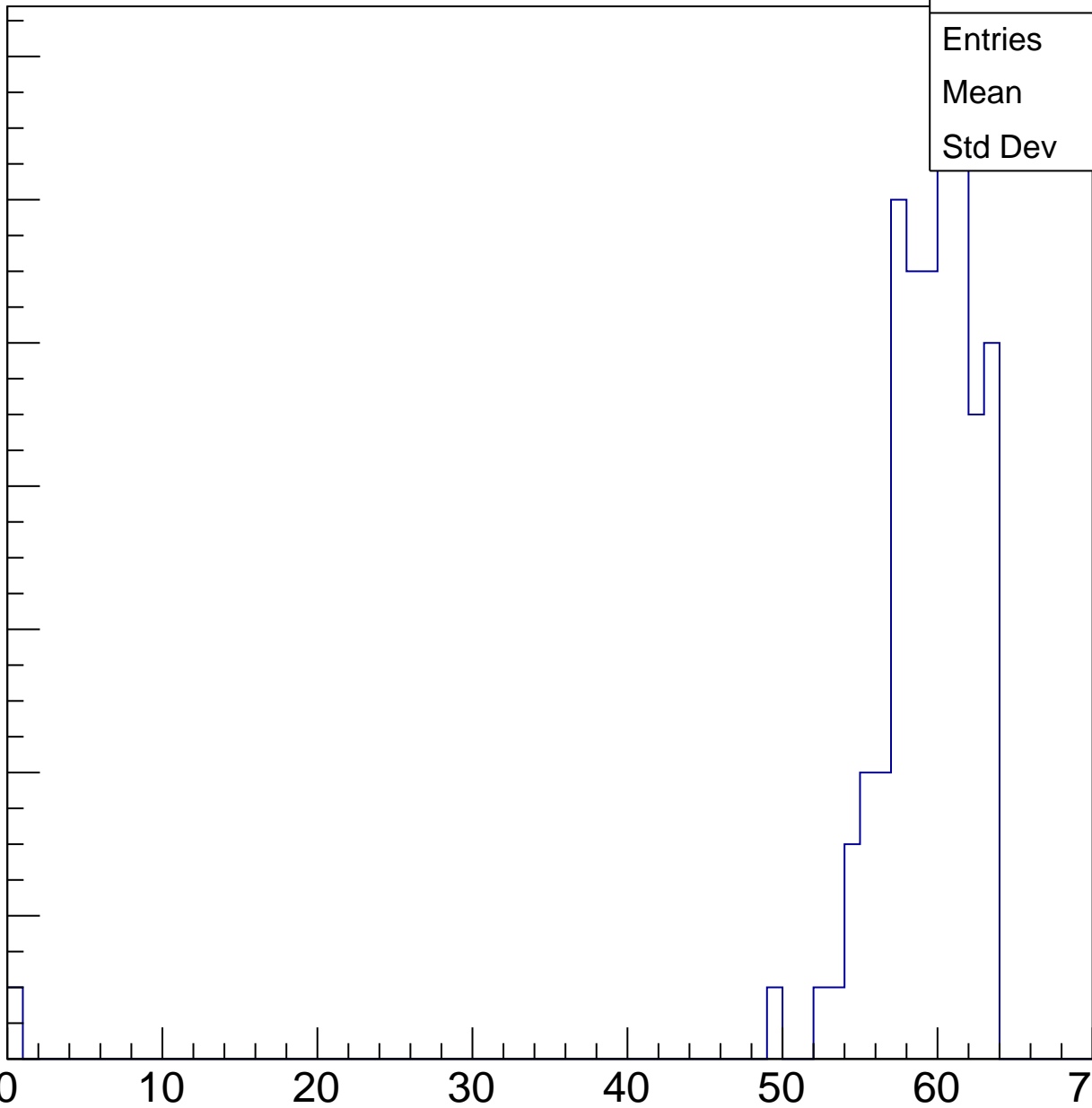
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.44
Std Dev	6.631

ampl



B0L101S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

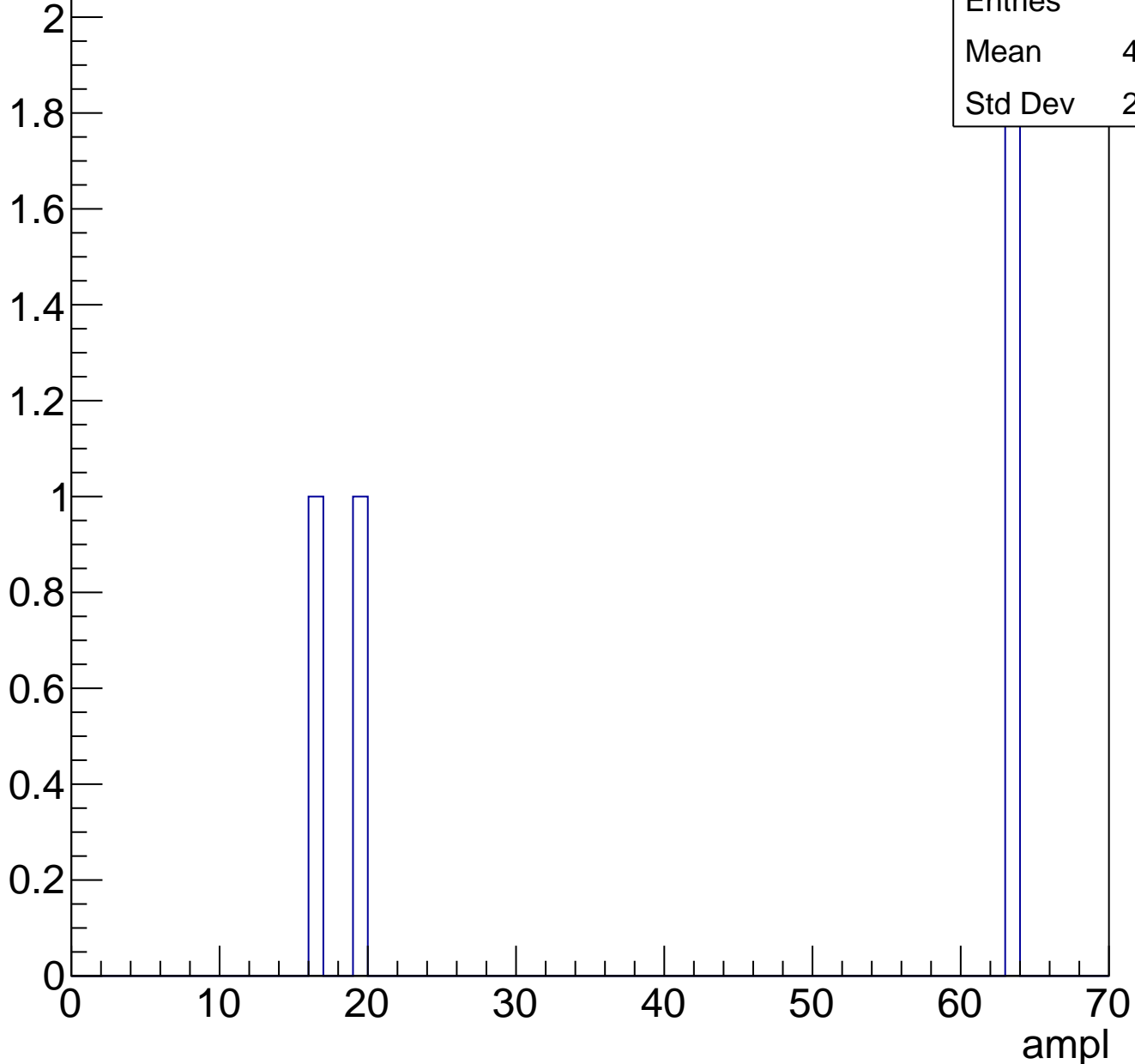
ampl

Entries	25
Mean	59.52
Std Dev	12.18

B0L101S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch80, adc0

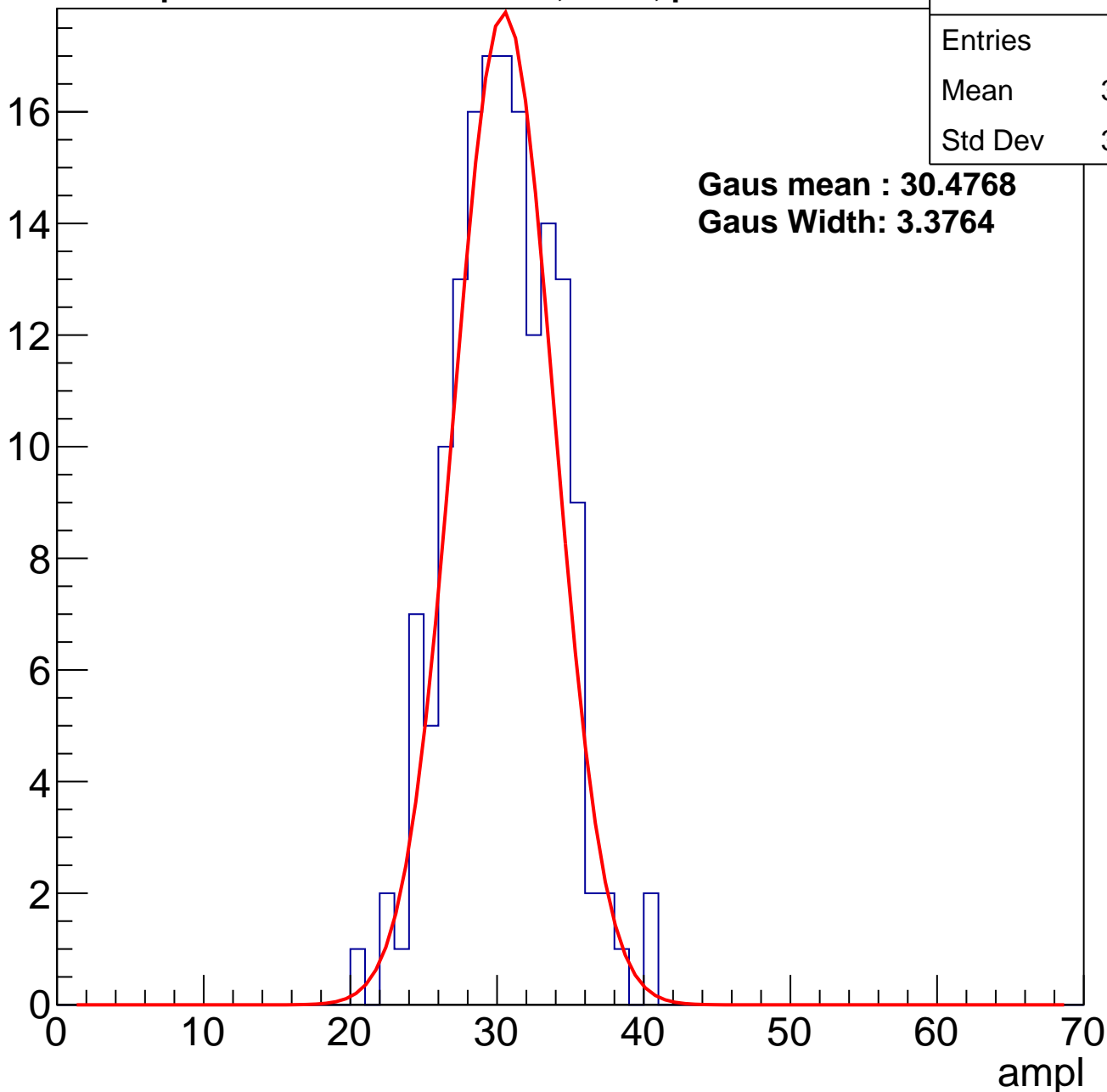
calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	30.03
Std Dev	3.596

Gaus mean : 30.4768

Gaus Width: 3.3764

Entry

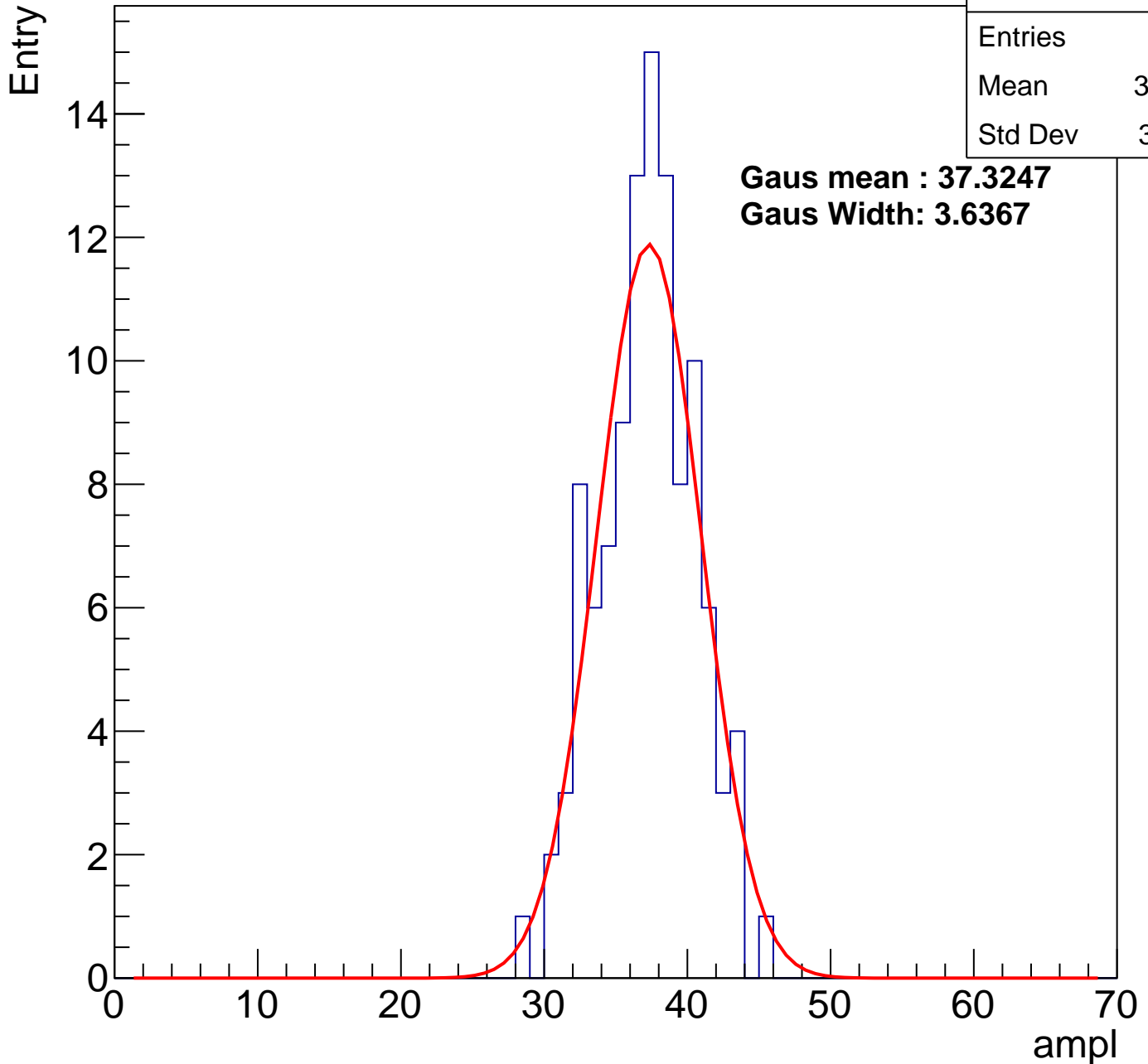


B0L101S, U15-ch80, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	36.75
Std Dev	3.321

Gaus mean : 37.3247
Gaus Width: 3.6367



B0L101S, U15-ch80, adc2

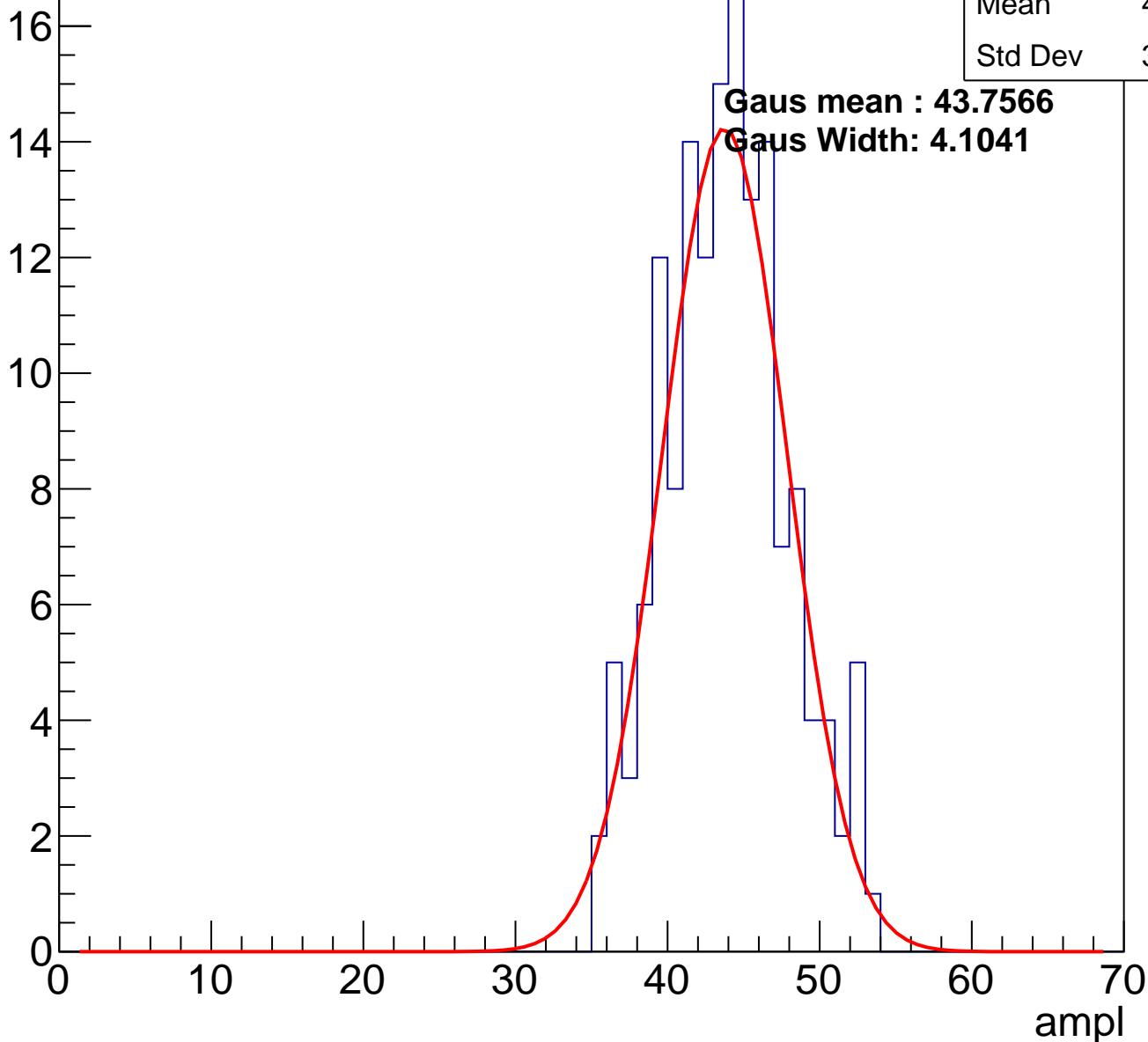
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	152
Mean	43.43
Std Dev	3.986

Gaus mean : 43.7566

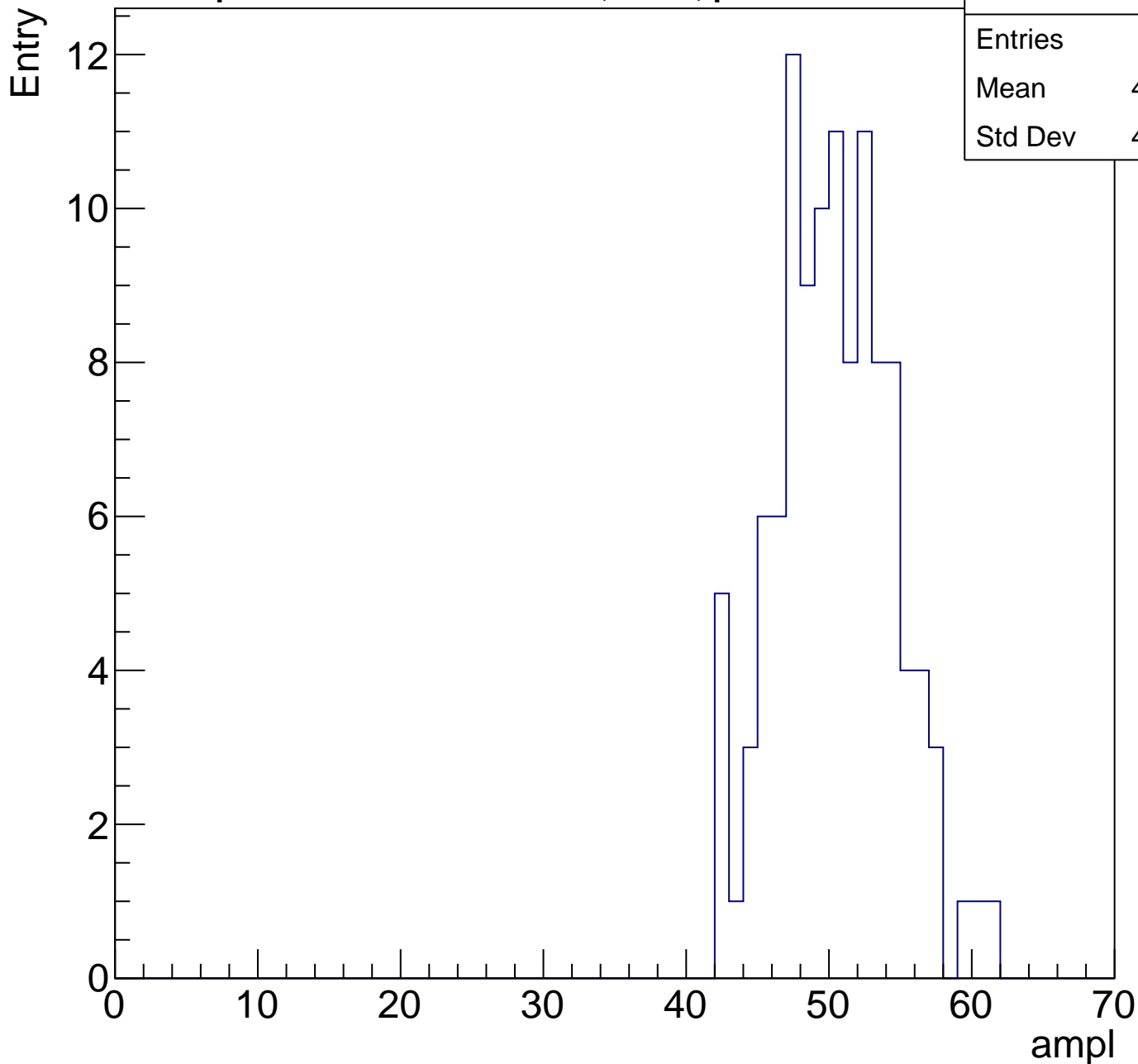
Gaus Width: 4.1041



B0L101S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	49.98
Std Dev	4.033



B0L101S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.08
Std Dev	3.62

Entry

12

10

8

6

4

2

0

0

10

20

30

40

ampl

60

70

B0L101S, U15-ch80, adc5

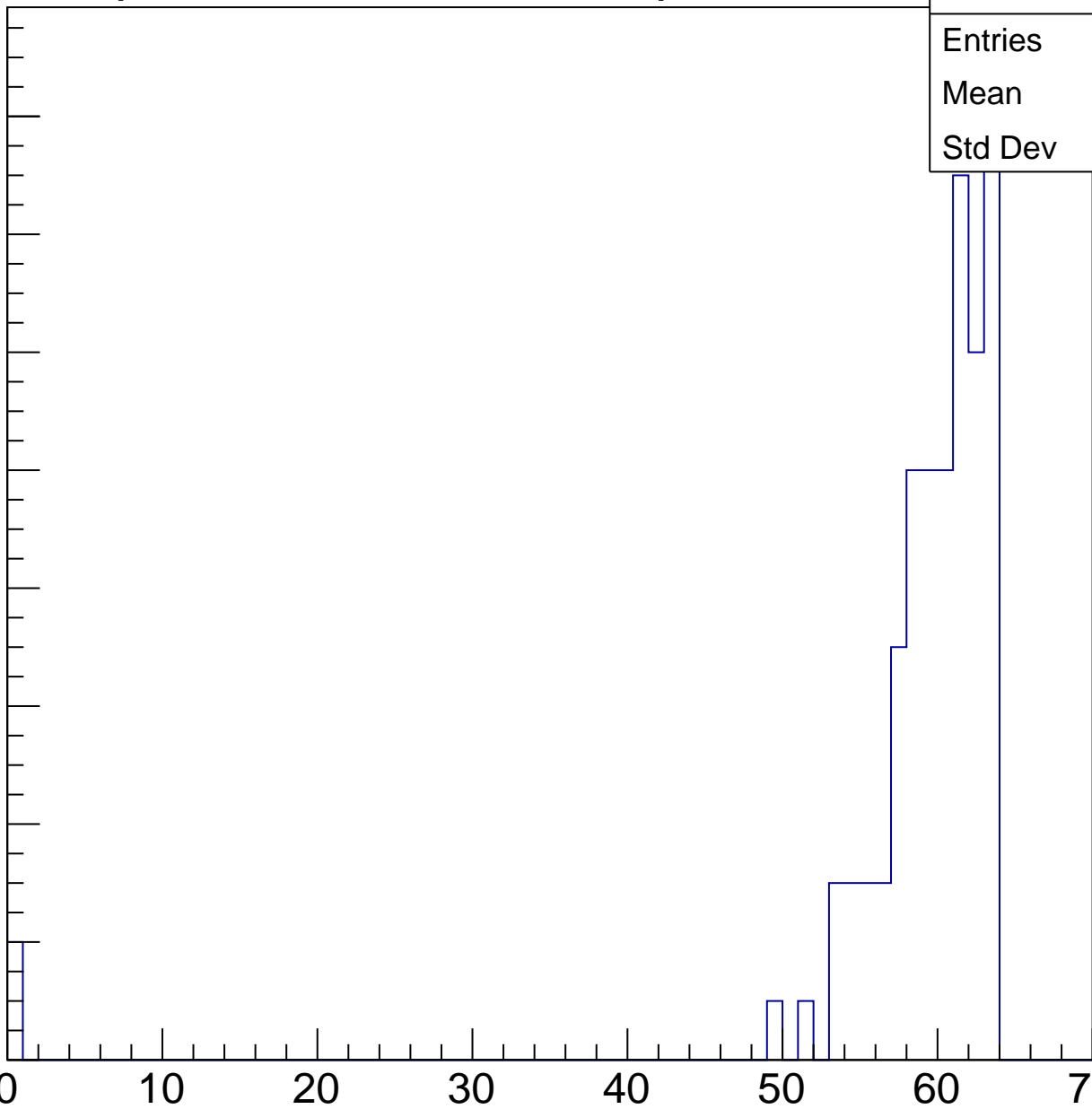
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.28
Std Dev	8.98

ampl

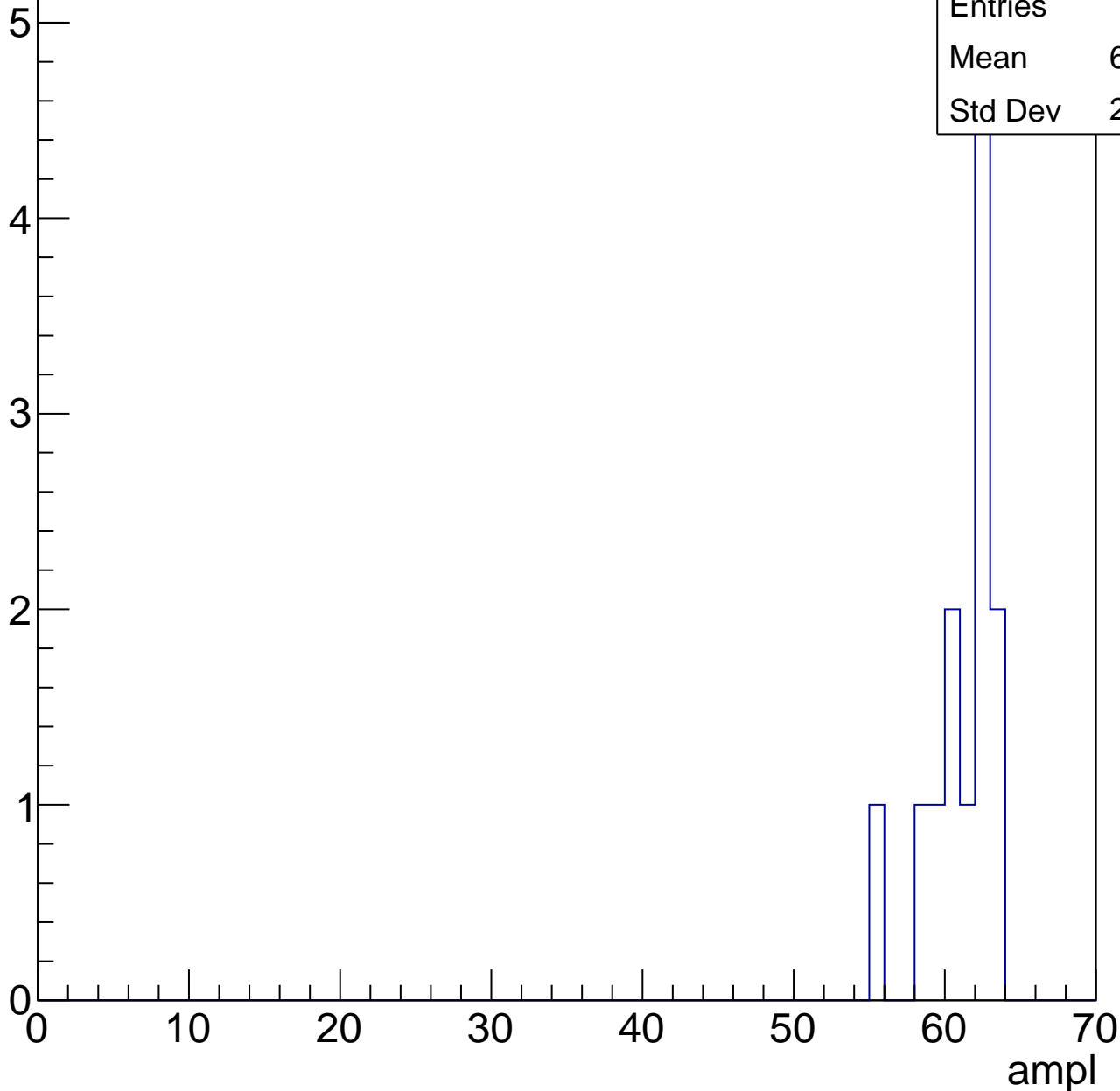


B0L101S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

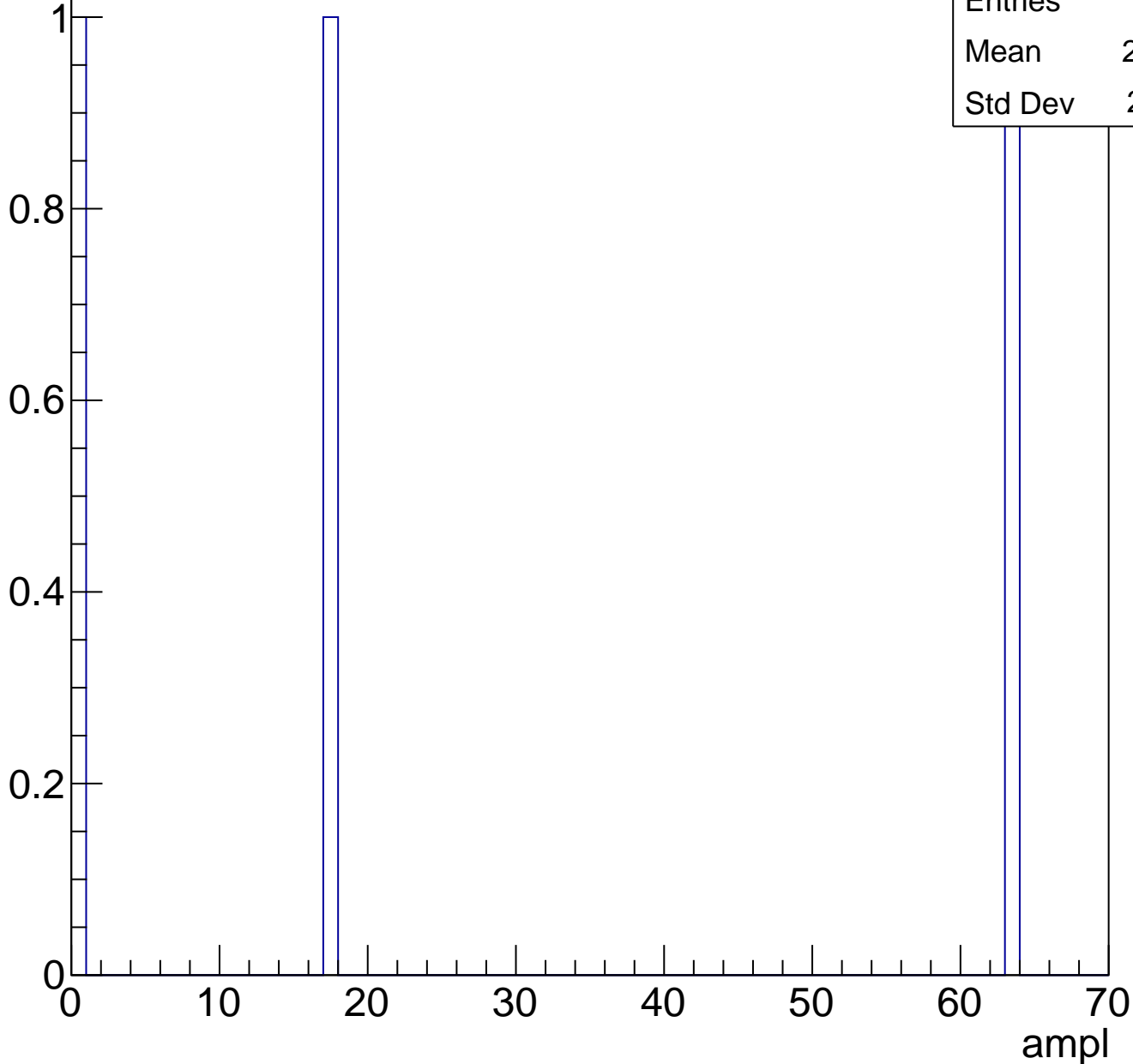
Entries	13
Mean	60.69
Std Dev	2.197



B0L101S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.67
Std Dev	26.61

B0L101S, U15-ch81, adc0

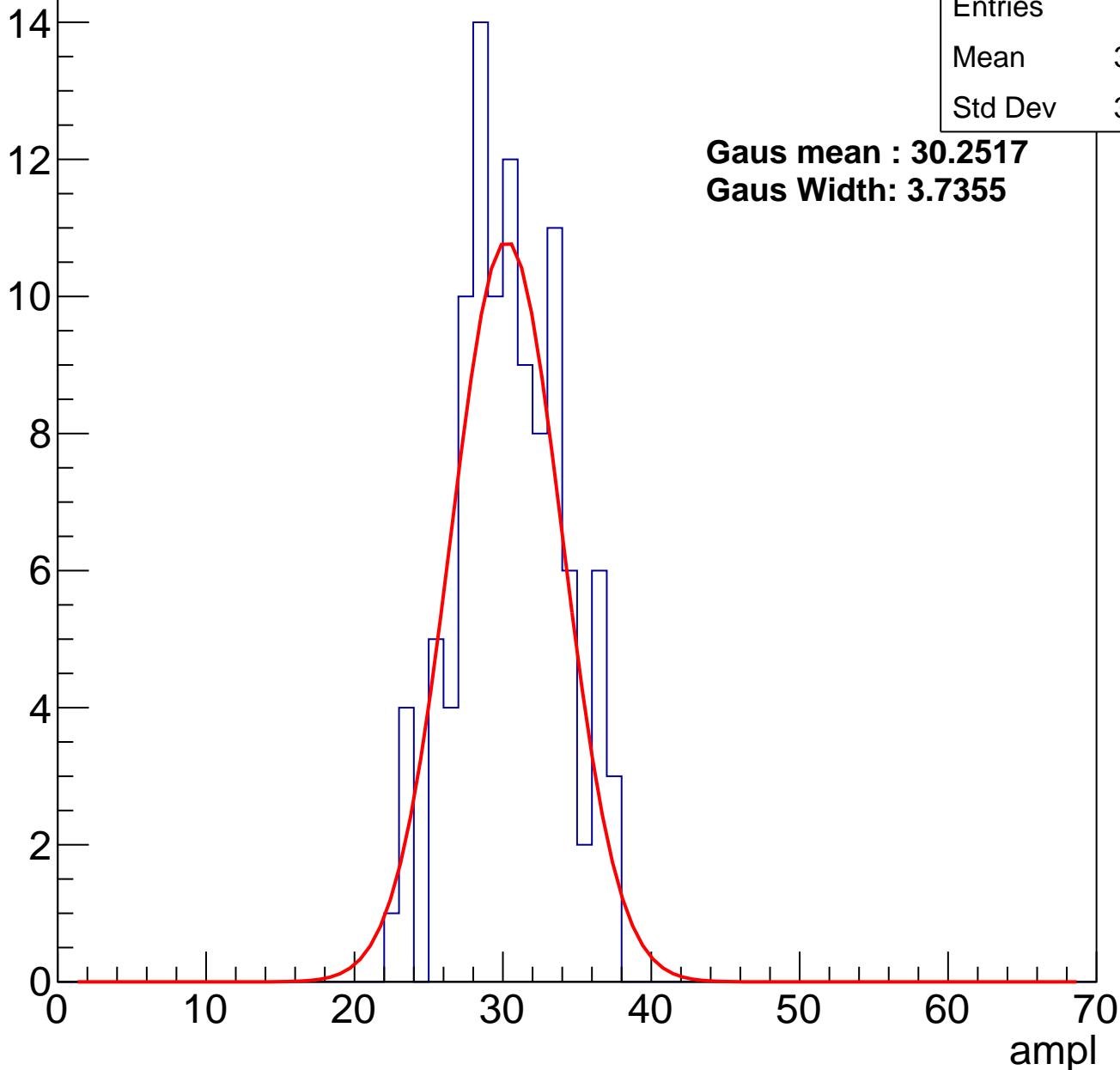
calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	30.04
Std Dev	3.456

Gaus mean : 30.2517

Gaus Width: 3.7355

Entry



B0L101S, U15-ch81, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	36.62
Std Dev	3.994

Gaus mean : 37.2082

Gaus Width: 4.0221

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

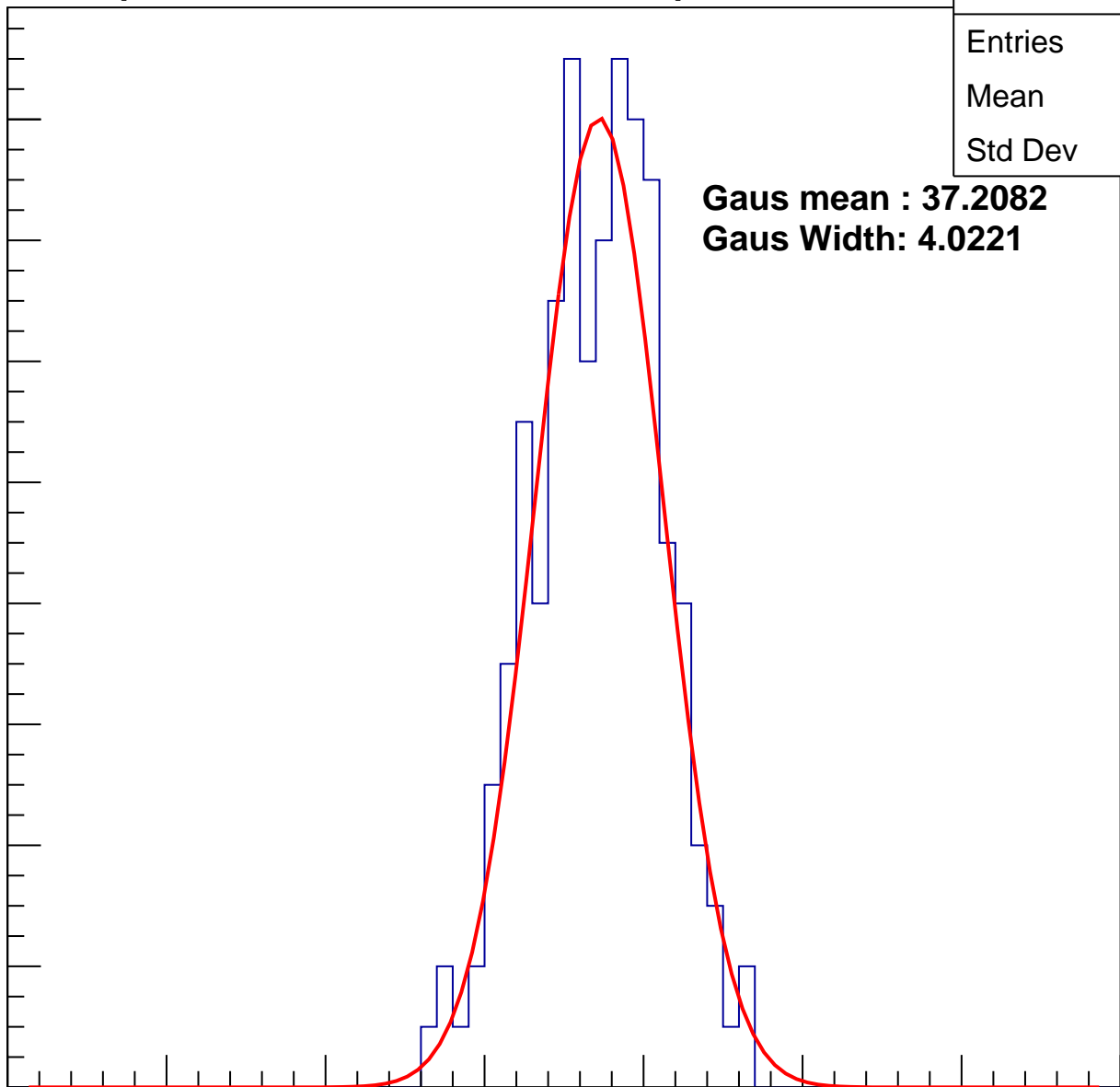
40

50

60

70

ampl



B0L101S, U15-ch81, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

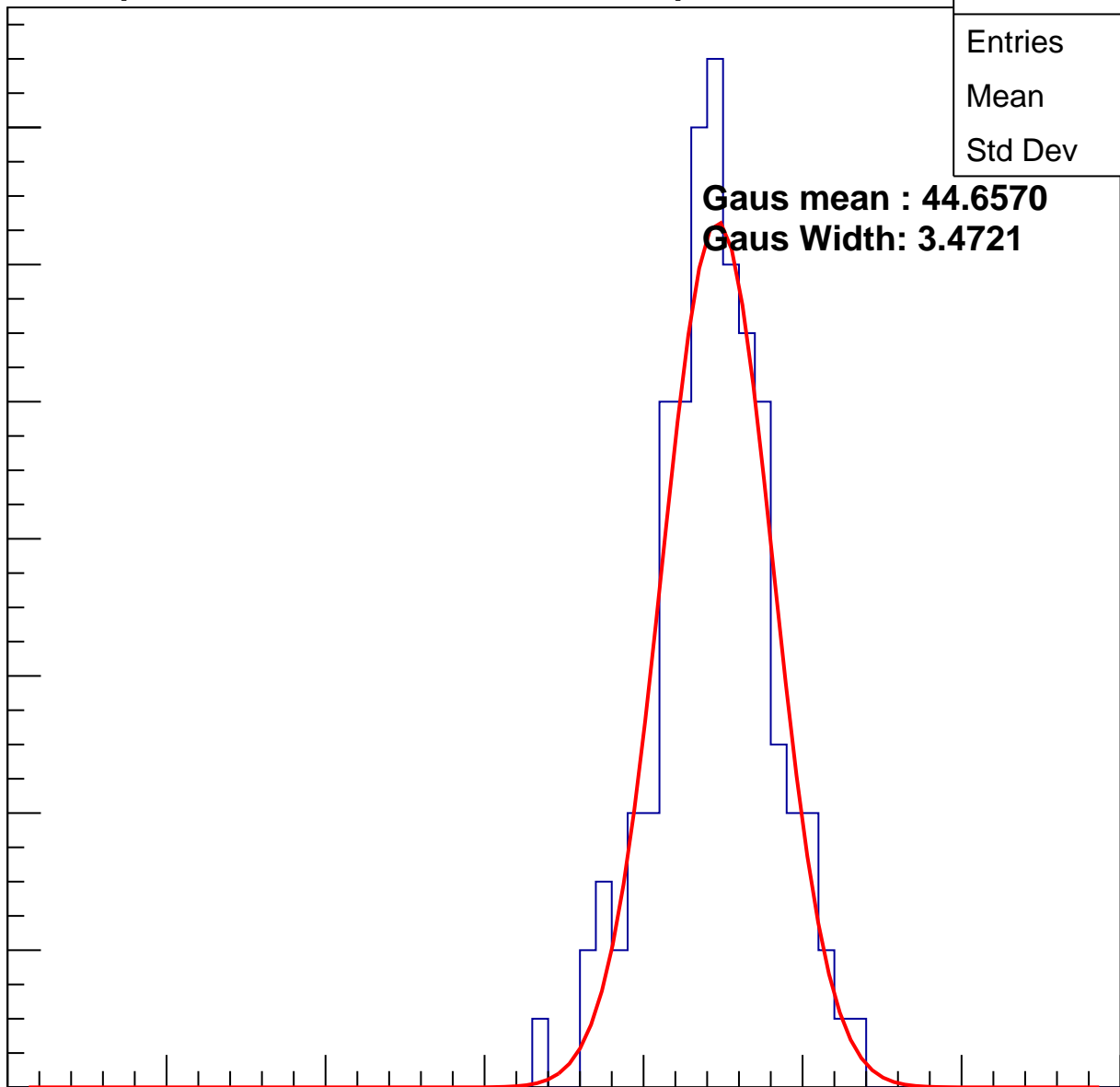
Entries	115
Mean	43.99
Std Dev	3.598

Gaus mean : 44.6570

Gaus Width: 3.4721

0 10 20 30 40 50 60 70

ampl

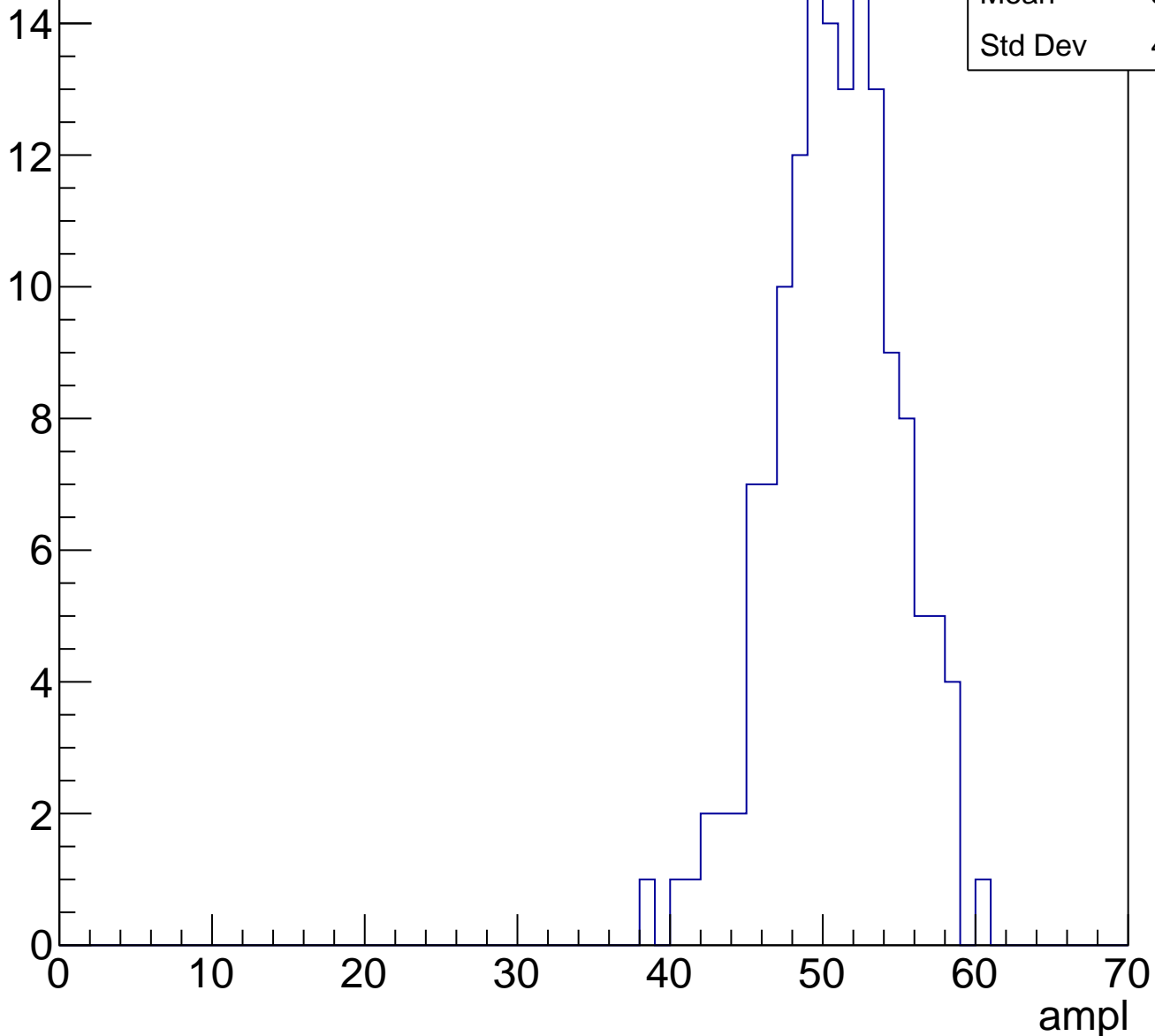


B0L101S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	50.41
Std Dev	4.001

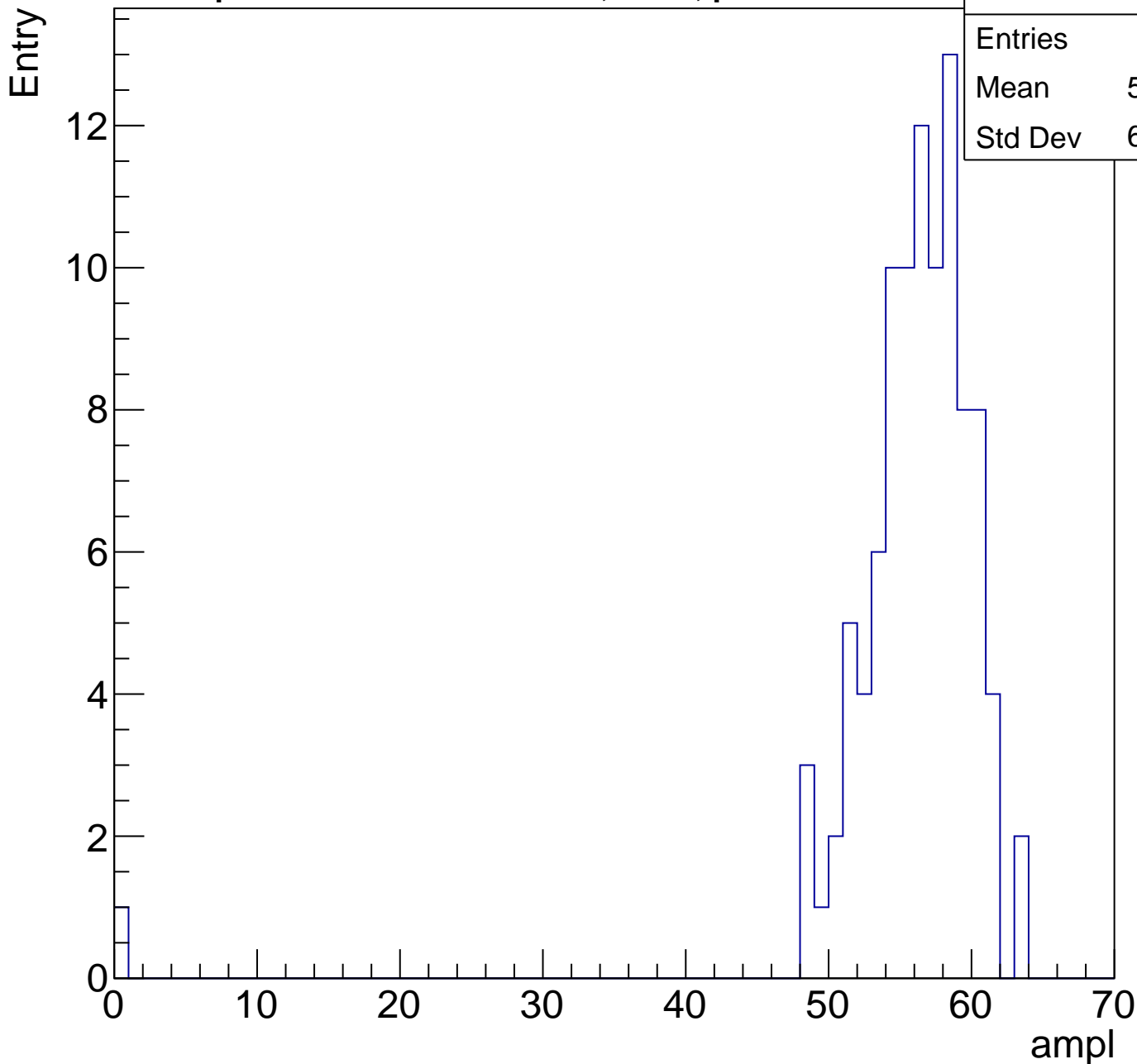
Entry



B0L101S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#1, port C1

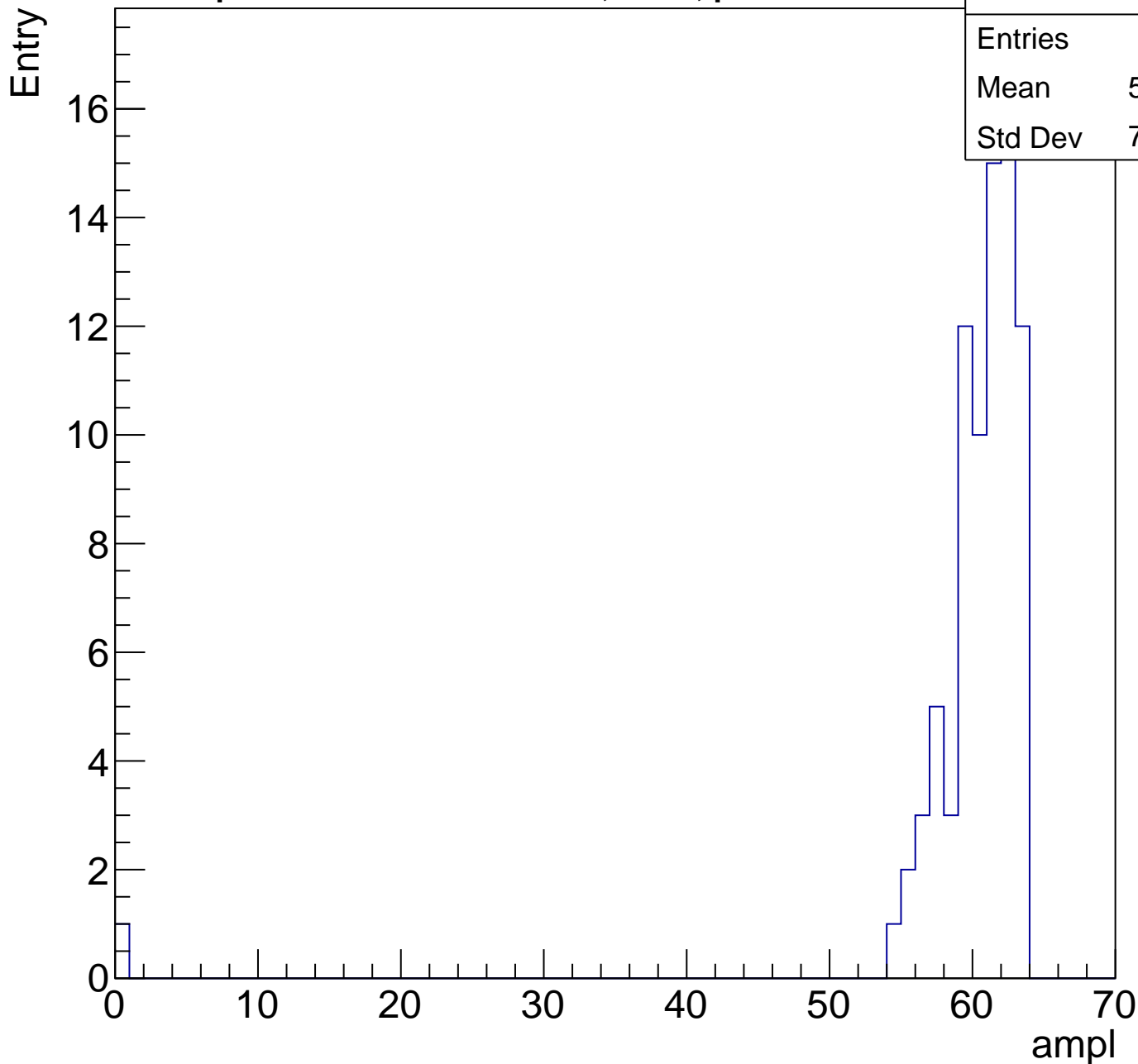
Entries	99
Mean	55.37
Std Dev	6.485



B0L101S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#1, port C1

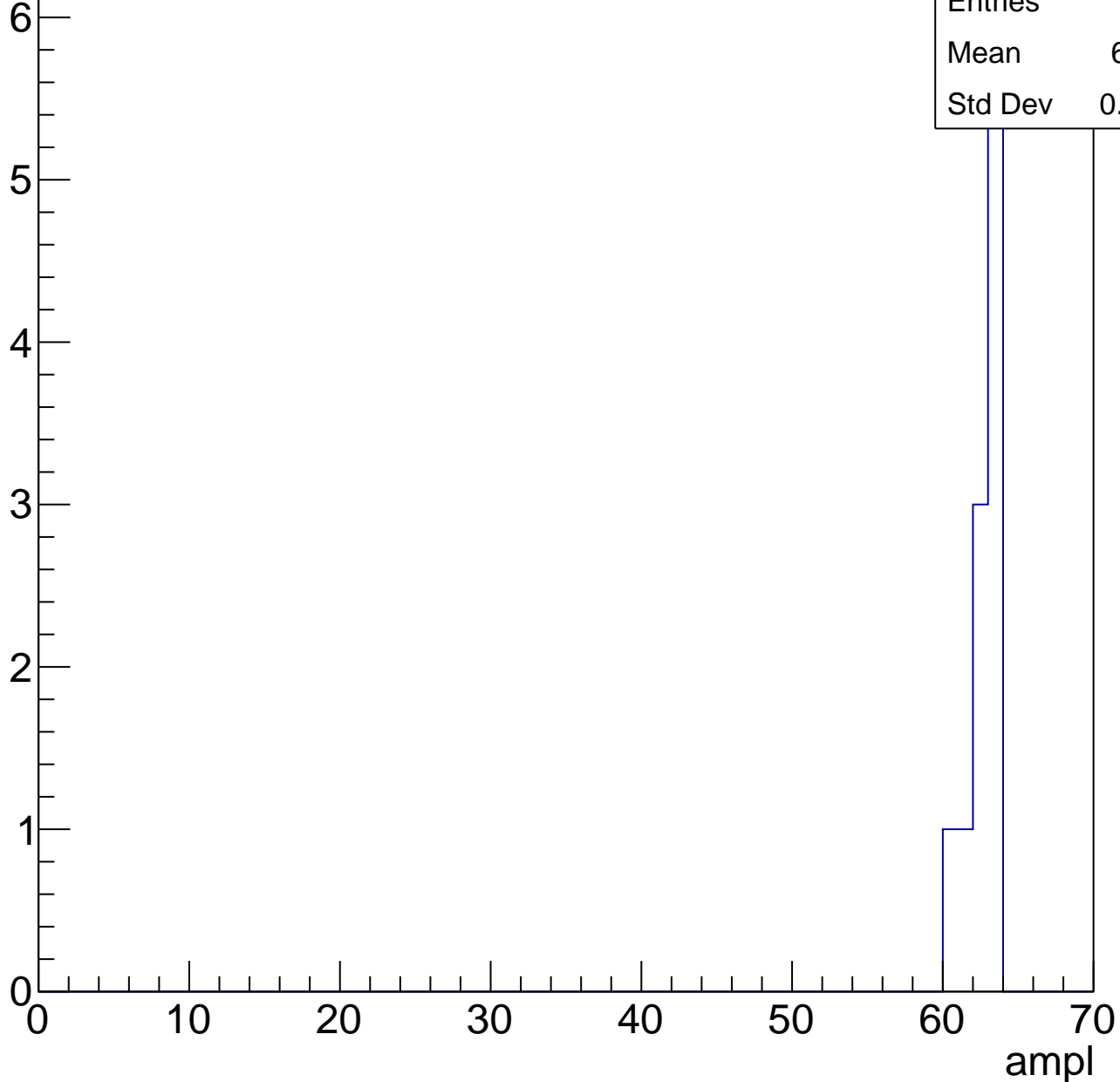
Entries	81
Mean	59.56
Std Dev	7.008



B0L101S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch82, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	28.3
Std Dev	4.912

Gaus mean : 29.2053

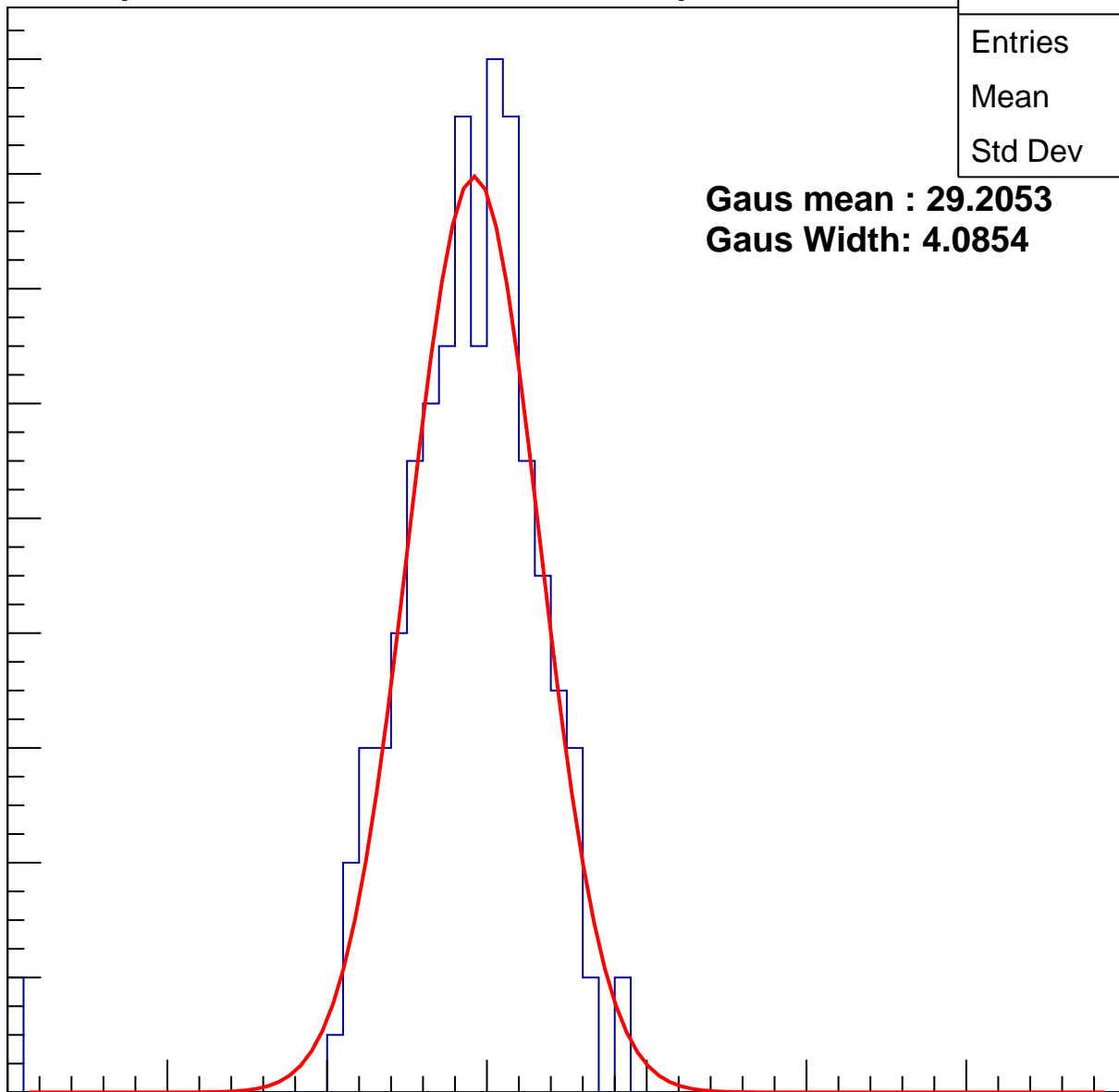
Gaus Width: 4.0854

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch82, adc1

calib_packv5_042523_0143.root, FC#1, port C1

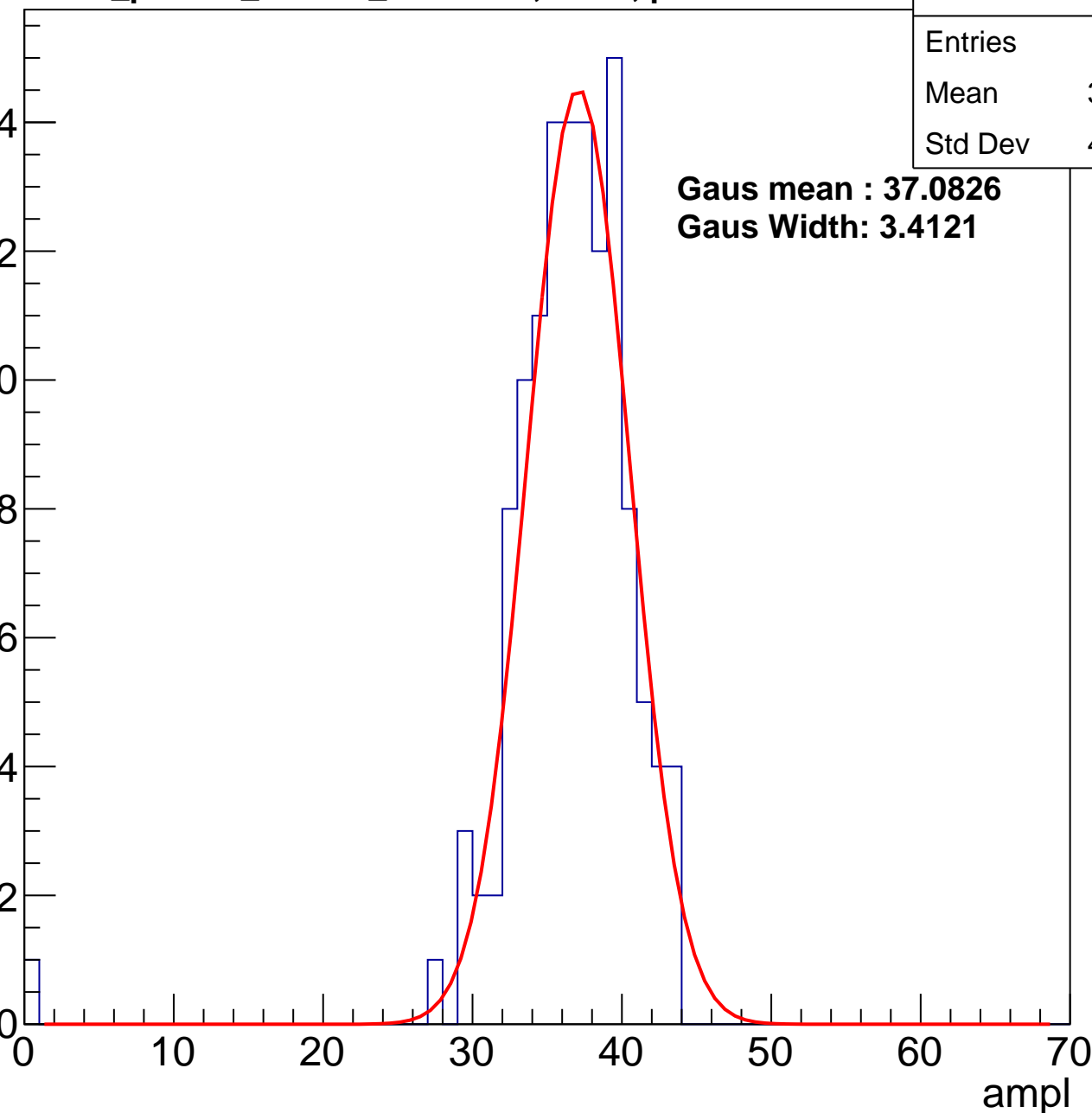
Entry

14
12
10
8
6
4
2
0

Entries	128
Mean	36.05
Std Dev	4.603

Gaus mean : 37.0826

Gaus Width: 3.4121



B0L101S, U15-ch82, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

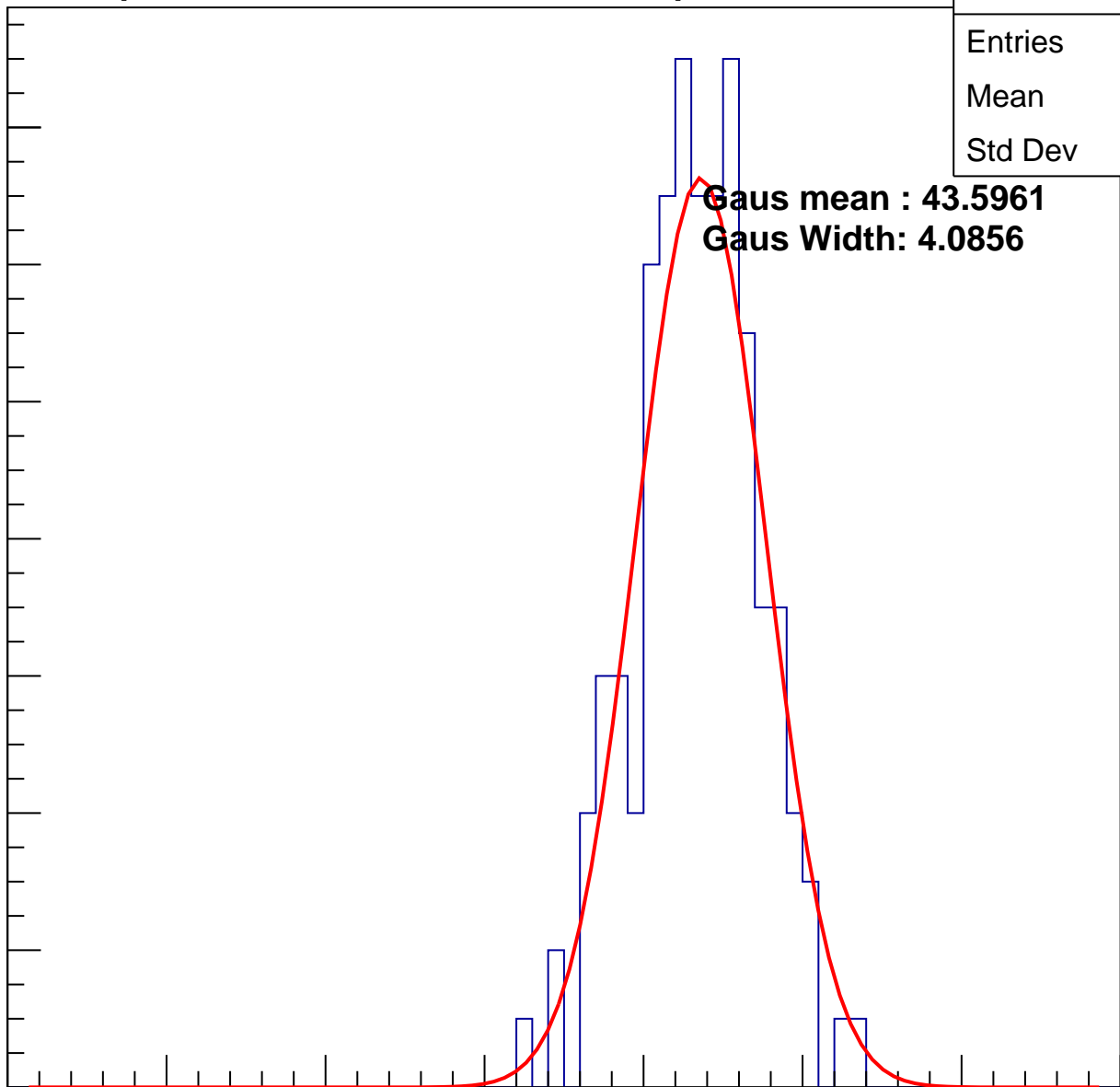
70

ampl

Entries	138
Mean	42.91
Std Dev	3.82

Gaus mean : 43.5961

Gaus Width: 4.0856



B0L101S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#1, port C1

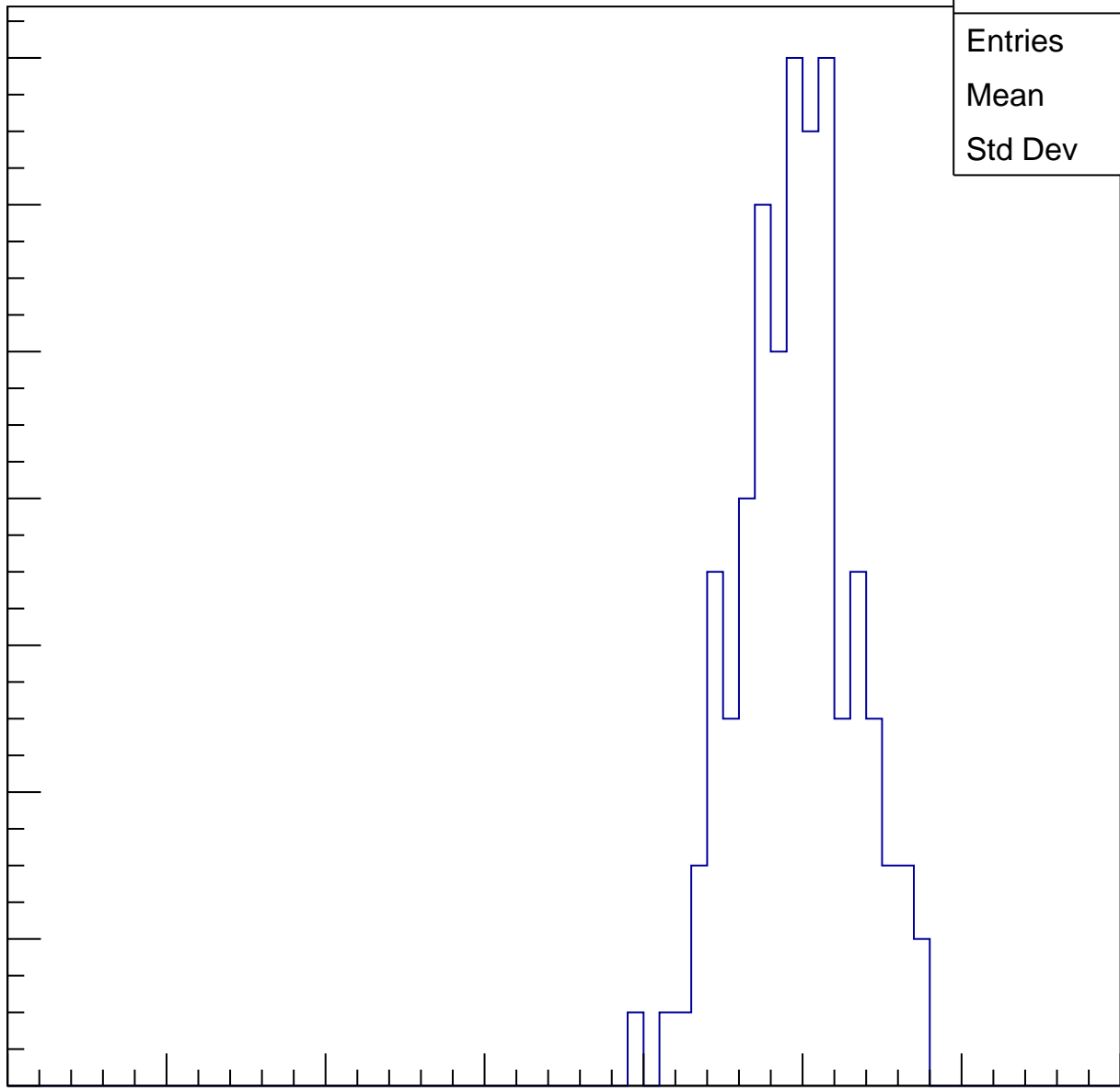
Entries	114
Mean	49.07
Std Dev	3.553

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

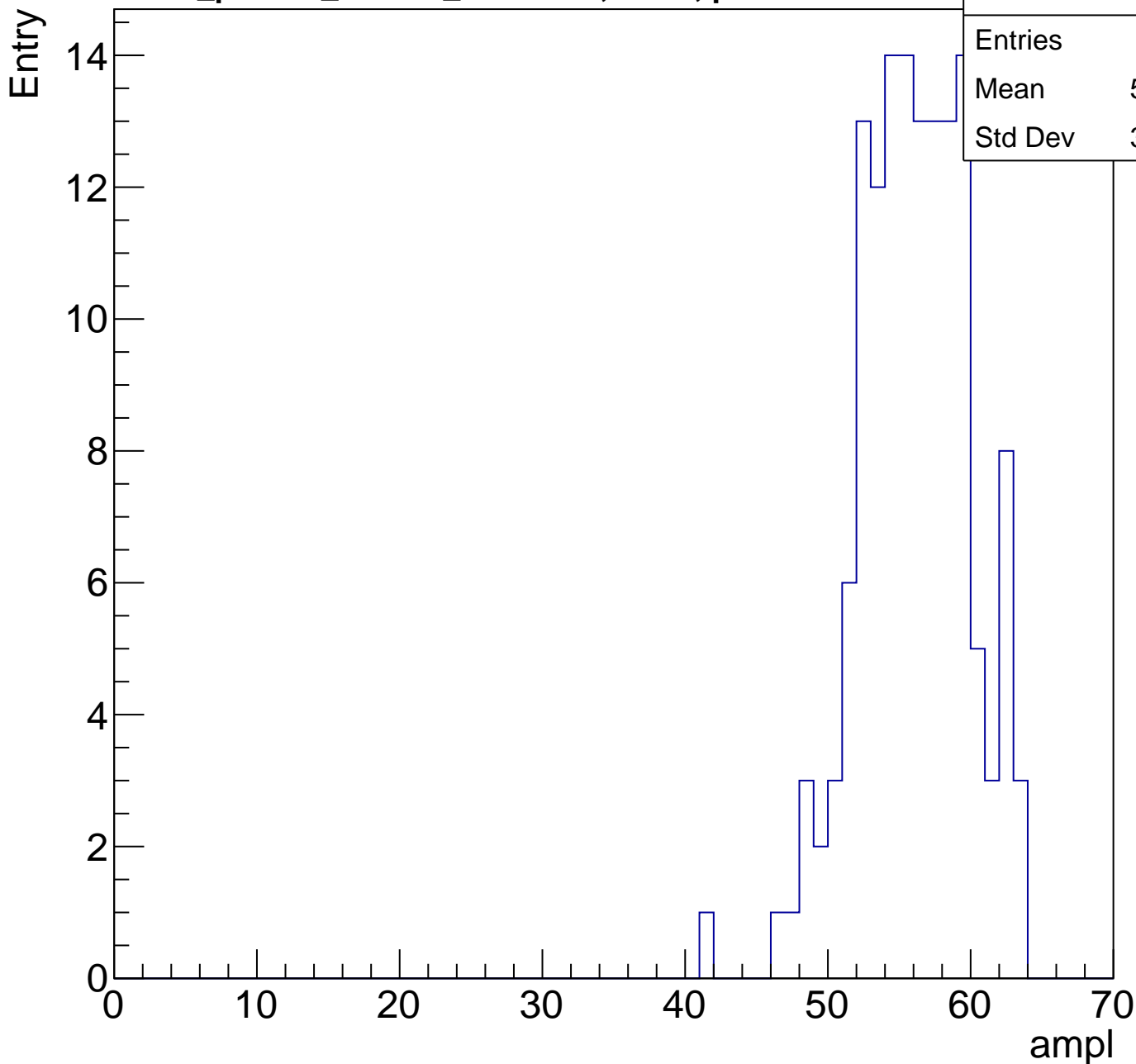
ampl



B0L101S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	55.54
Std Dev	3.838



B0L101S, U15-ch82, adc5

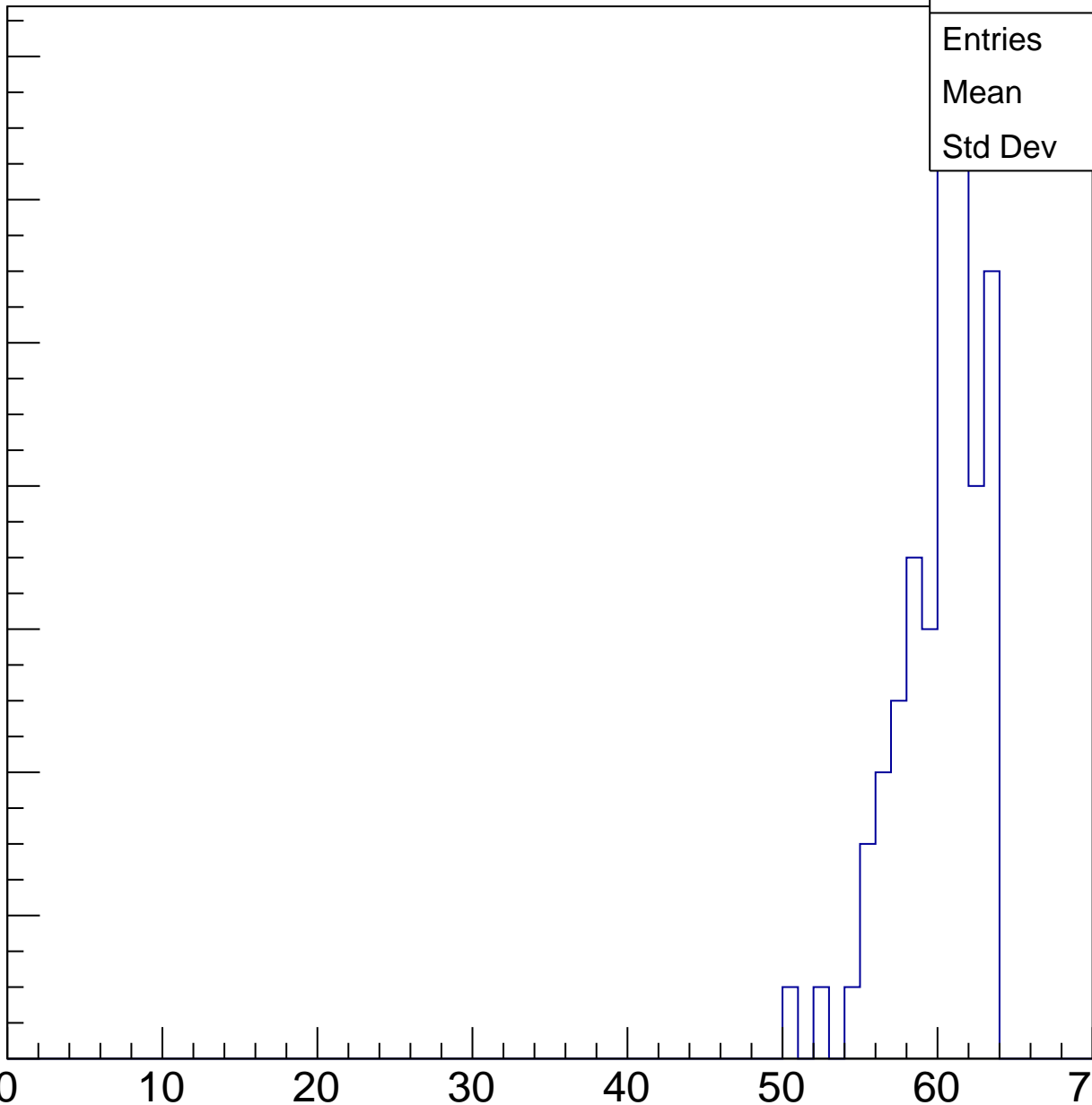
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	59.64
Std Dev	2.734

ampl

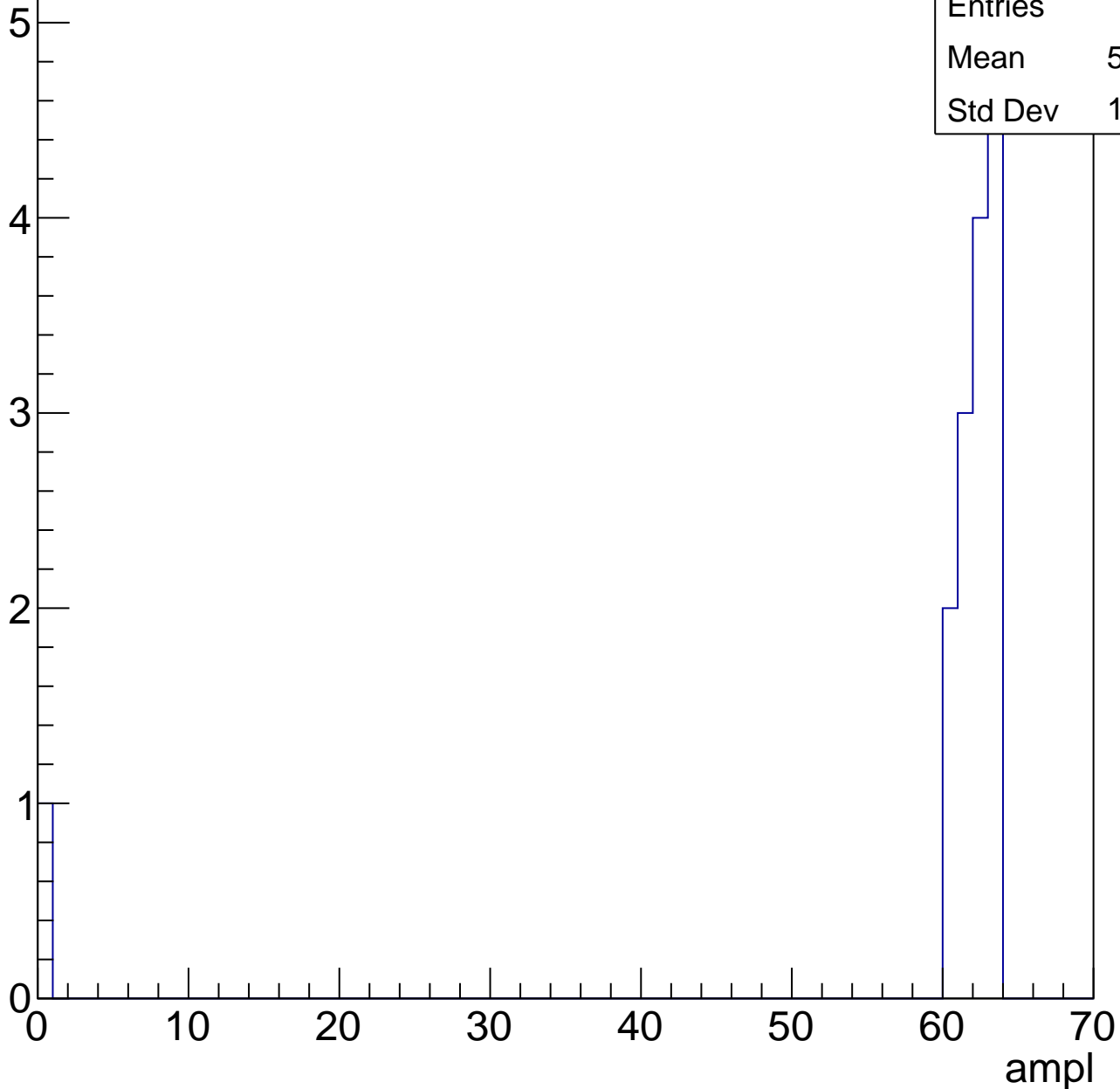


B0L101S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	57.73
Std Dev	15.46



B0L101S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch83, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	29.41
Std Dev	3.554

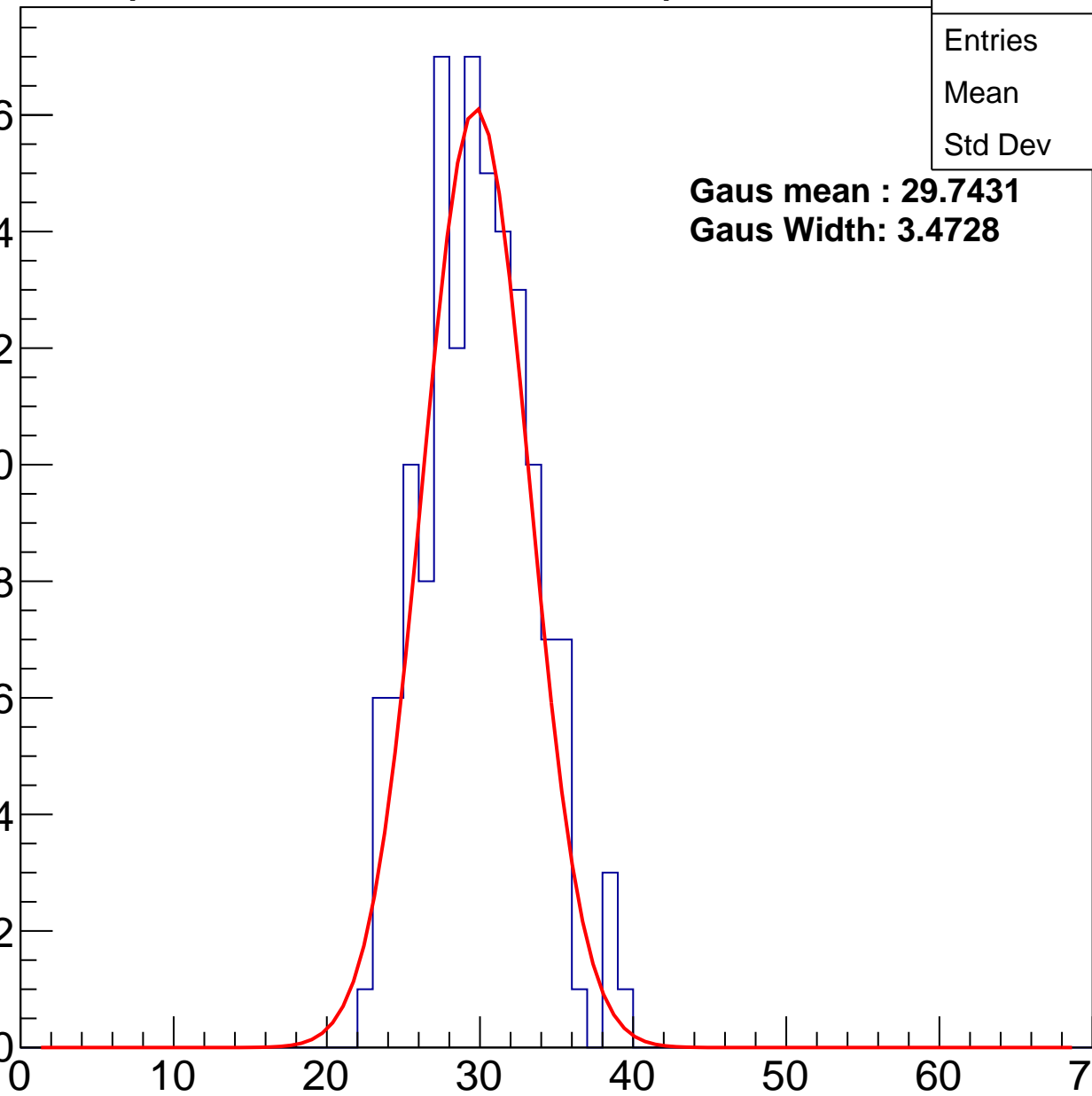
Gaus mean : 29.7431

Gaus Width: 3.4728

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch83, adc1

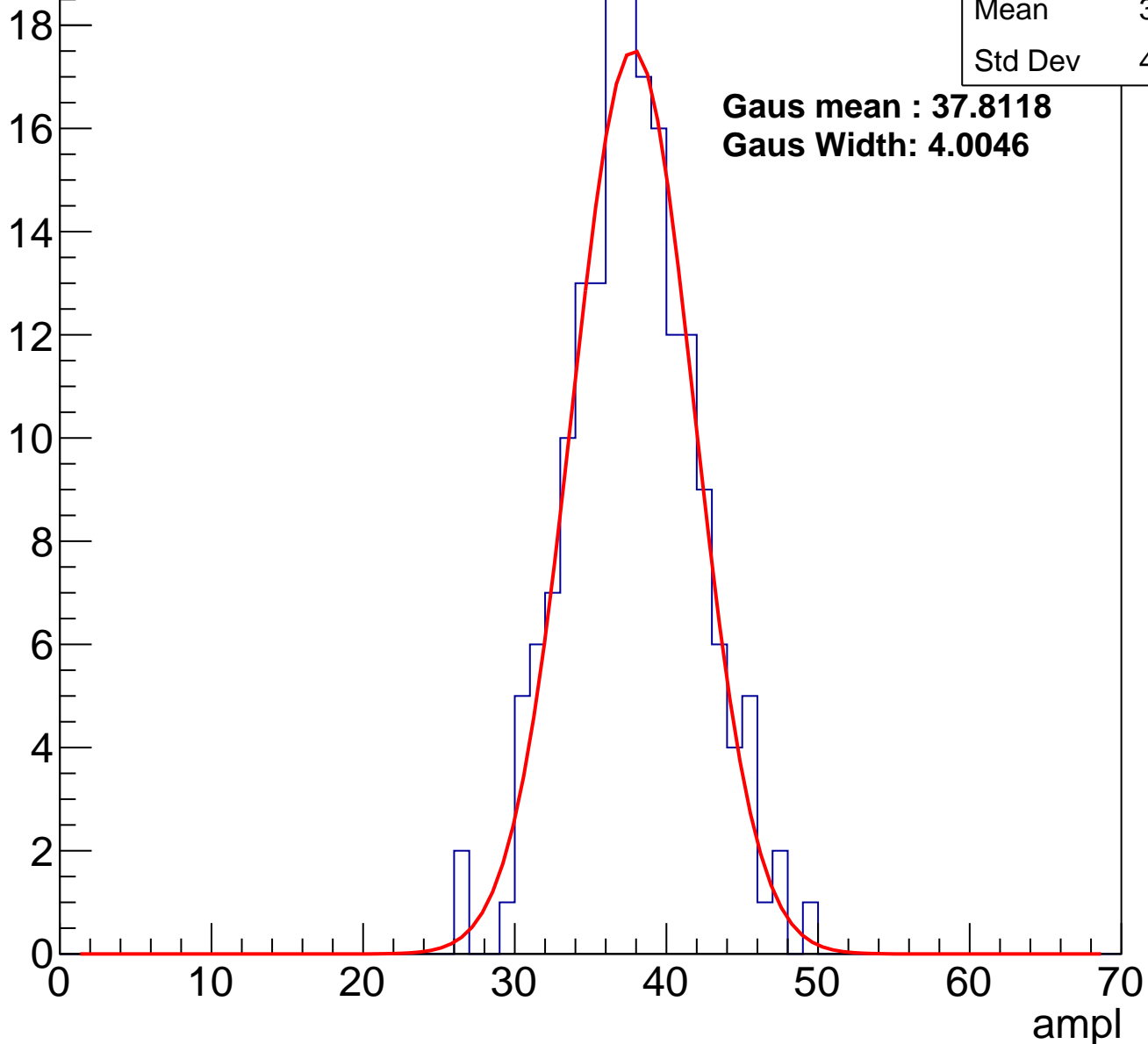
calib_packv5_042523_0143.root, FC#1, port C1

Entries	180
Mean	37.35
Std Dev	4.103

Gaus mean : 37.8118

Gaus Width: 4.0046

Entry



B0L101S, U15-ch83, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	89
Mean	44.1
Std Dev	2.957

Gaus mean : 44.4604

Gaus Width: 3.2830

0

2

4

6

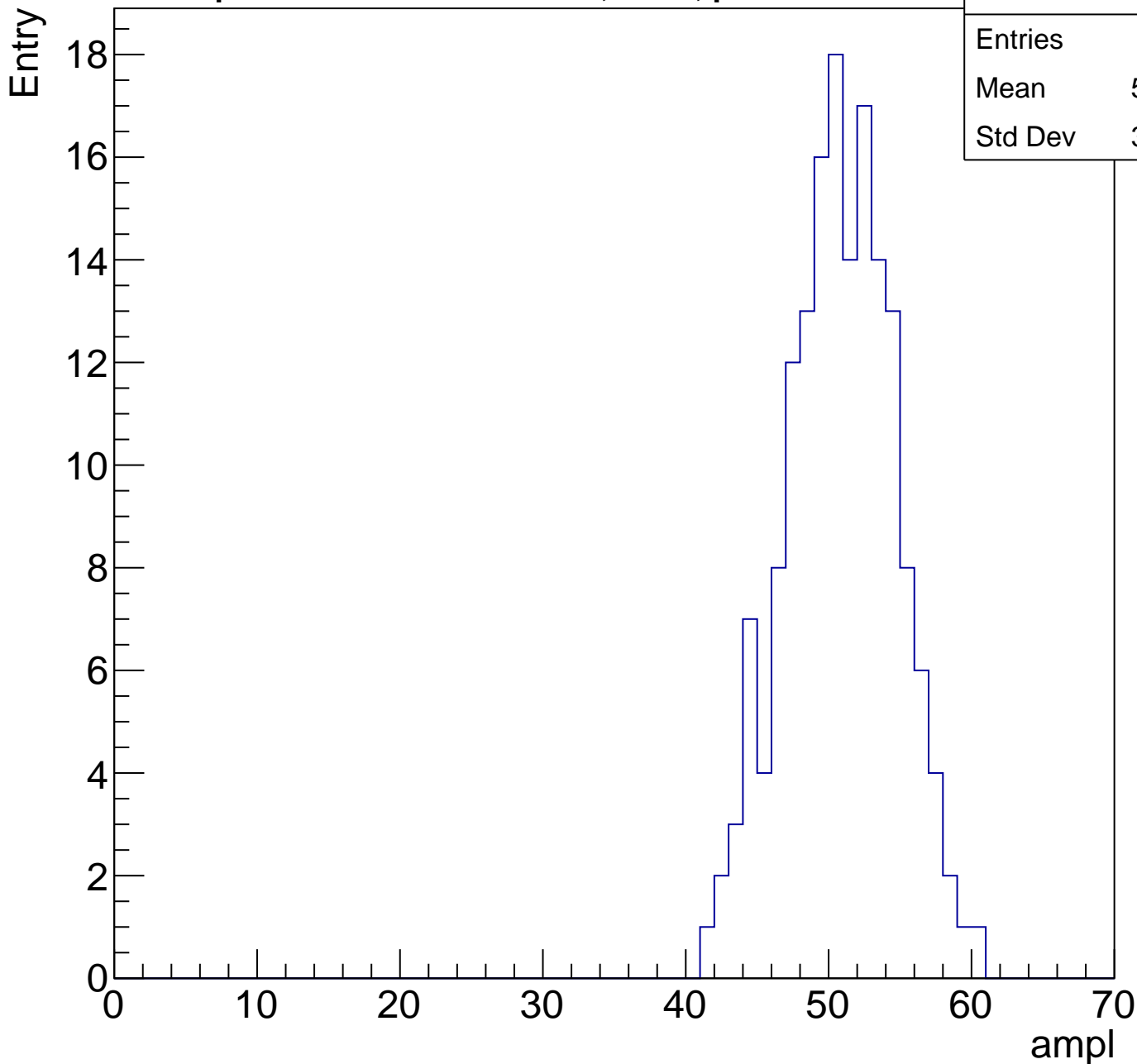
8

10

B0L101S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	50.38
Std Dev	3.765



B0L101S, U15-ch83, adc4

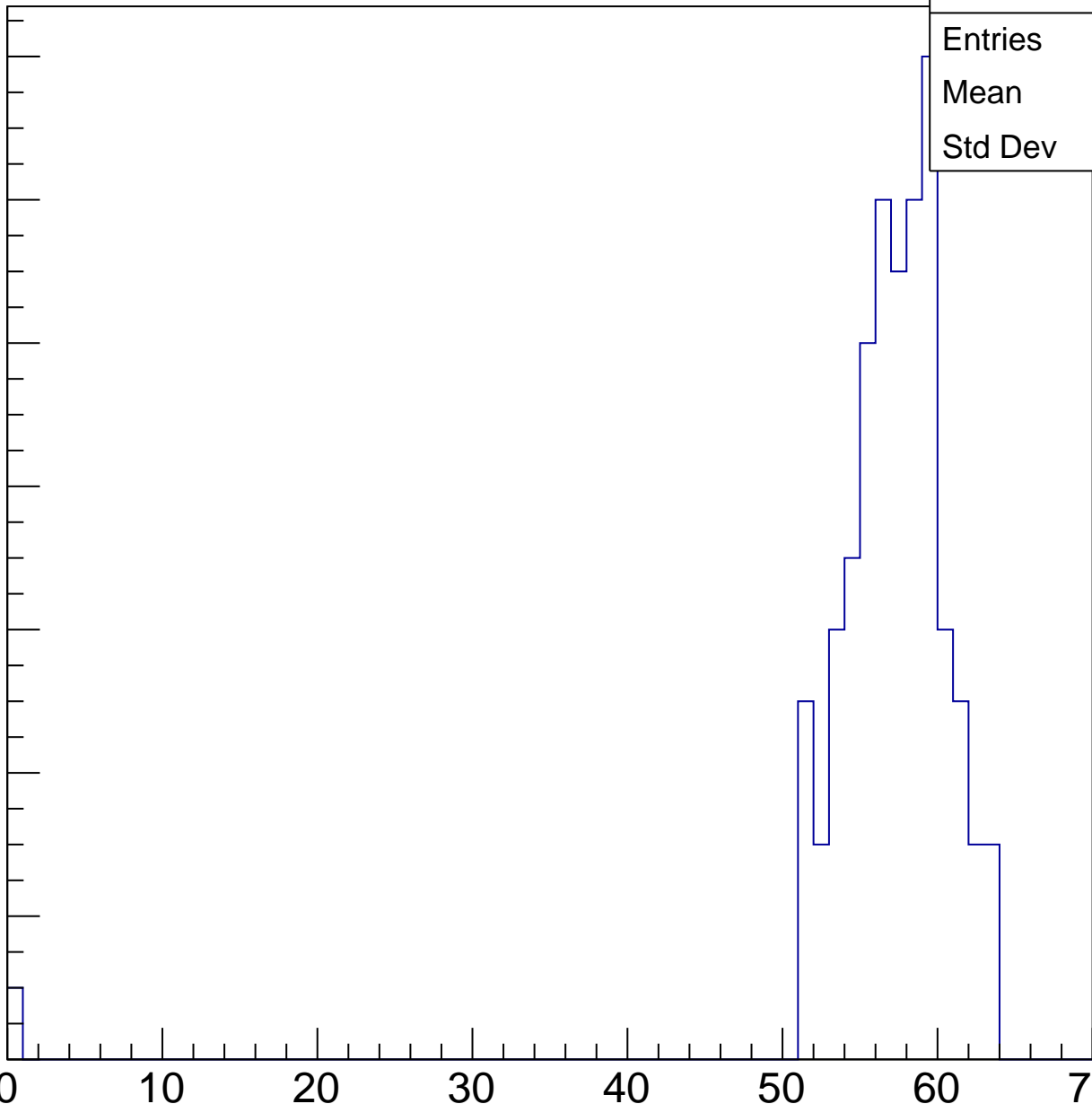
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	98
Mean	56.31
Std Dev	6.431

ampl



B0L101S, U15-ch83, adc5

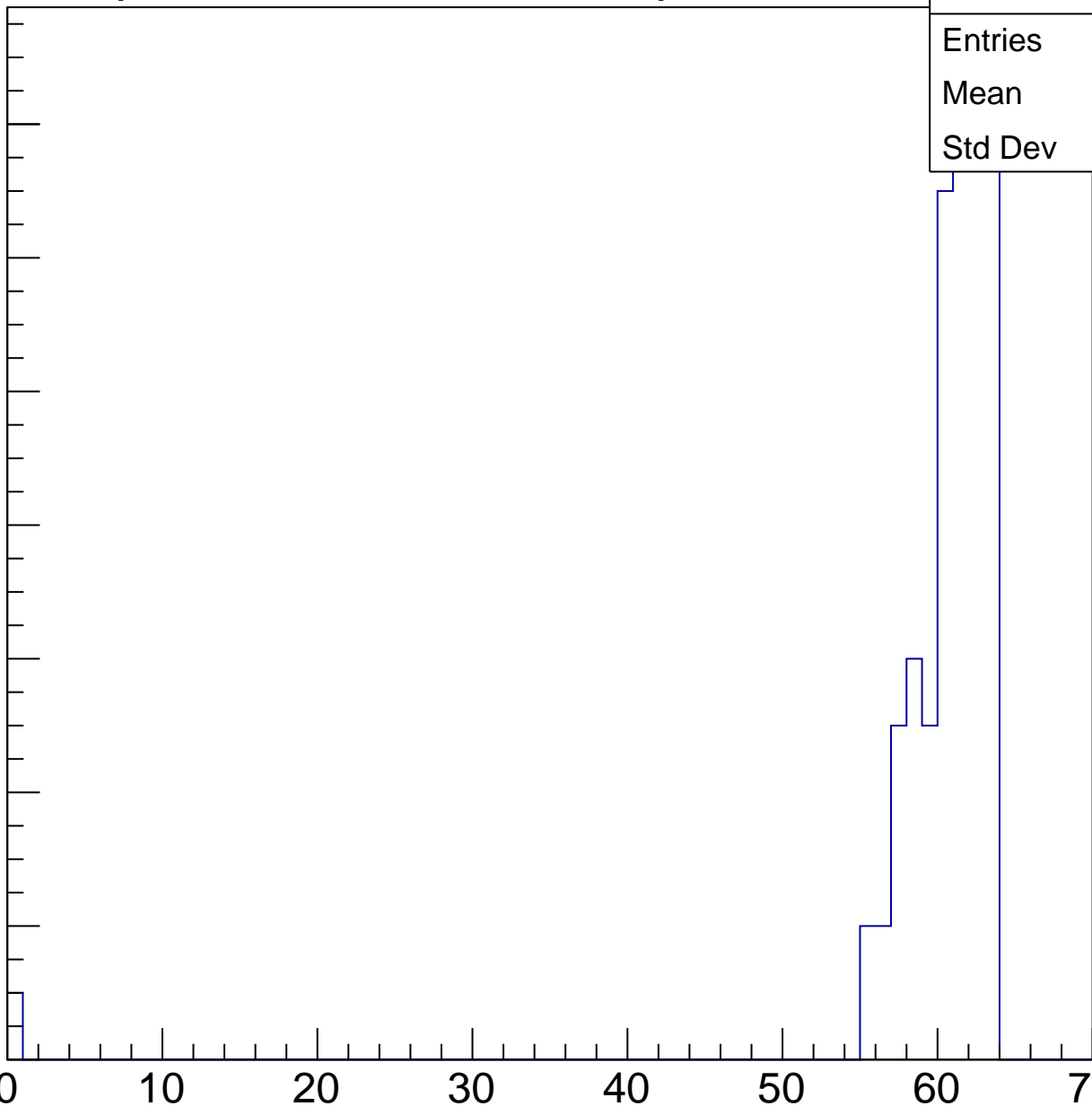
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	77
Mean	59.69
Std Dev	7.161

ampl



B0L101S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



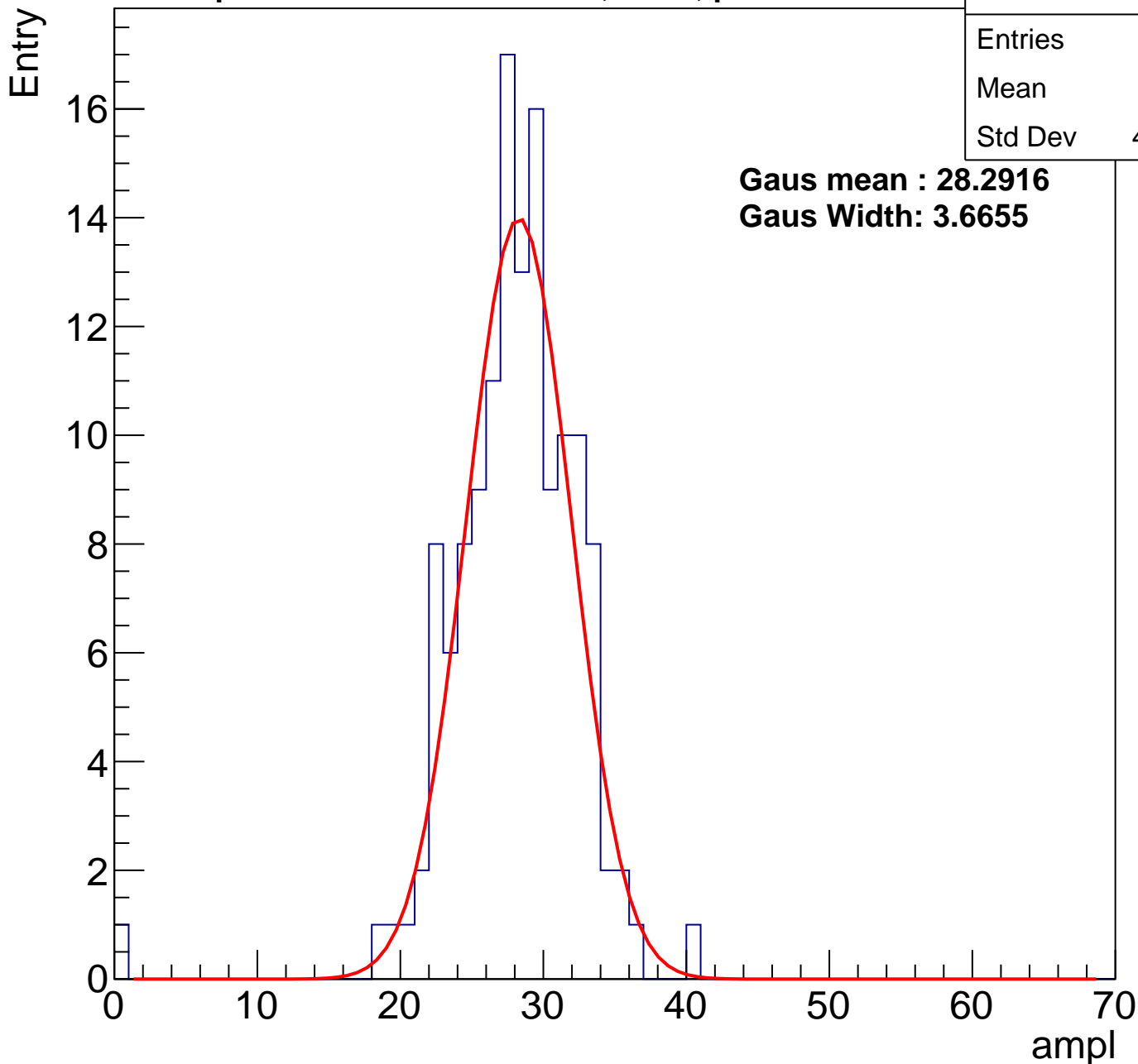
B0L101S, U15-ch84, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	27.6
Std Dev	4.434

Gaus mean : 28.2916

Gaus Width: 3.6655



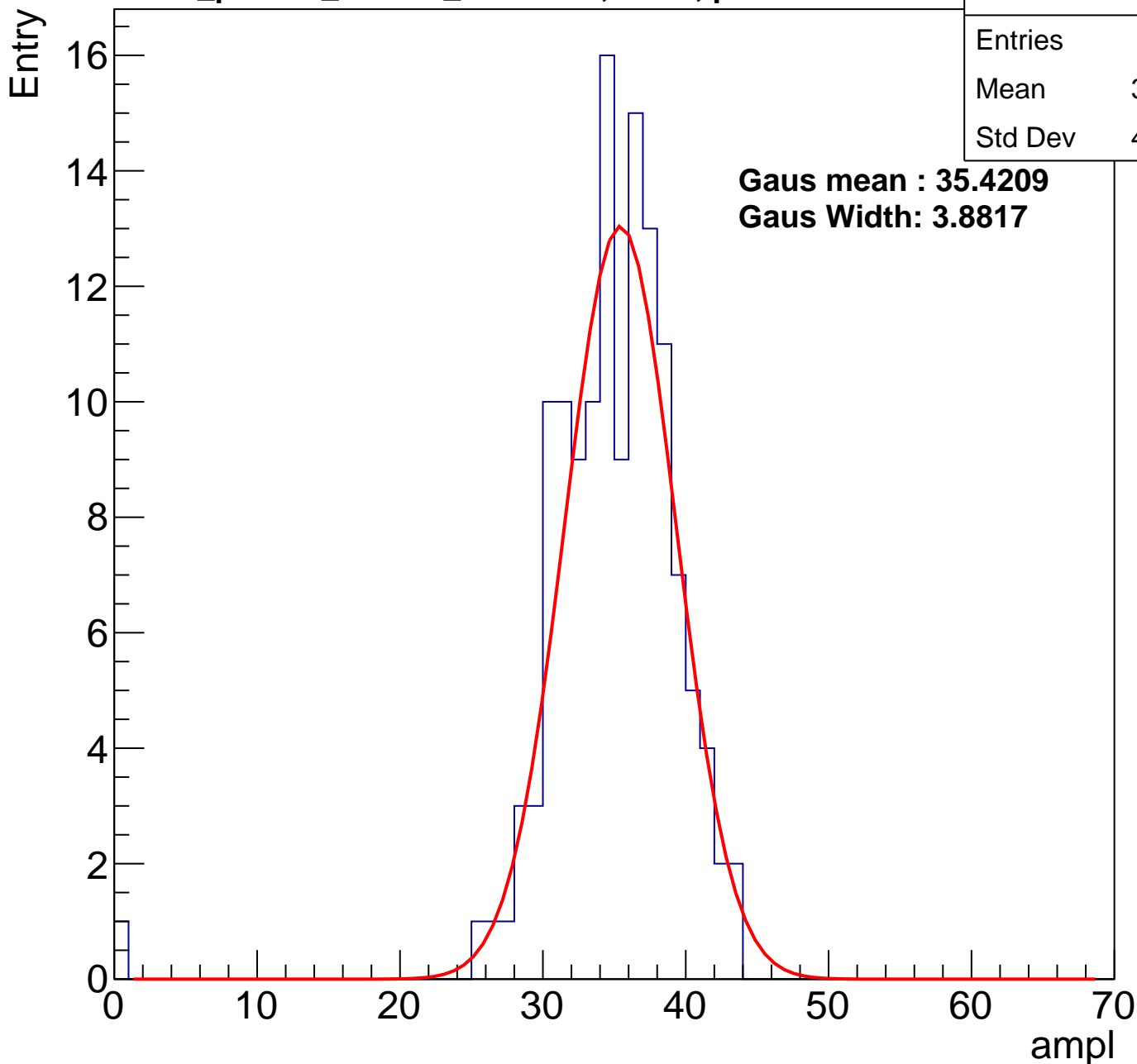
B0L101S, U15-ch84, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	34.45
Std Dev	4.753

Gaus mean : 35.4209

Gaus Width: 3.8817



B0L101S, U15-ch84, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	40.9
Std Dev	3.875

Gaus mean : 41.5787

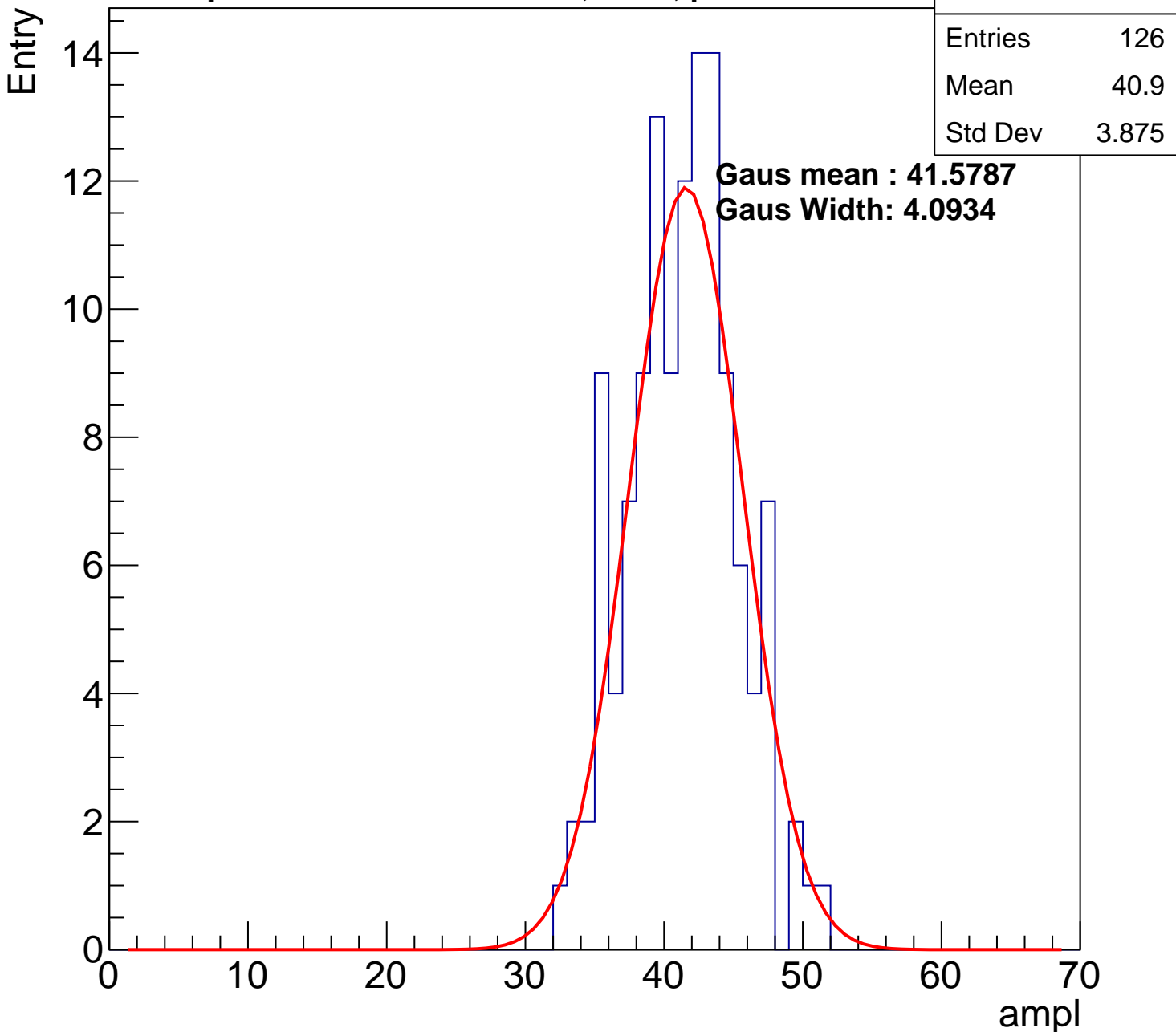
Gaus Width: 4.0934

Entry

14
12
10
8
6
4
2
0

ampl

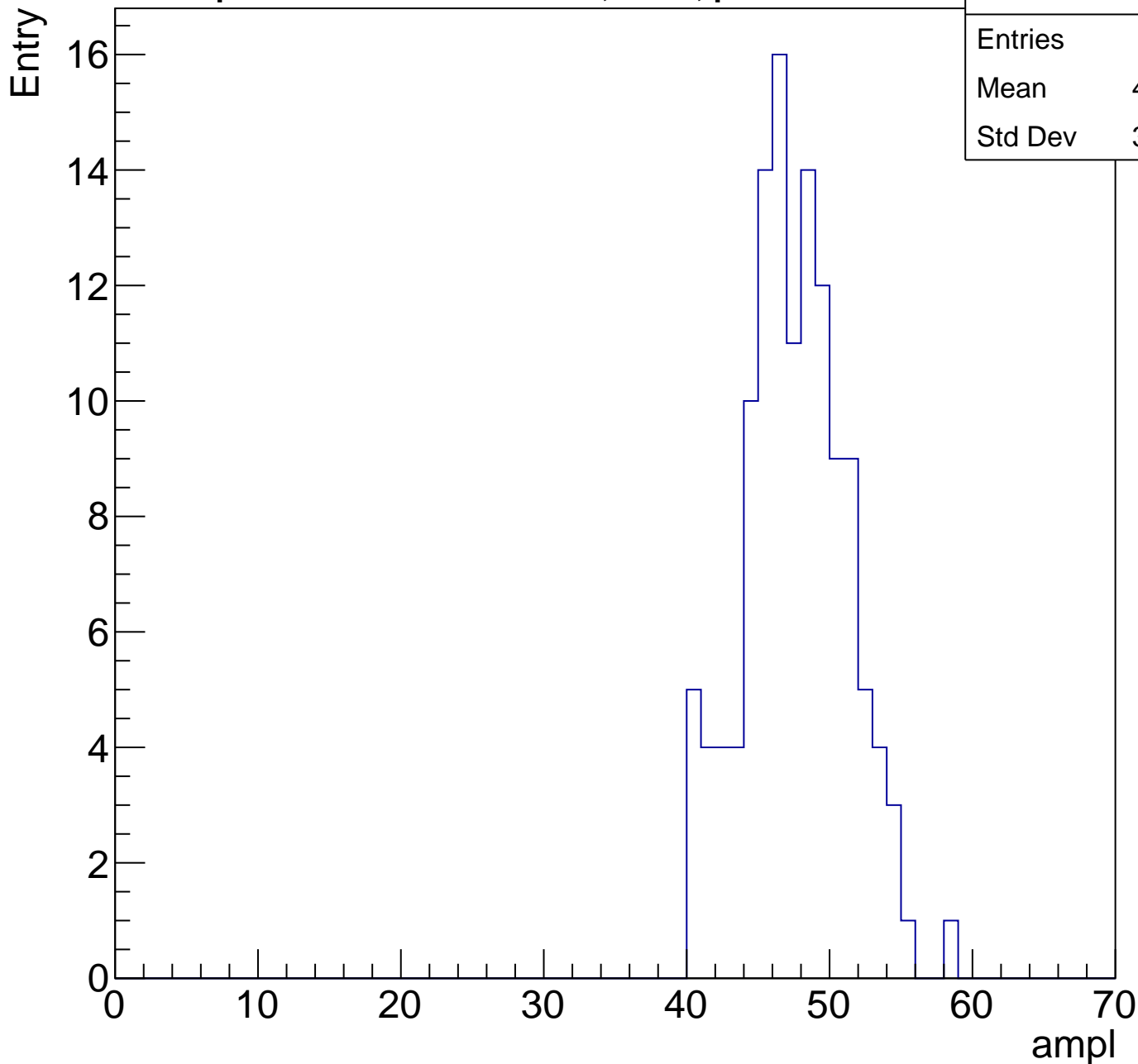
0 10 20 30 40 50 60 70



B0L101S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#1, port C1

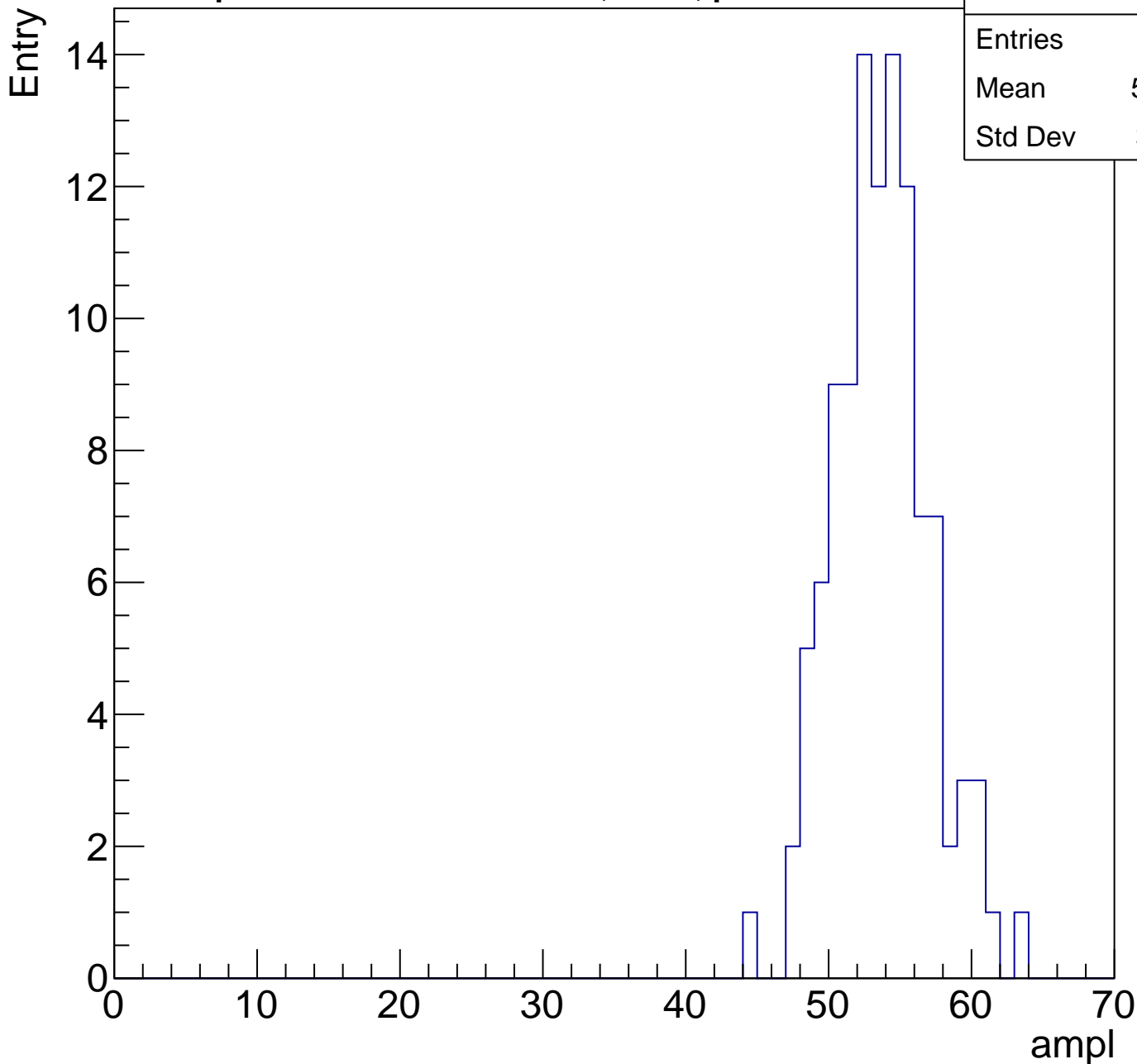
Entries	126
Mean	47.17
Std Dev	3.563



B0L101S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#1, port C1

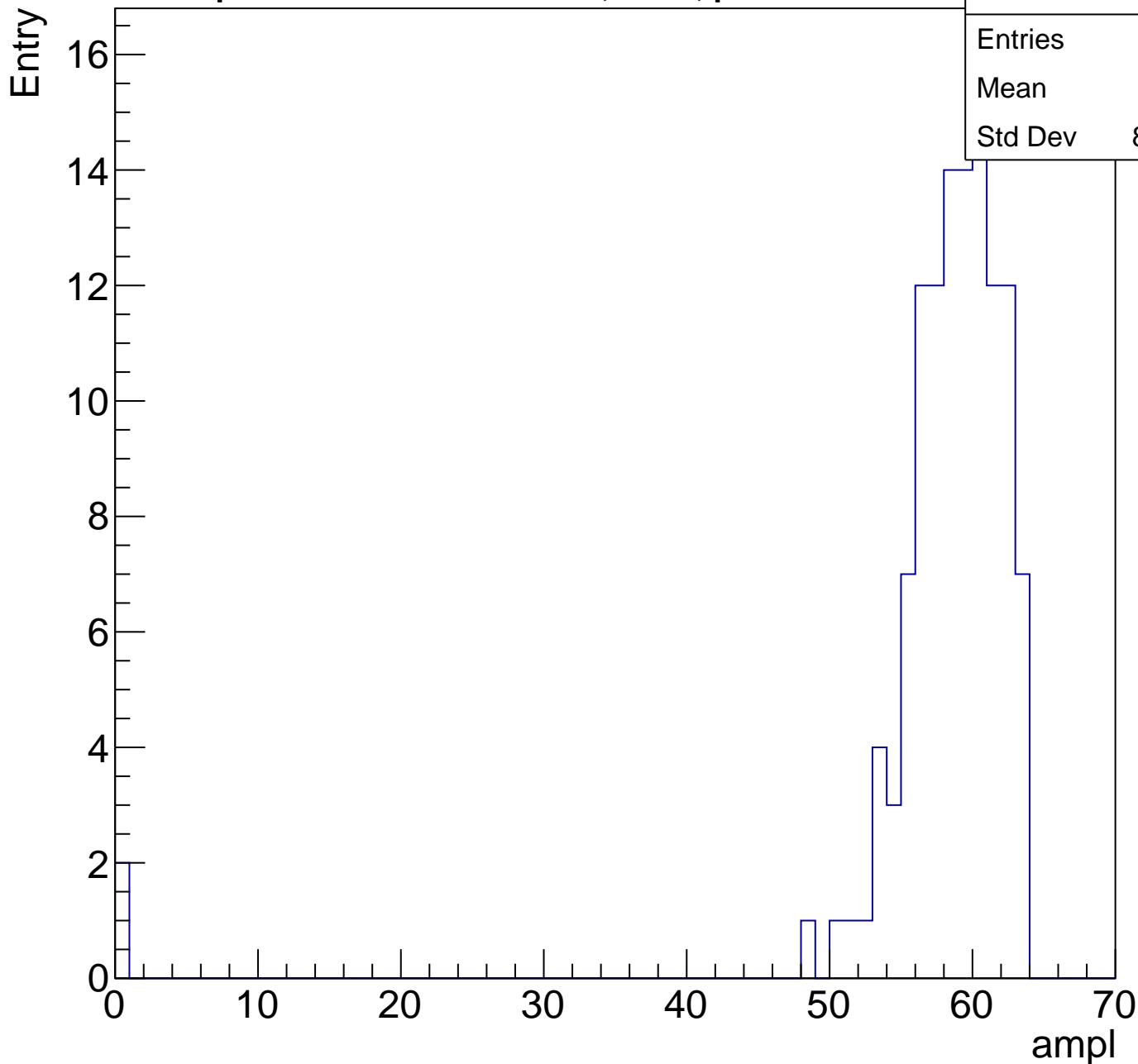
Entries	108
Mean	53.23
Std Dev	3.341



B0L101S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	57.4
Std Dev	8.075



B0L101S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

34

Mean

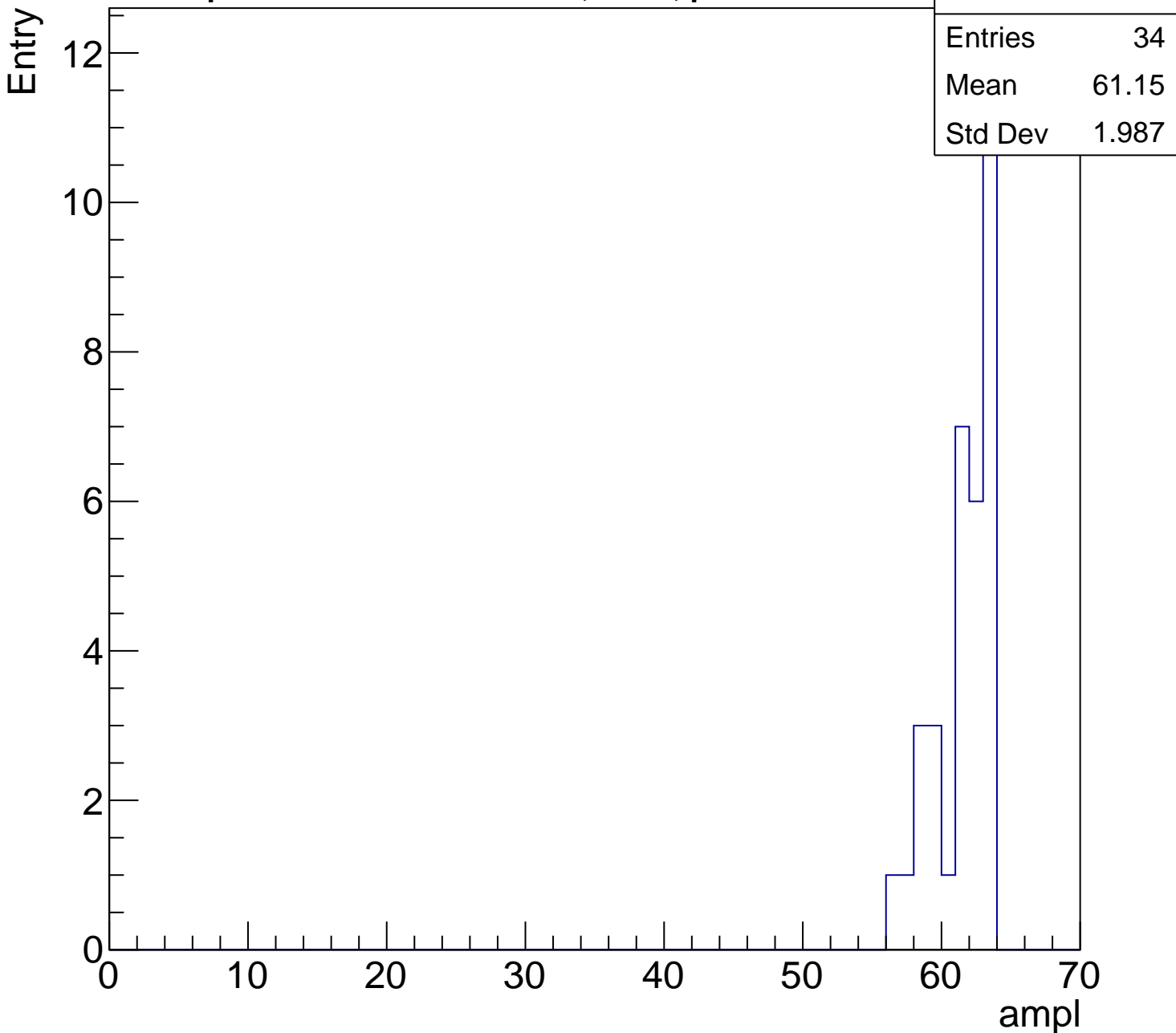
61.15

Std Dev

1.987

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



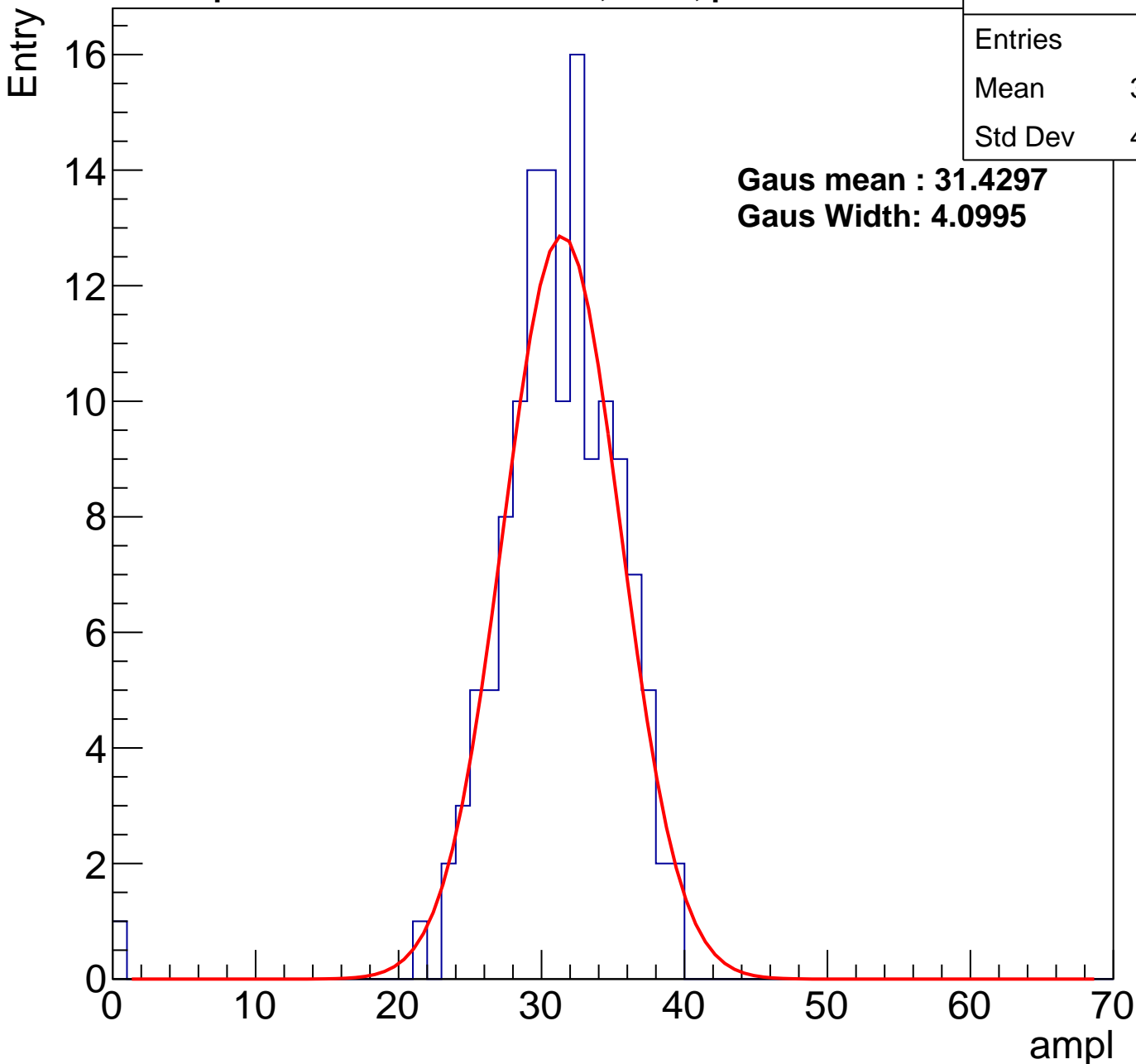
B0L101S, U15-ch85, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	30.68
Std Dev	4.574

Gaus mean : 31.4297

Gaus Width: 4.0995



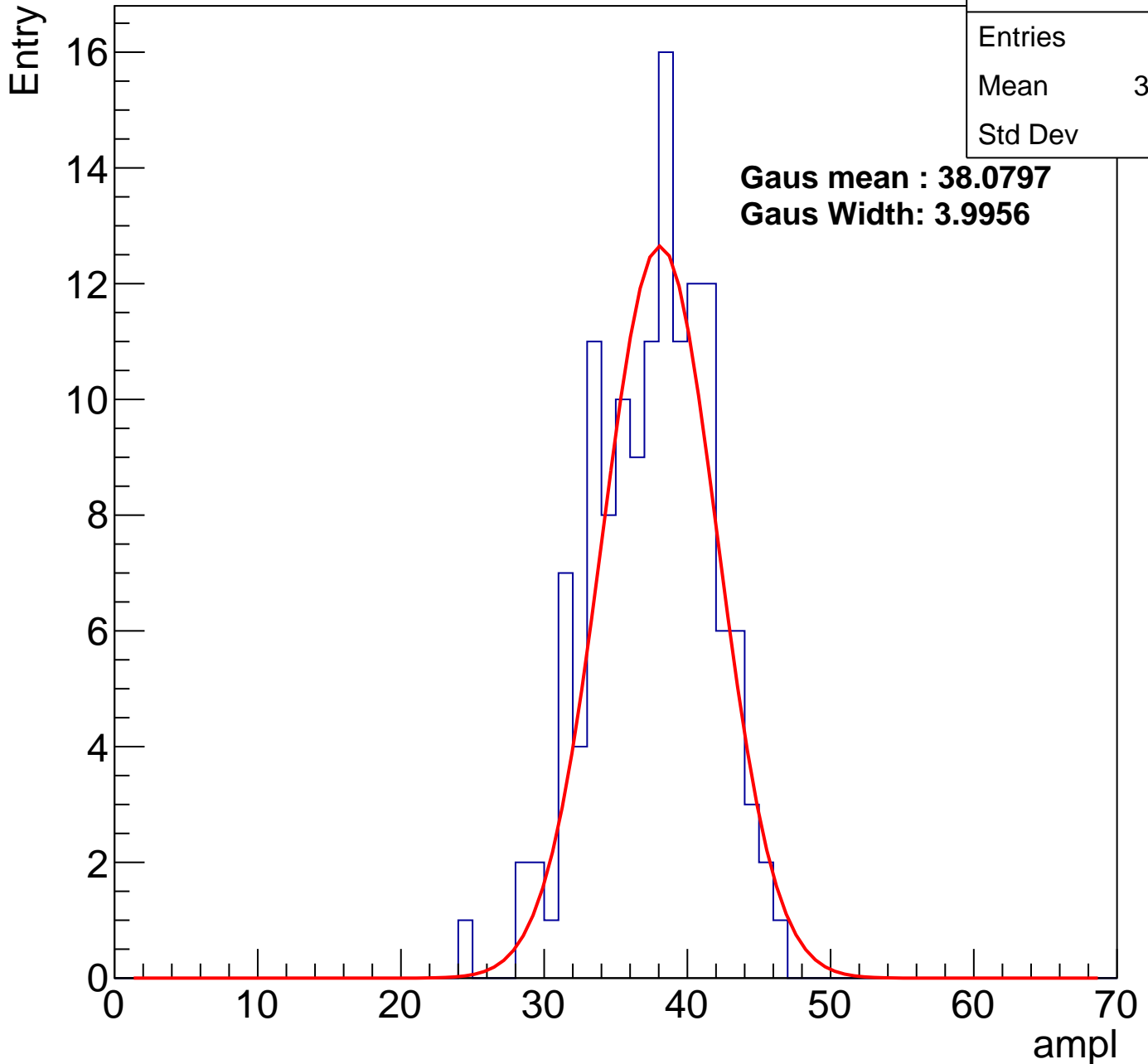
B0L101S, U15-ch85, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	37.16
Std Dev	4.07

Gaus mean : 38.0797

Gaus Width: 3.9956



B0L101S, U15-ch85, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

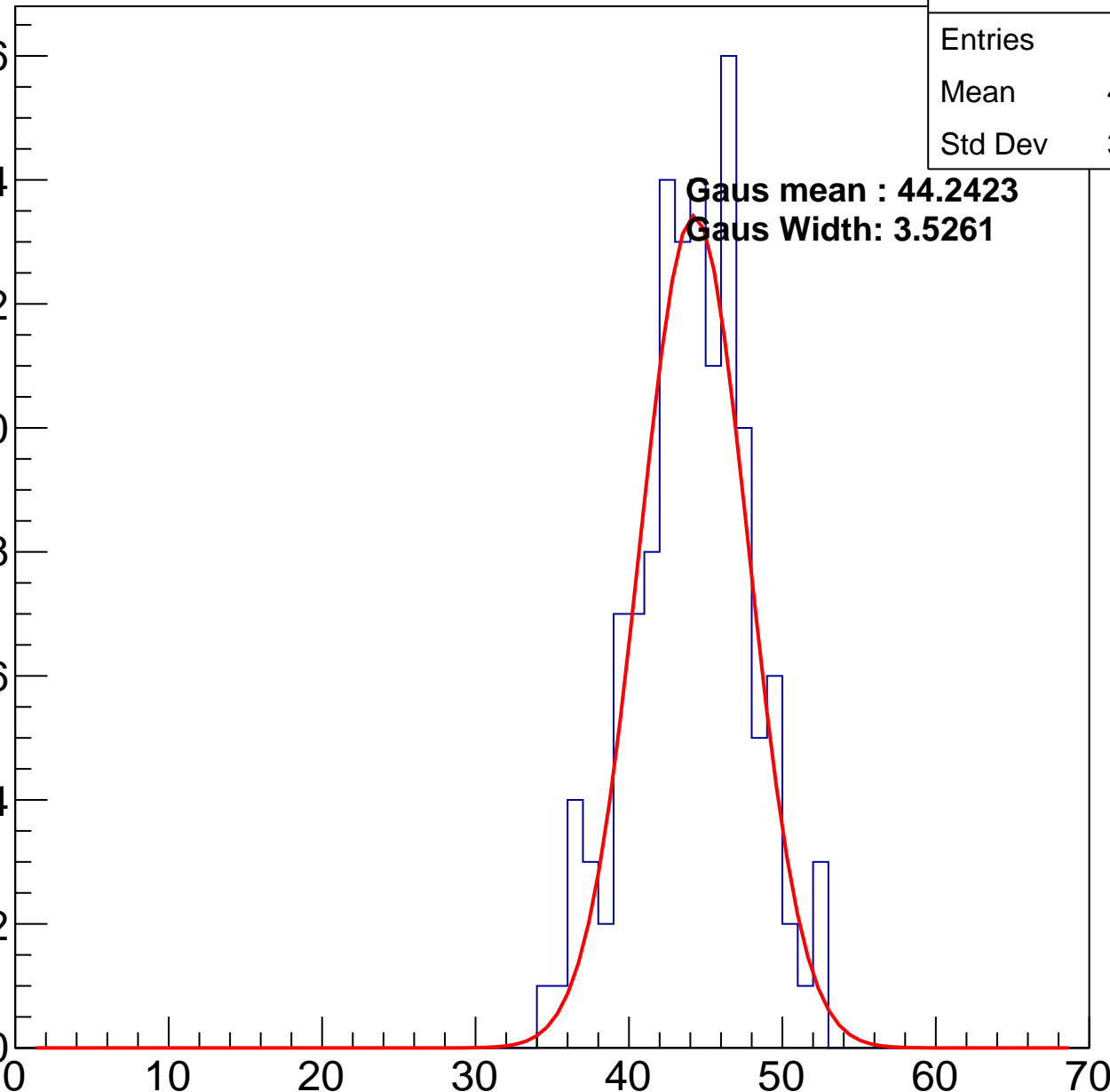
16
14
12
10
8
6
4
2
0

Entries	128
Mean	43.64
Std Dev	3.724

Gaus mean : 44.2423

Gaus Width: 3.5261

ampl



B0L101S, U15-ch85, adc3

calib_packv5_042523_0143.root, FC#1, port C1

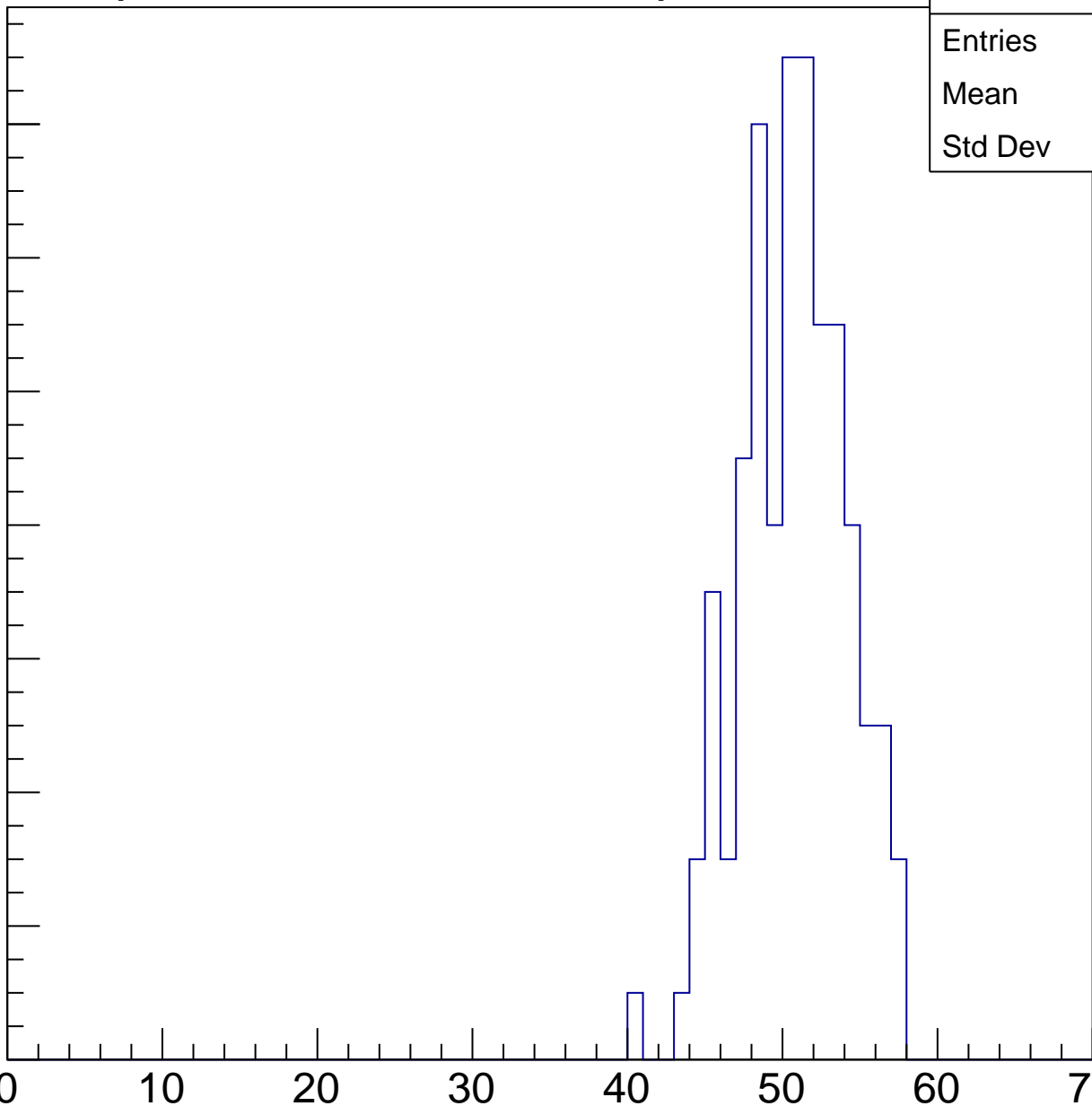
Entries	119
Mean	50.28
Std Dev	3.373

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch85, adc4

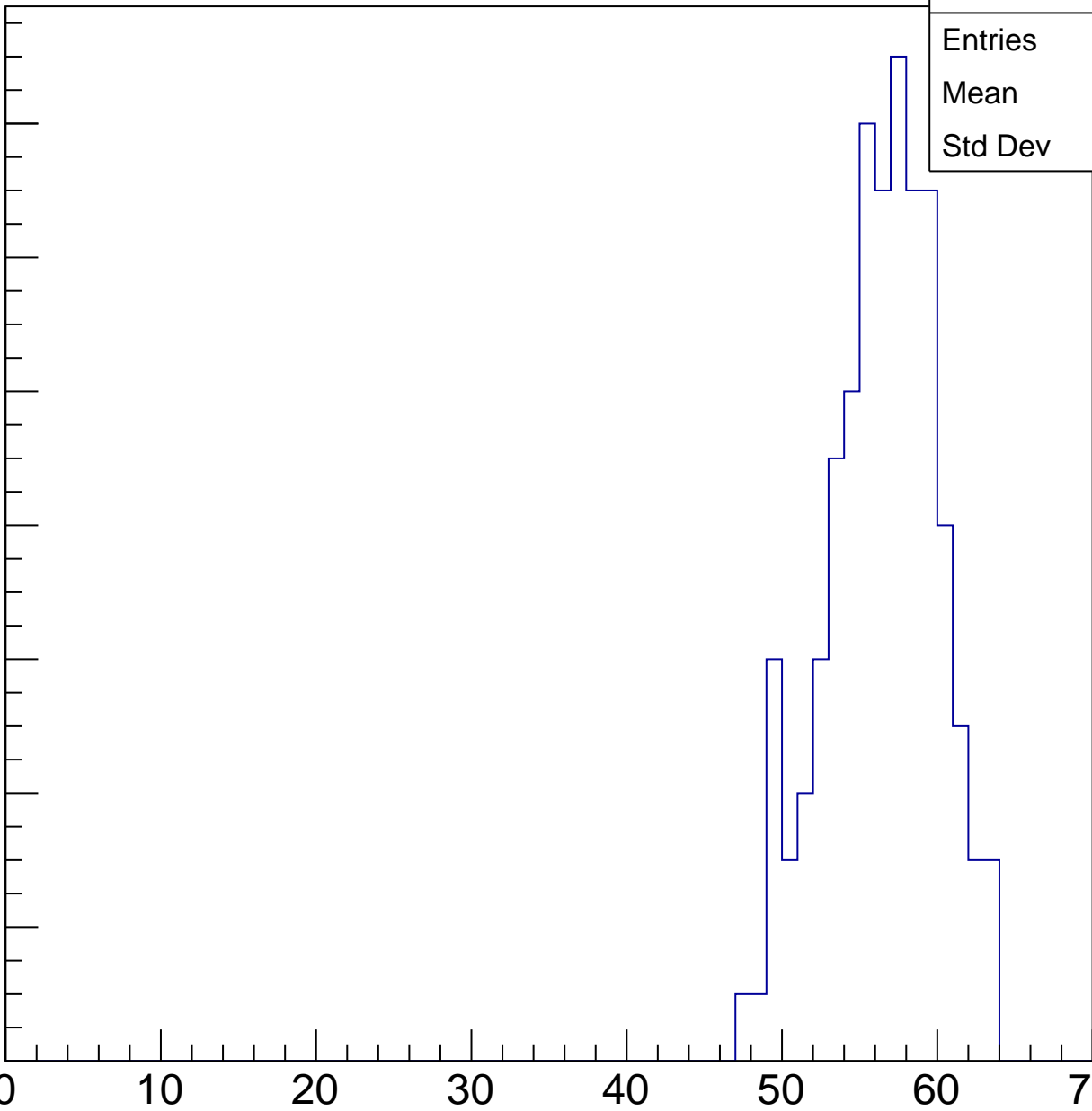
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	55.95
Std Dev	3.516

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch85, adc5

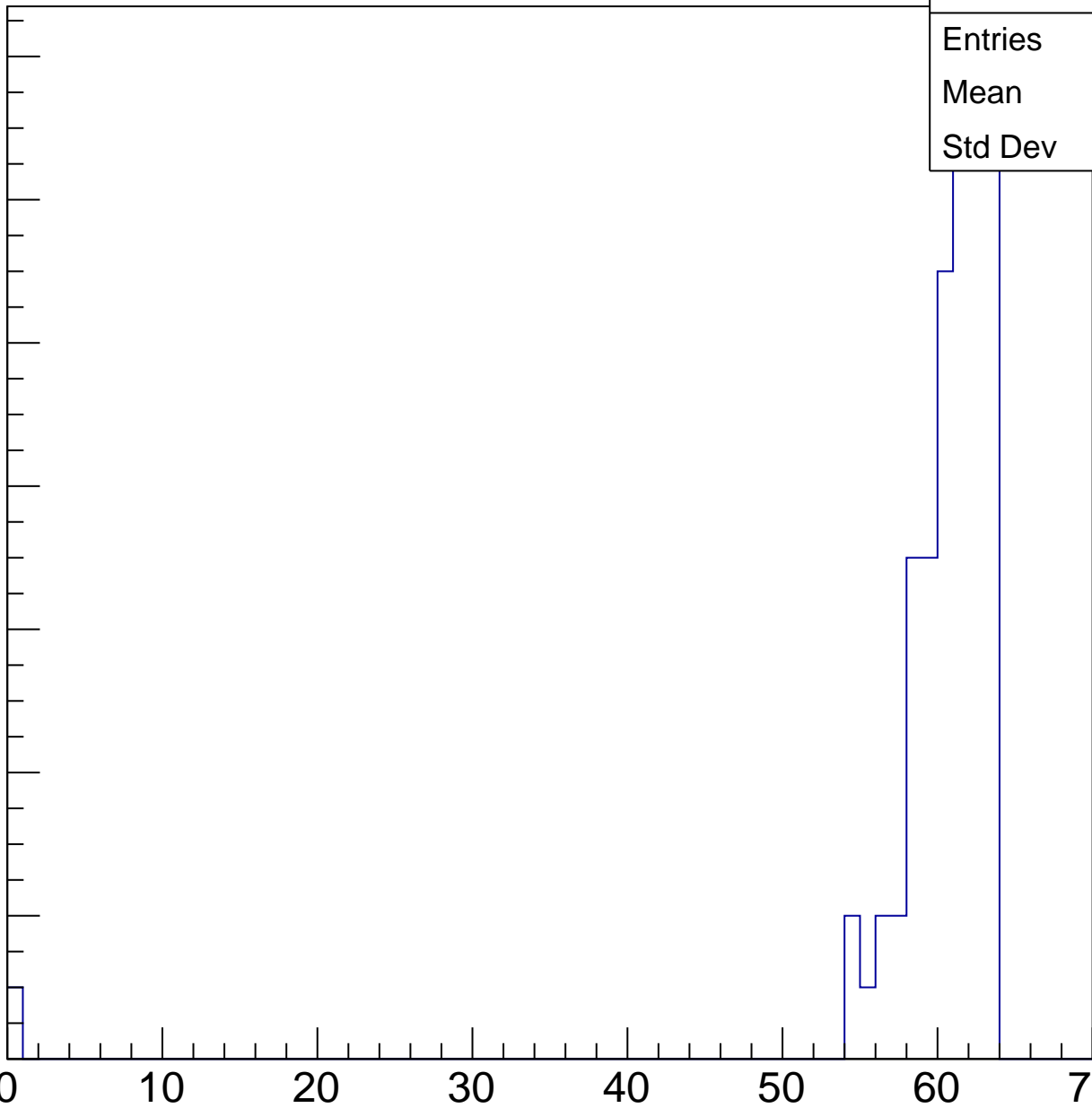
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	74
Mean	59.59
Std Dev	7.317

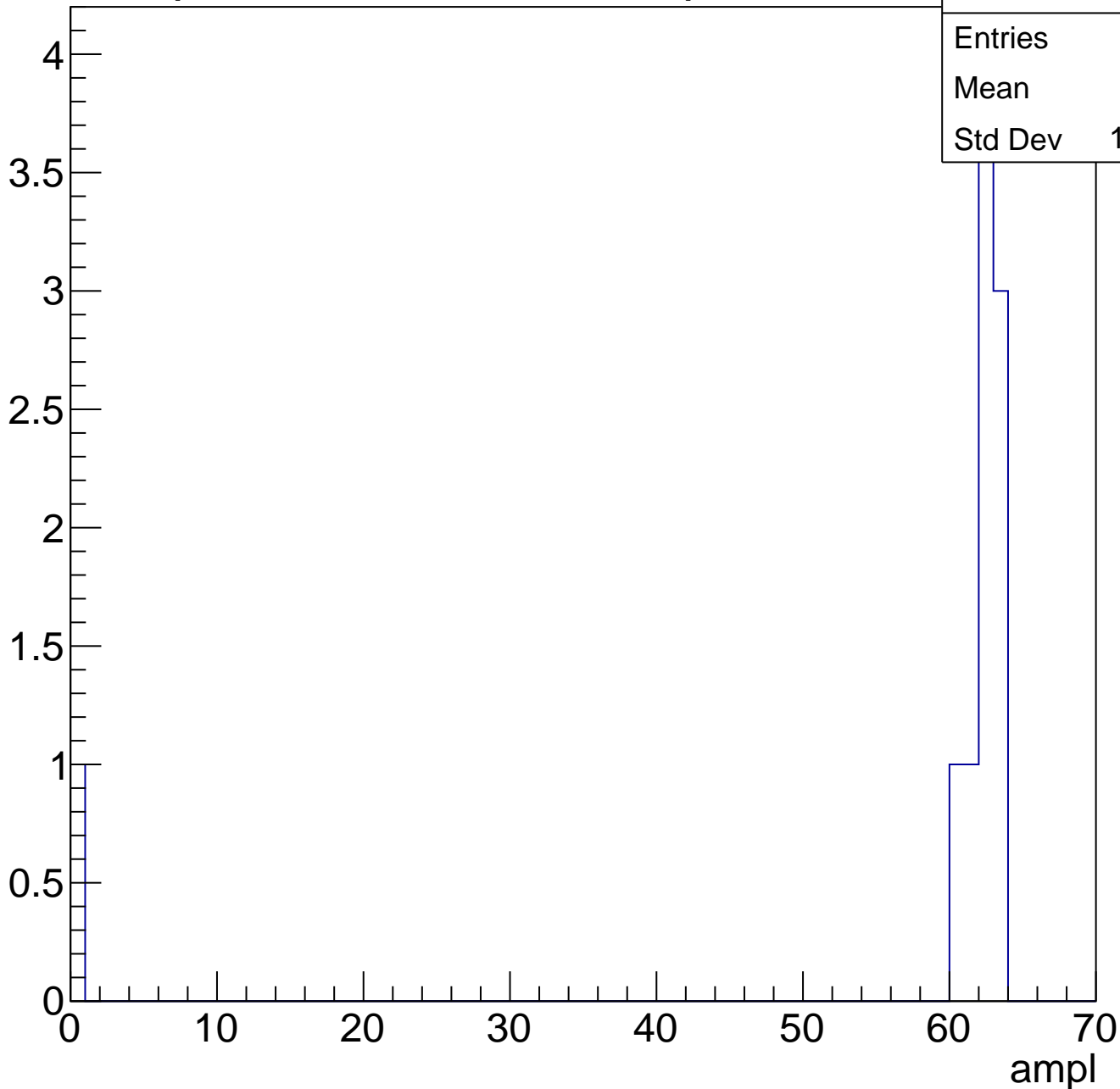
ampl



B0L101S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

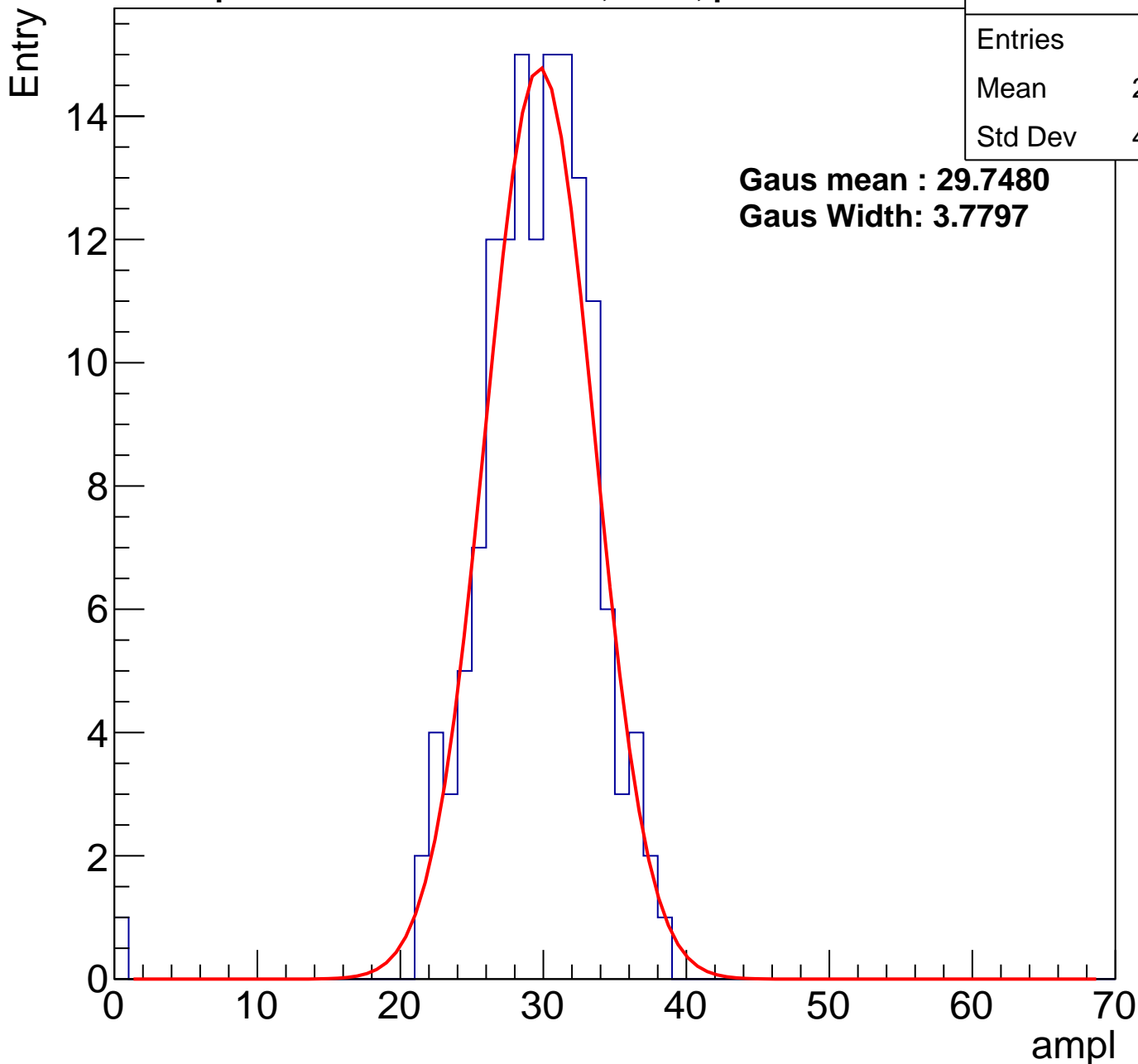
B0L101S, U15-ch86, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	29.07
Std Dev	4.354

Gaus mean : 29.7480

Gaus Width: 3.7797



B0L101S, U15-ch86, adc1

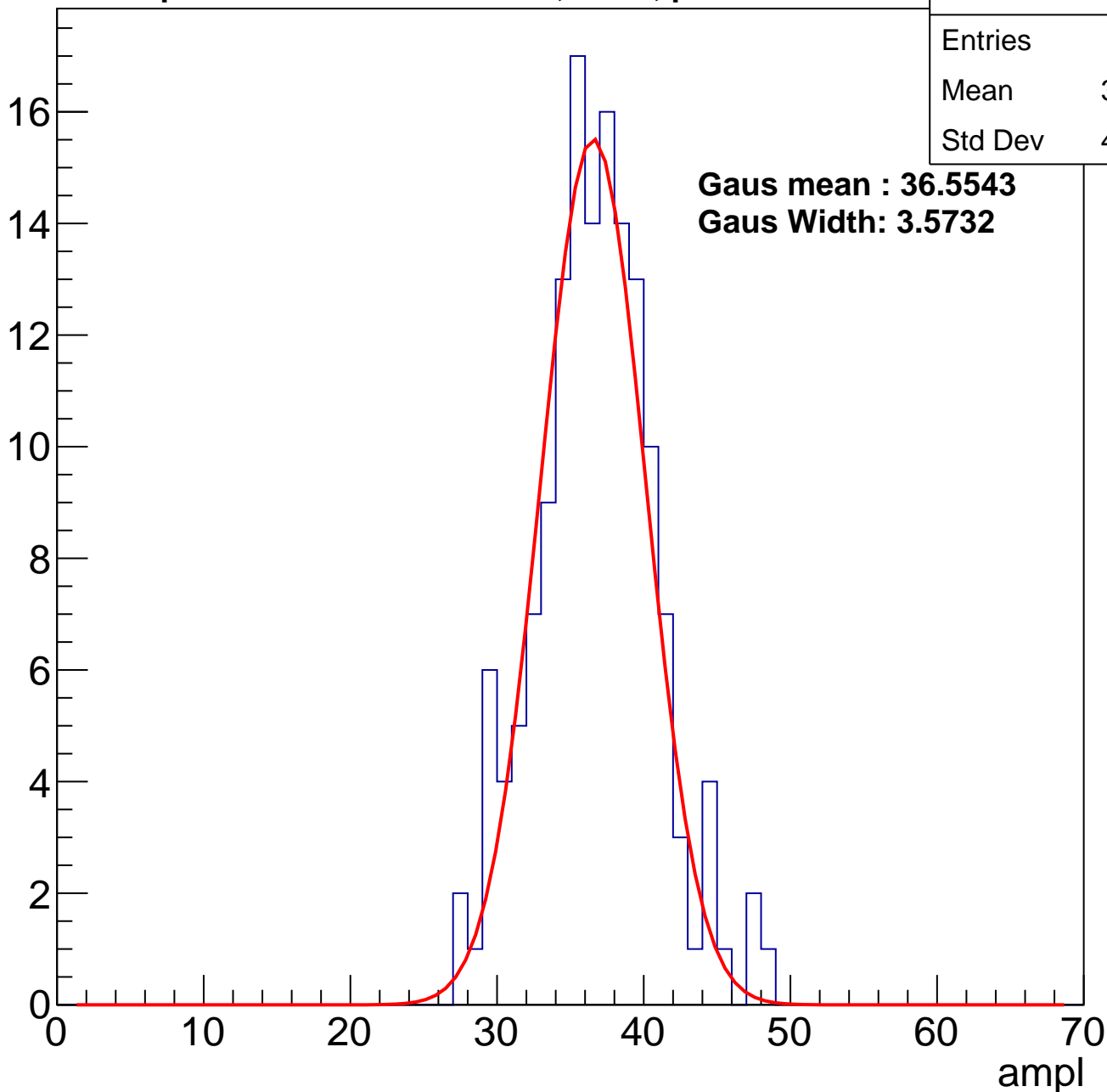
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	36.29
Std Dev	4.035

Gaus mean : 36.5543

Gaus Width: 3.5732

Entry



B0L101S, U15-ch86, adc2

calib_packv5_042523_0143.root, FC#1, port C1

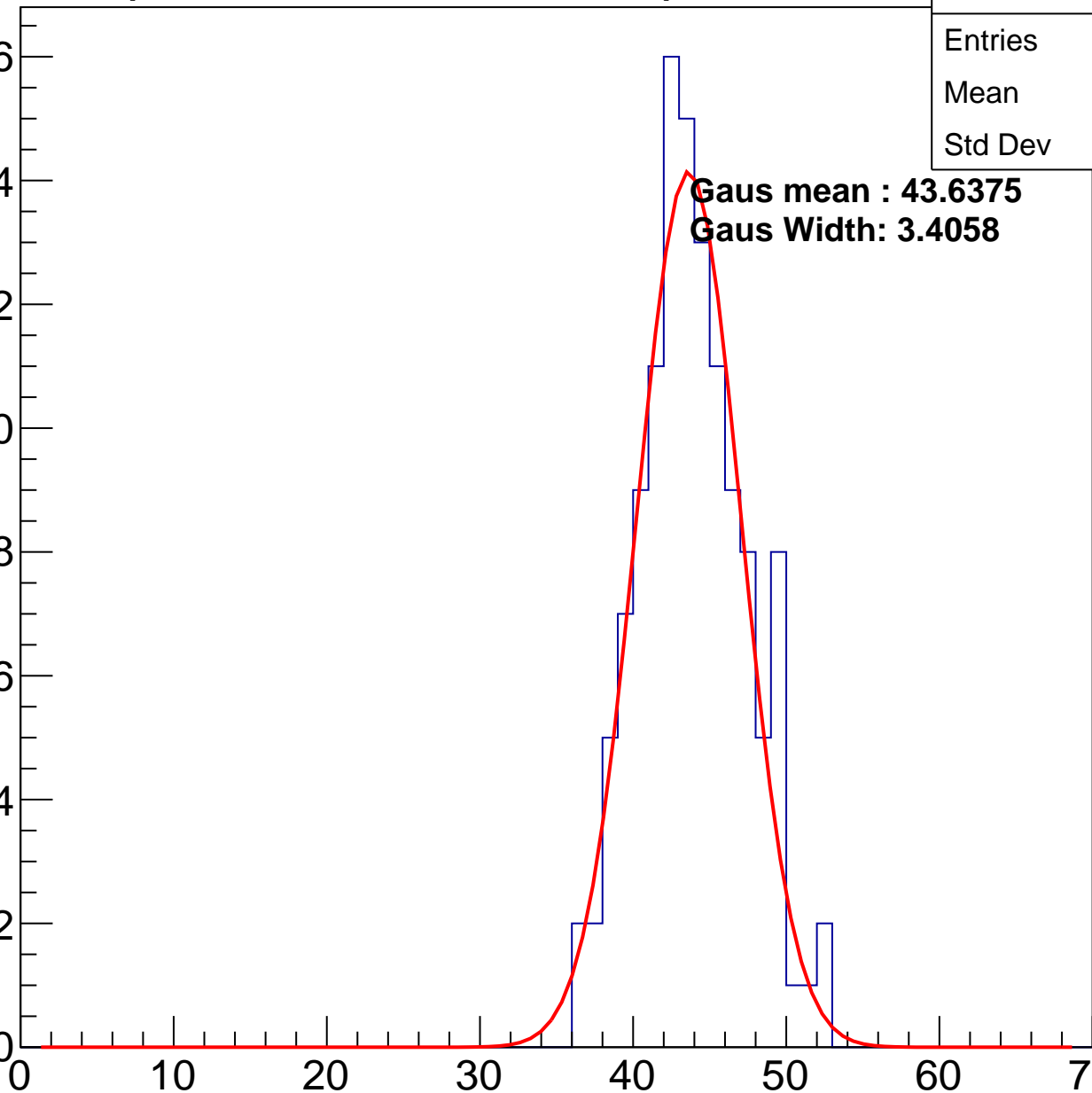
Entry

16
14
12
10
8
6
4
2
0

Entries	125
Mean	43.45
Std Dev	3.437

Gaus mean : 43.6375
Gaus Width: 3.4058

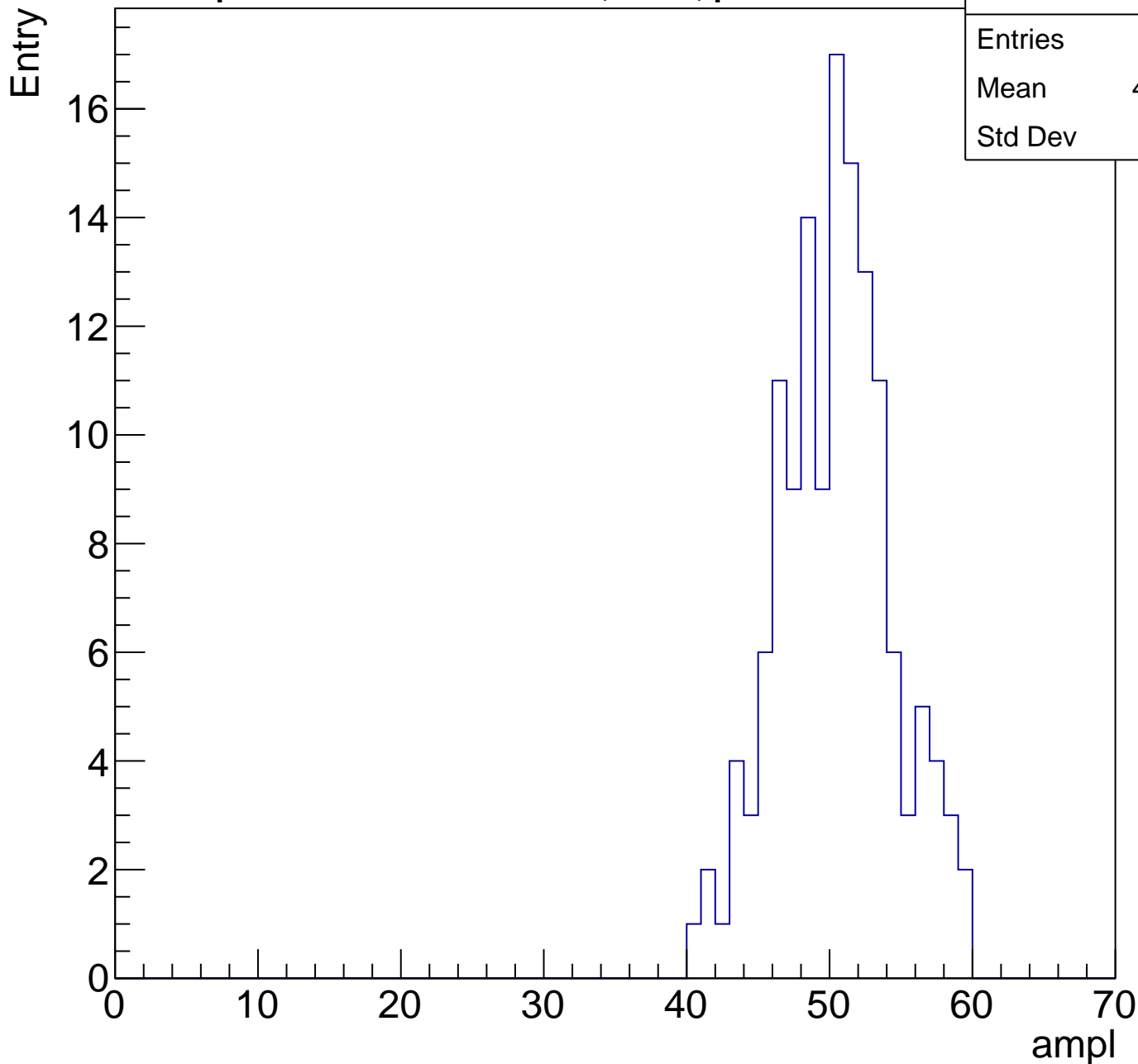
ampl



B0L101S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	49.95
Std Dev	3.95



B0L101S, U15-ch86, adc4

calib_packv5_042523_0143.root, FC#1, port C1

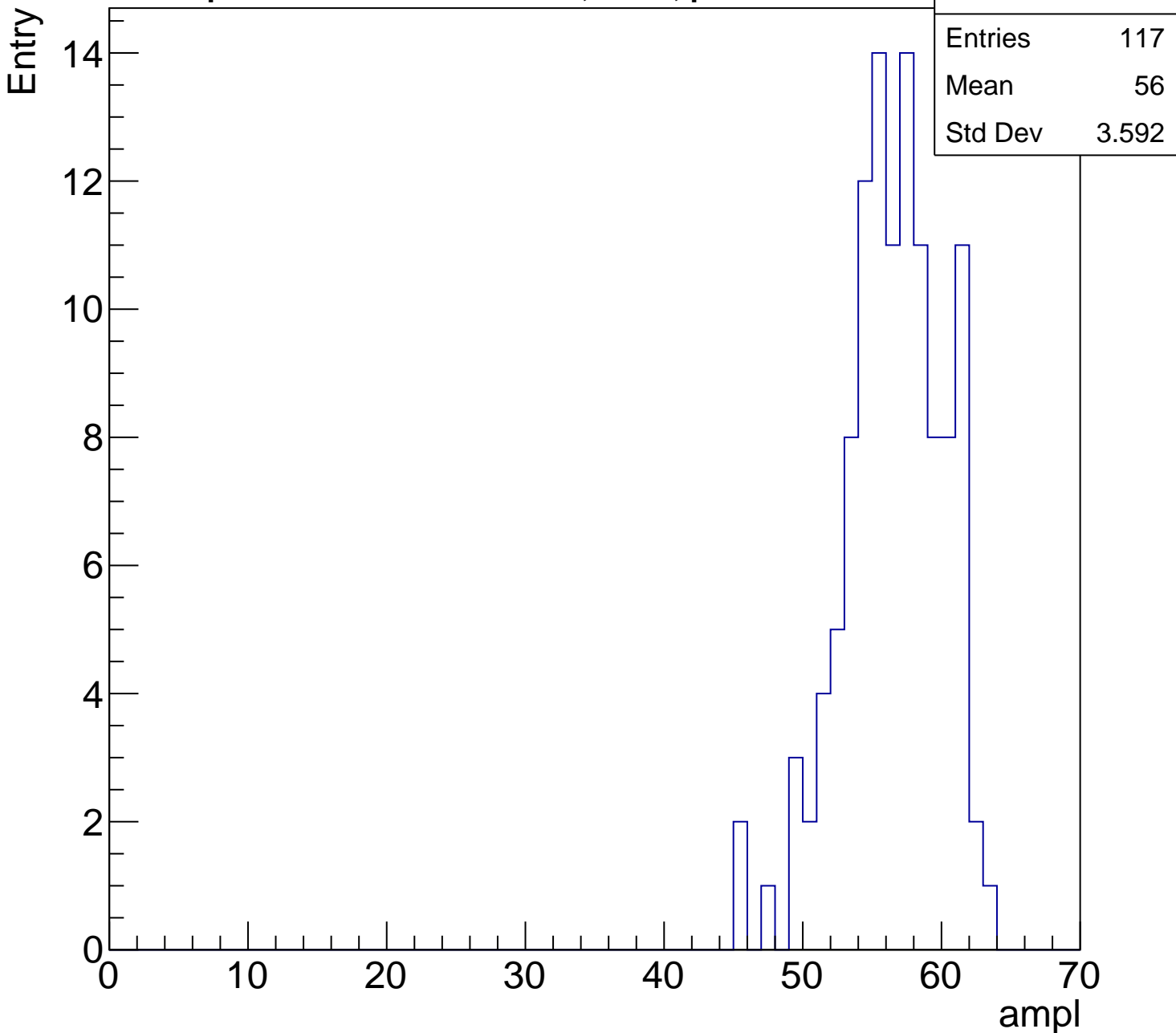
Entries	117
Mean	56
Std Dev	3.592

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch86, adc5

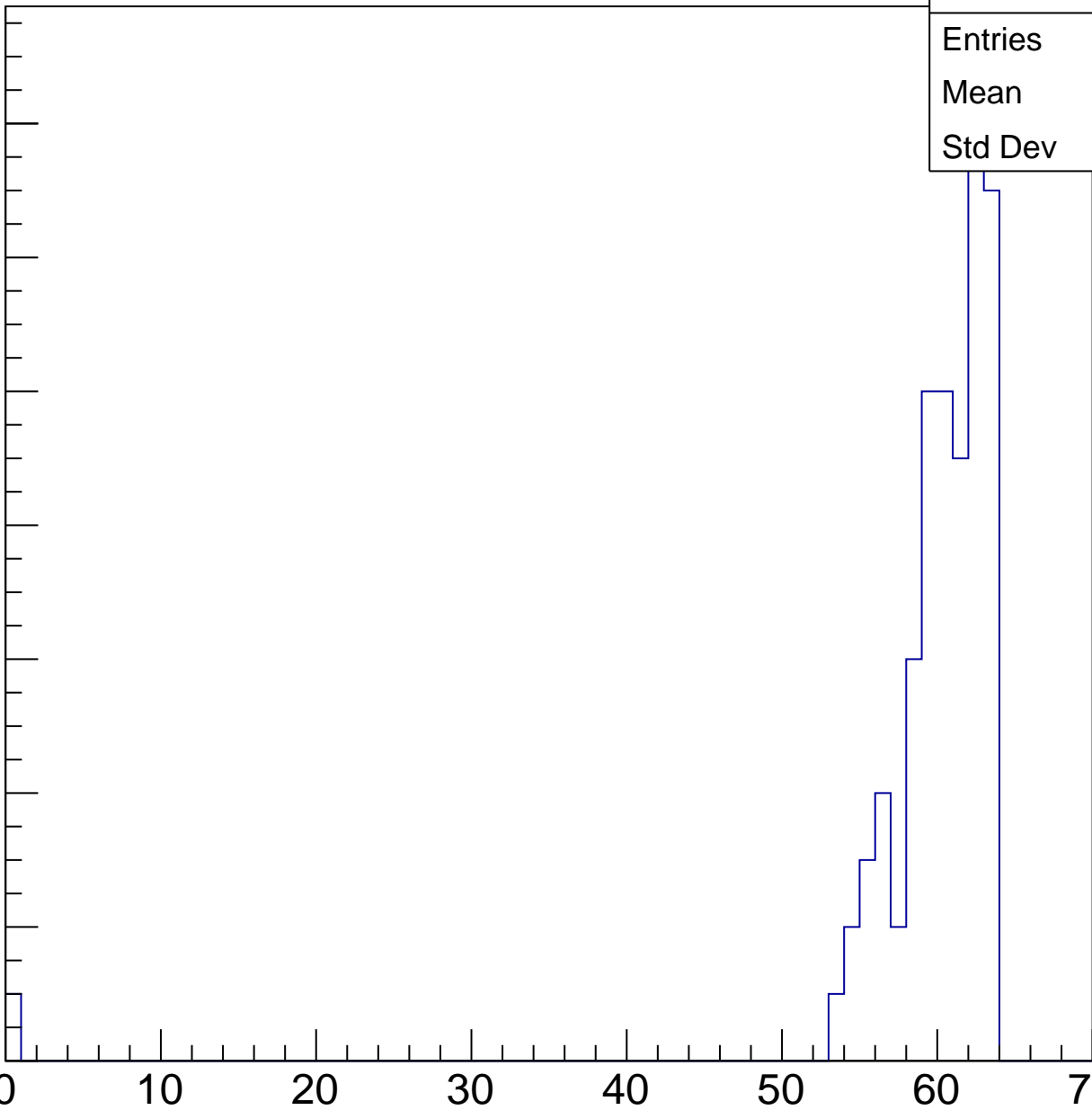
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.21
Std Dev	7.301

ampl

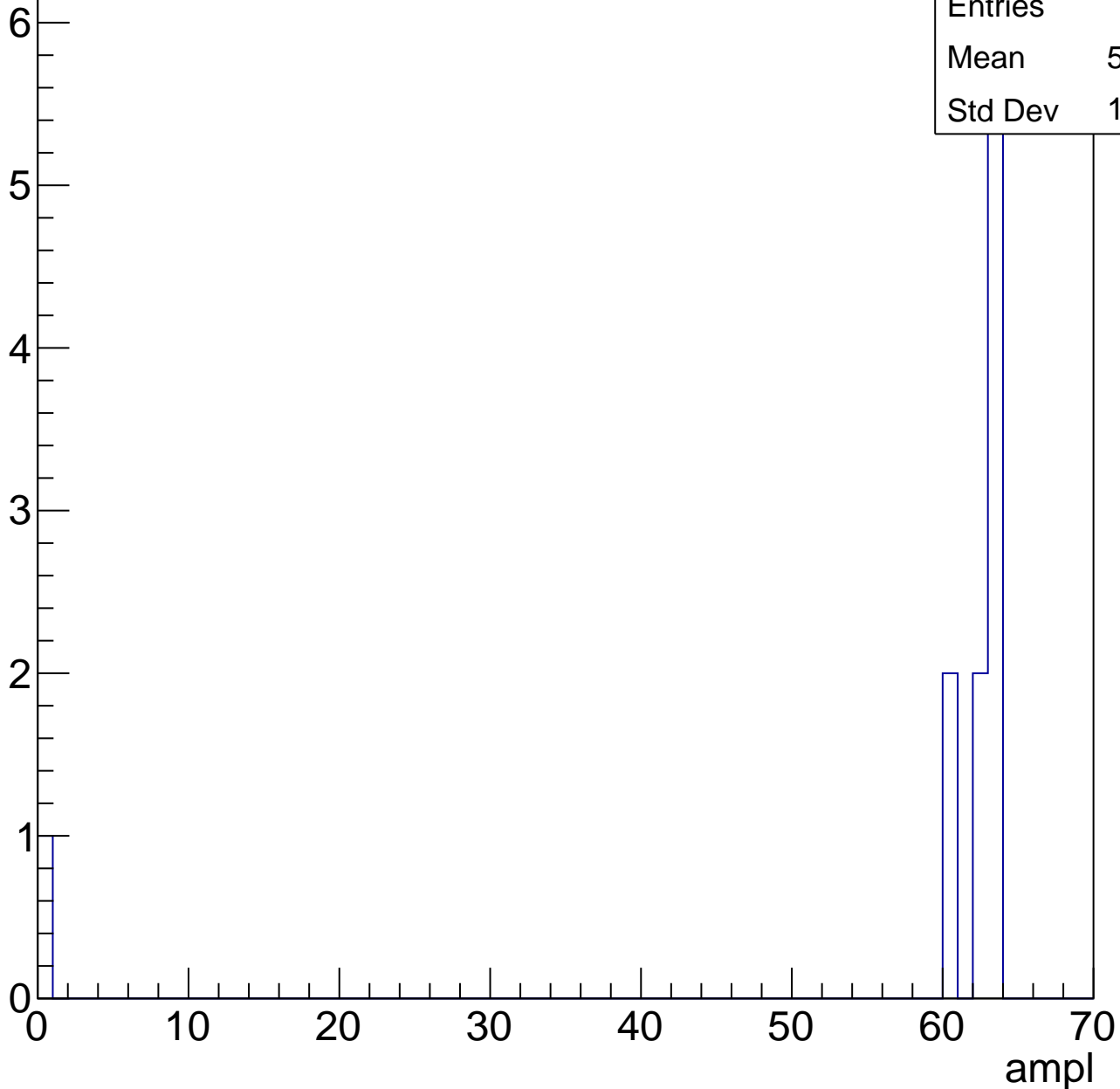


B0L101S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

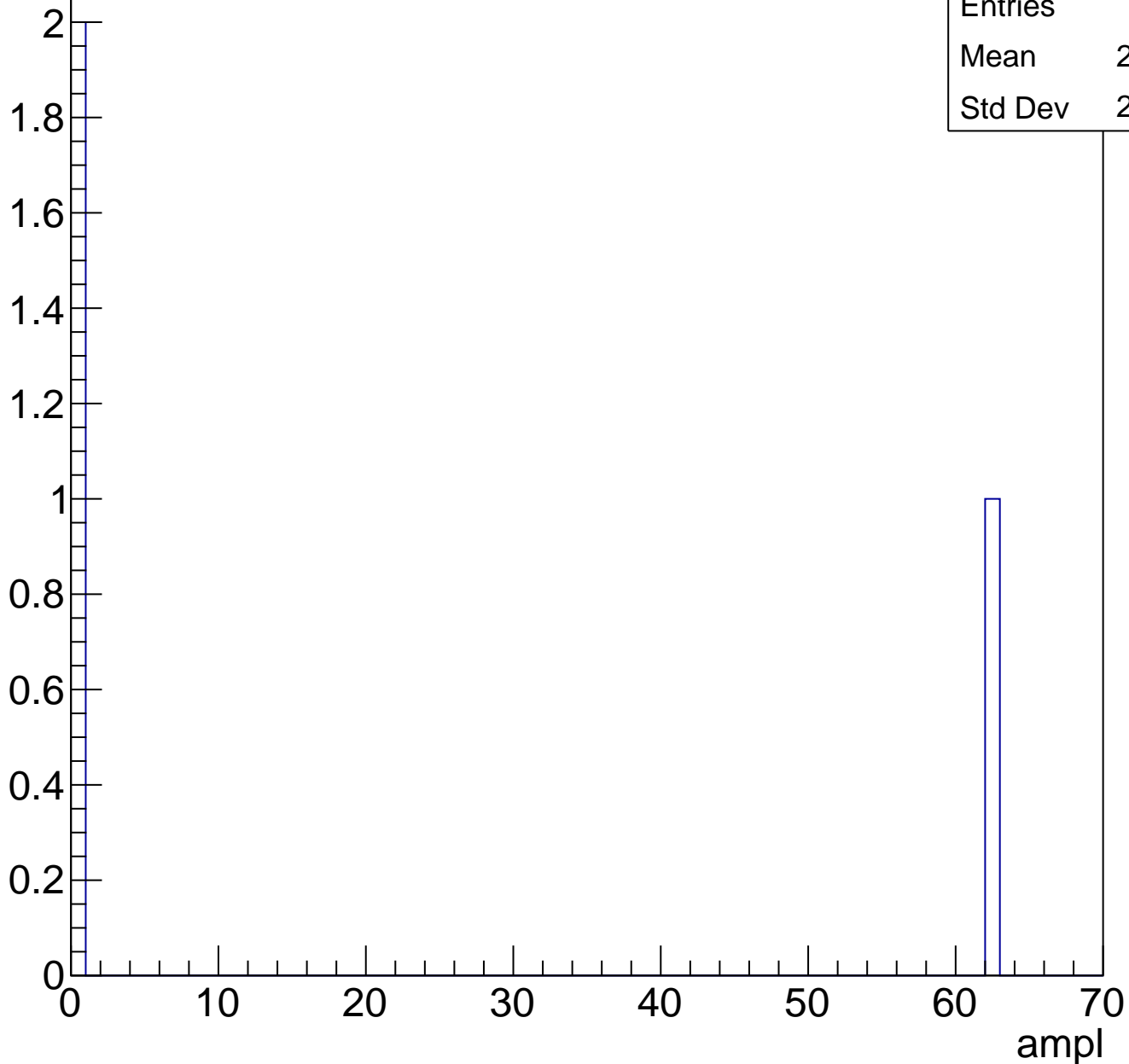
Entries	11
Mean	56.55
Std Dev	17.92



B0L101S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch87, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

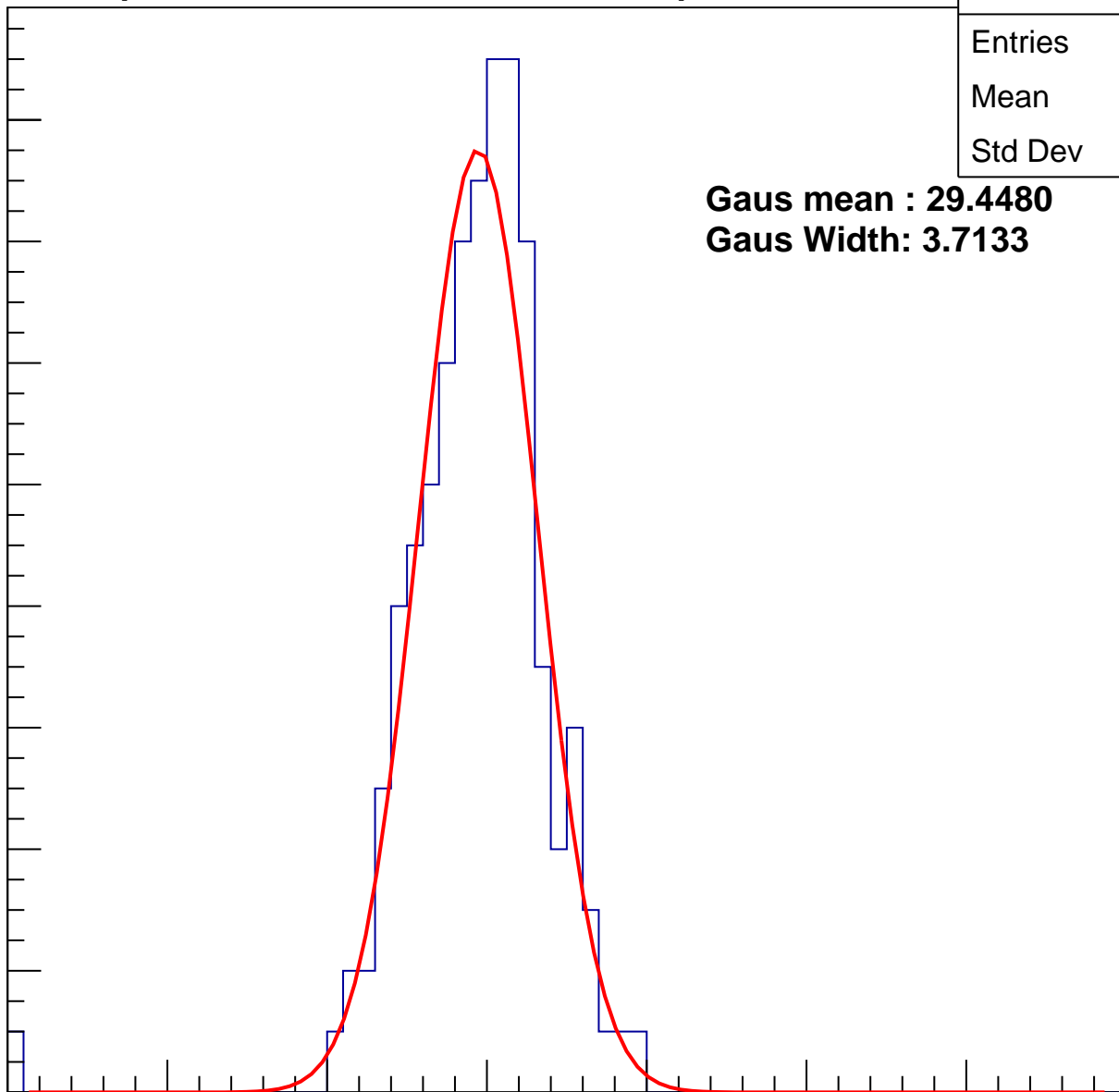
Entries	150
Mean	28.89
Std Dev	4.361

Gaus mean : 29.4480

Gaus Width: 3.7133

0 10 20 30 40 50 60 70

ampl

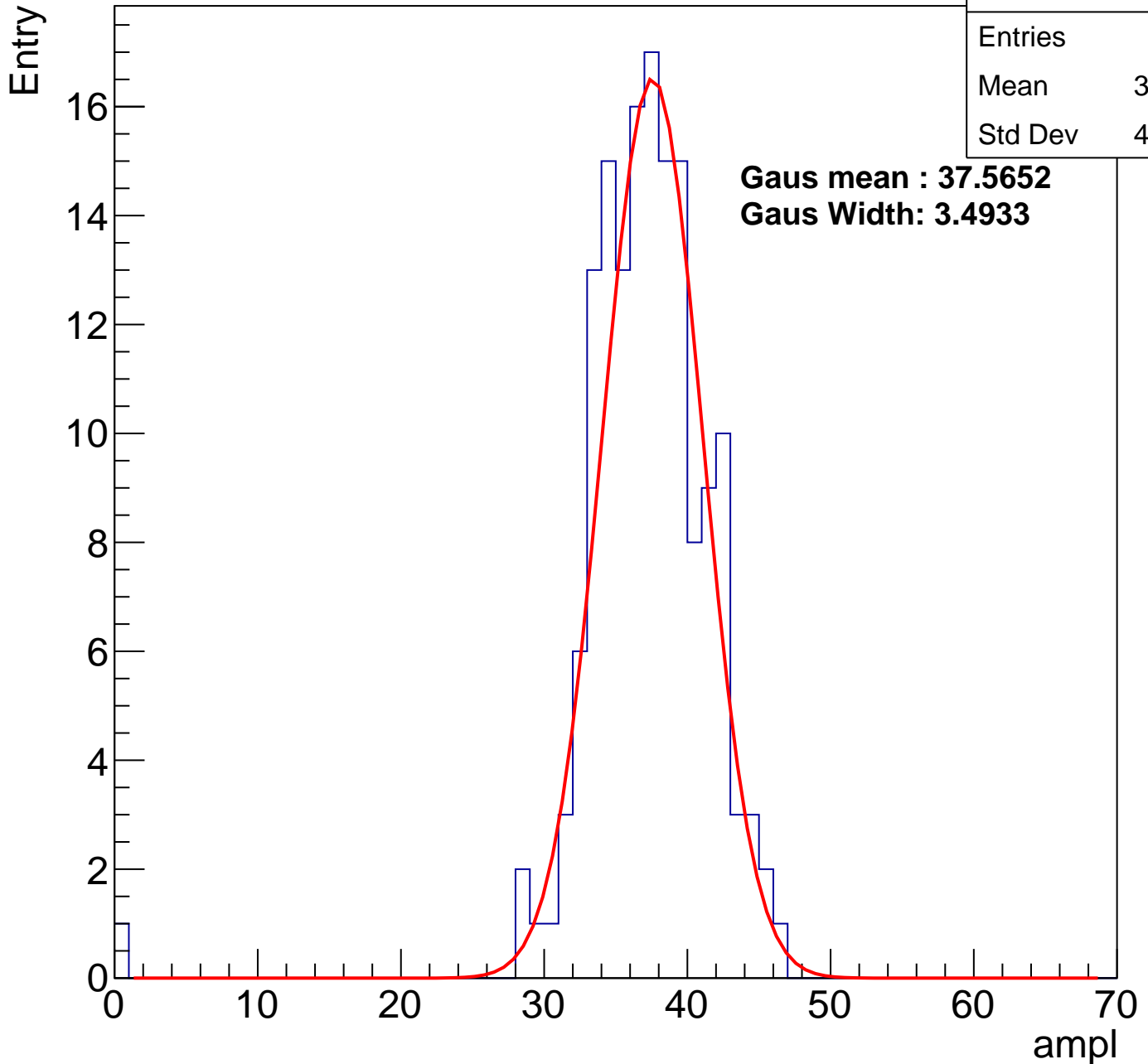


B0L101S, U15-ch87, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	36.75
Std Dev	4.635

Gaus mean : 37.5652
Gaus Width: 3.4933



B0L101S, U15-ch87, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	43.66
Std Dev	3.563

Gaus mean : 44.4161

Gaus Width: 3.7668

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch87, adc3

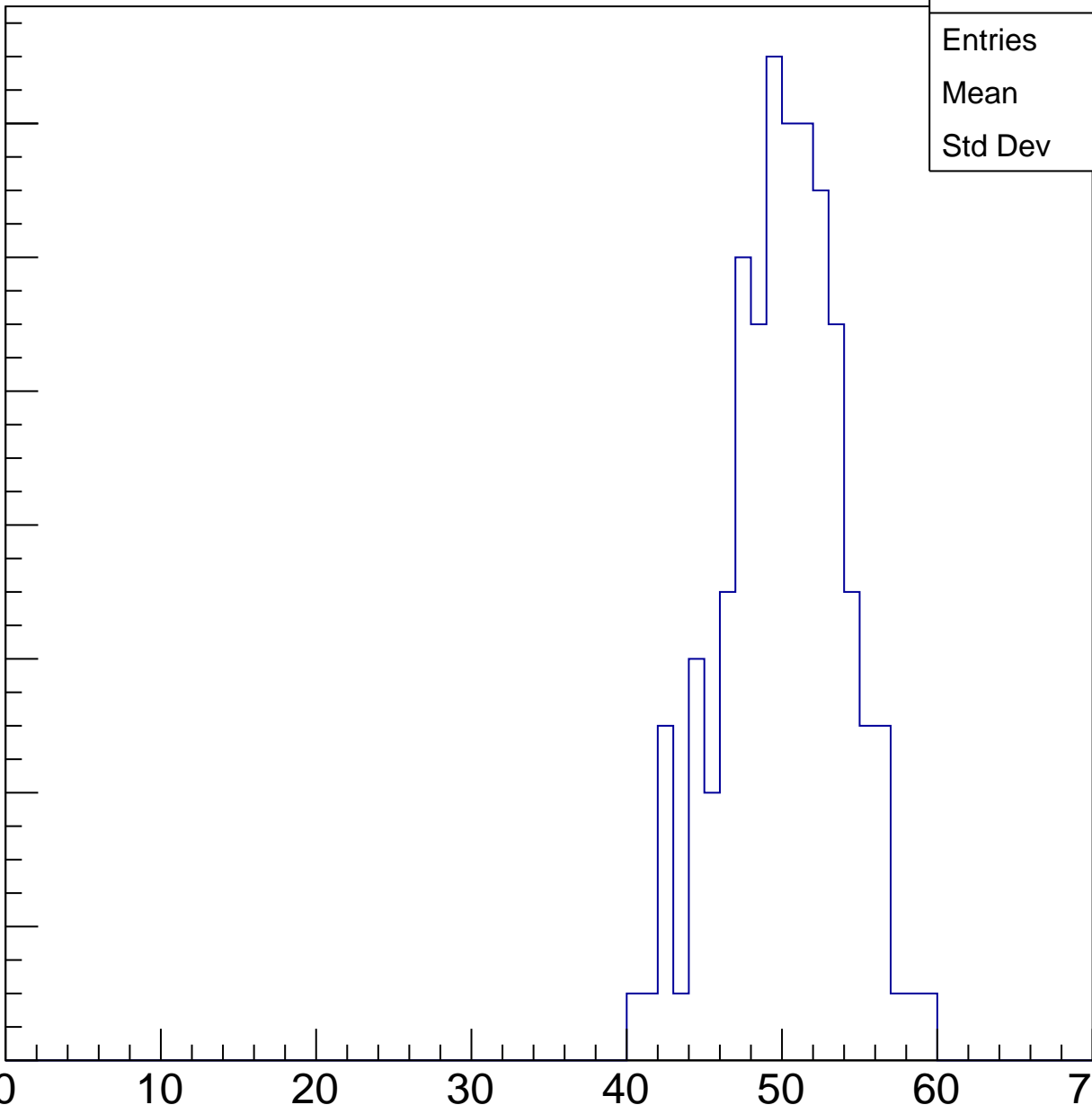
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	135
Mean	49.68
Std Dev	3.759

ampl



B0L101S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#1, port C1

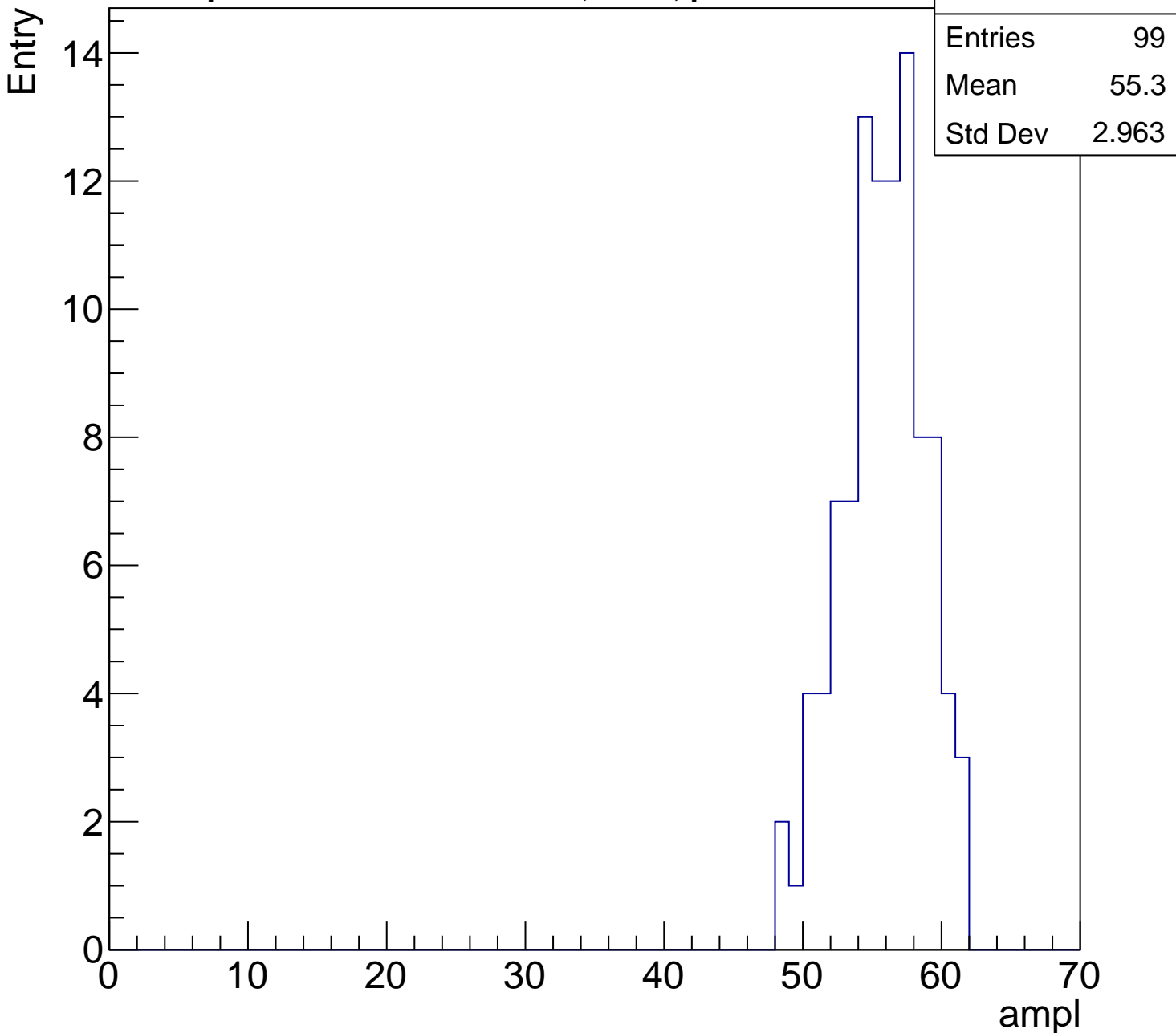
Entries	99
Mean	55.3
Std Dev	2.963

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch87, adc5

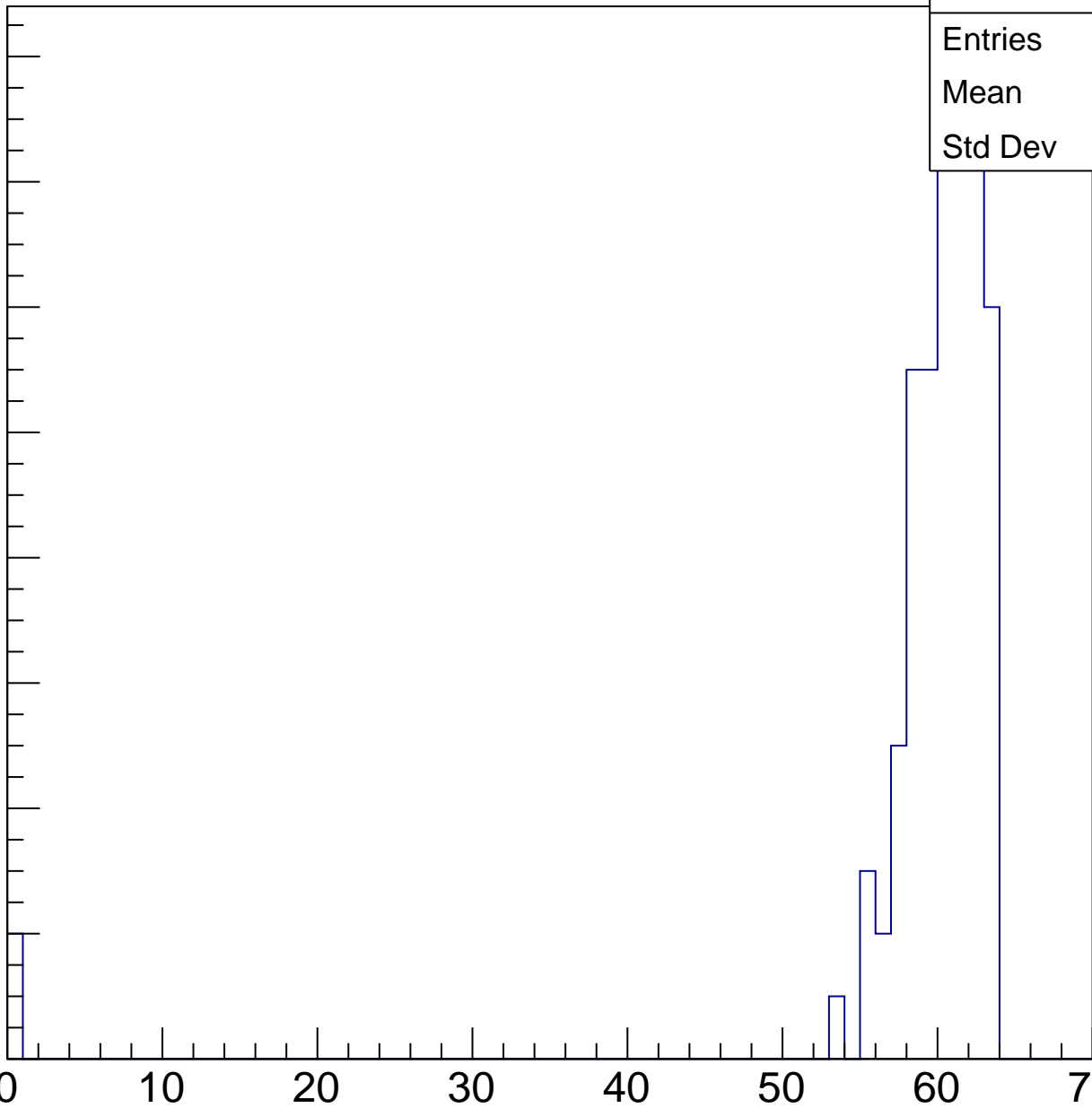
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	94
Mean	58.77
Std Dev	8.93

ampl

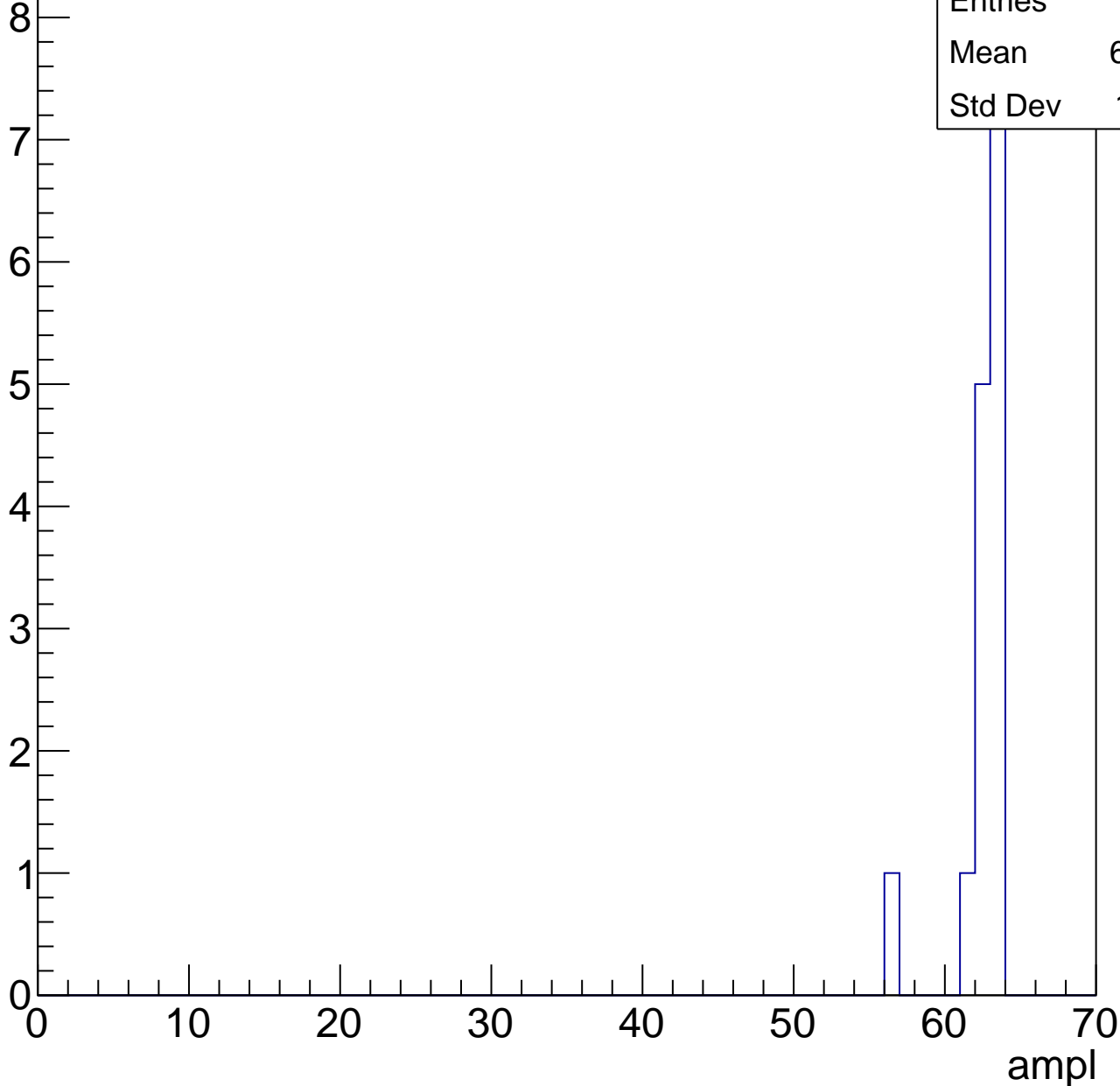


B0L101S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

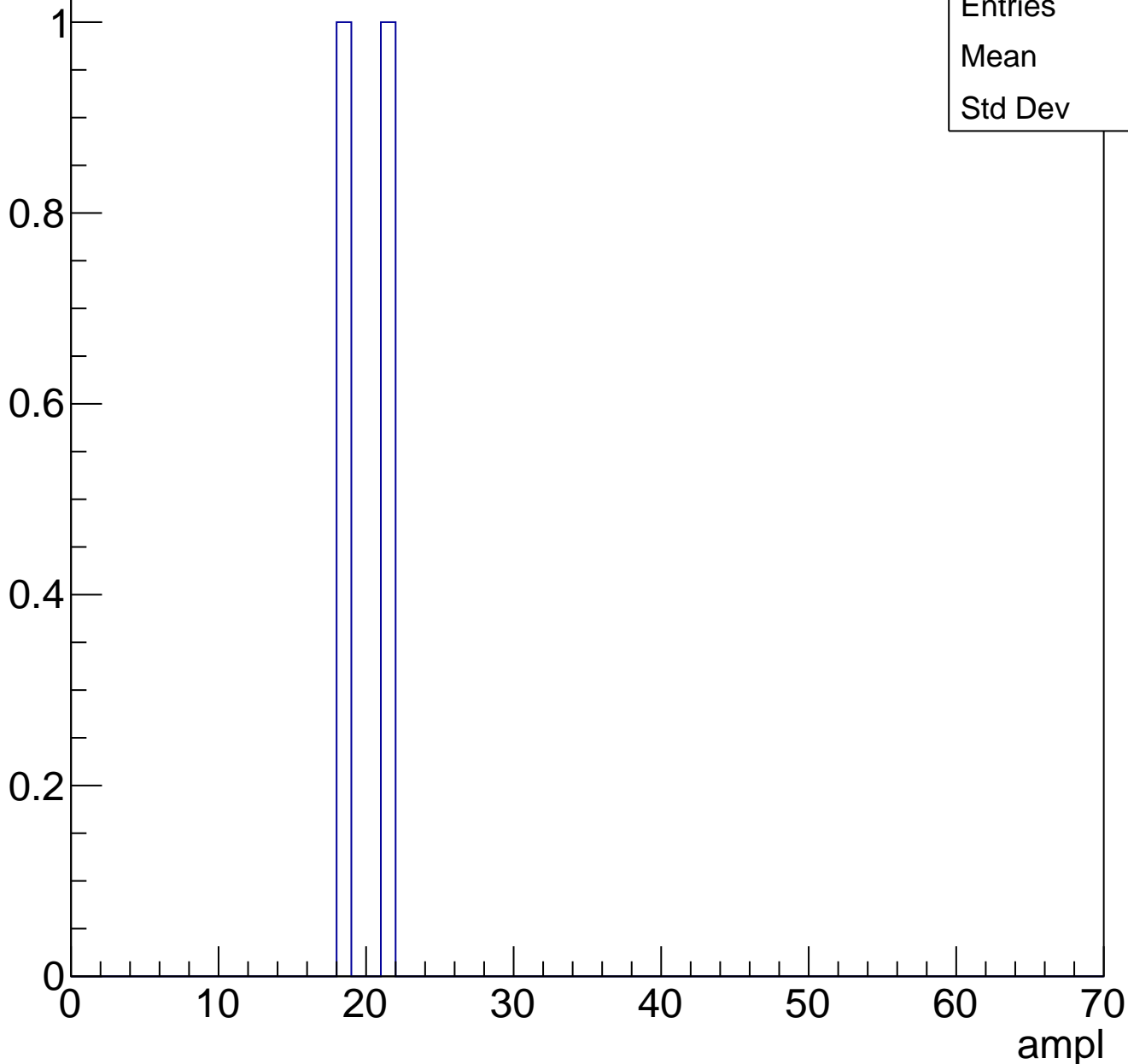
Entries	15
Mean	62.07
Std Dev	1.731



B0L101S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#1, port C1

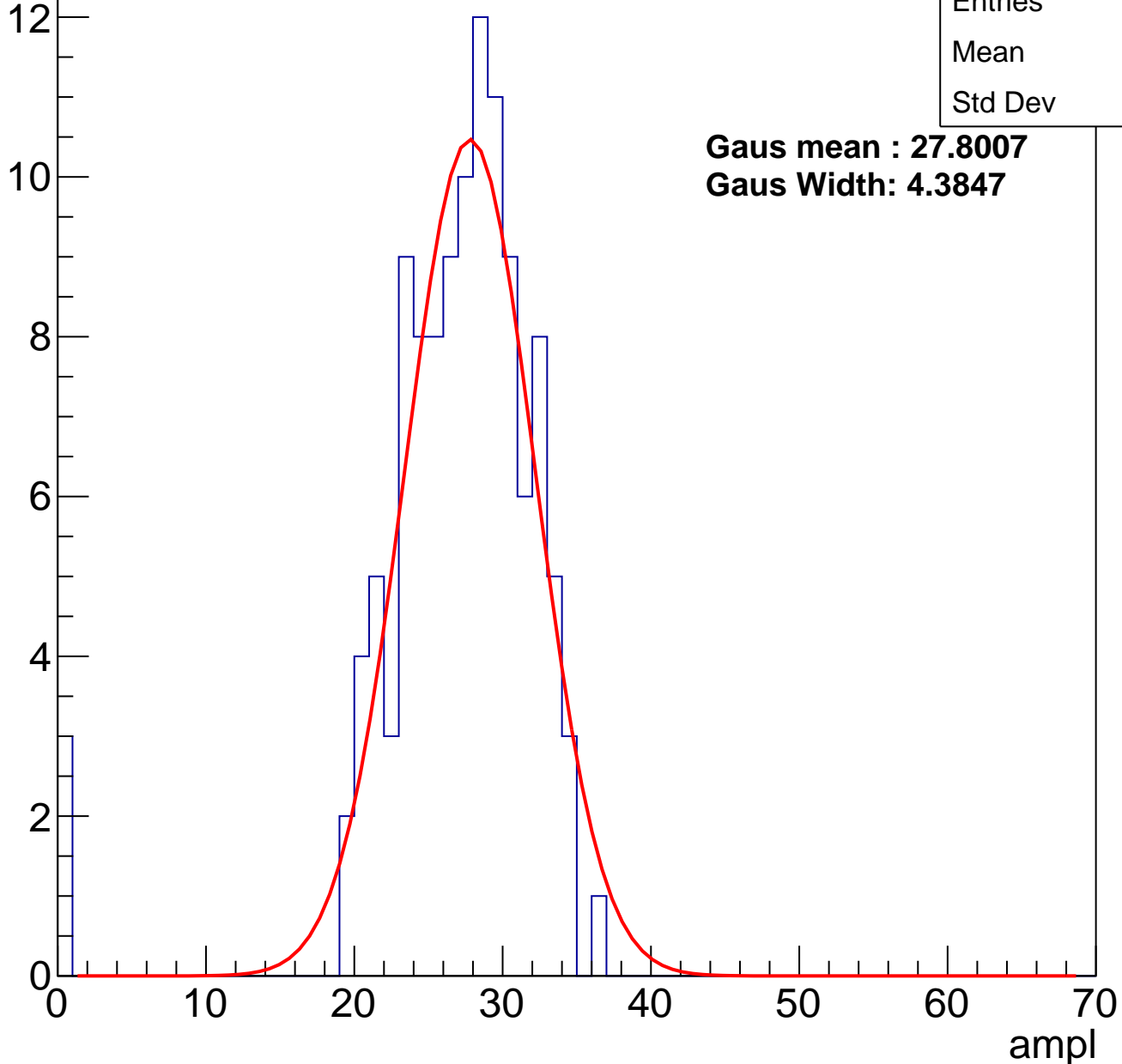
Entry



B0L101S, U15-ch88, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	116
Mean	26.4
Std Dev	5.74

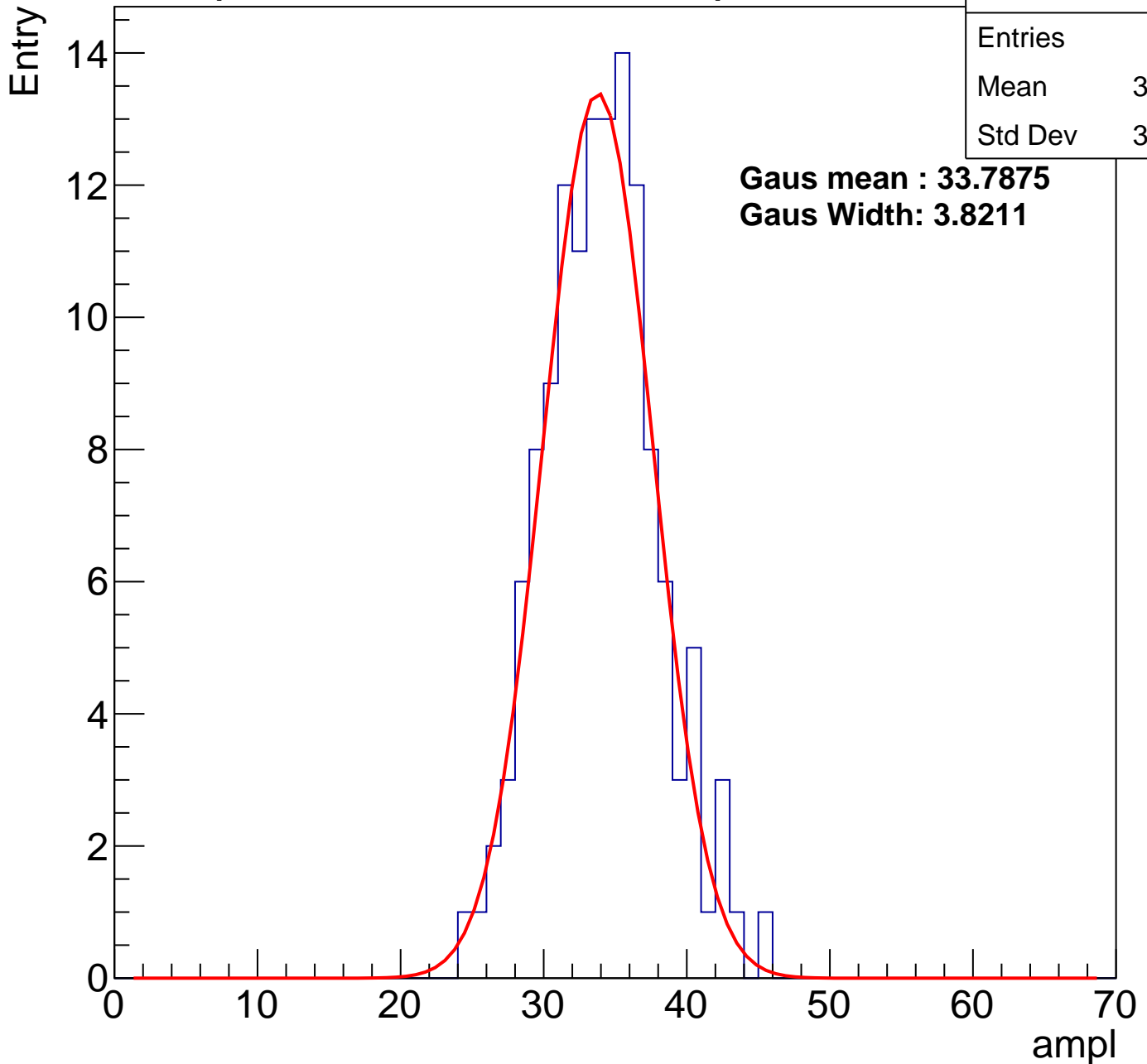
Gaus mean : 27.8007
Gaus Width: 4.3847

B0L101S, U15-ch88, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	33.57
Std Dev	3.959

Gaus mean : 33.7875
Gaus Width: 3.8211



B0L101S, U15-ch88, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	40.14
Std Dev	3.606

Gaus mean : 40.6325

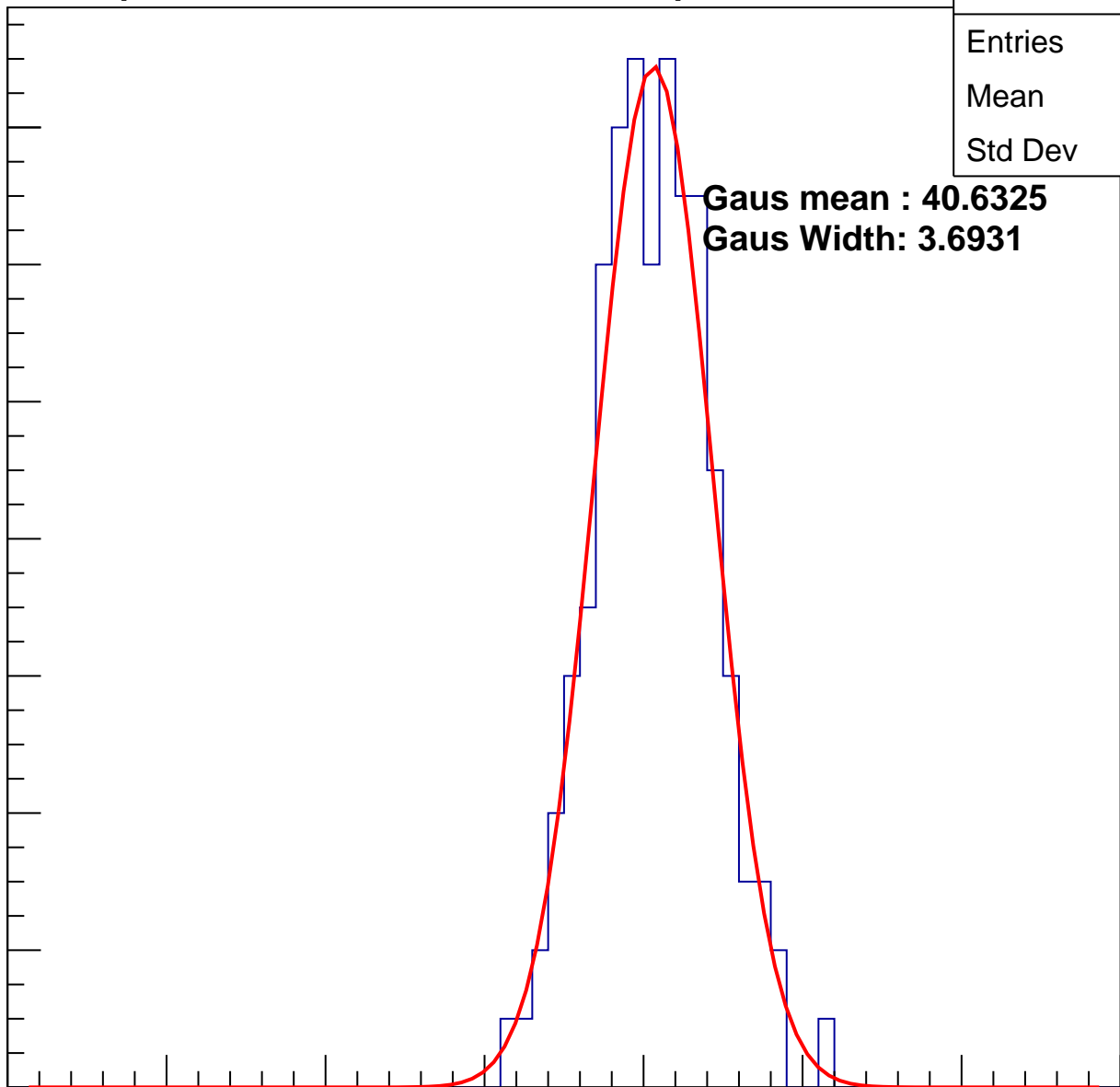
Gaus Width: 3.6931

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#1, port C1

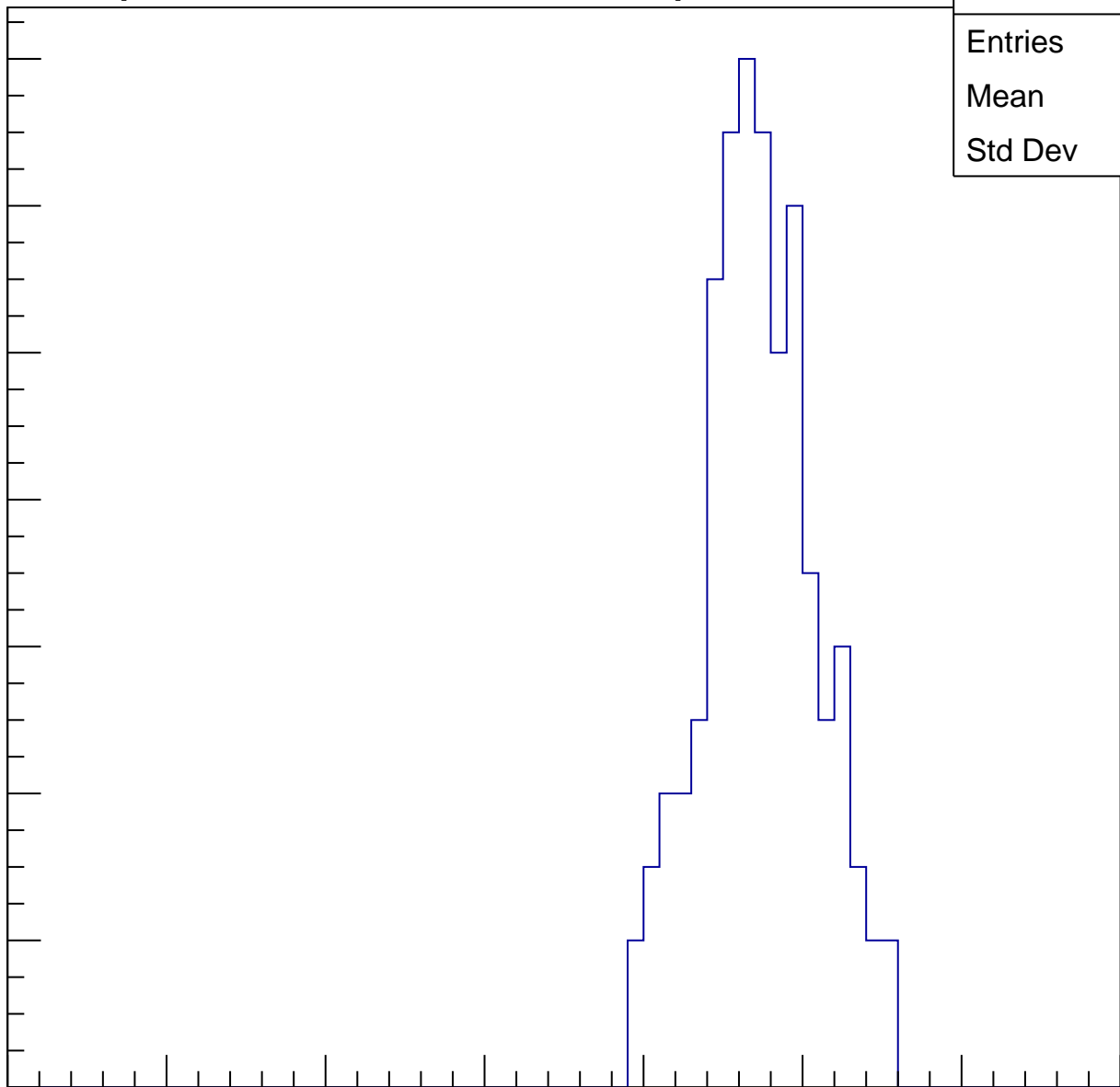
Entries	116
Mean	46.82
Std Dev	3.53

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch88, adc4

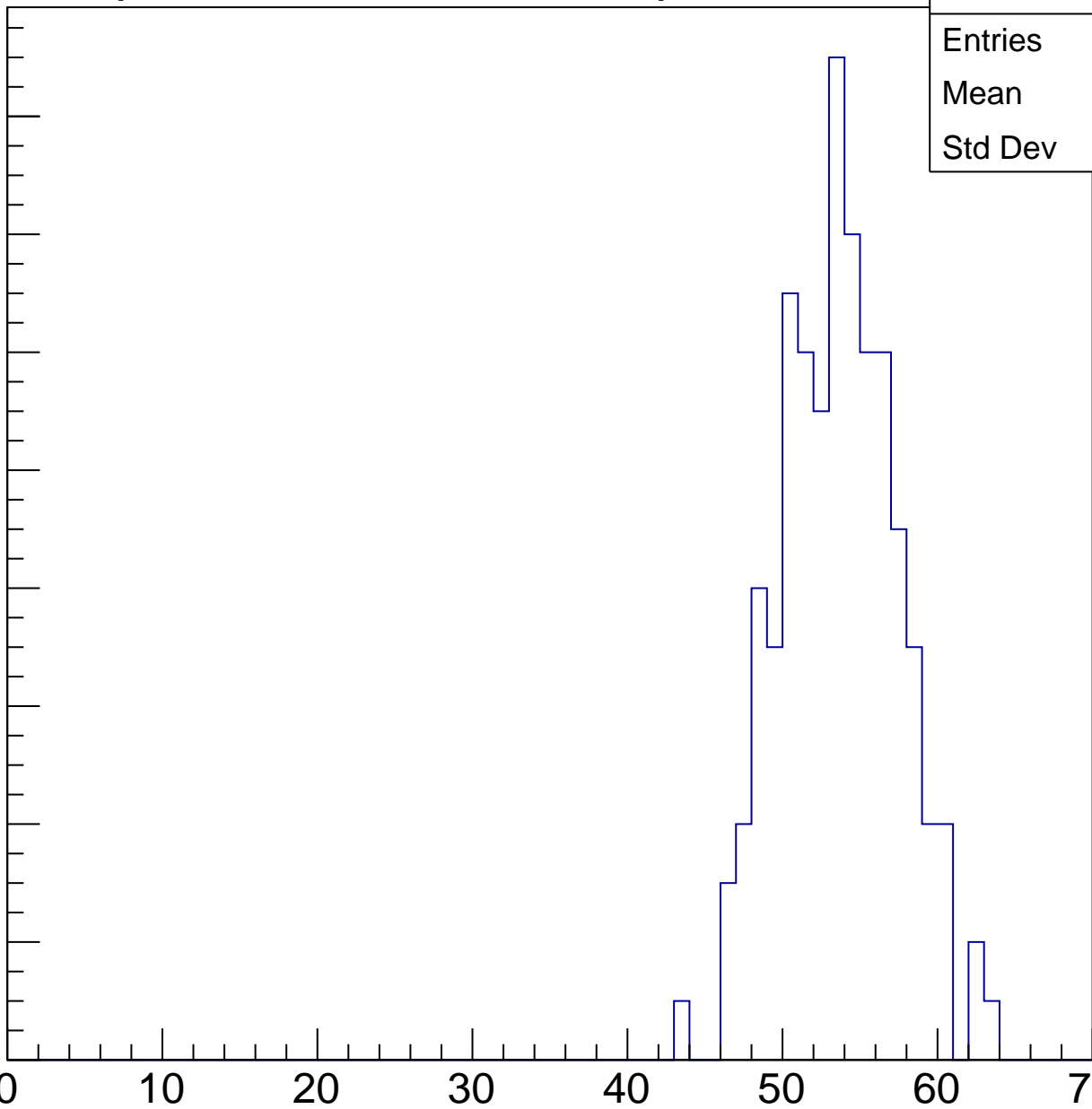
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	141
Mean	53.2
Std Dev	3.733

ampl



B0L101S, U15-ch88, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

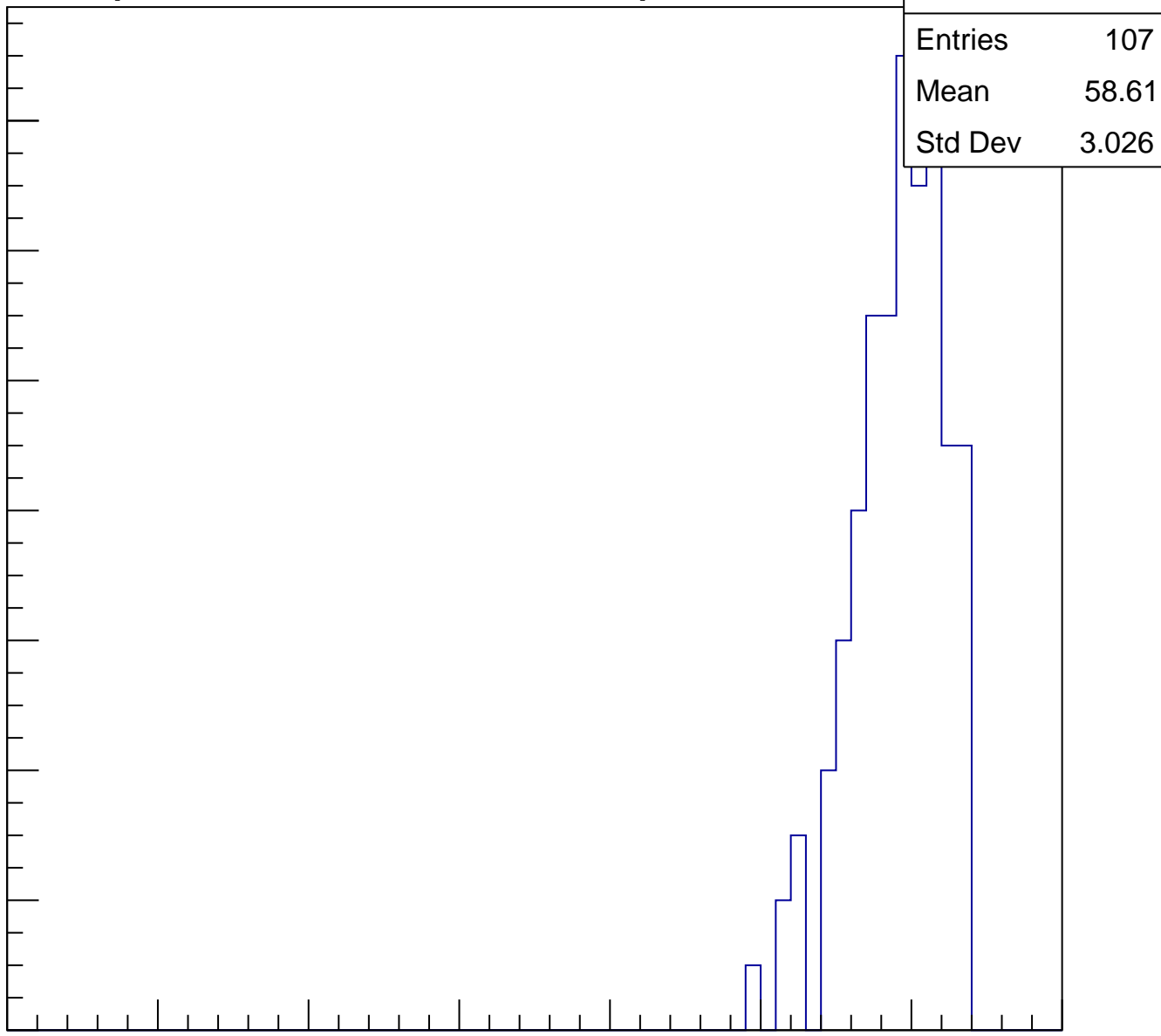
107

Mean

58.61

Std Dev

3.026

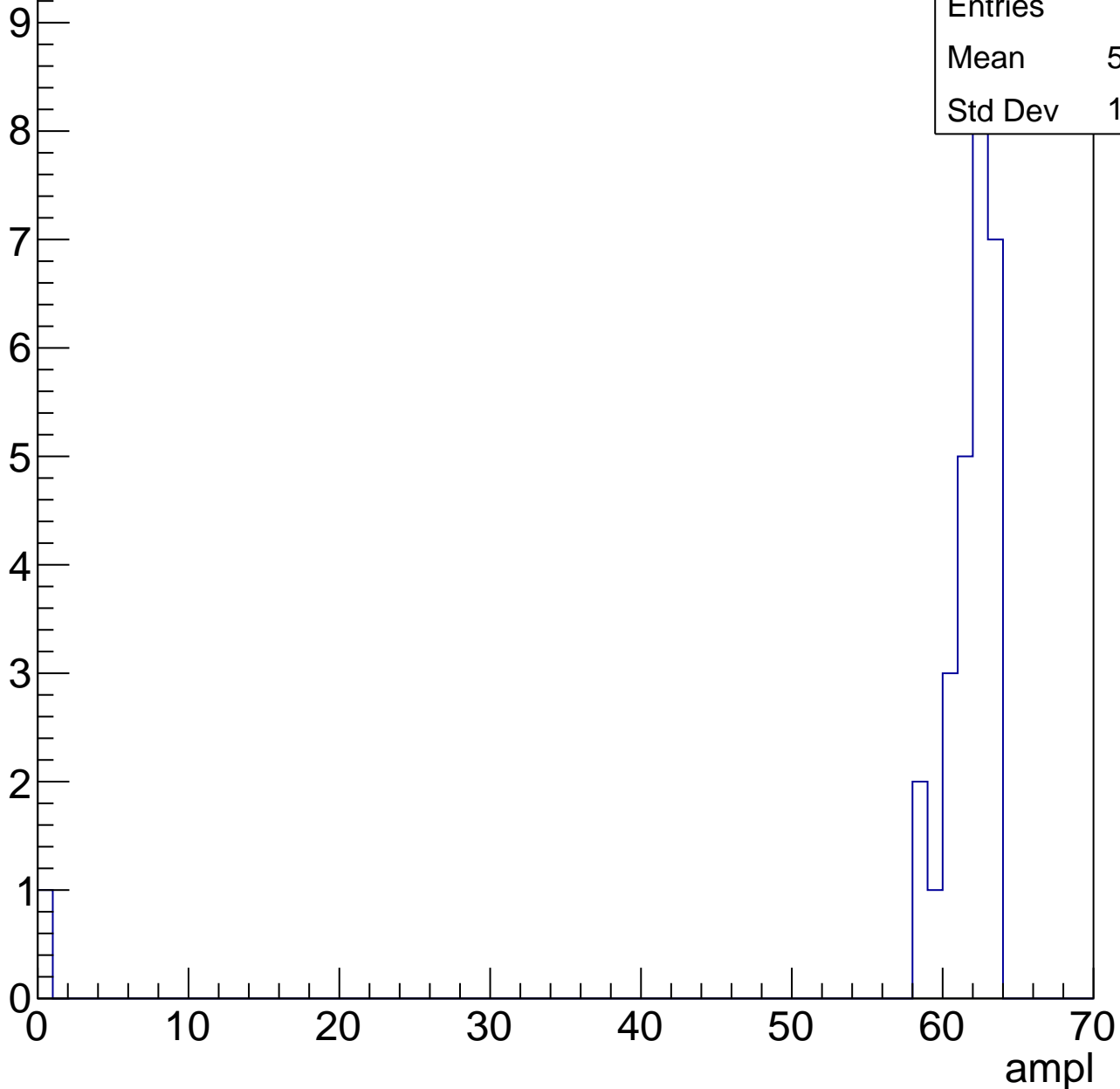


B0L101S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

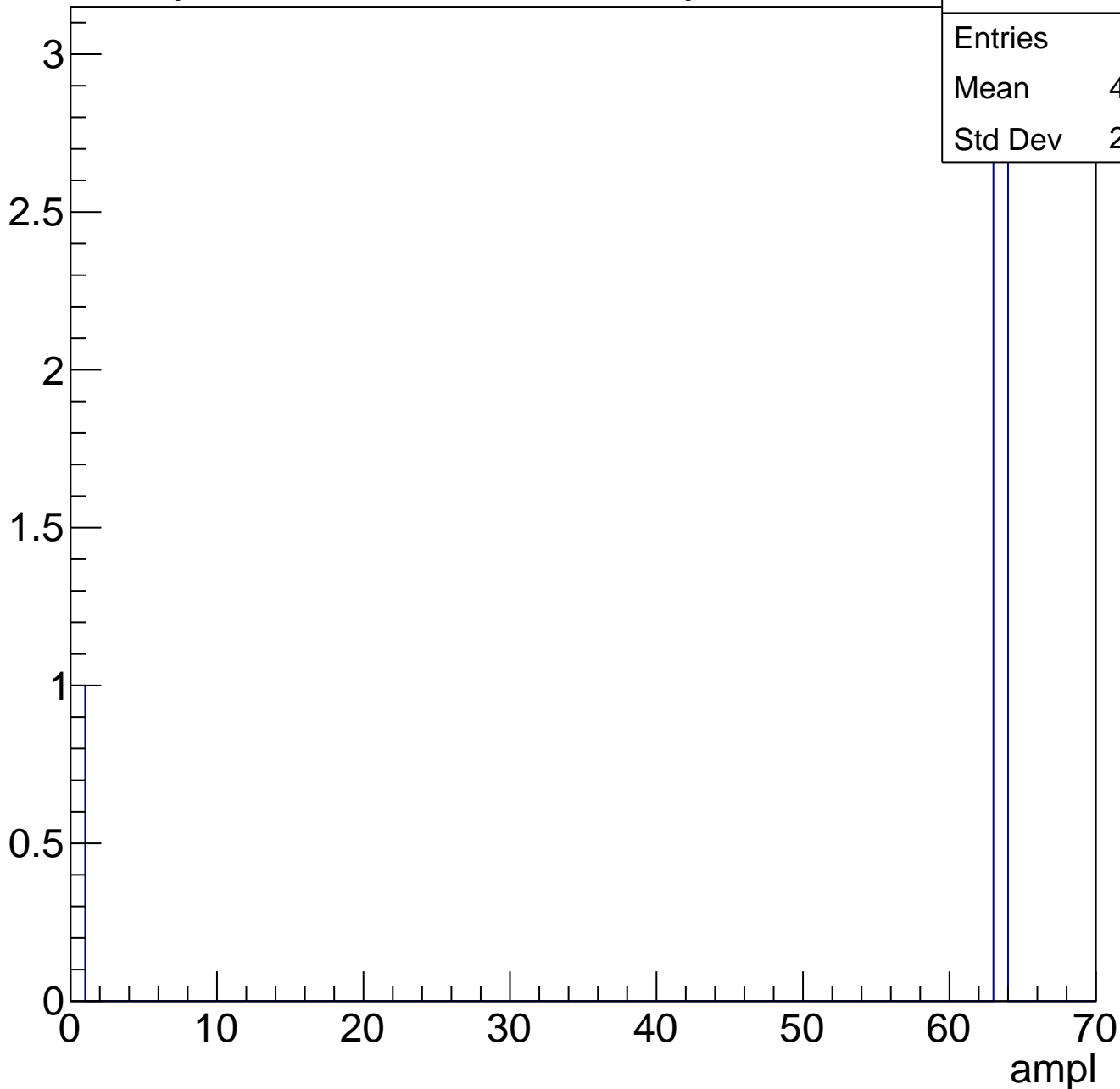
Entries	28
Mean	59.25
Std Dev	11.49



B0L101S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch89, adc0

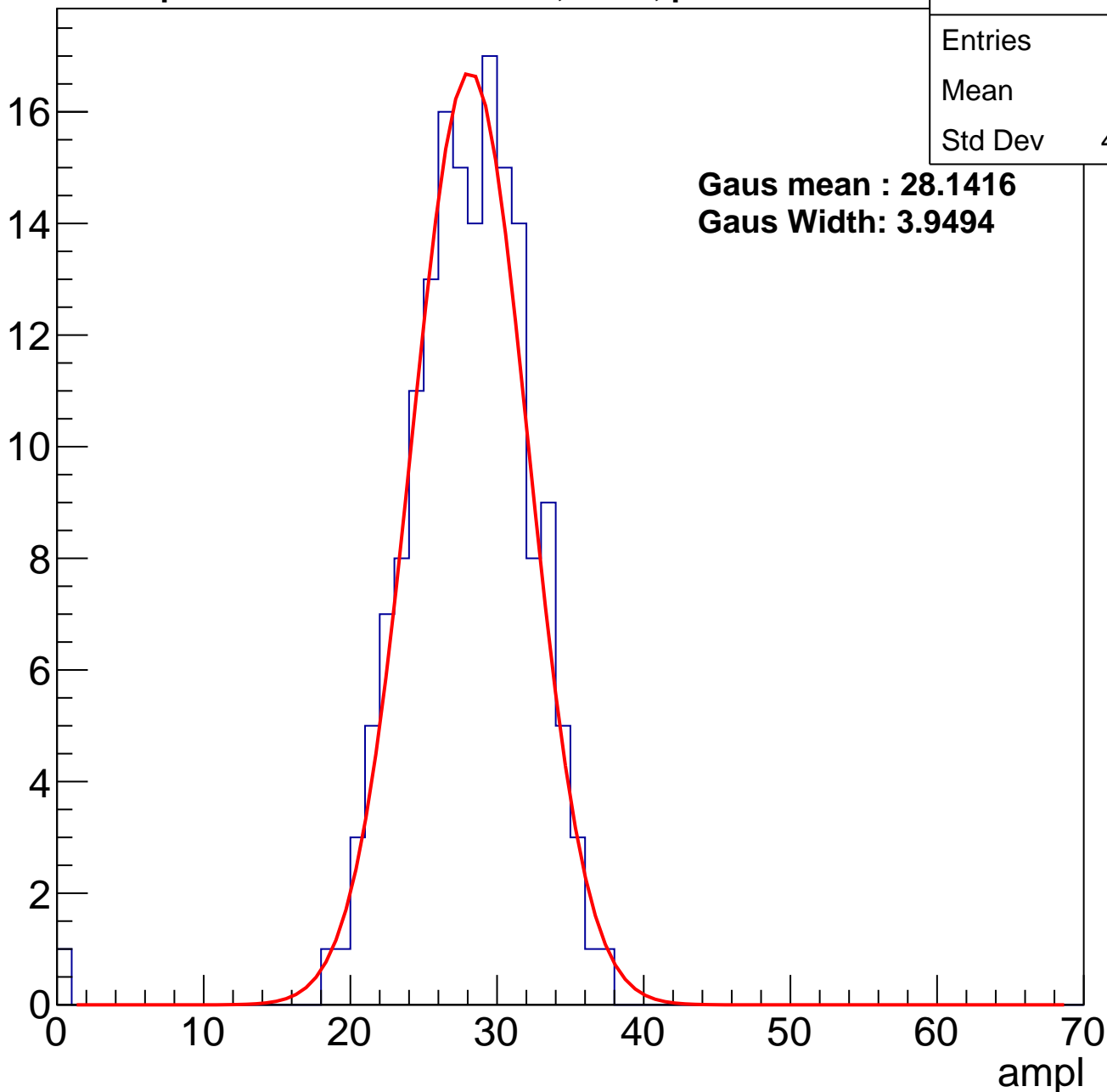
calib_packv5_042523_0143.root, FC#1, port C1

Entries	168
Mean	27.5
Std Dev	4.373

Gaus mean : 28.1416

Gaus Width: 3.9494

Entry

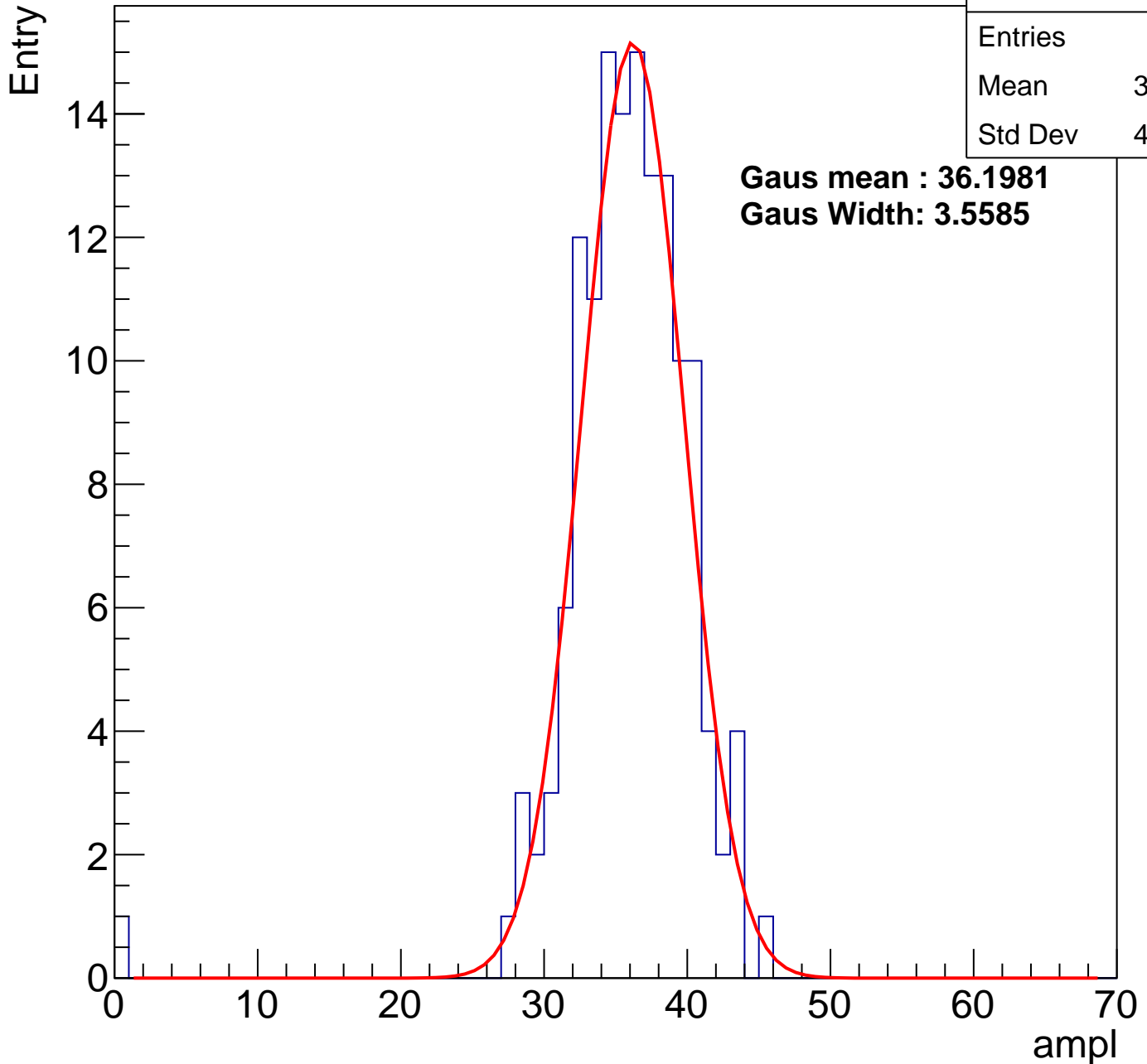


B0L101S, U15-ch89, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	35.44
Std Dev	4.637

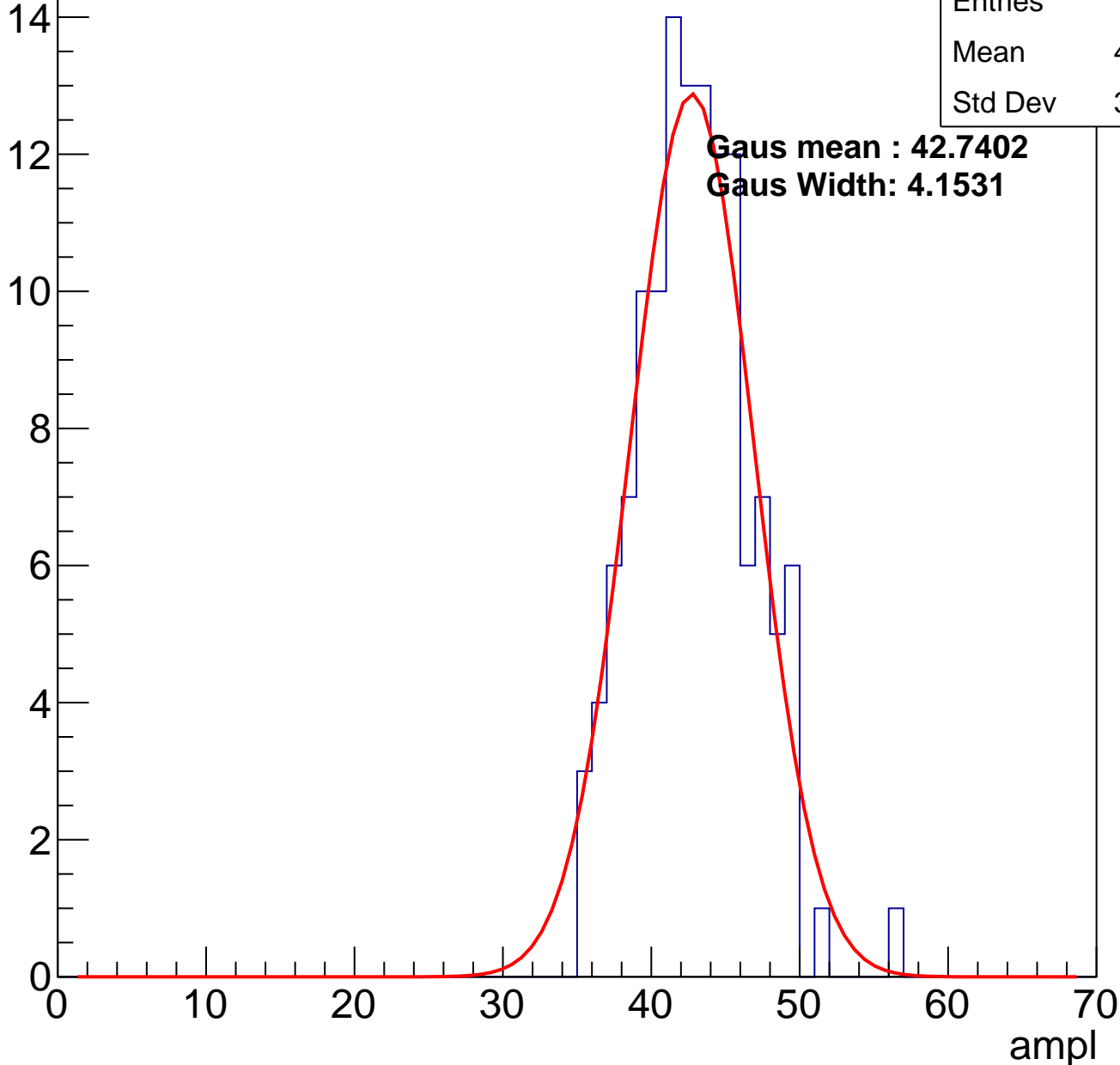
Gaus mean : 36.1981
Gaus Width: 3.5585



B0L101S, U15-ch89, adc2

calib_packv5_042523_0143.root, FC#1, port C1

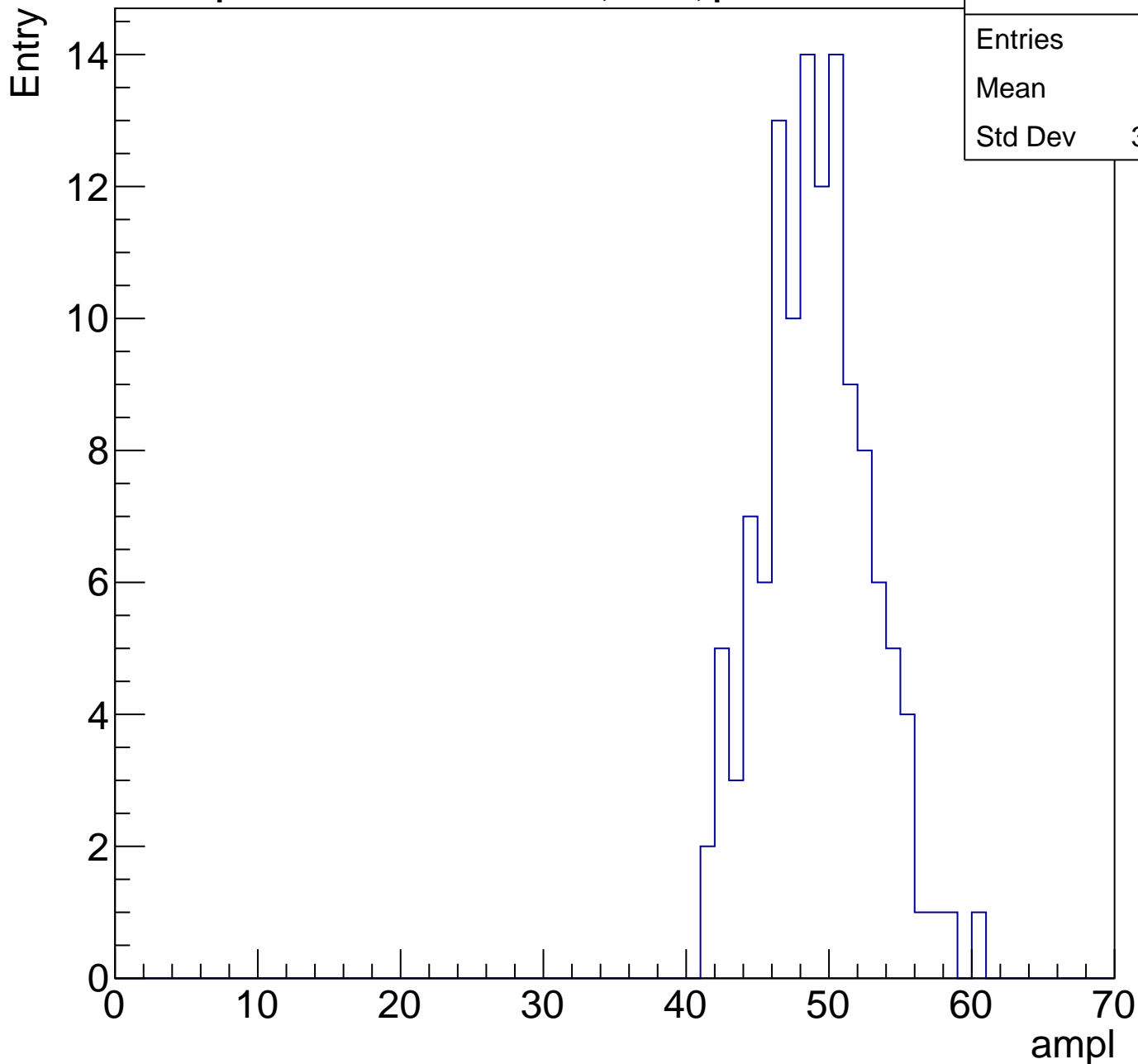
Entry



B0L101S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	48.7
Std Dev	3.748



B0L101S, U15-ch89, adc4

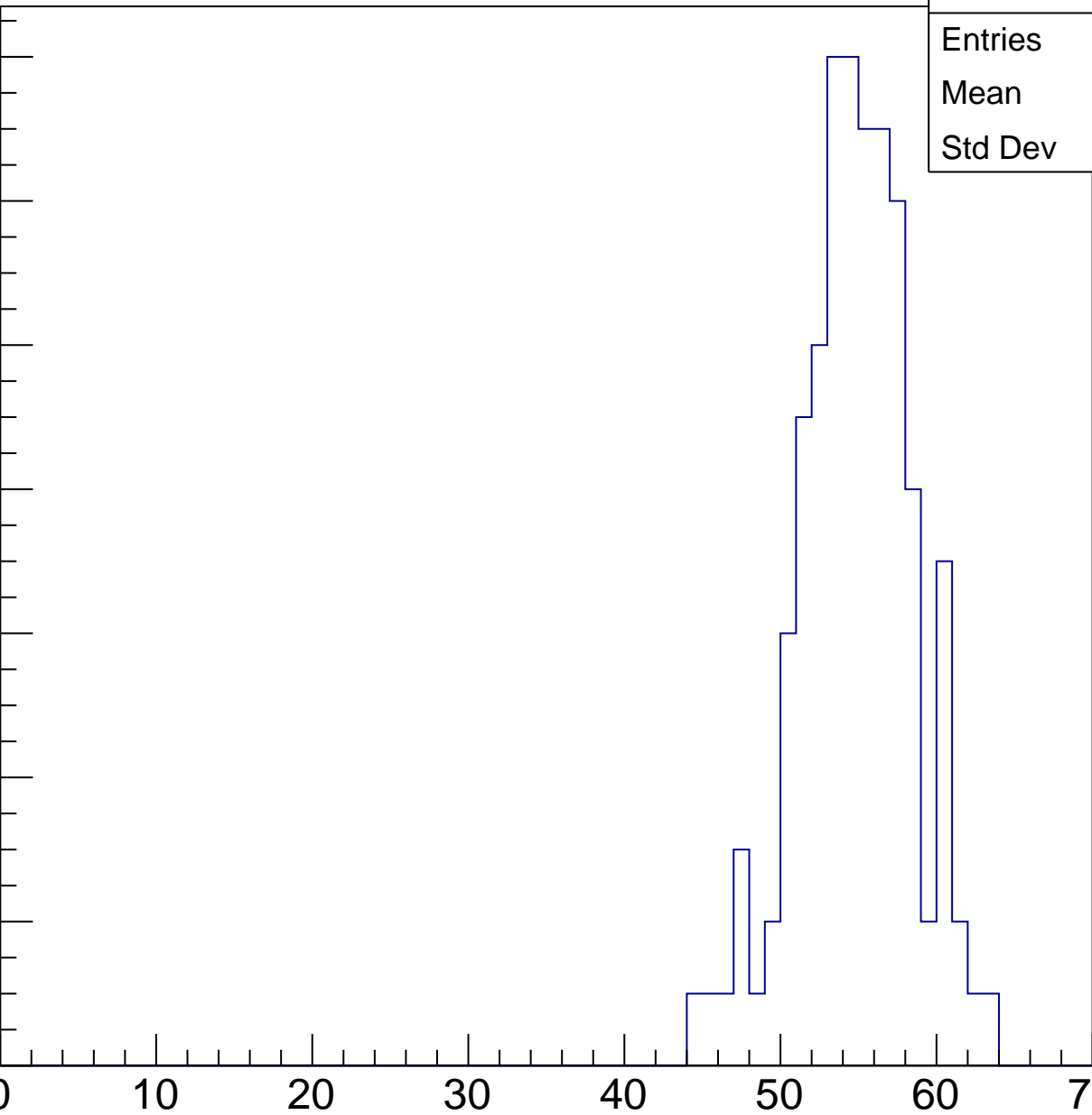
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	54.34
Std Dev	3.569

ampl



B0L101S, U15-ch89, adc5

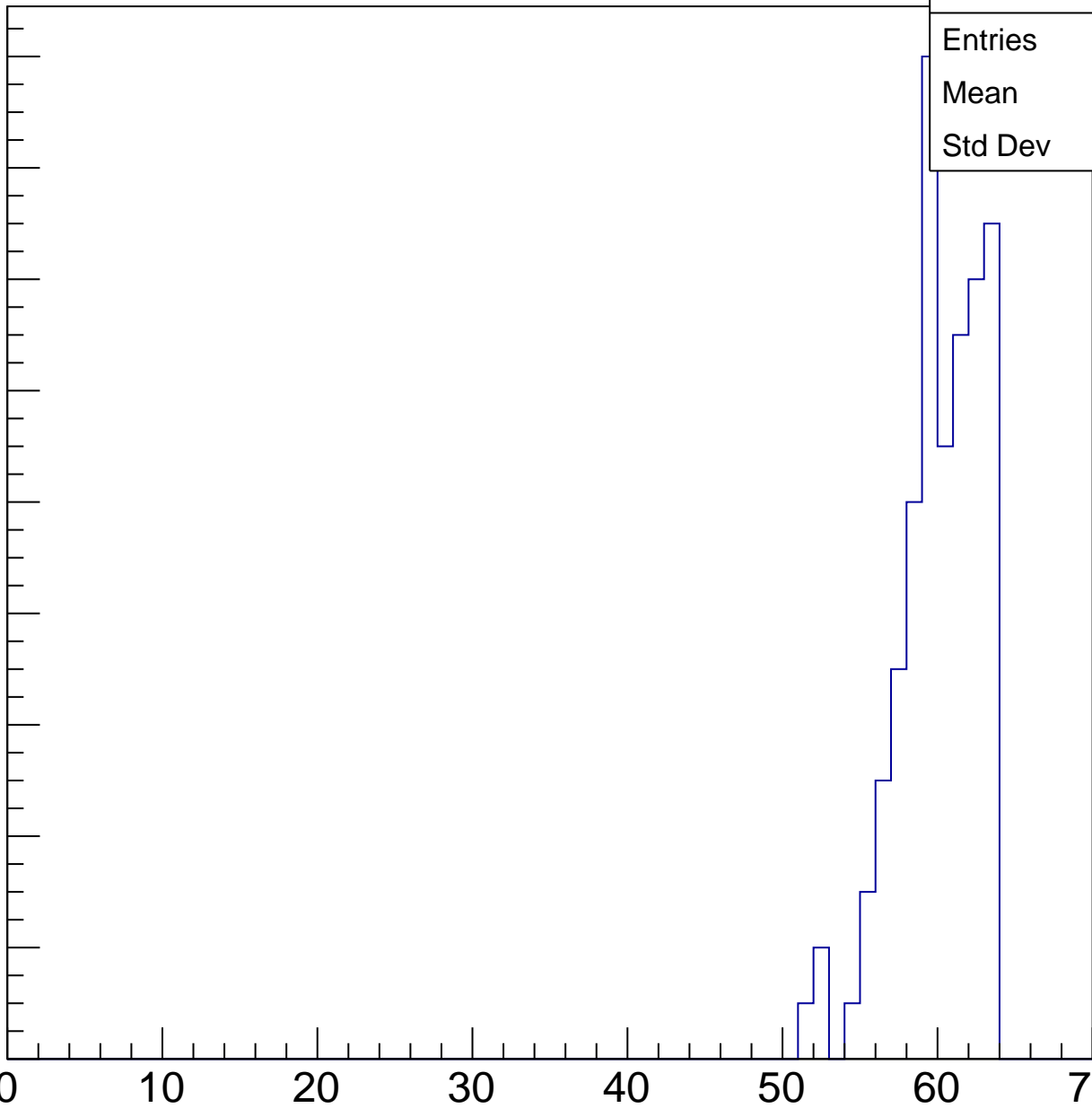
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.61
Std Dev	2.664

ampl

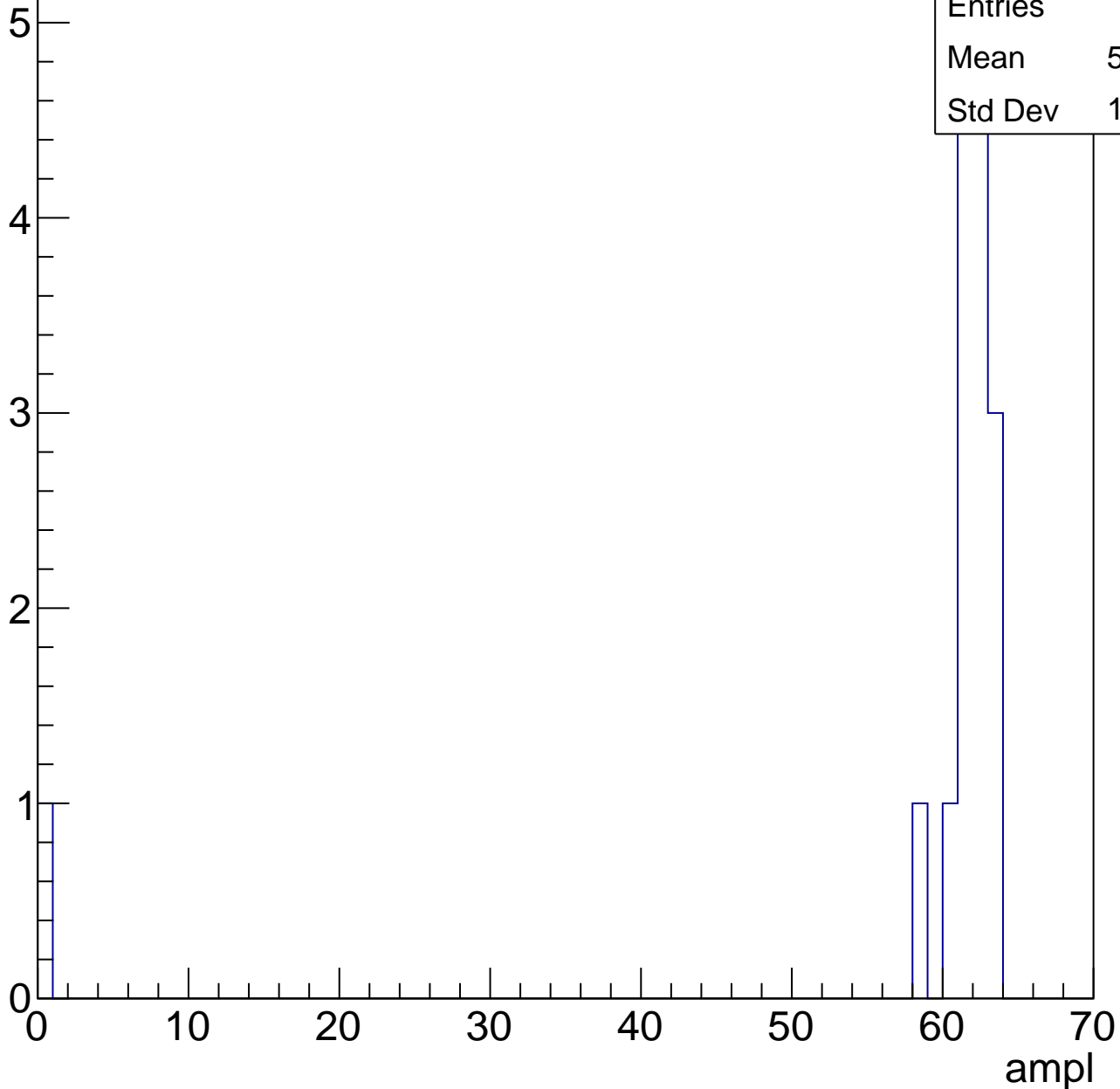


B0L101S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	57.62
Std Dev	14.93



B0L101S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



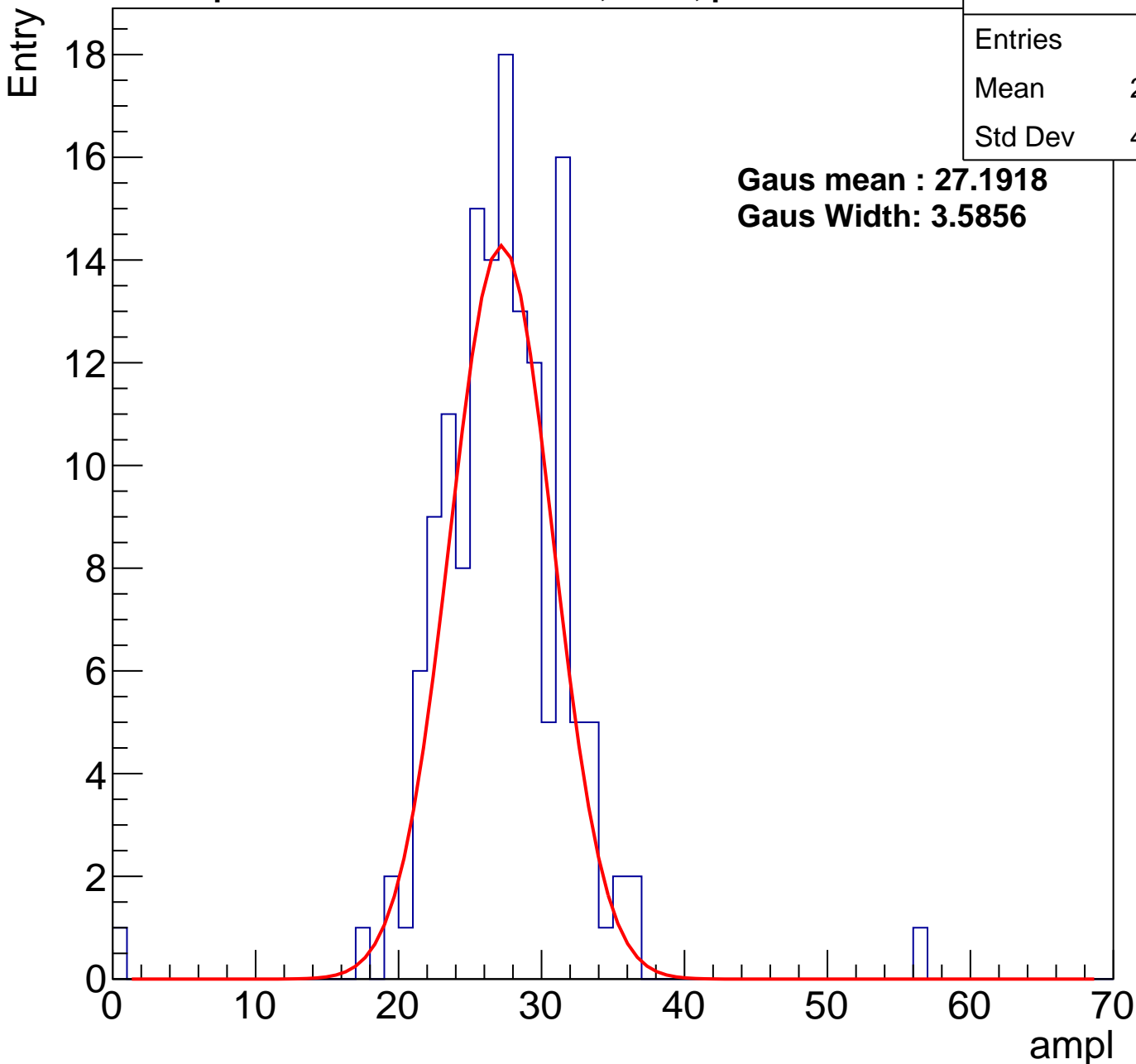
B0L101S, U15-ch90, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	148
Mean	26.92
Std Dev	4.942

Gaus mean : 27.1918

Gaus Width: 3.5856



B0L101S, U15-ch90, adc1

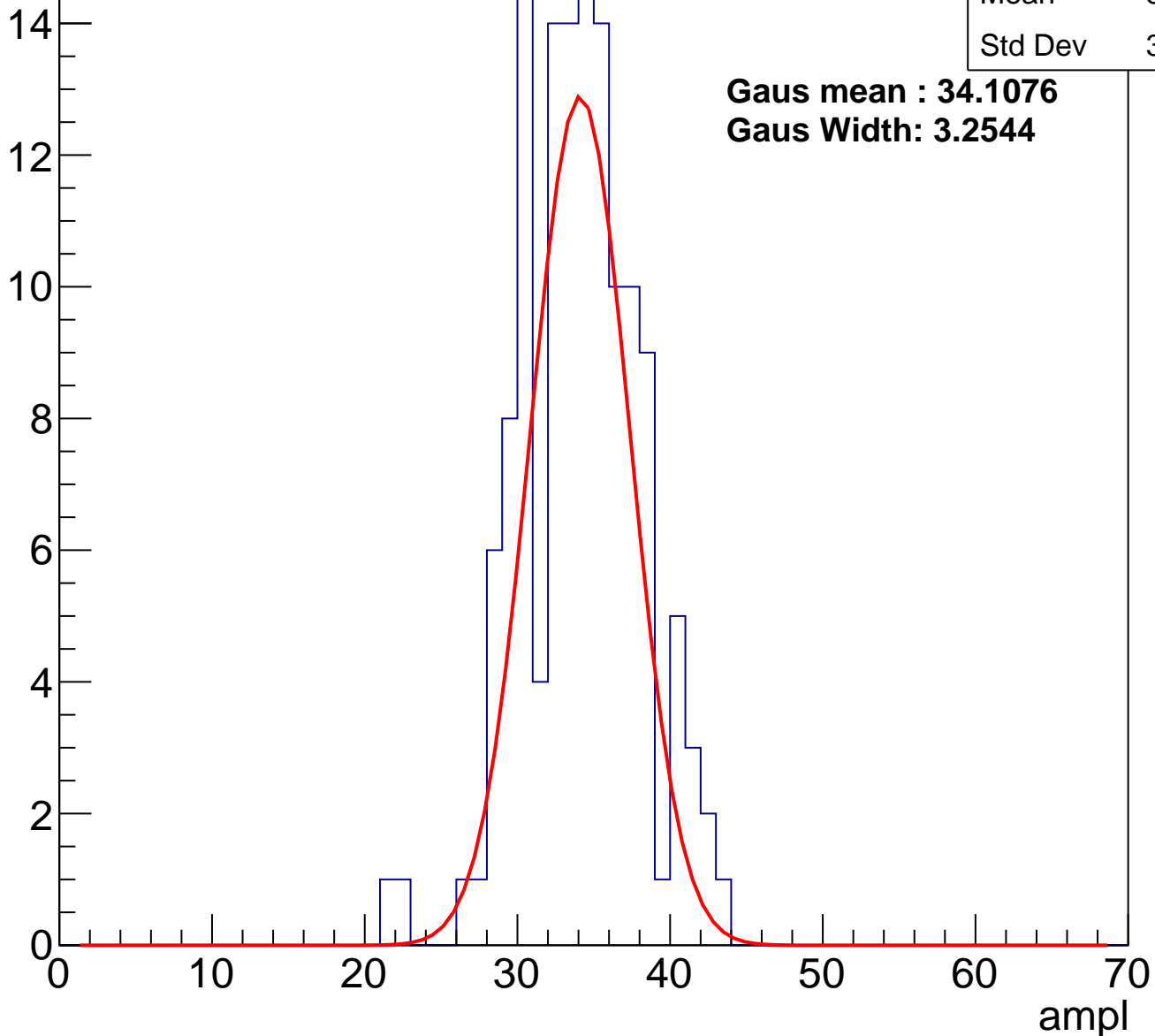
calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	33.64
Std Dev	3.867

Gaus mean : 34.1076

Gaus Width: 3.2544

Entry



B0L101S, U15-ch90, adc2

calib_packv5_042523_0143.root, FC#1, port C1

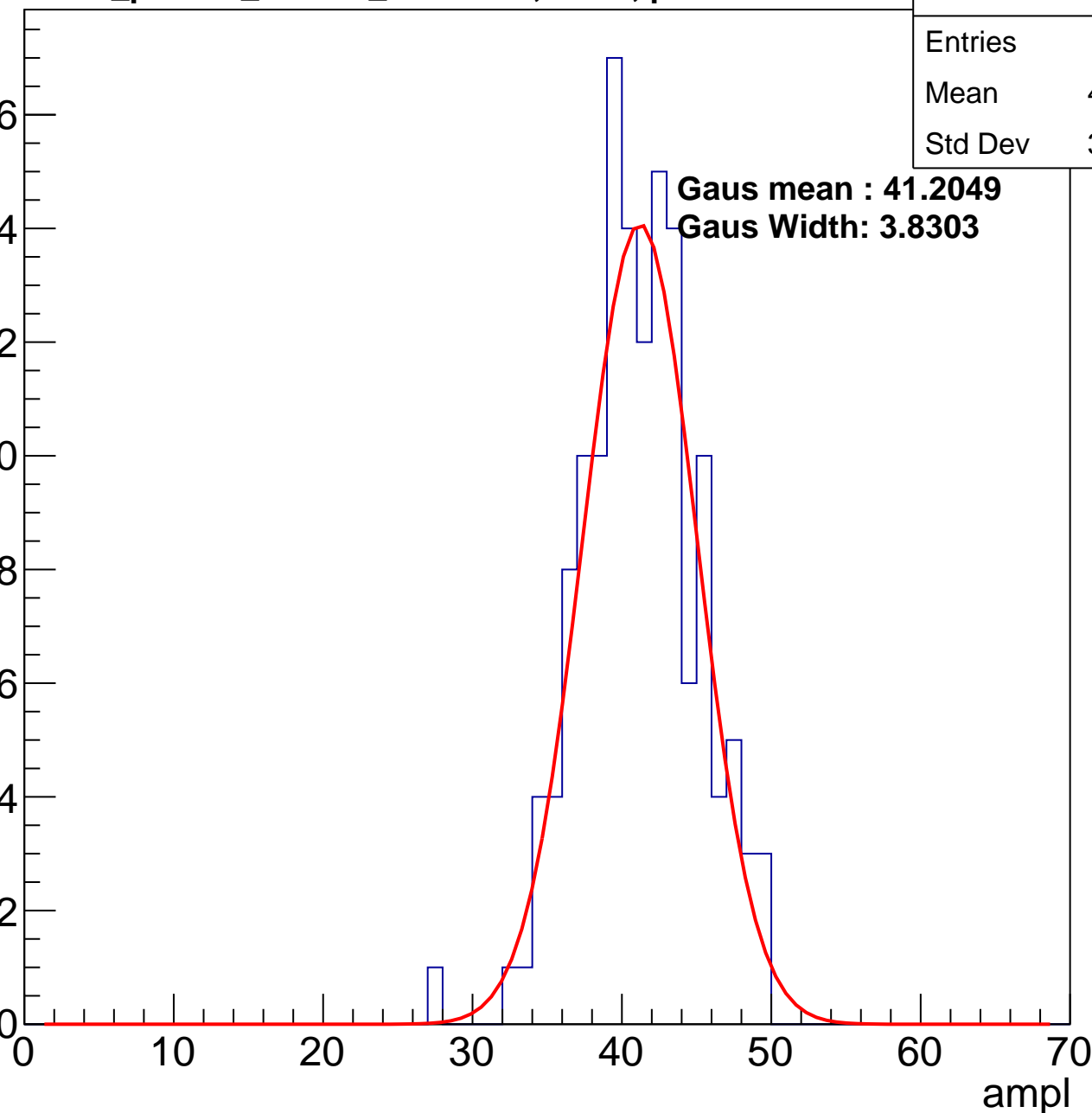
Entry

16
14
12
10
8
6
4
2
0

Entries	142
Mean	40.68
Std Dev	3.848

Gaus mean : 41.2049

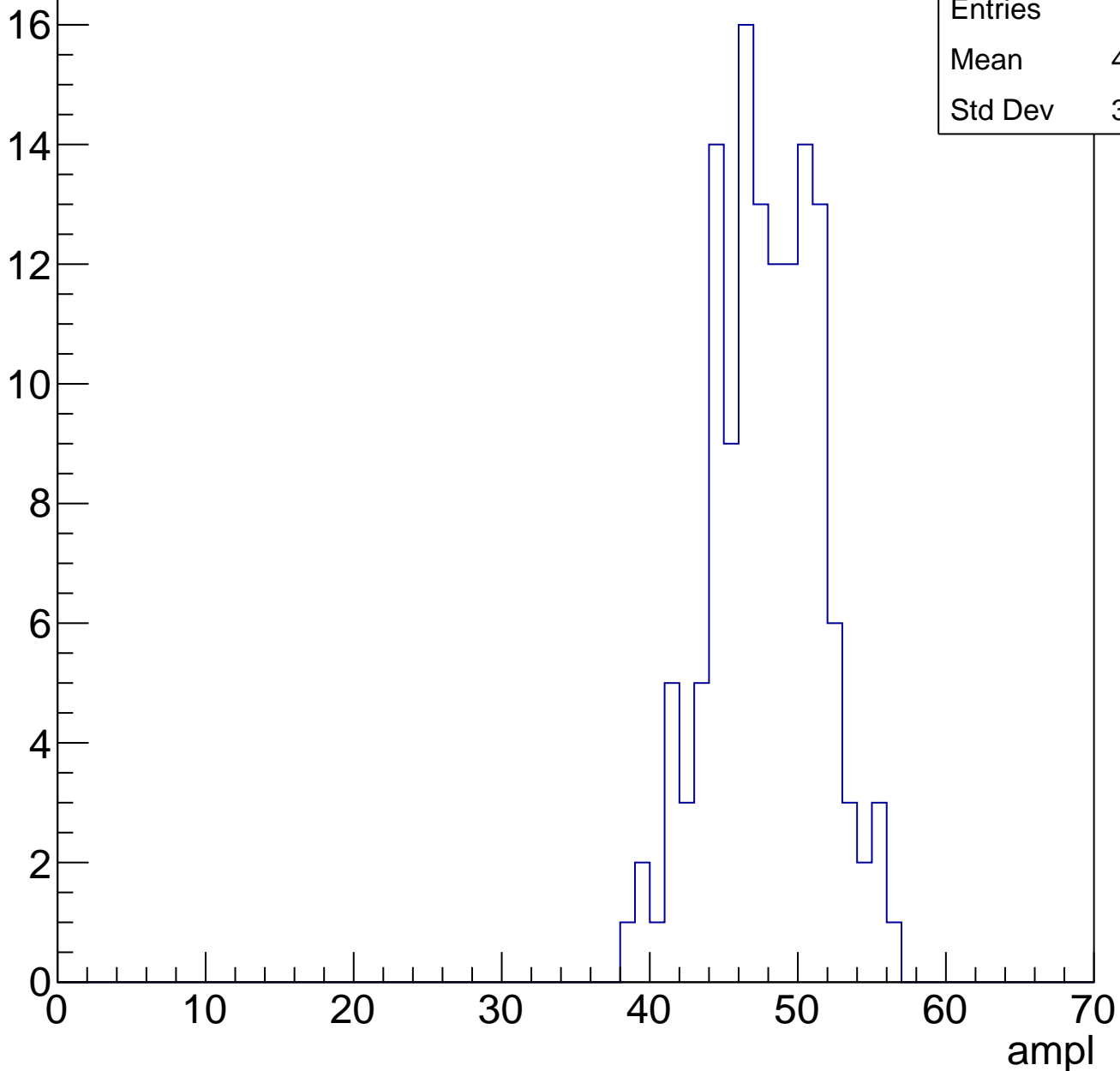
Gaus Width: 3.8303



B0L101S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

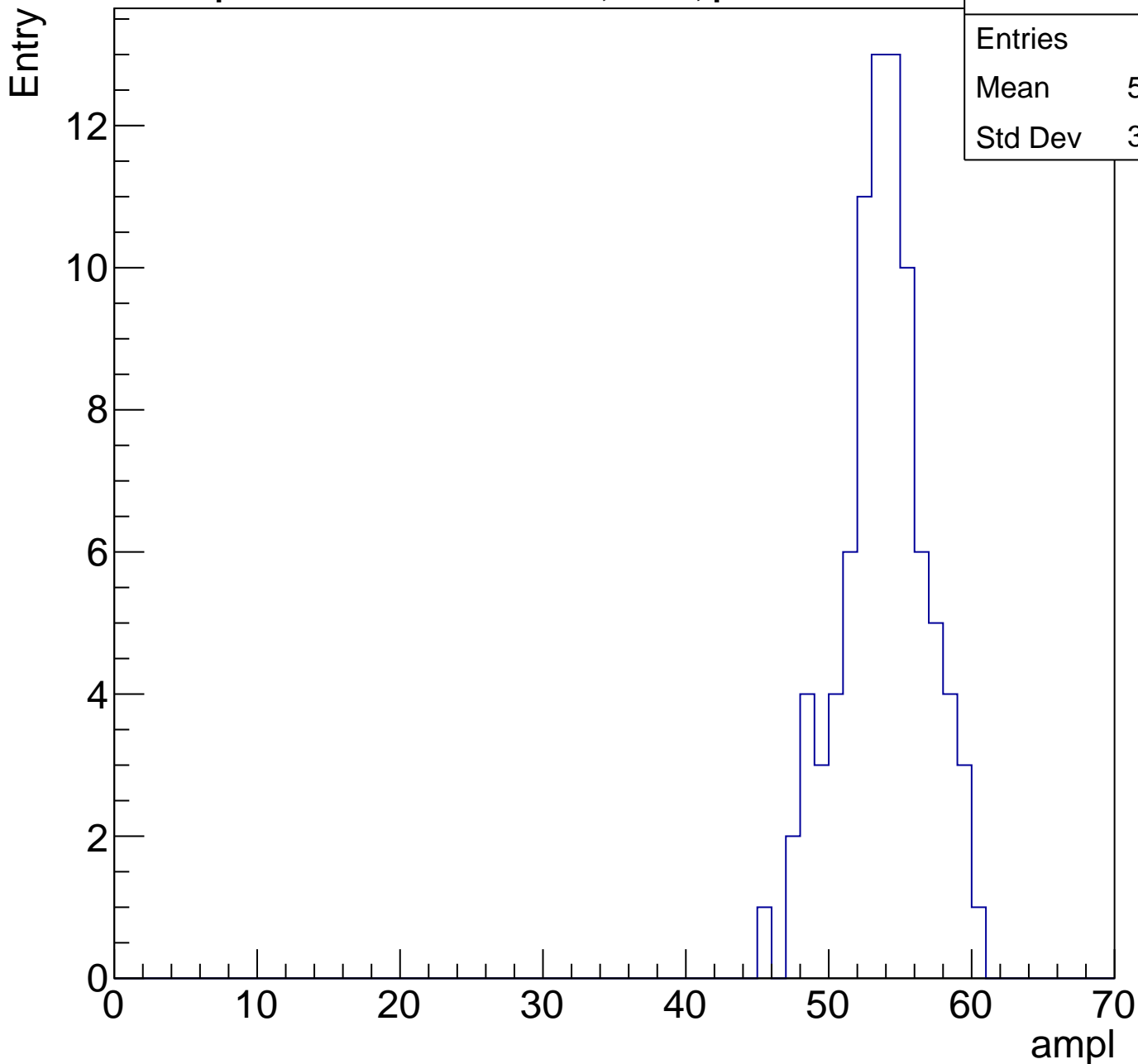


Entries	135
Mean	47.39
Std Dev	3.625

B0L101S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	86
Mean	53.34
Std Dev	3.029

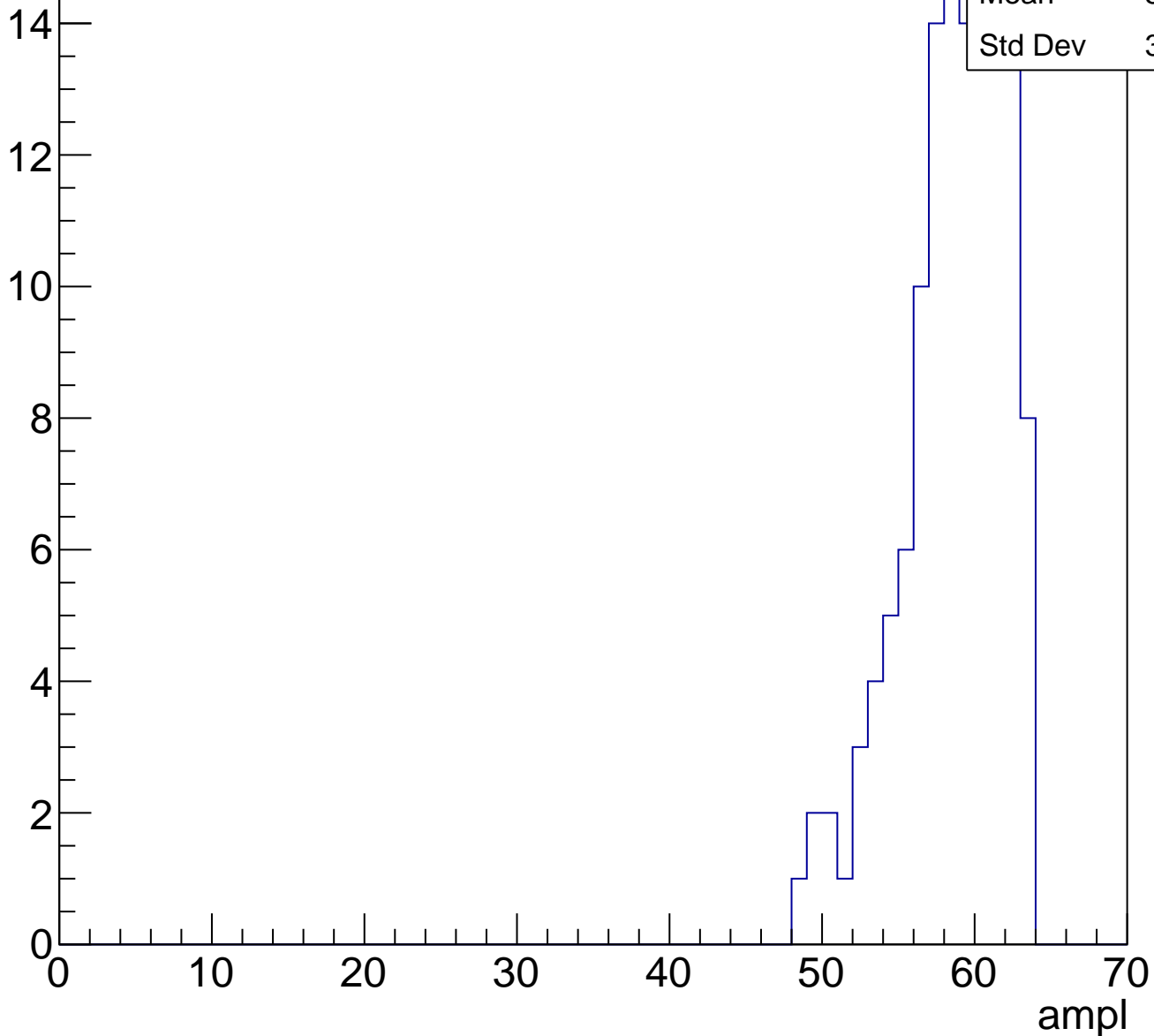


B0L101S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	58.18
Std Dev	3.385

Entry



B0L101S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

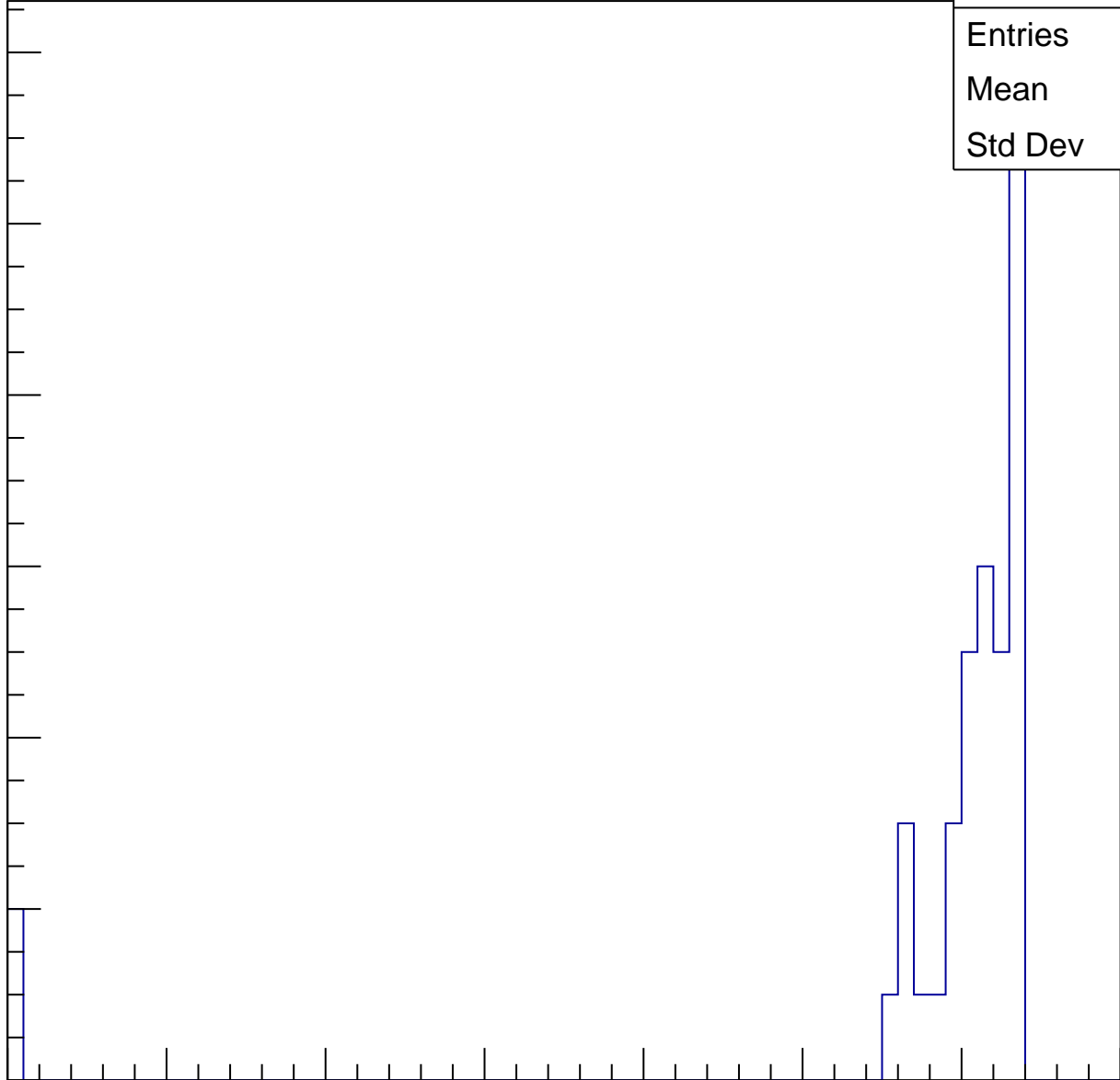
2

0

Entries	39
Mean	57.62
Std Dev	13.59

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



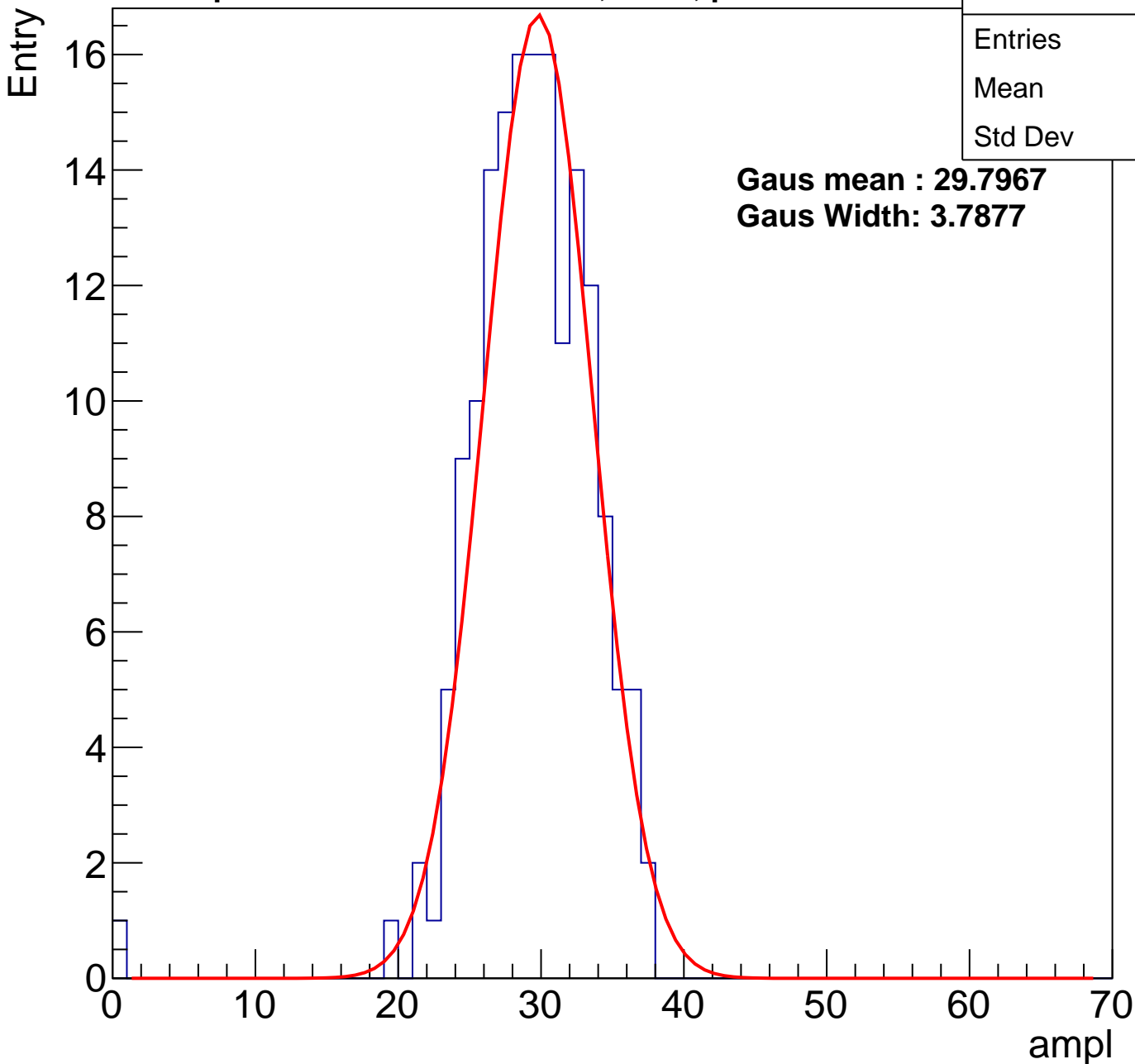
B0L101S, U15-ch91, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	163
Mean	28.9
Std Dev	4.3

Gaus mean : 29.7967

Gaus Width: 3.7877



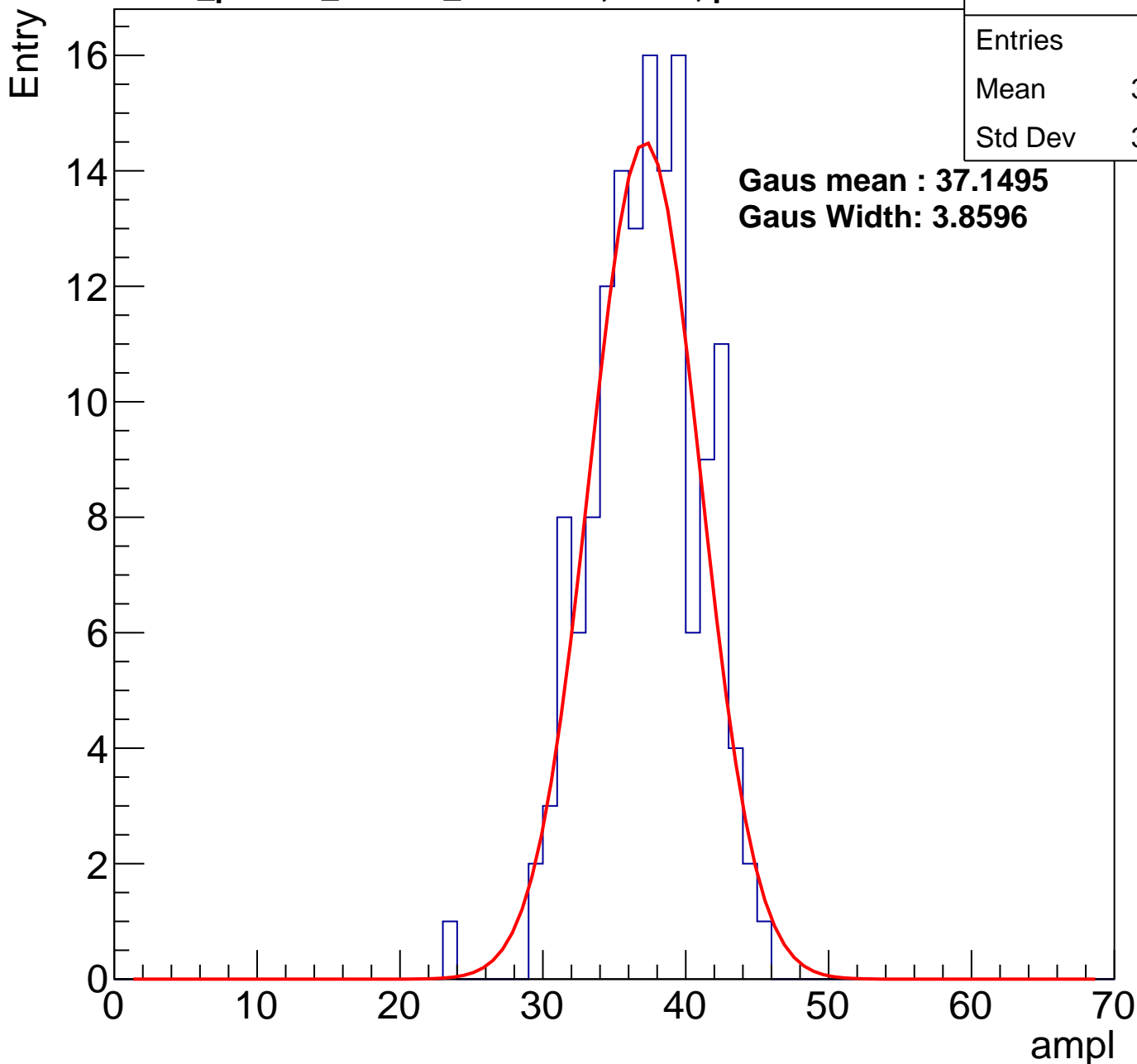
B0L101S, U15-ch91, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	36.75
Std Dev	3.758

Gaus mean : 37.1495

Gaus Width: 3.8596



B0L101S, U15-ch91, adc2

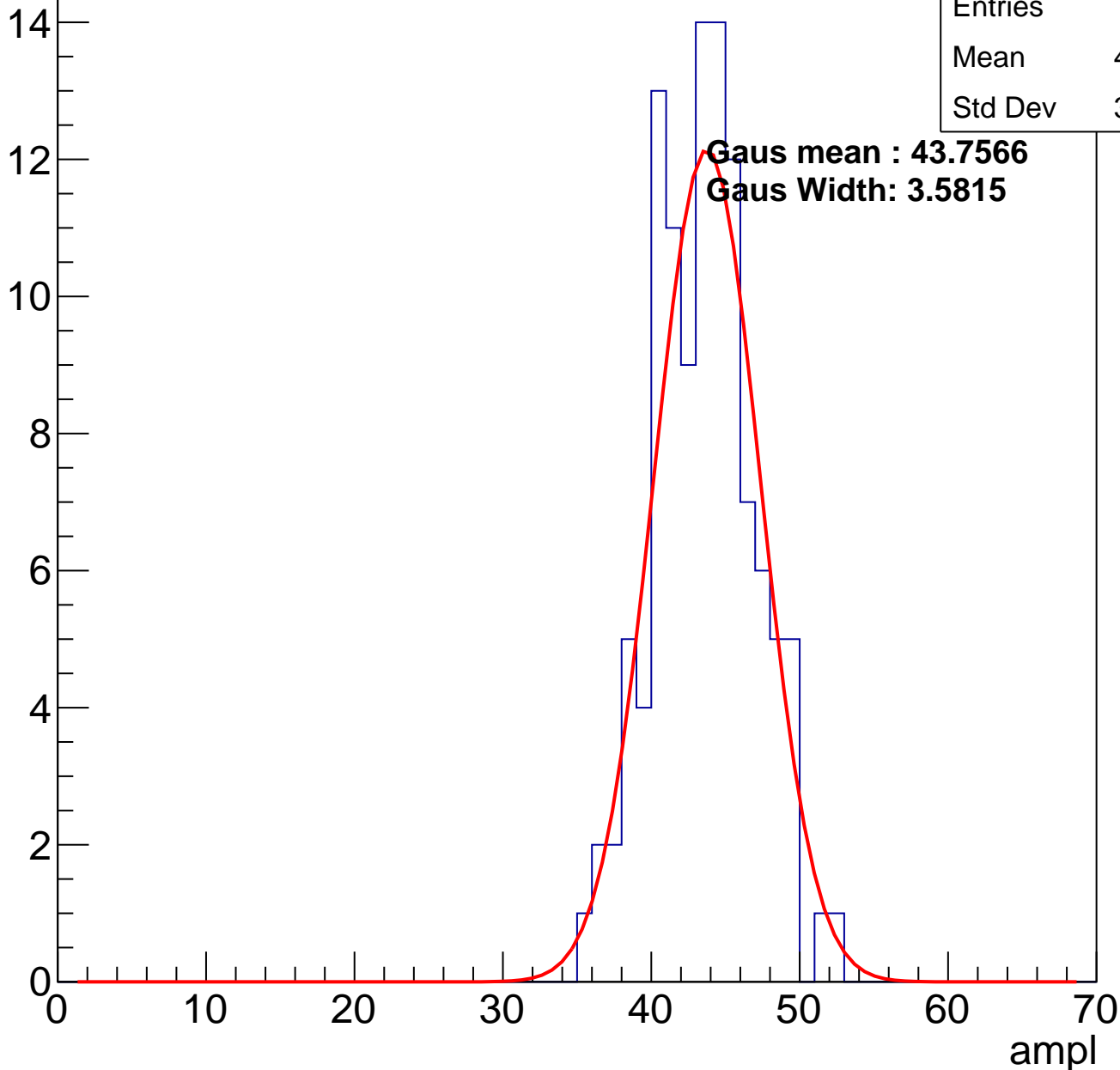
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	112
Mean	43.09
Std Dev	3.358

Gaus mean : 43.7566

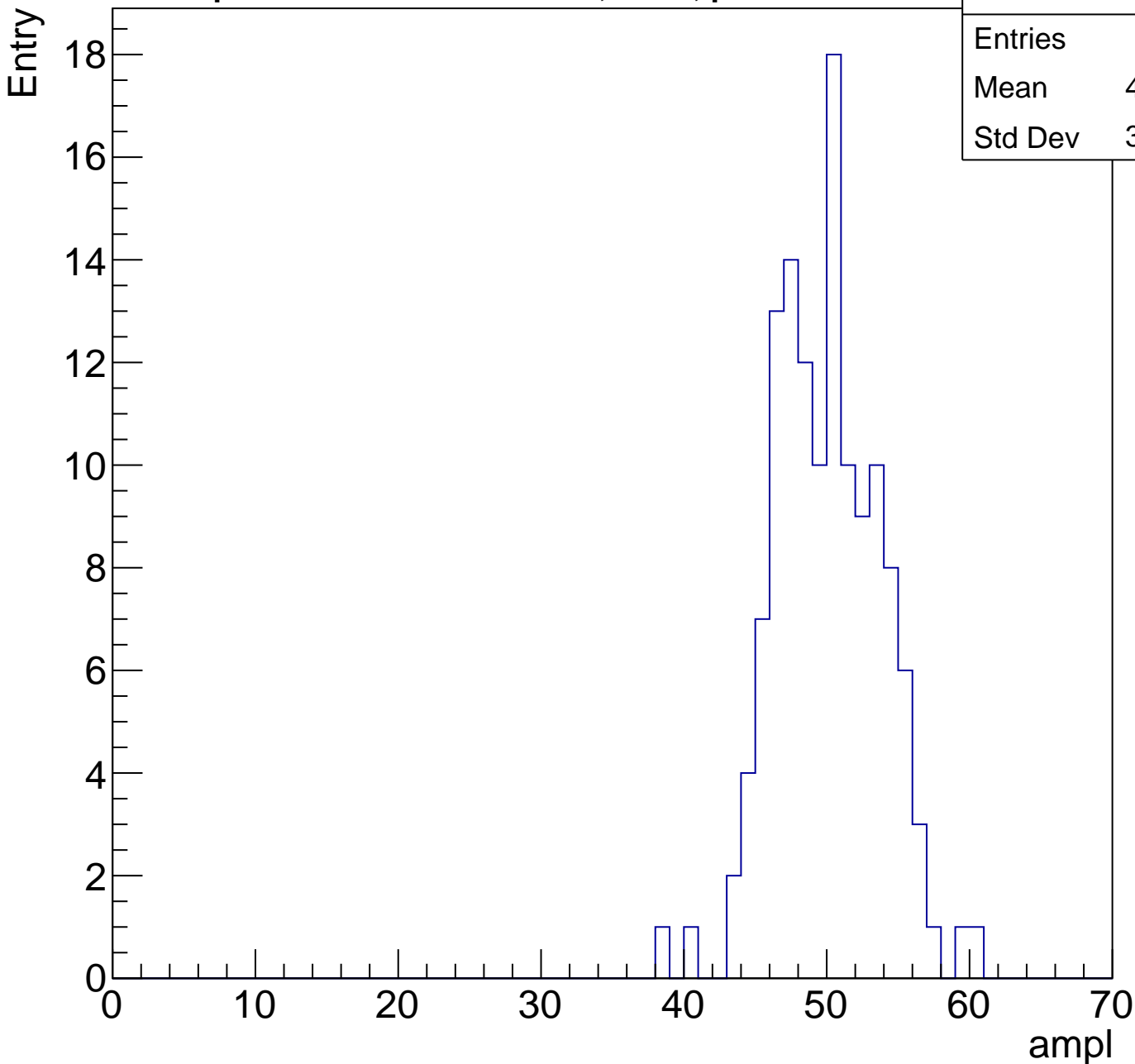
Gaus Width: 3.5815



B0L101S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#1, port C1

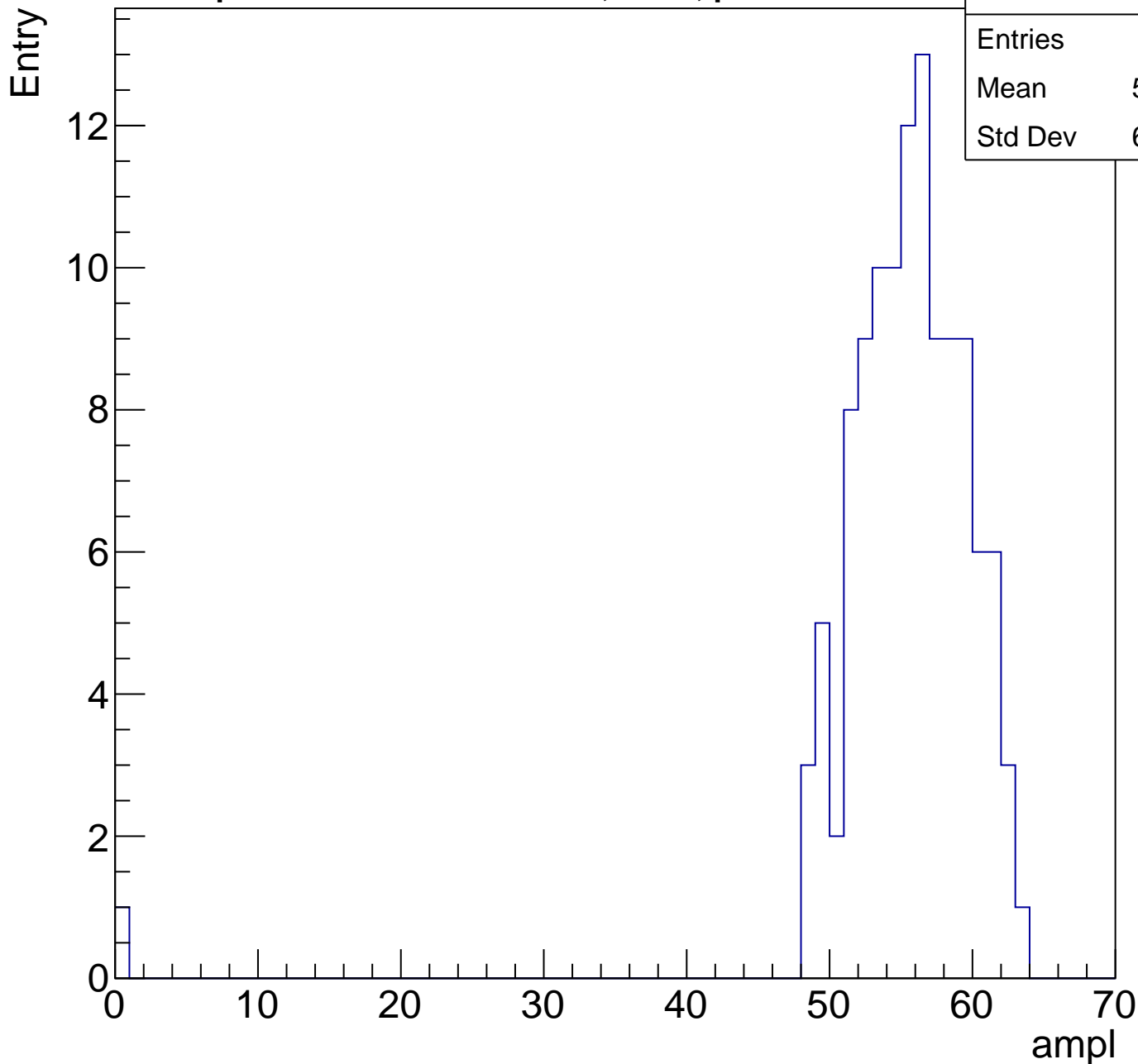
Entries	131
Mean	49.55
Std Dev	3.685



B0L101S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	54.86
Std Dev	6.235



B0L101S, U15-ch91, adc5

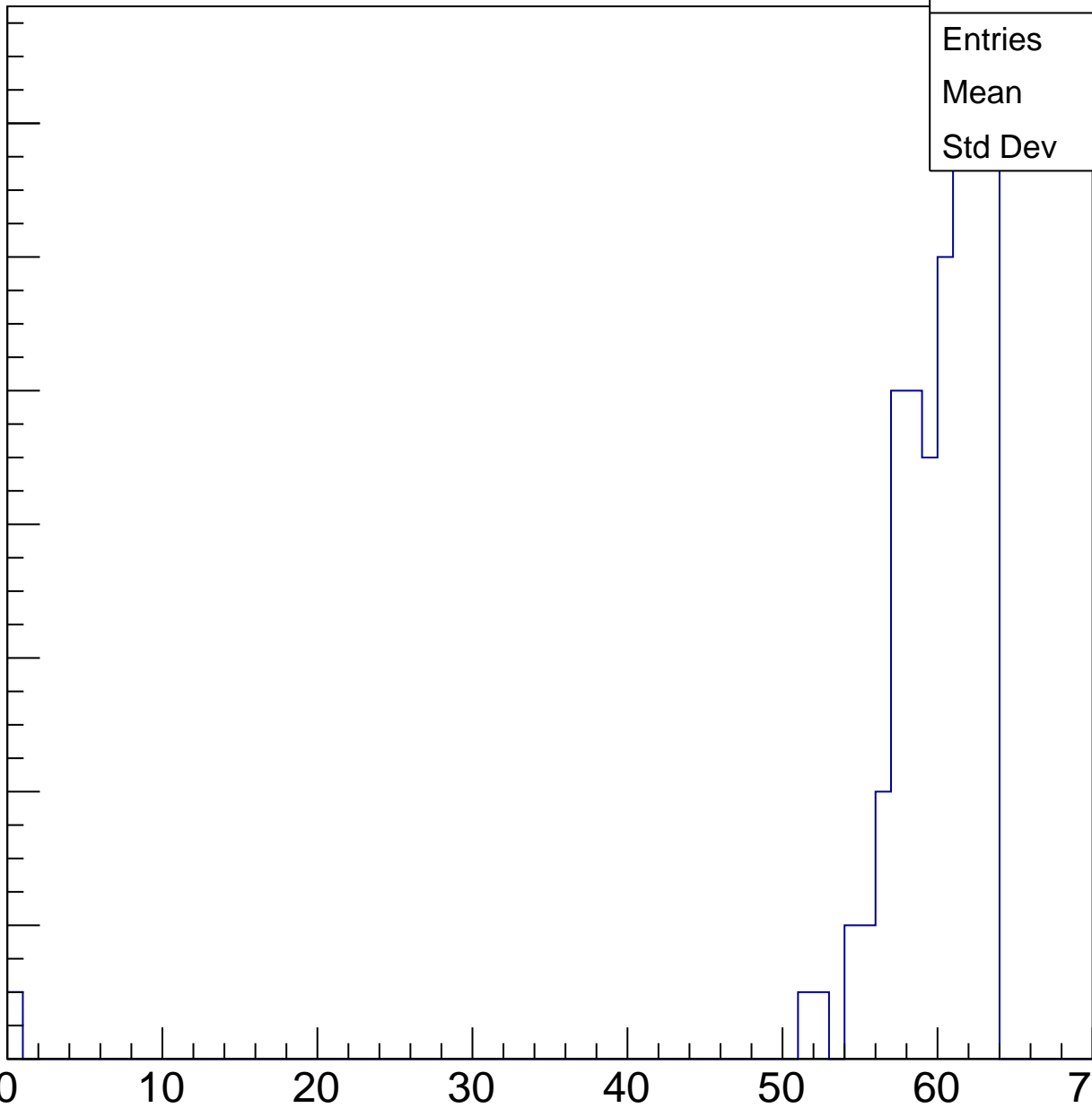
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	59.07
Std Dev	6.64

ampl

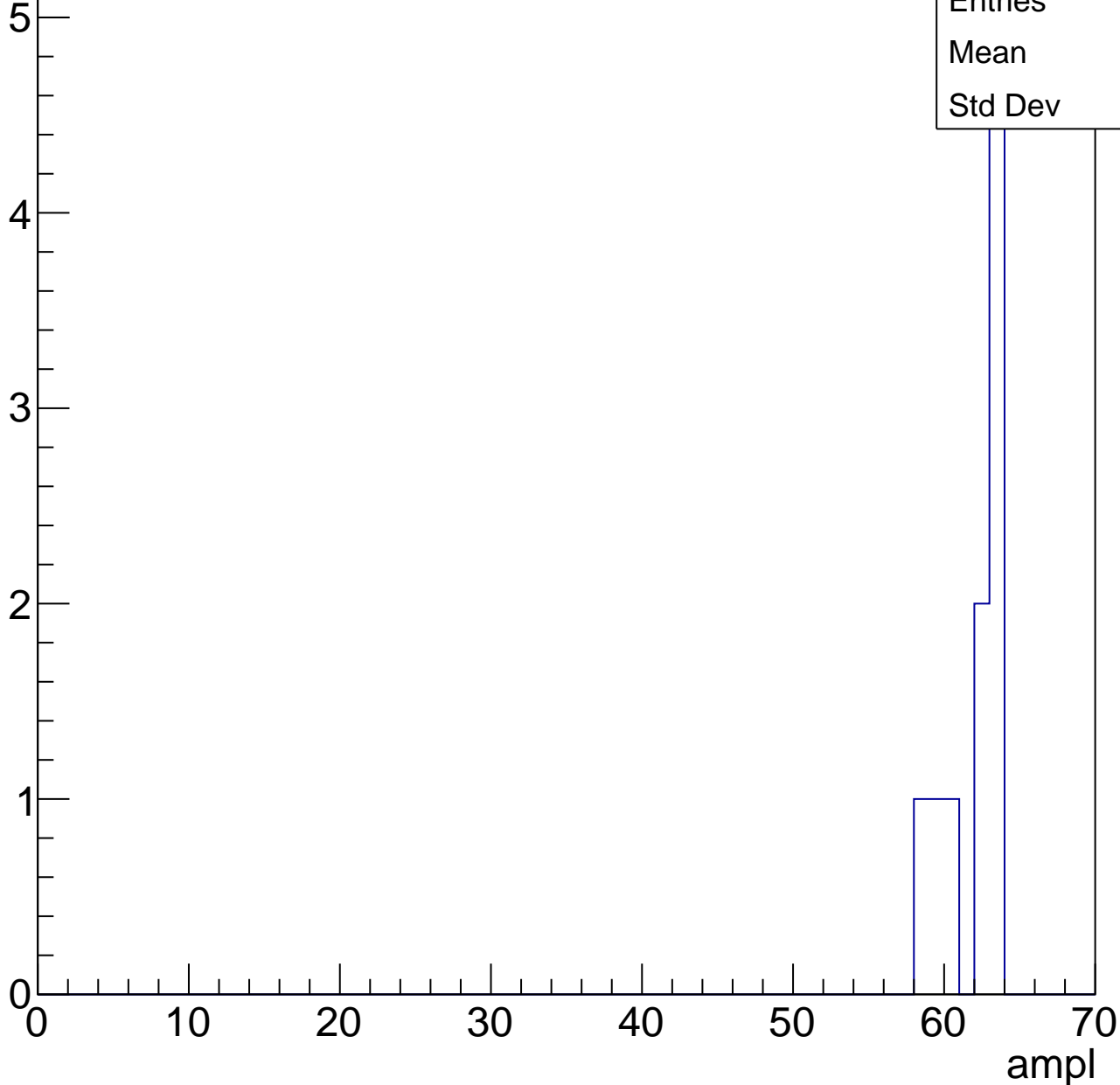


B0L101S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

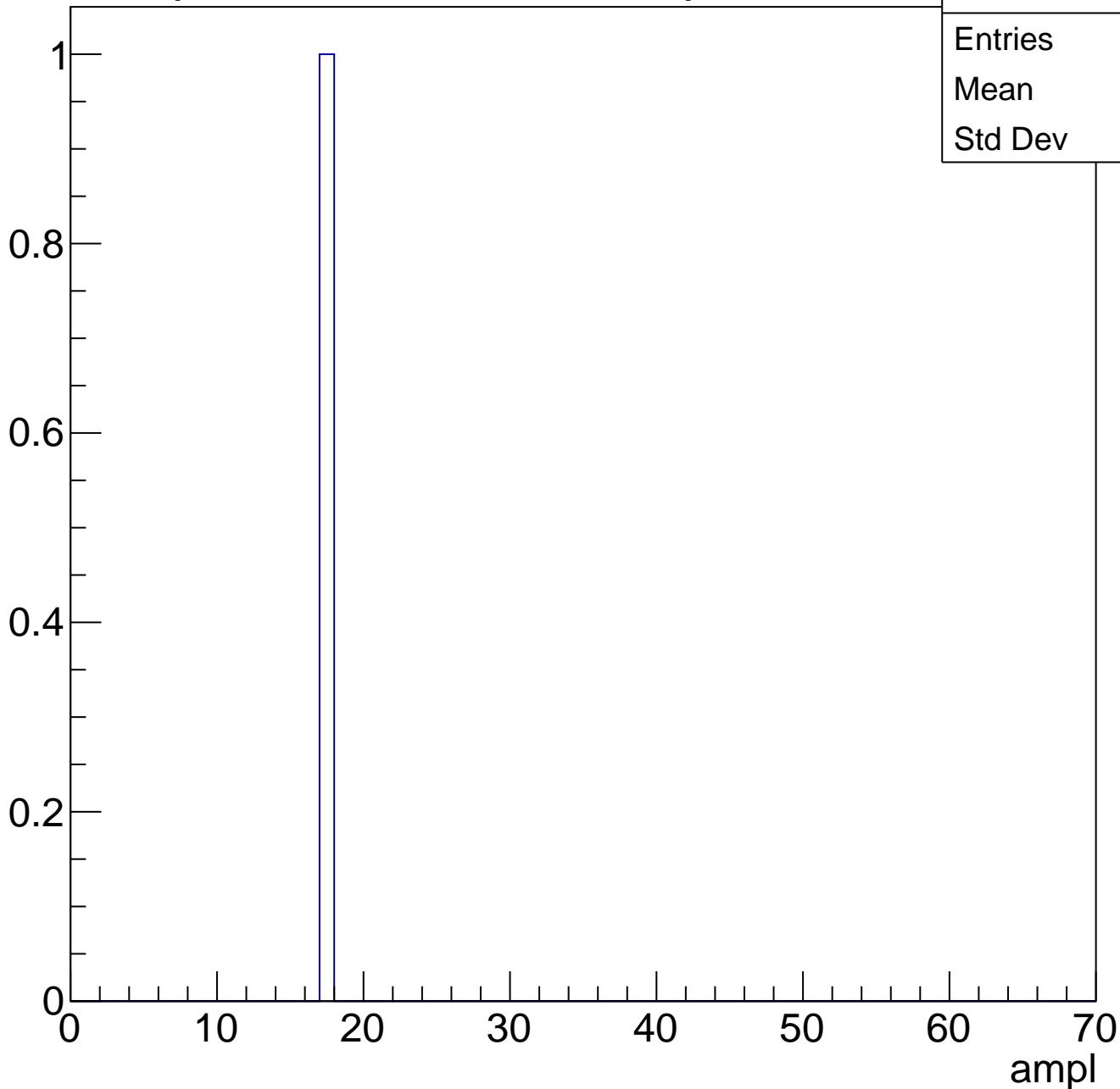
Entries	10
Mean	61.6
Std Dev	1.8



B0L101S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



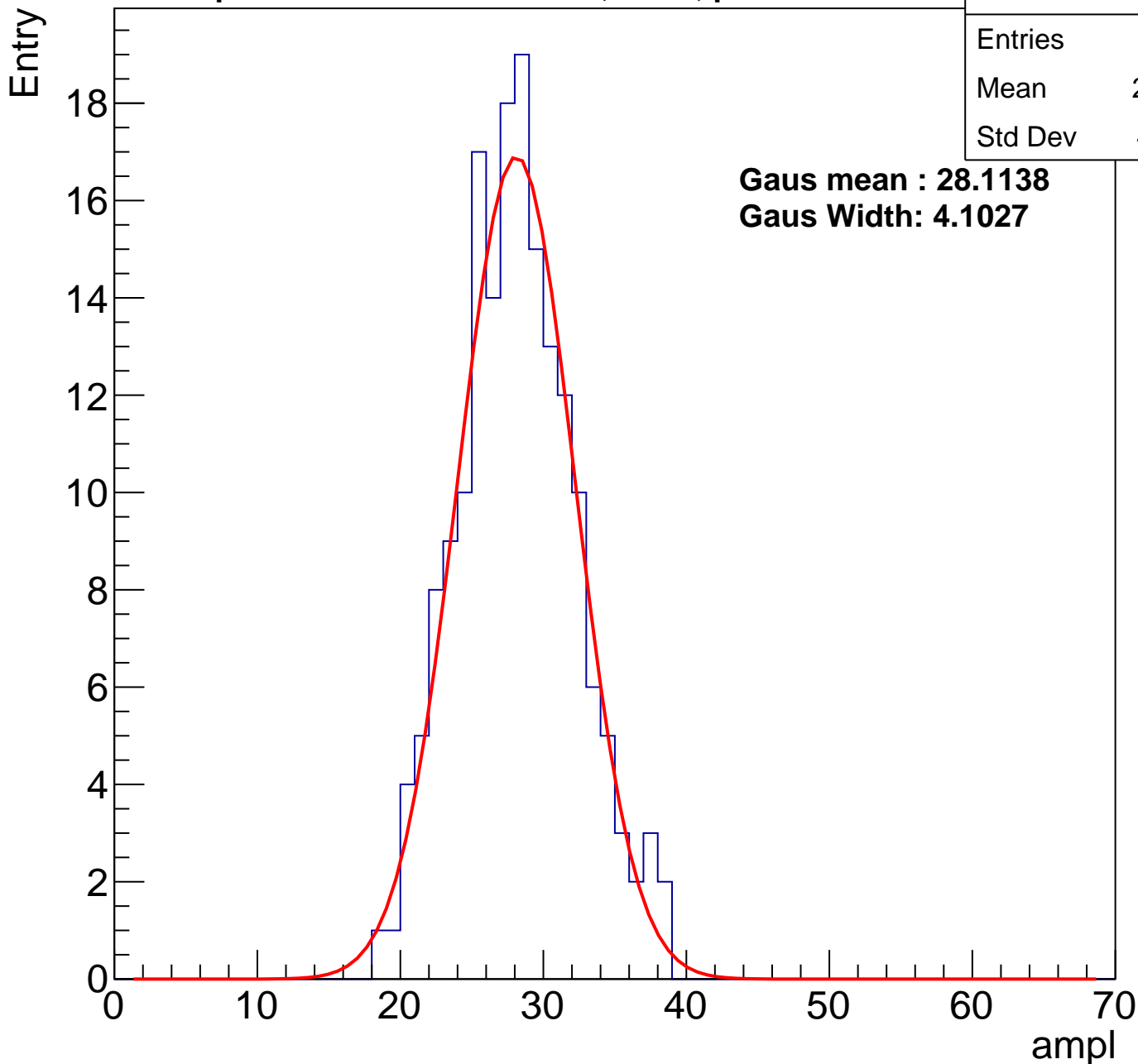
B0L101S, U15-ch92, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	177
Mean	27.69
Std Dev	4.081

Gaus mean : 28.1138

Gaus Width: 4.1027



B0L101S, U15-ch92, adc1

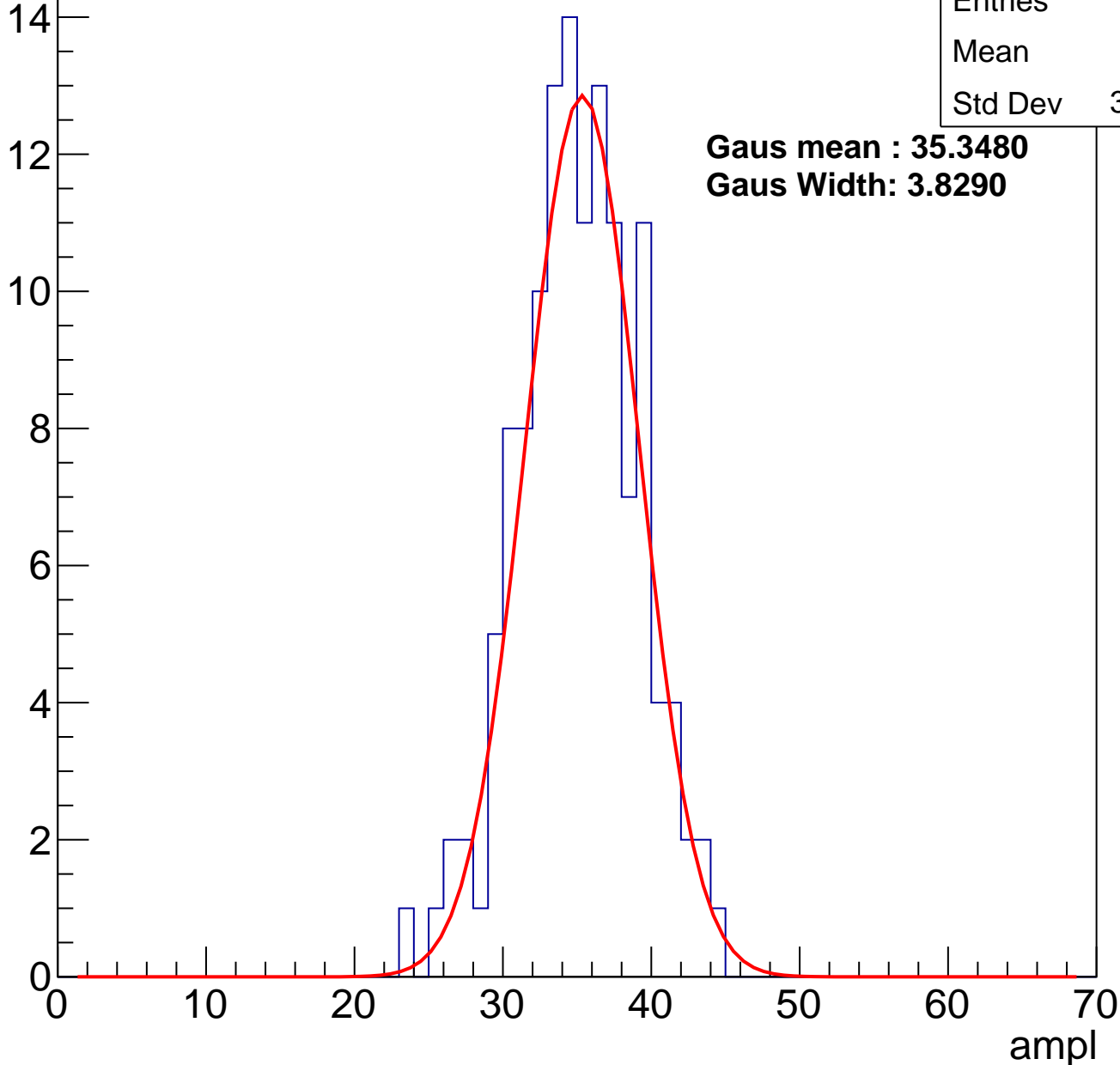
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	34.6
Std Dev	3.978

Gaus mean : 35.3480

Gaus Width: 3.8290

Entry



B0L101S, U15-ch92, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

122

Mean

40.94

Std Dev

3.533

Gaus mean : 41.5110

Gaus Width: 3.4912

0

10

20

30

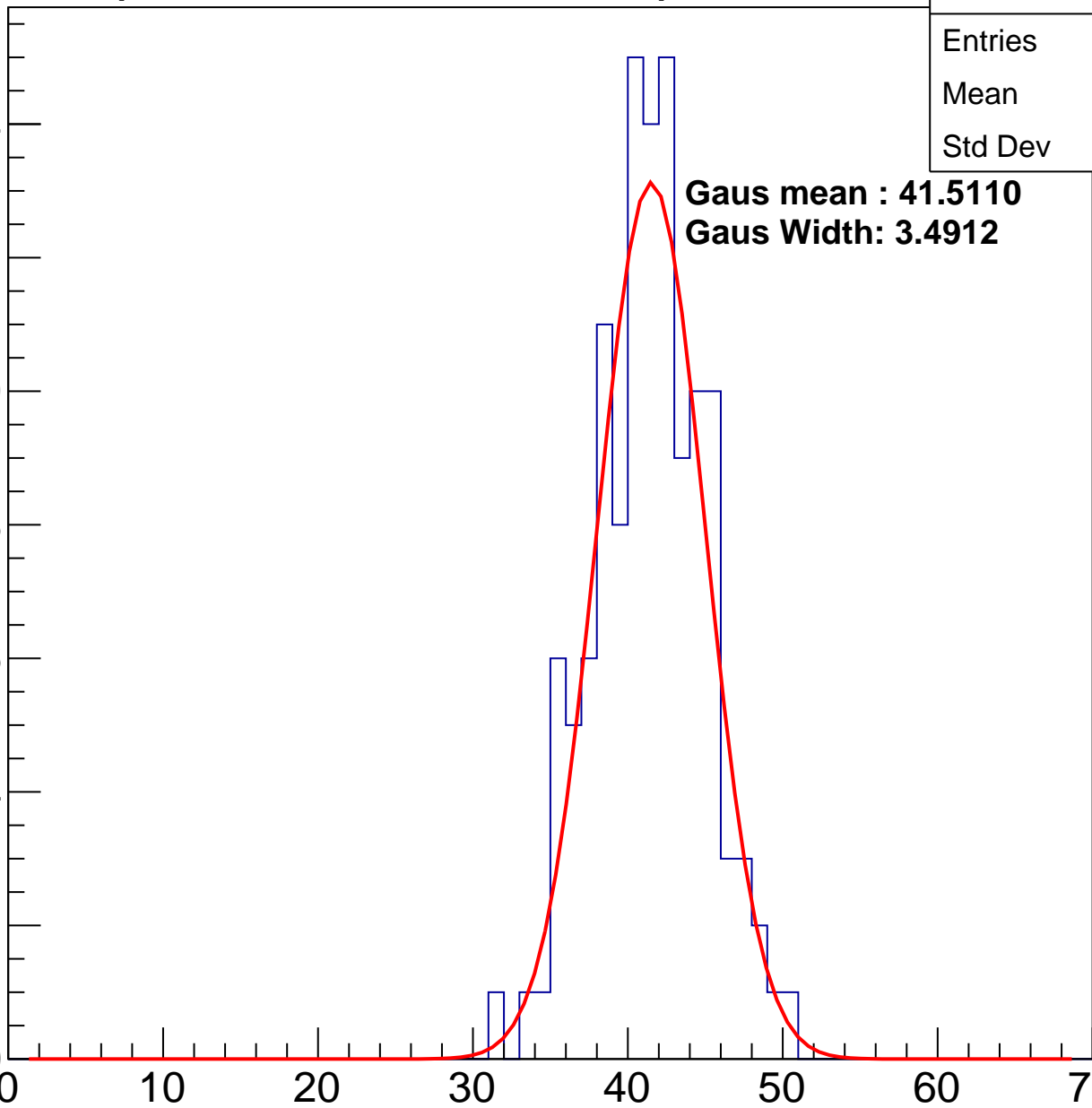
40

50

60

70

ampl

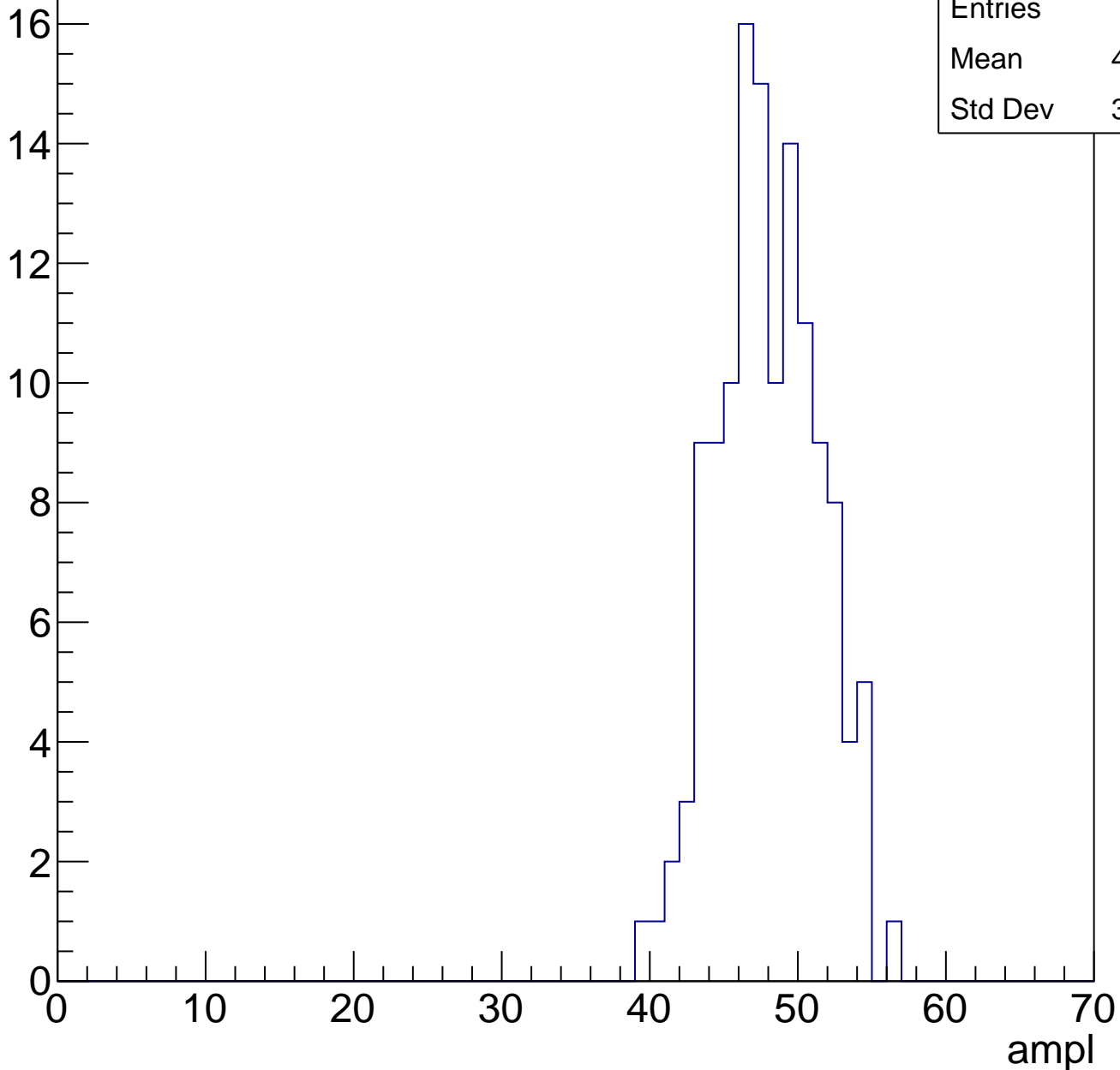


B0L101S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	47.58
Std Dev	3.397

Entry



B0L101S, U15-ch92, adc4

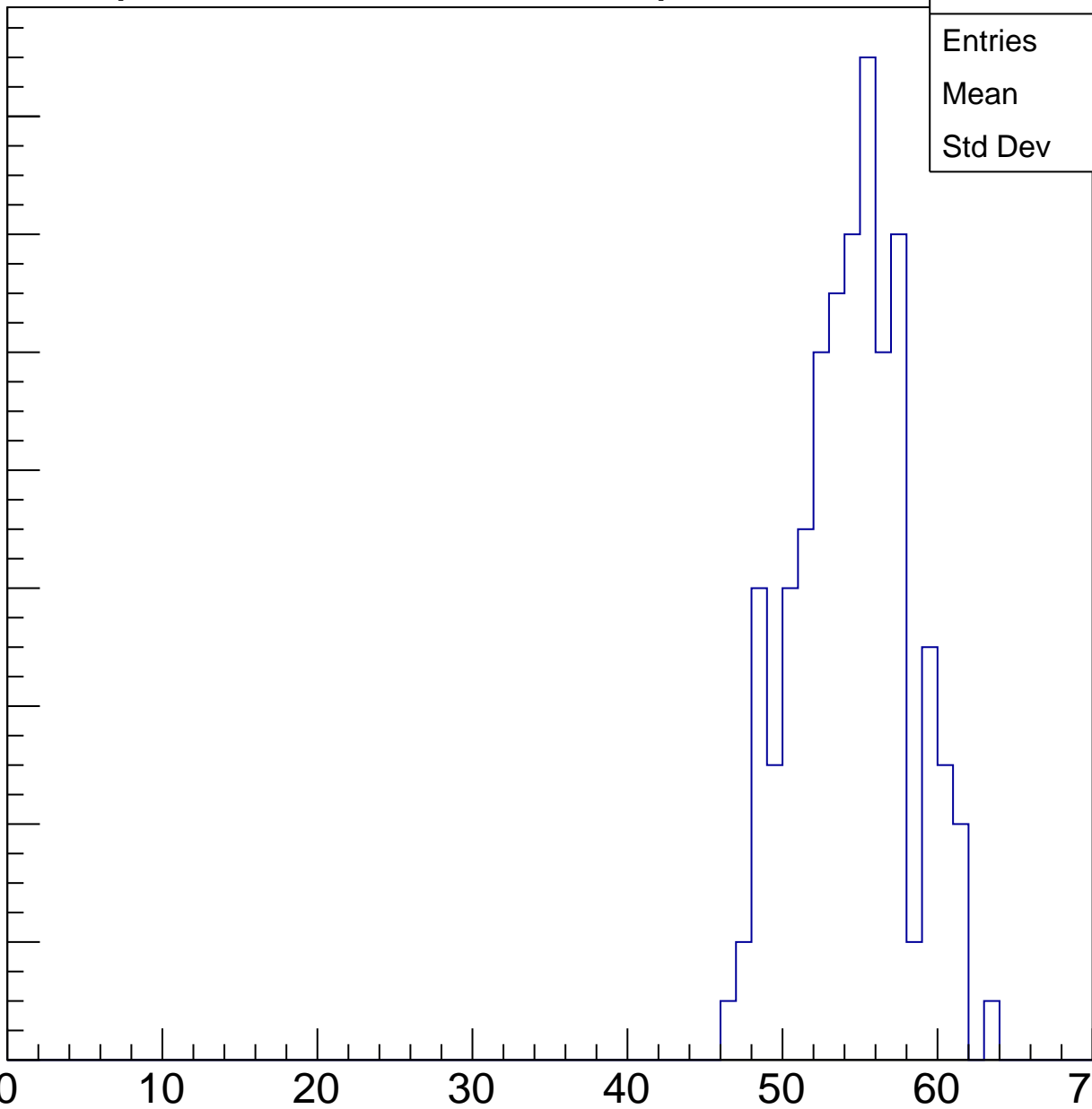
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	134
Mean	54.01
Std Dev	3.562

ampl



B0L101S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

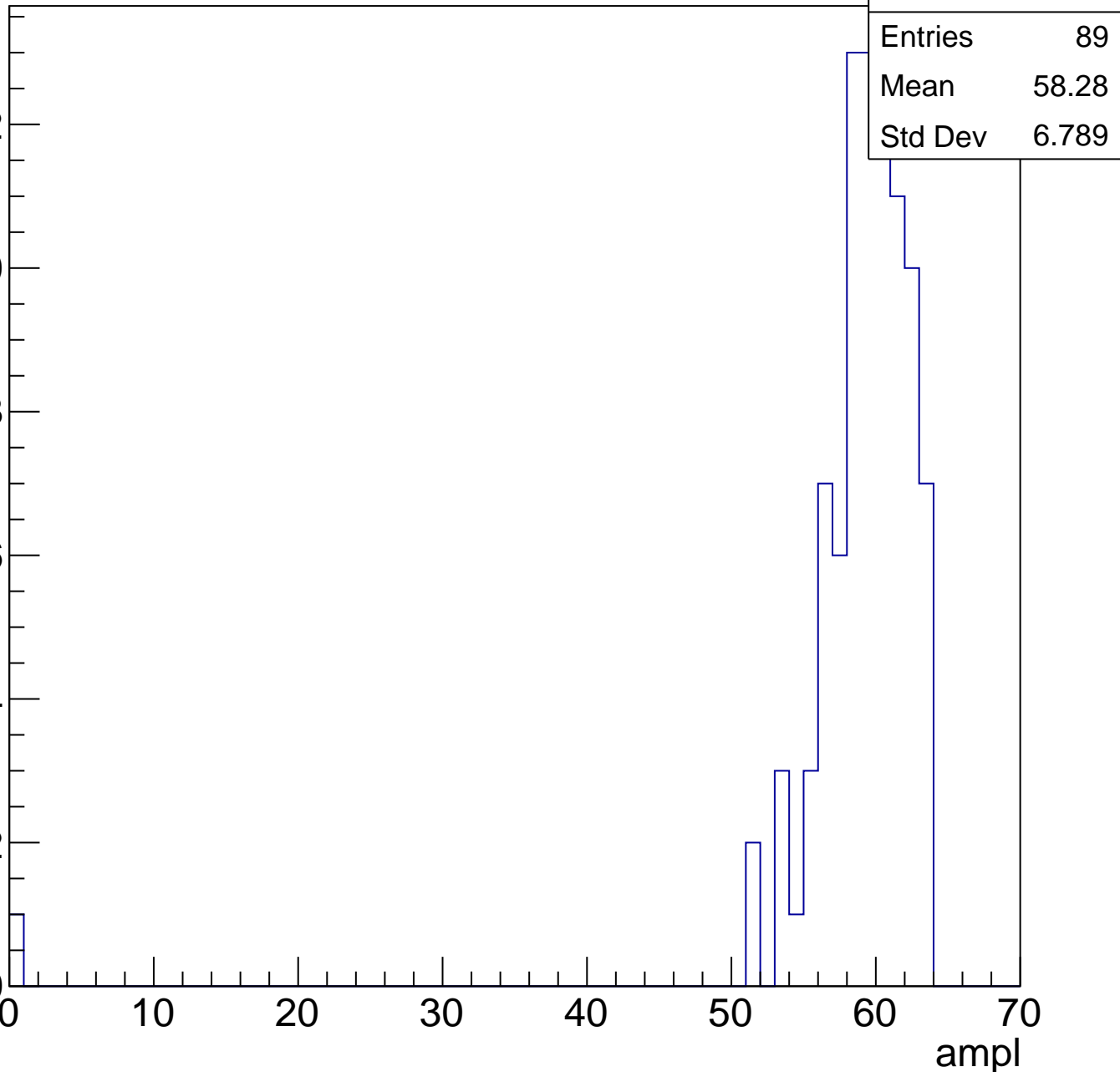
40

50

60

ampl

Entries	89
Mean	58.28
Std Dev	6.789



B0L101S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

35

Mean

59.69

Std Dev

10.37

ampl

0

10

20

30

40

50

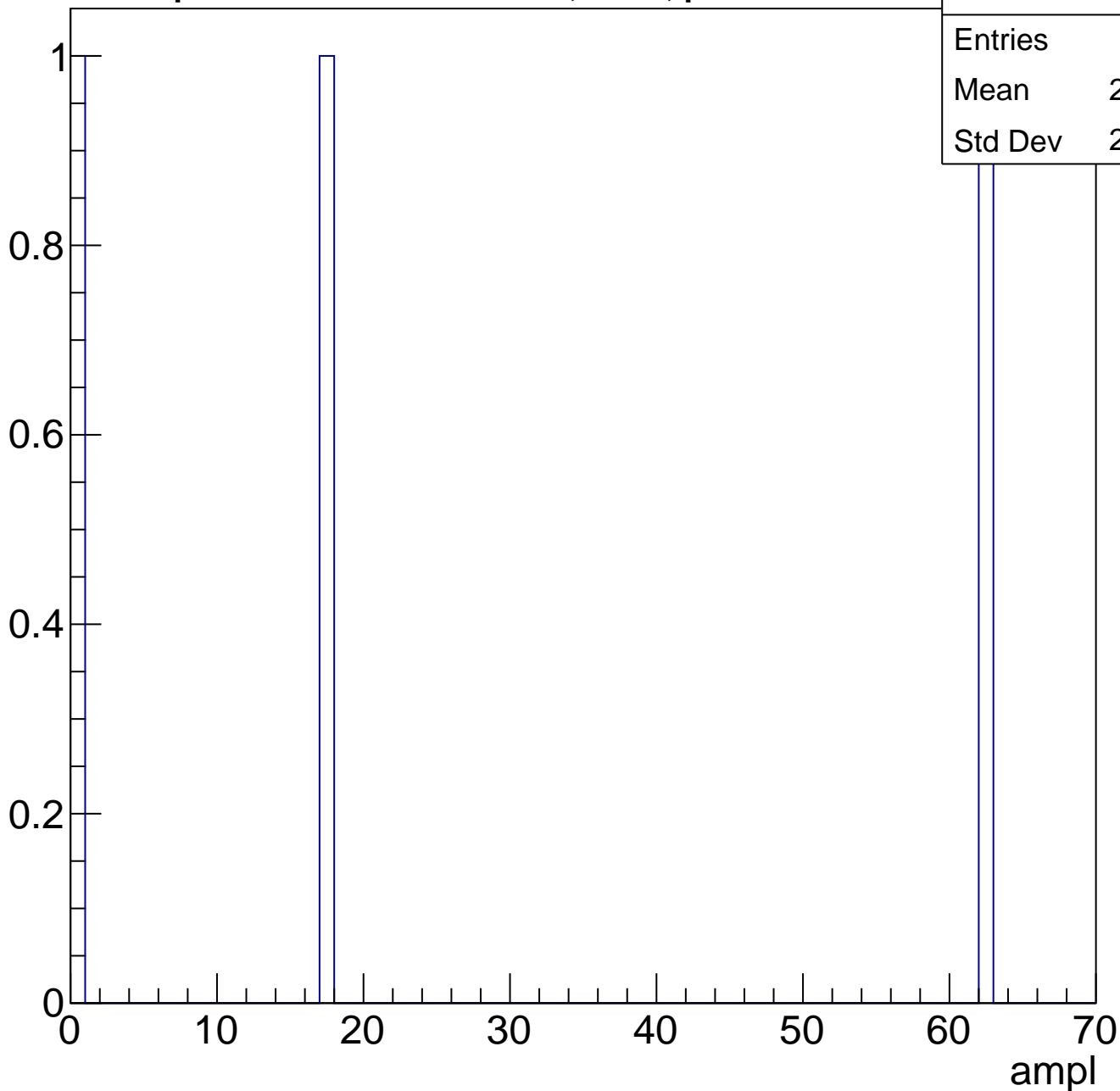
60

70

B0L101S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	26.33
Std Dev	26.16

B0L101S, U15-ch93, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	28.88
Std Dev	4.145

Gaus mean : 29.5015

Gaus Width: 3.1648

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

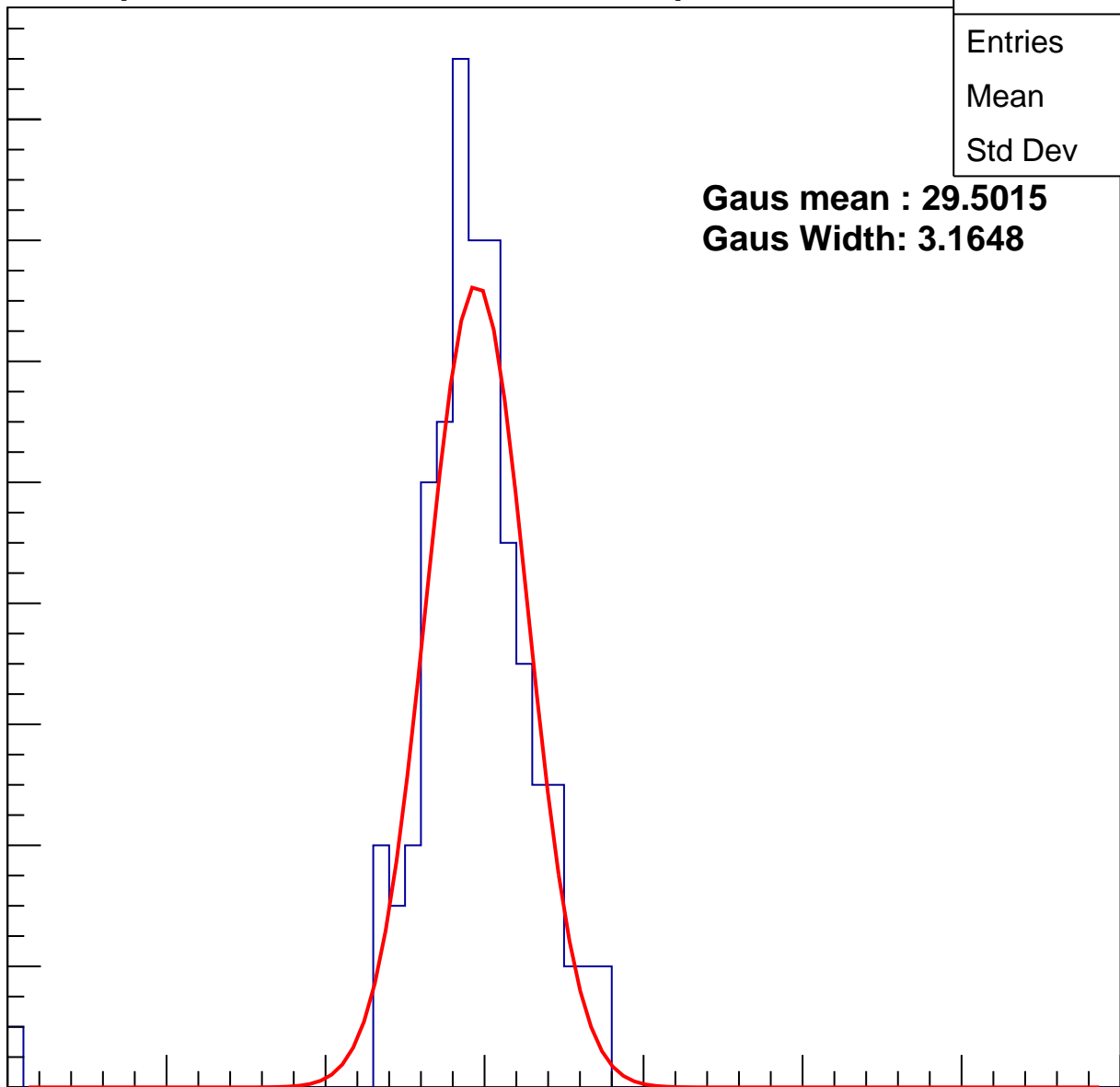
40

50

60

70

ampl



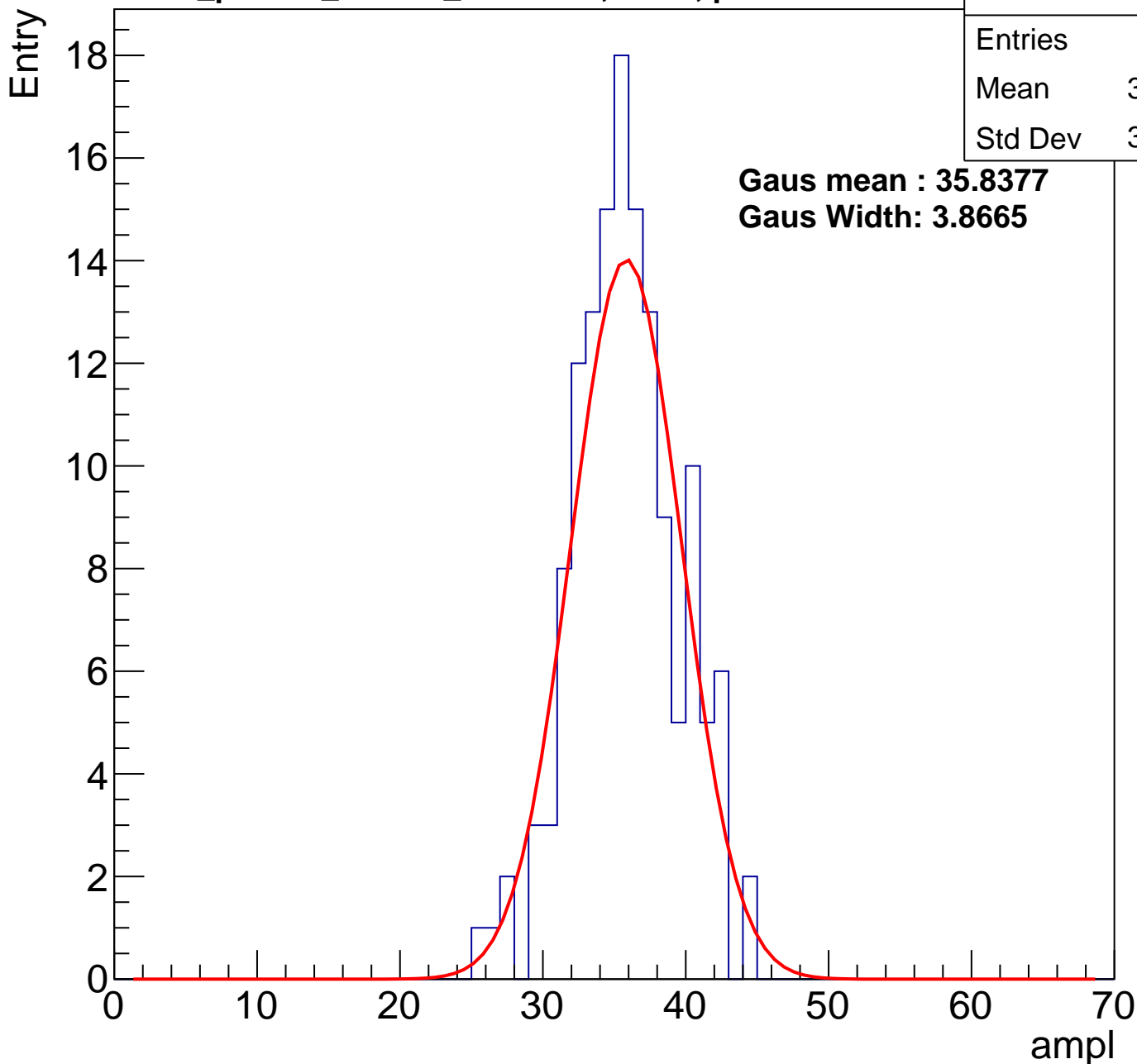
B0L101S, U15-ch93, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	35.36
Std Dev	3.662

Gaus mean : 35.8377

Gaus Width: 3.8665



B0L101S, U15-ch93, adc2

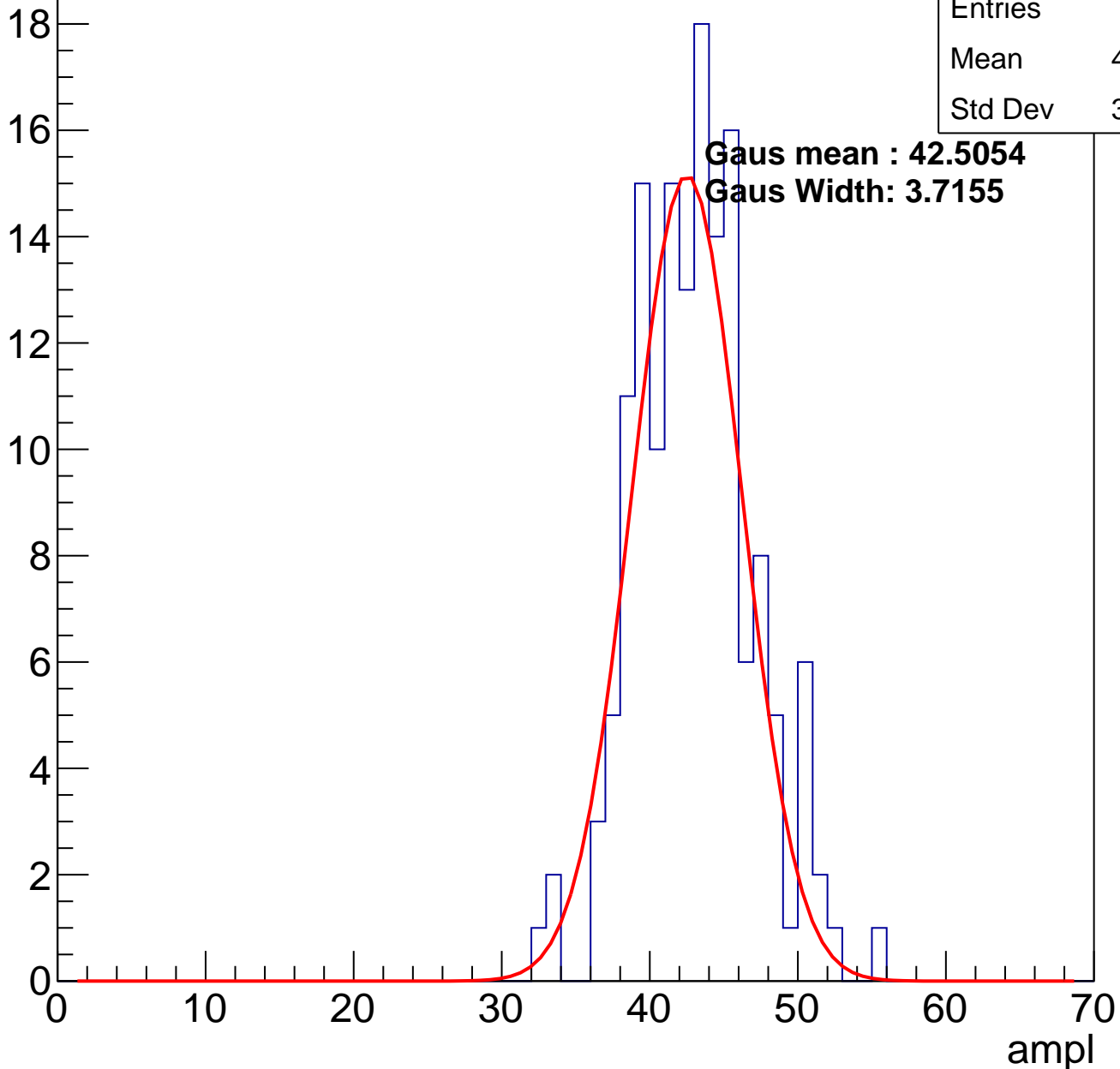
calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	42.58
Std Dev	3.933

Gaus mean : 42.5054

Gaus Width: 3.7155

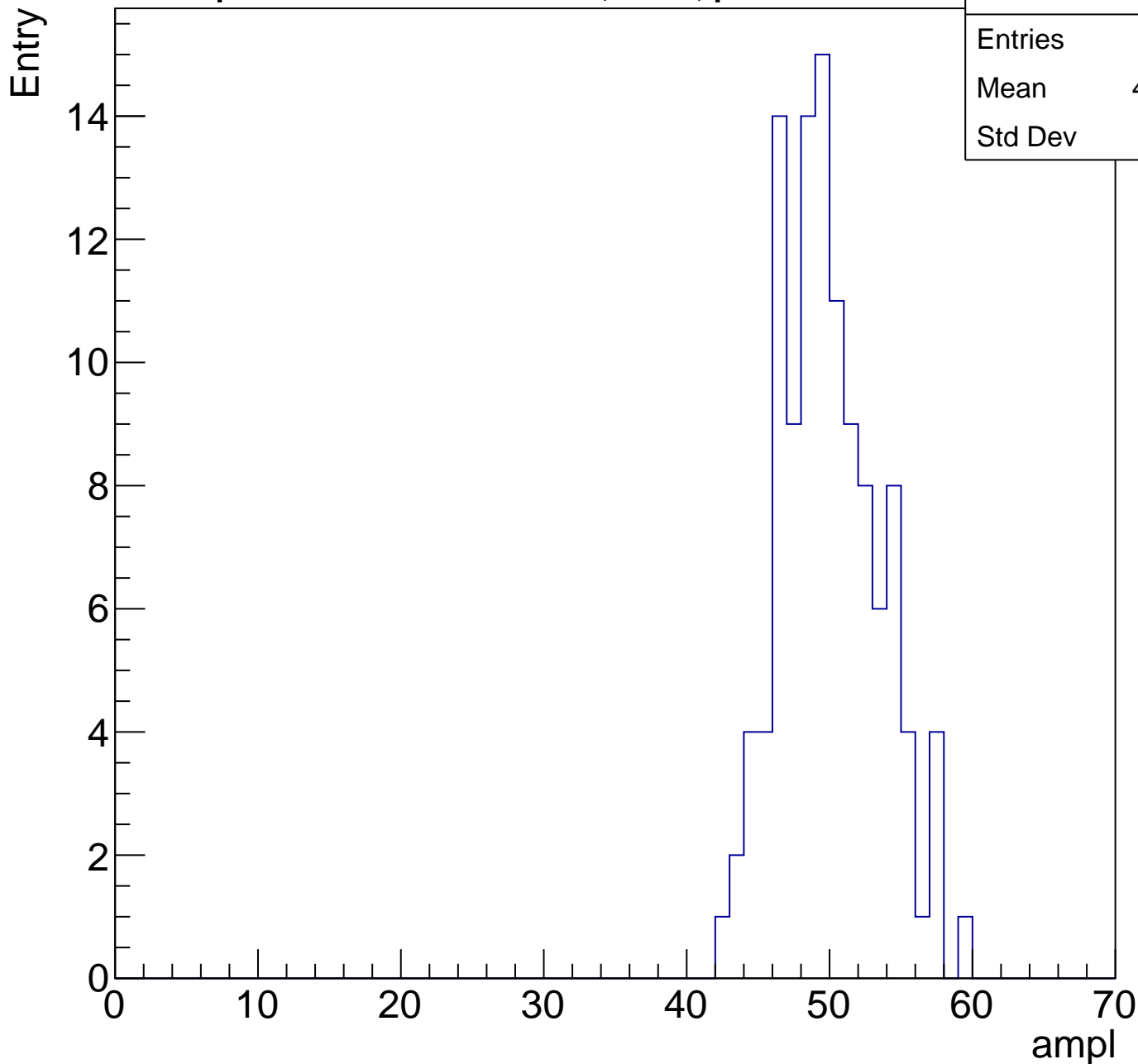
Entry



B0L101S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	49.53
Std Dev	3.48



B0L101S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

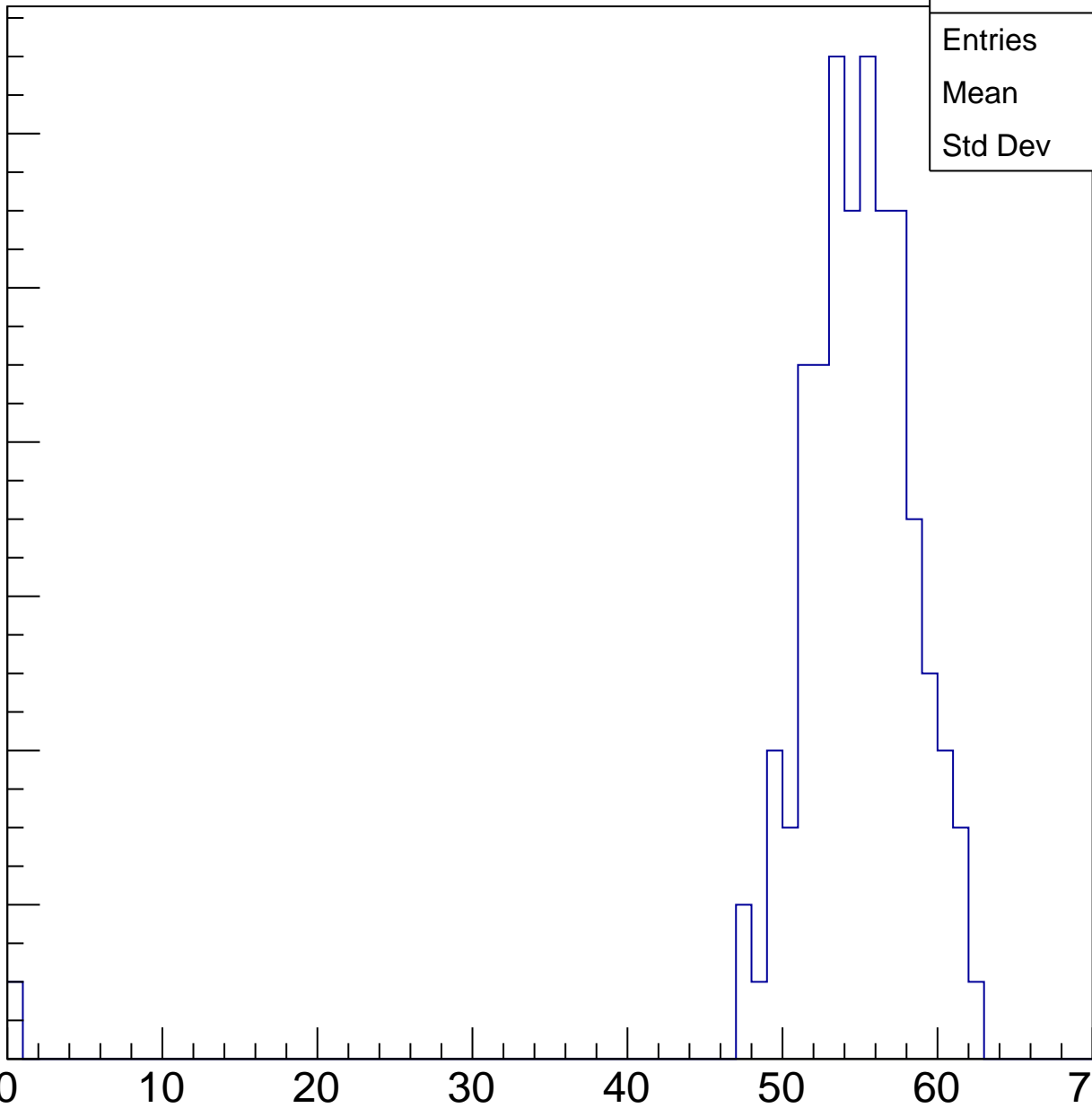
40

50

60

ampl

Entries	108
Mean	54.09
Std Dev	6.143



B0L101S, U15-ch93, adc5

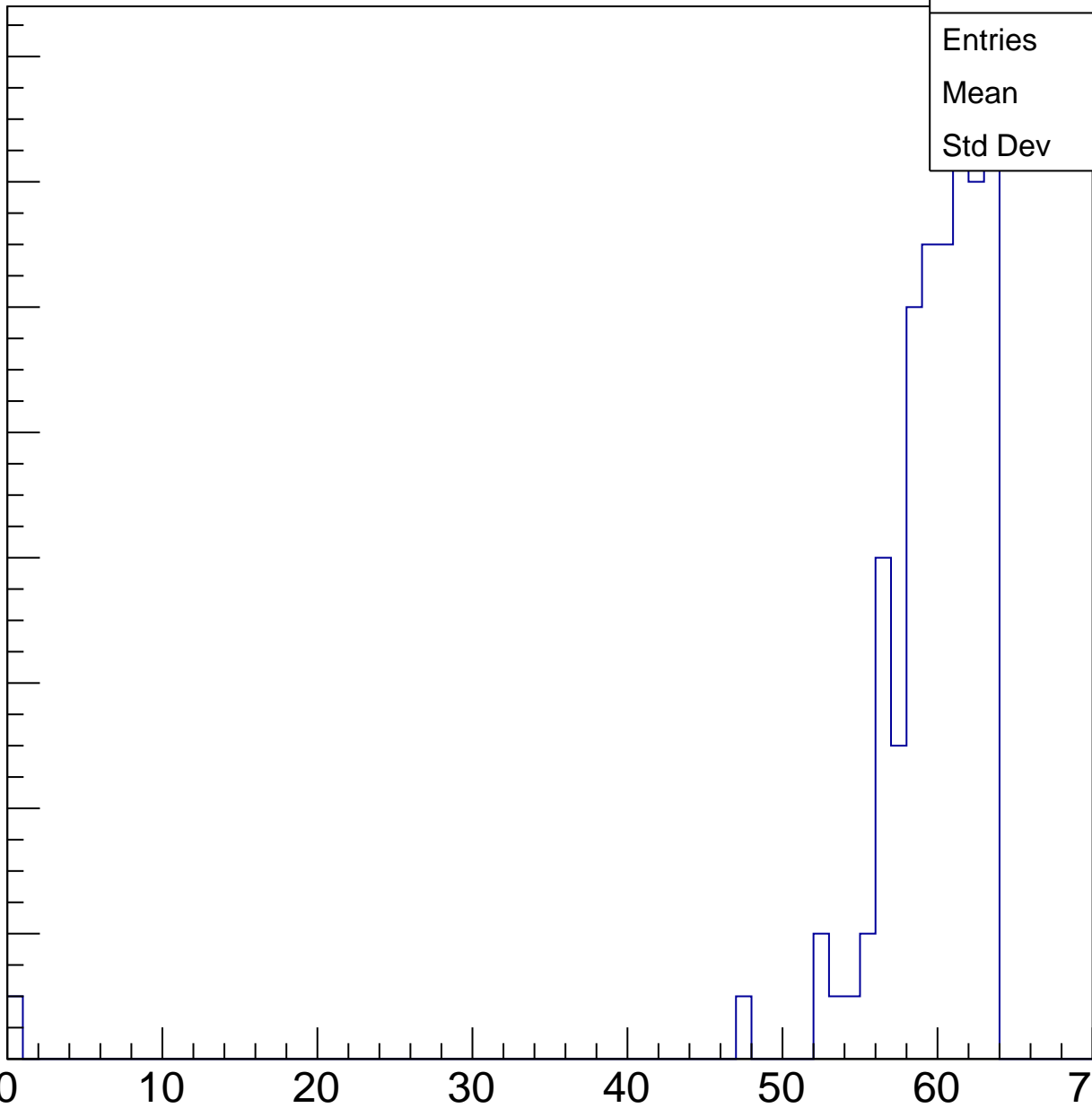
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.97
Std Dev	6.477

ampl

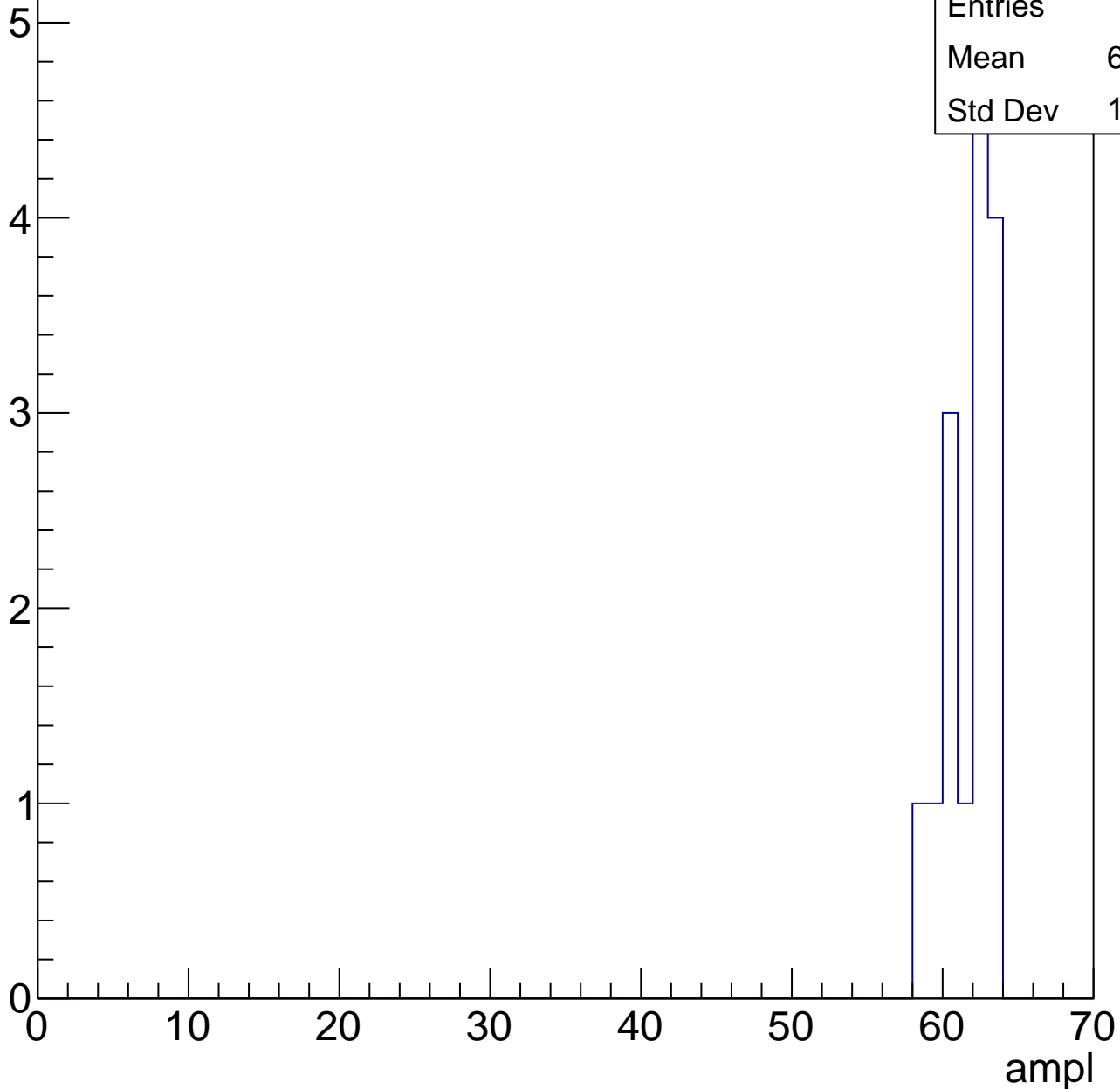


B0L101S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	15
Mean	61.33
Std Dev	1.535



B0L101S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch94, adc0

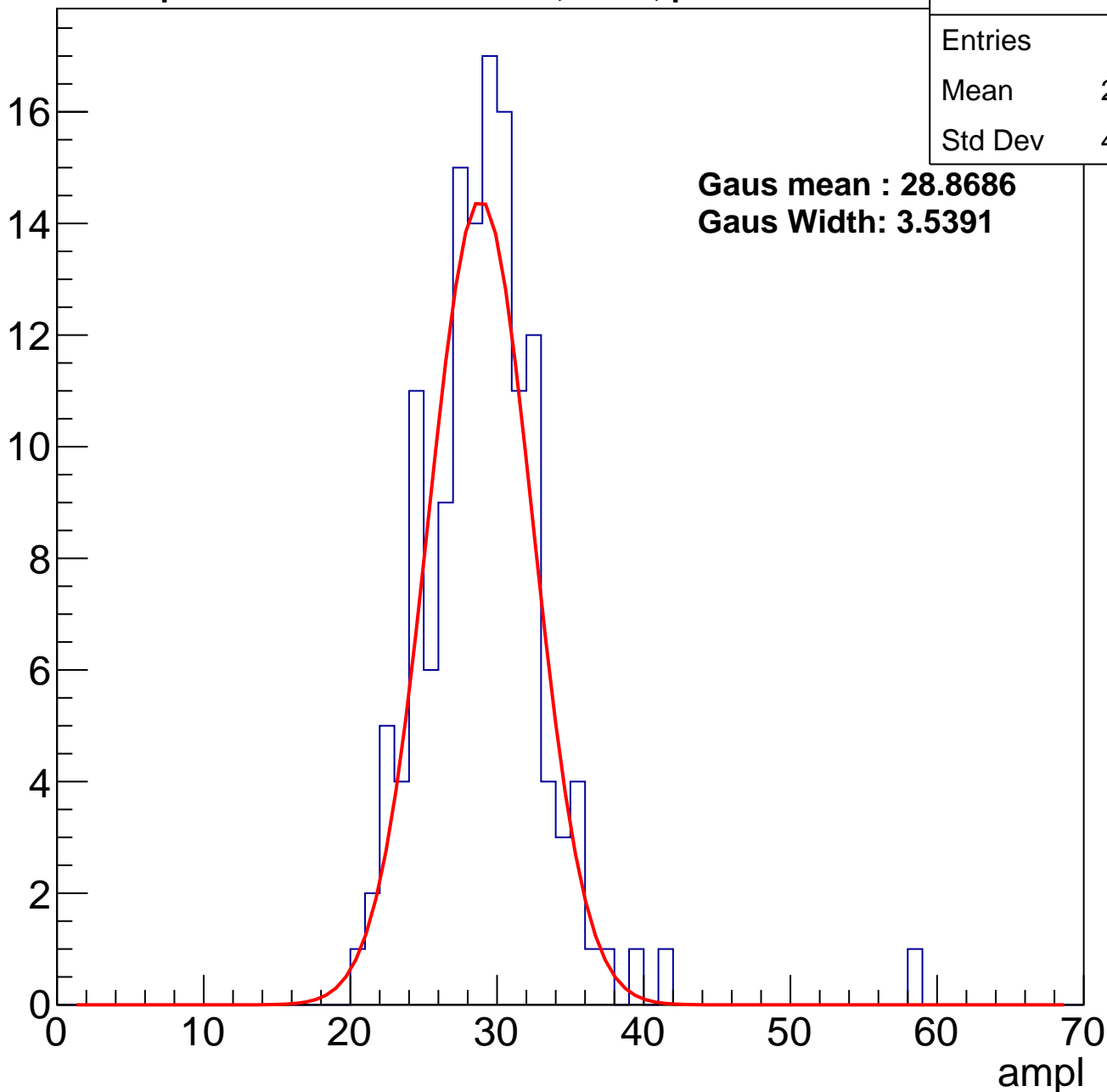
calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	28.72
Std Dev	4.458

Gaus mean : 28.8686

Gaus Width: 3.5391

Entry



B0L101S, U15-ch94, adc1

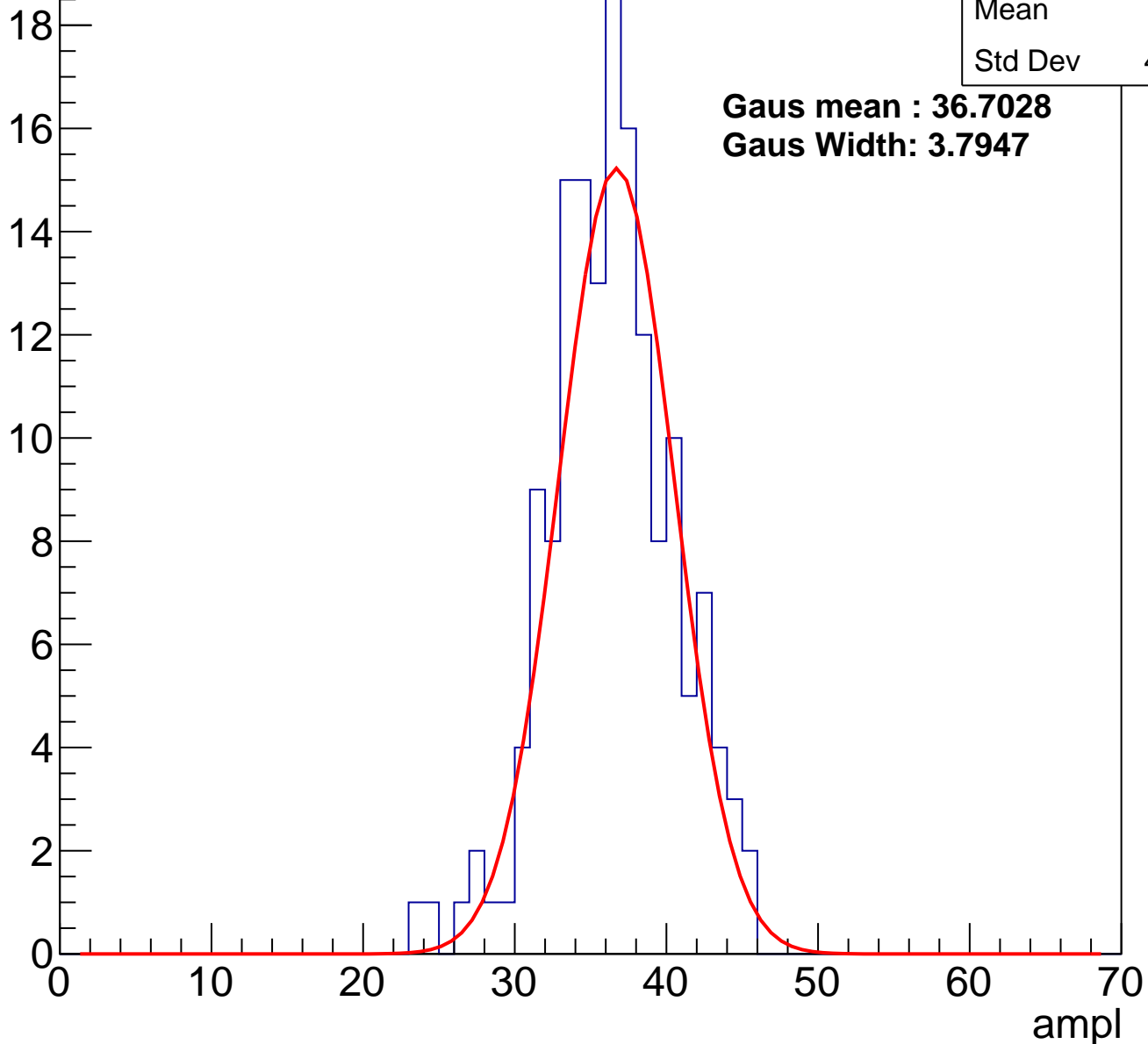
calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	35.9
Std Dev	4.081

Gaus mean : 36.7028

Gaus Width: 3.7947

Entry



B0L101S, U15-ch94, adc2

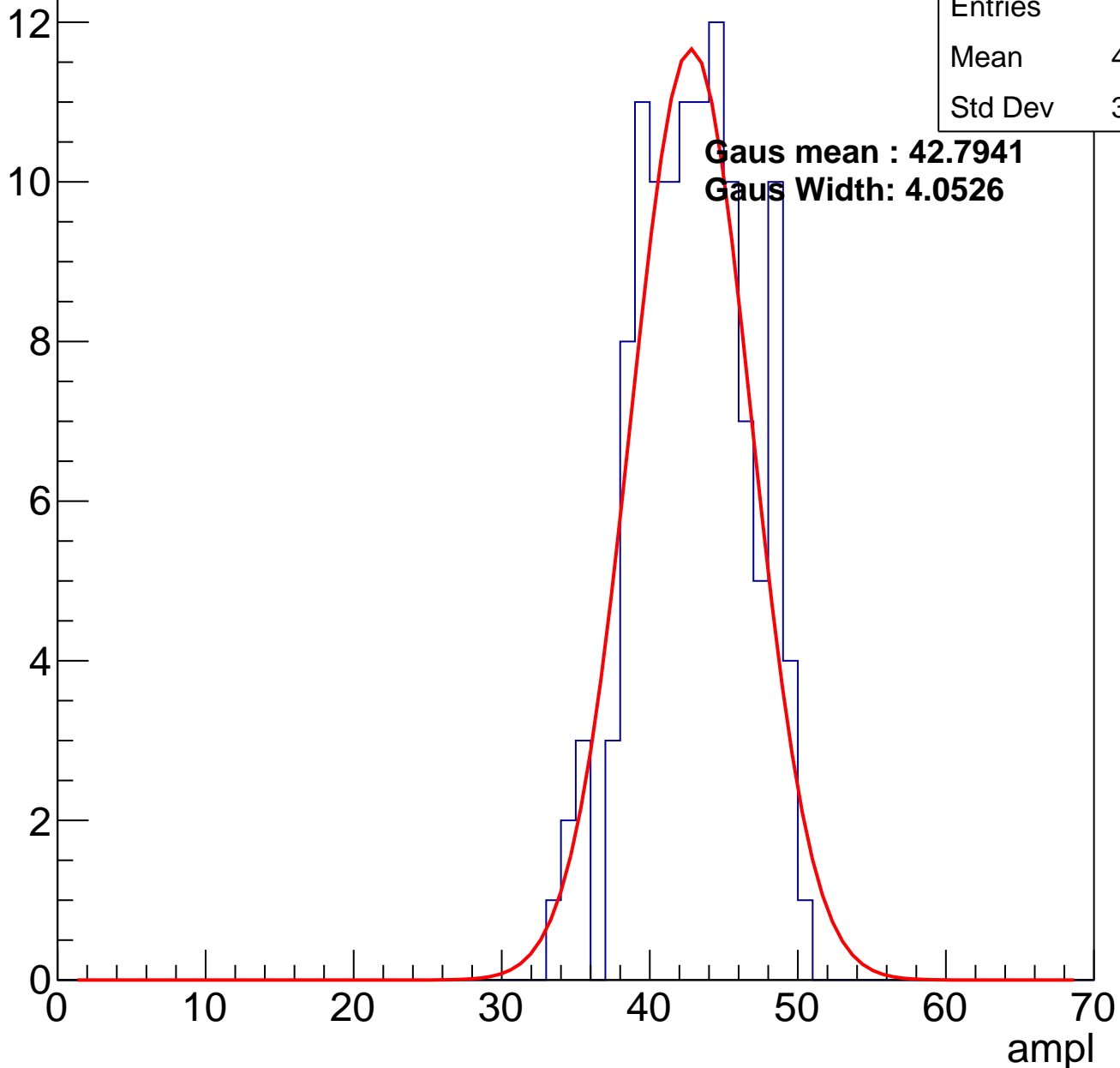
calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	42.49
Std Dev	3.764

Gaus mean : 42.7941

Gaus Width: 4.0526

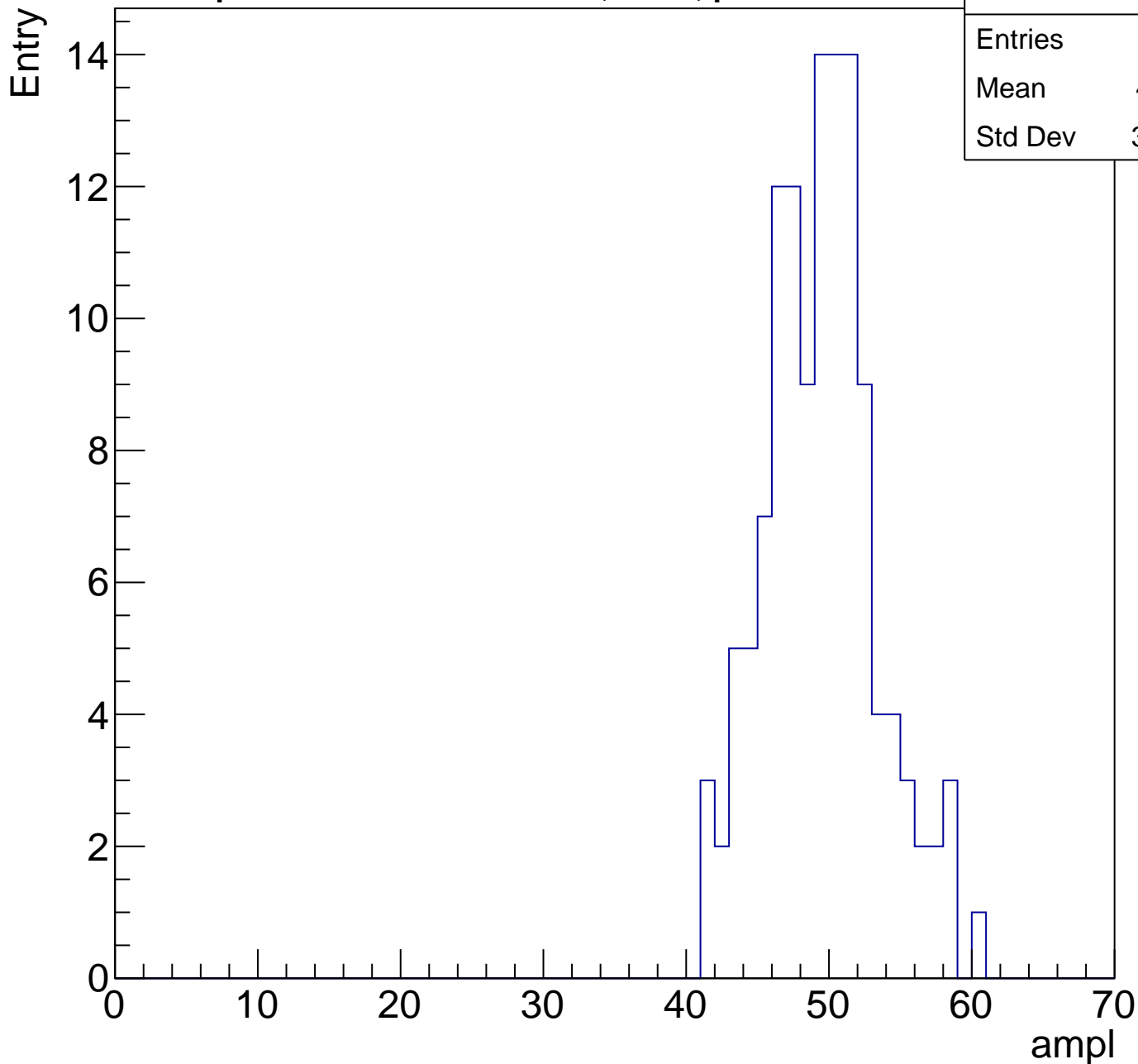
Entry



B0L101S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	125
Mean	49.01
Std Dev	3.883



B0L101S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#1, port C1

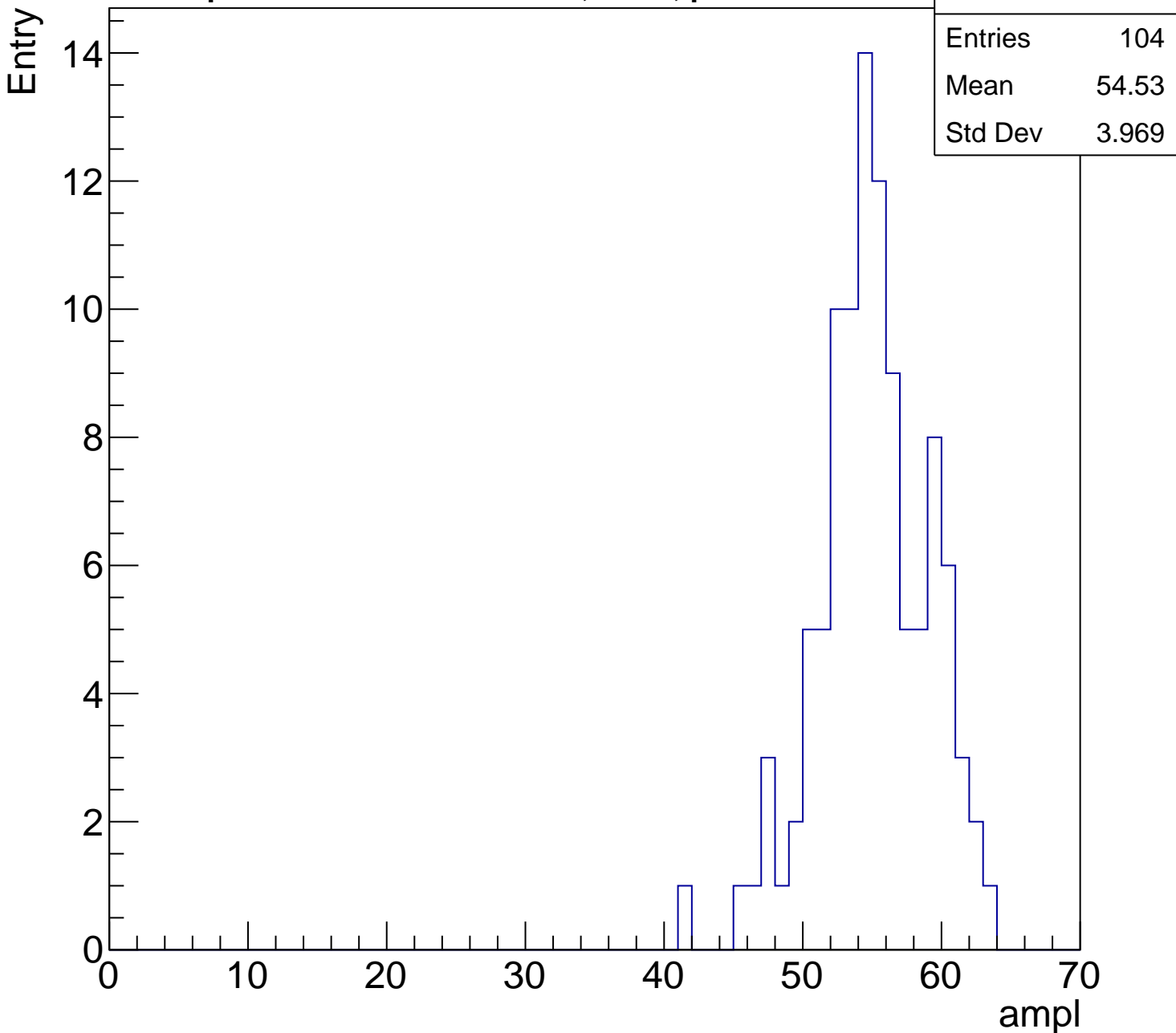
Entries	104
Mean	54.53
Std Dev	3.969

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch94, adc5

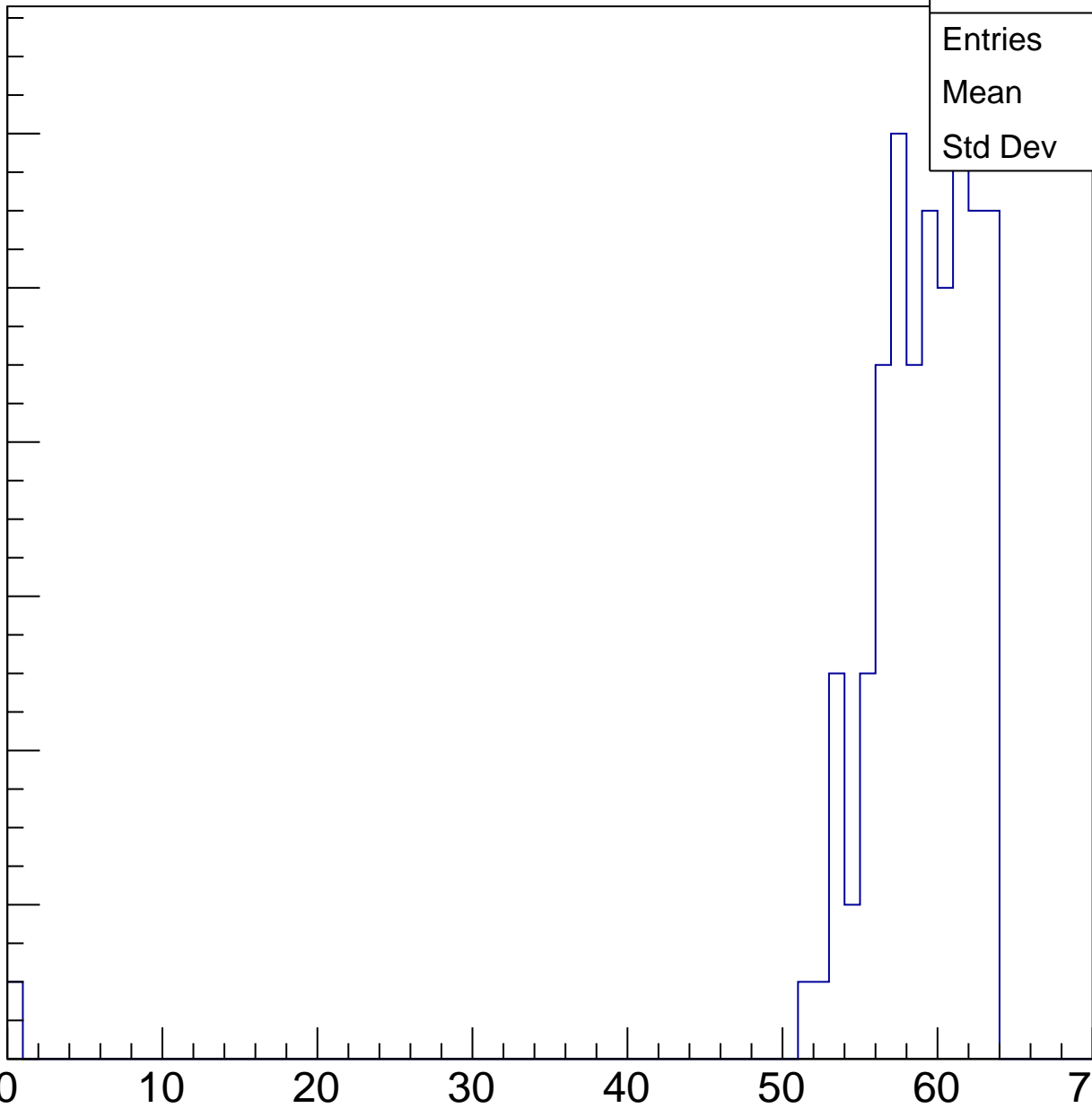
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	101
Mean	58.2
Std Dev	6.536

ampl

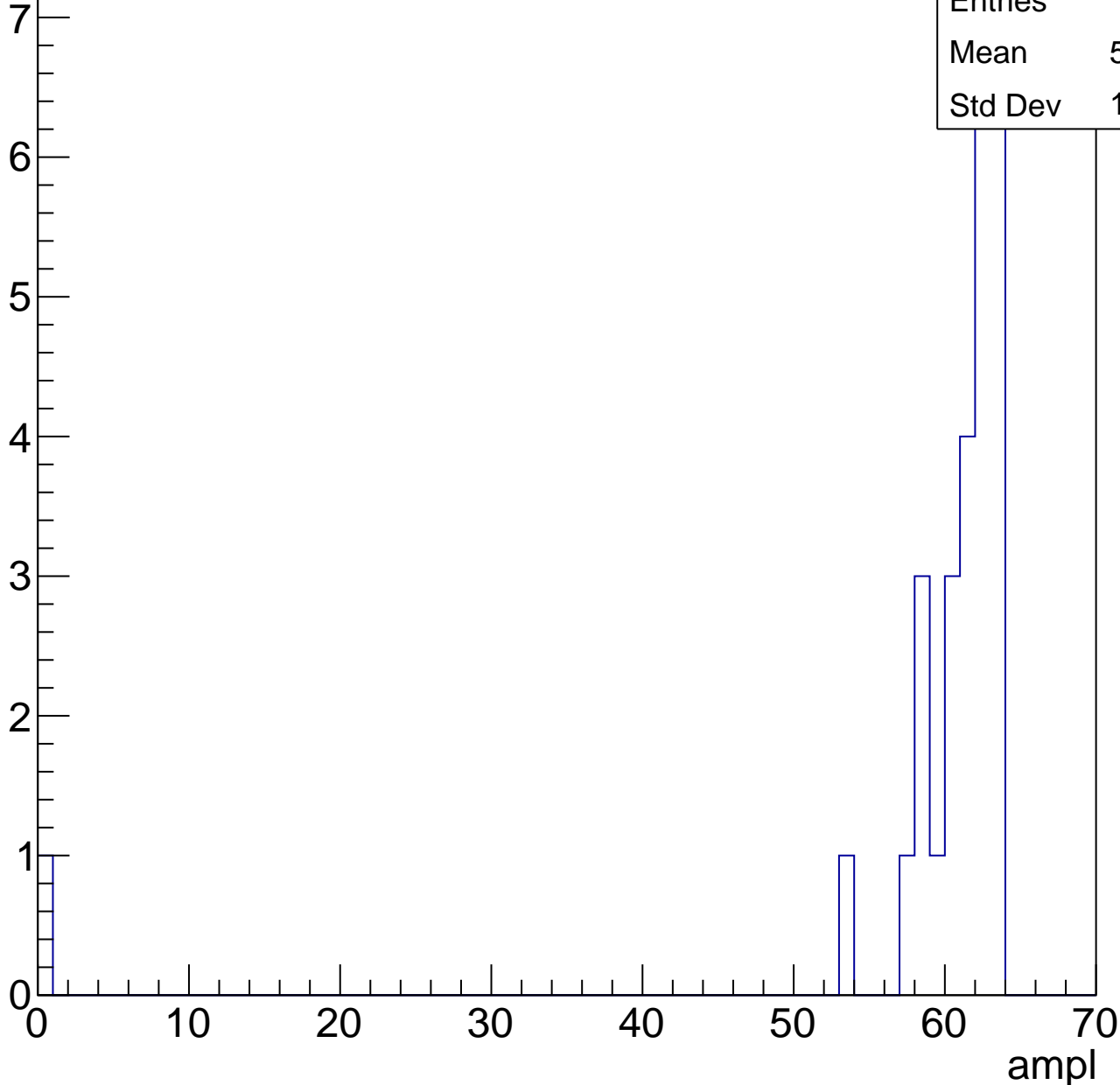


B0L101S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

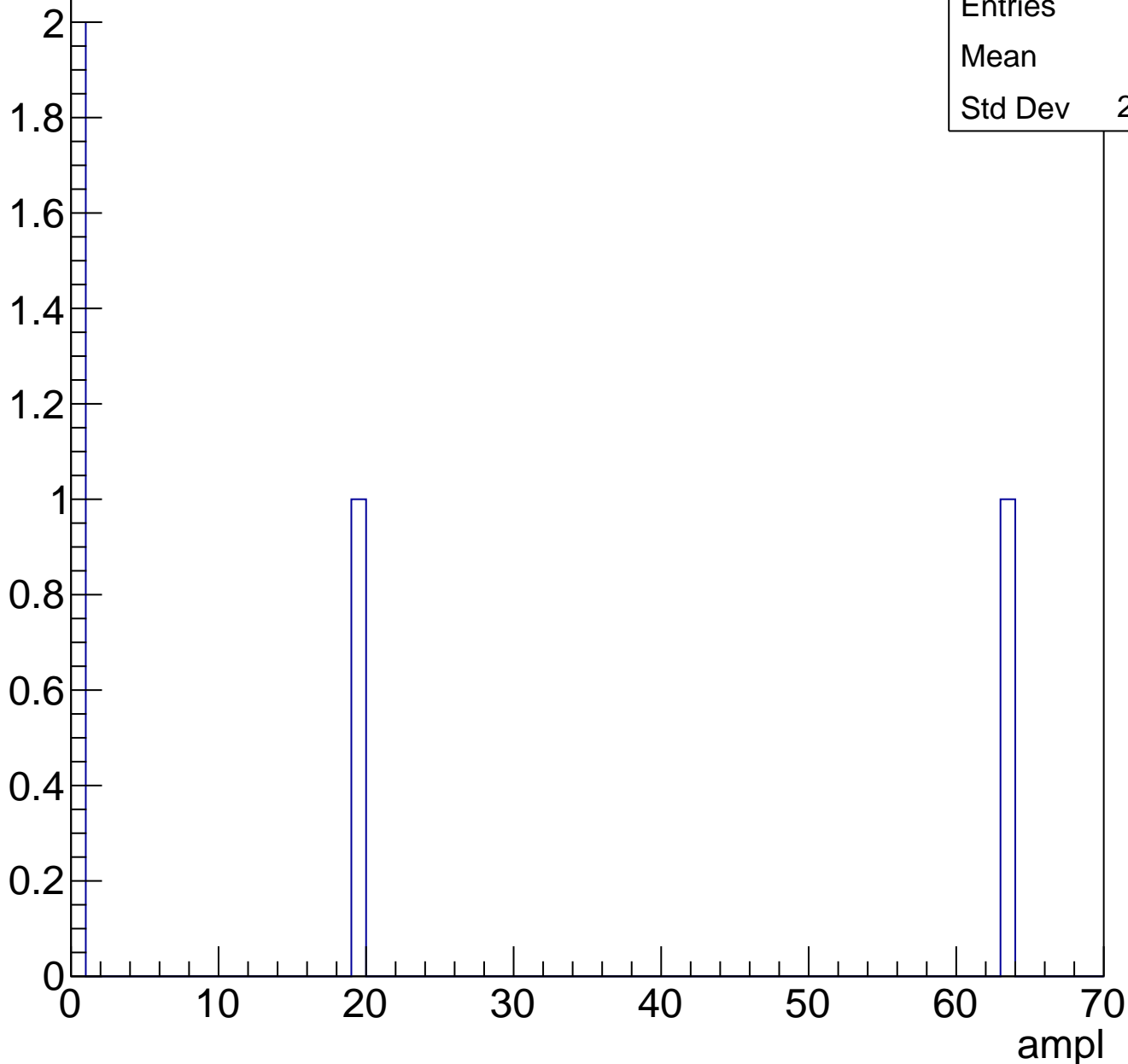
Entries	28
Mean	58.64
Std Dev	11.52



B0L101S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#1, port C1

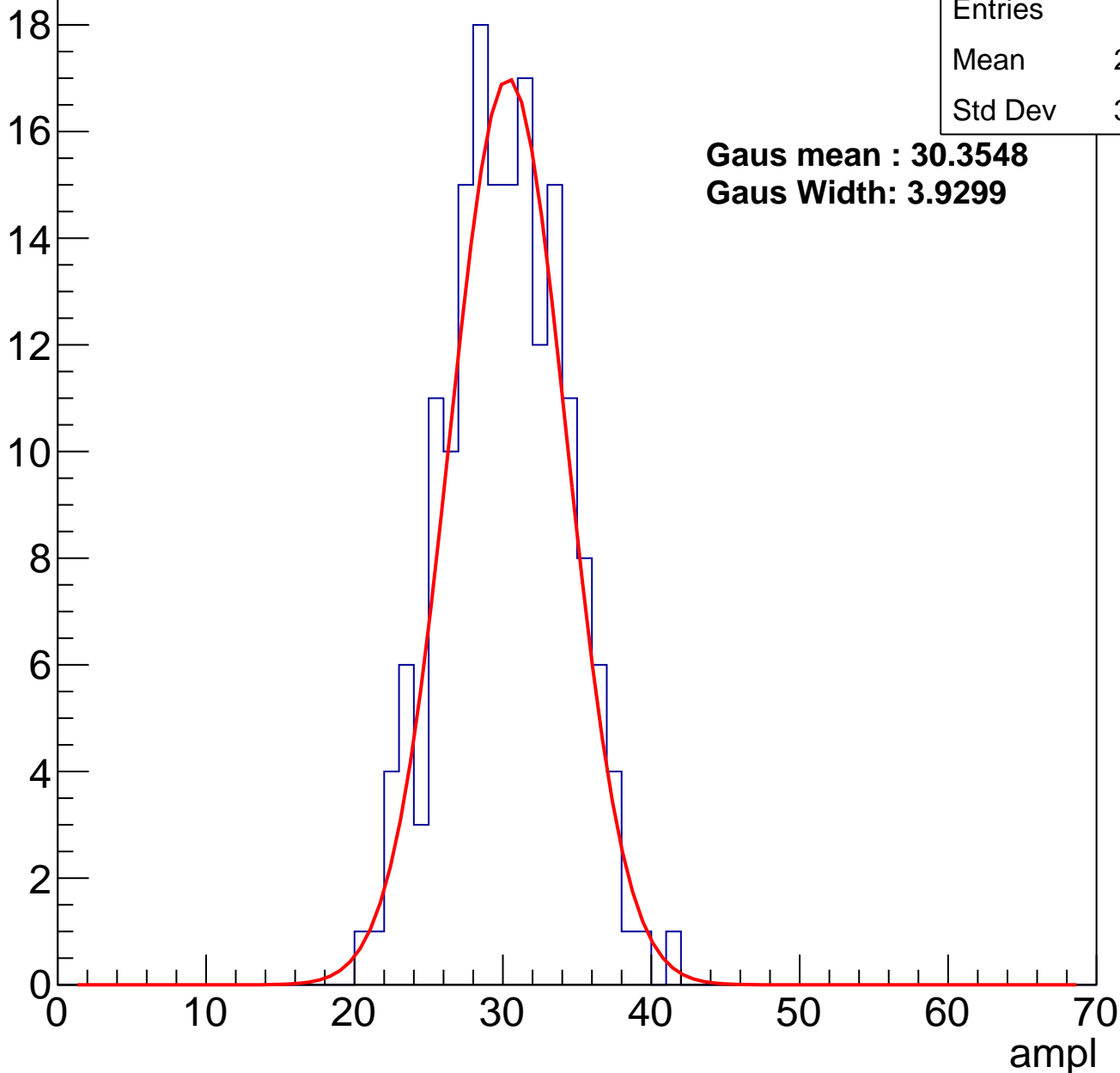
Entry



B0L101S, U15-ch95, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



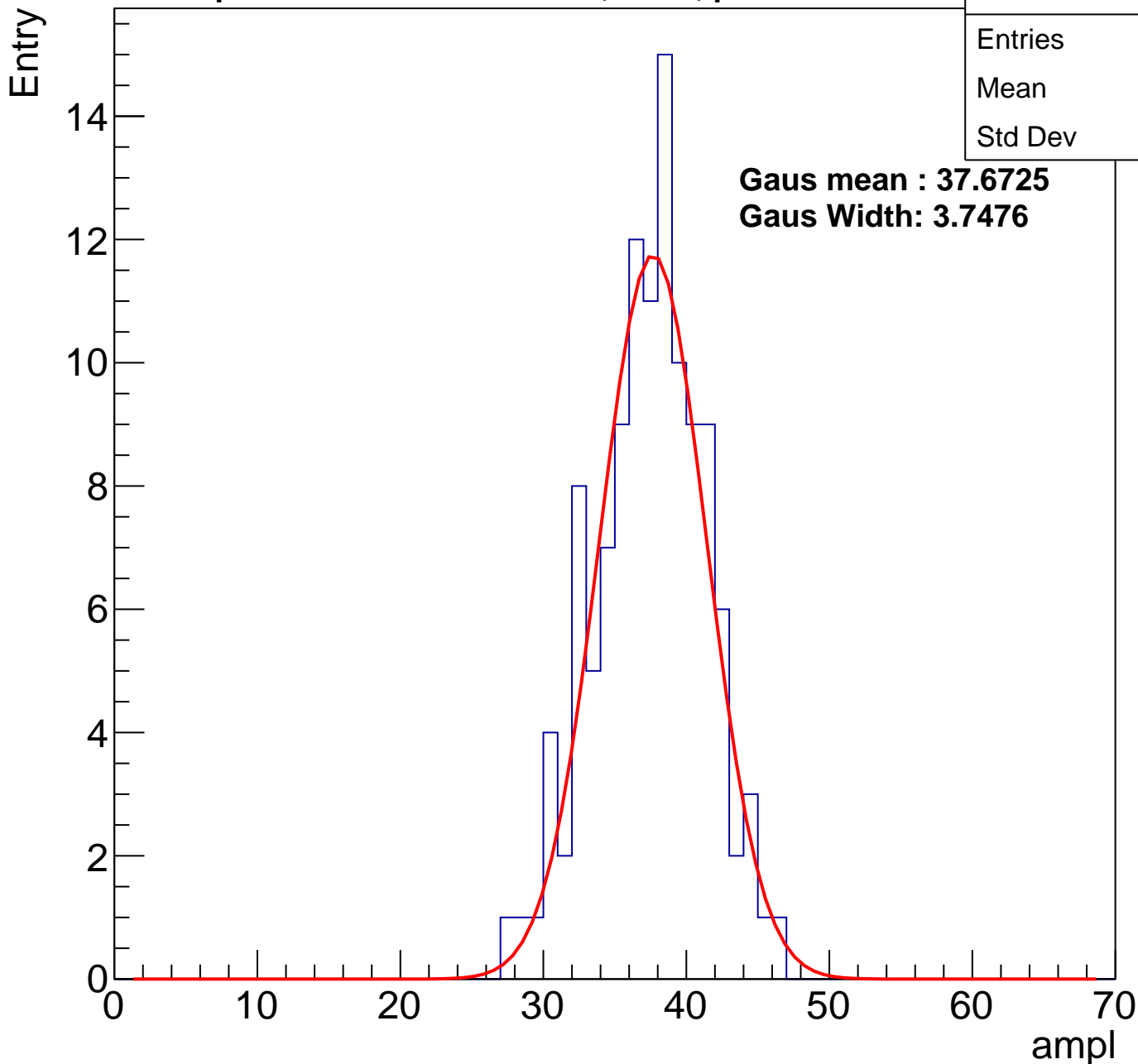
B0L101S, U15-ch95, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	37
Std Dev	3.81

Gaus mean : 37.6725

Gaus Width: 3.7476



B0L101S, U15-ch95, adc2

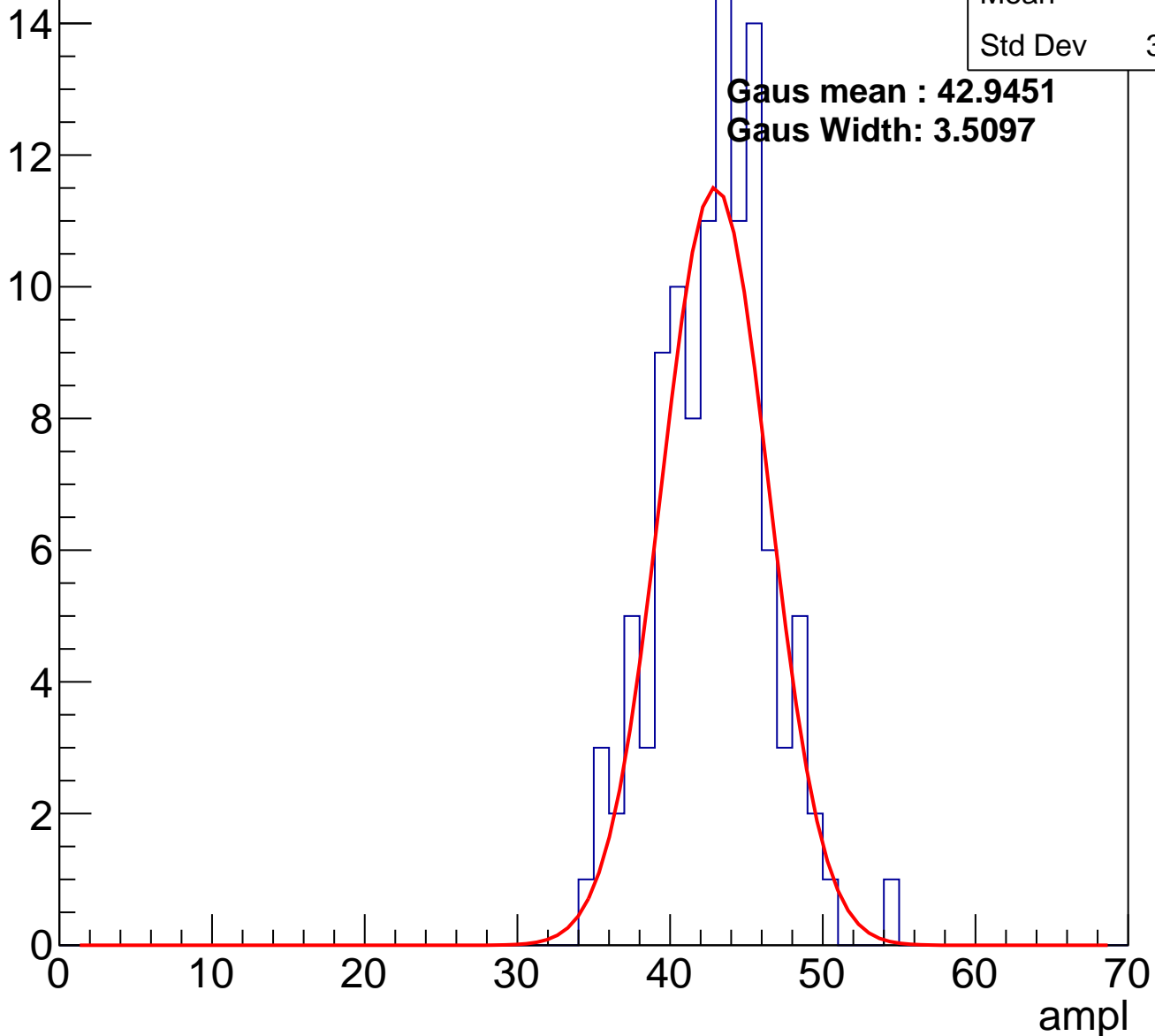
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	42.45
Std Dev	3.574

Gaus mean : 42.9451

Gaus Width: 3.5097

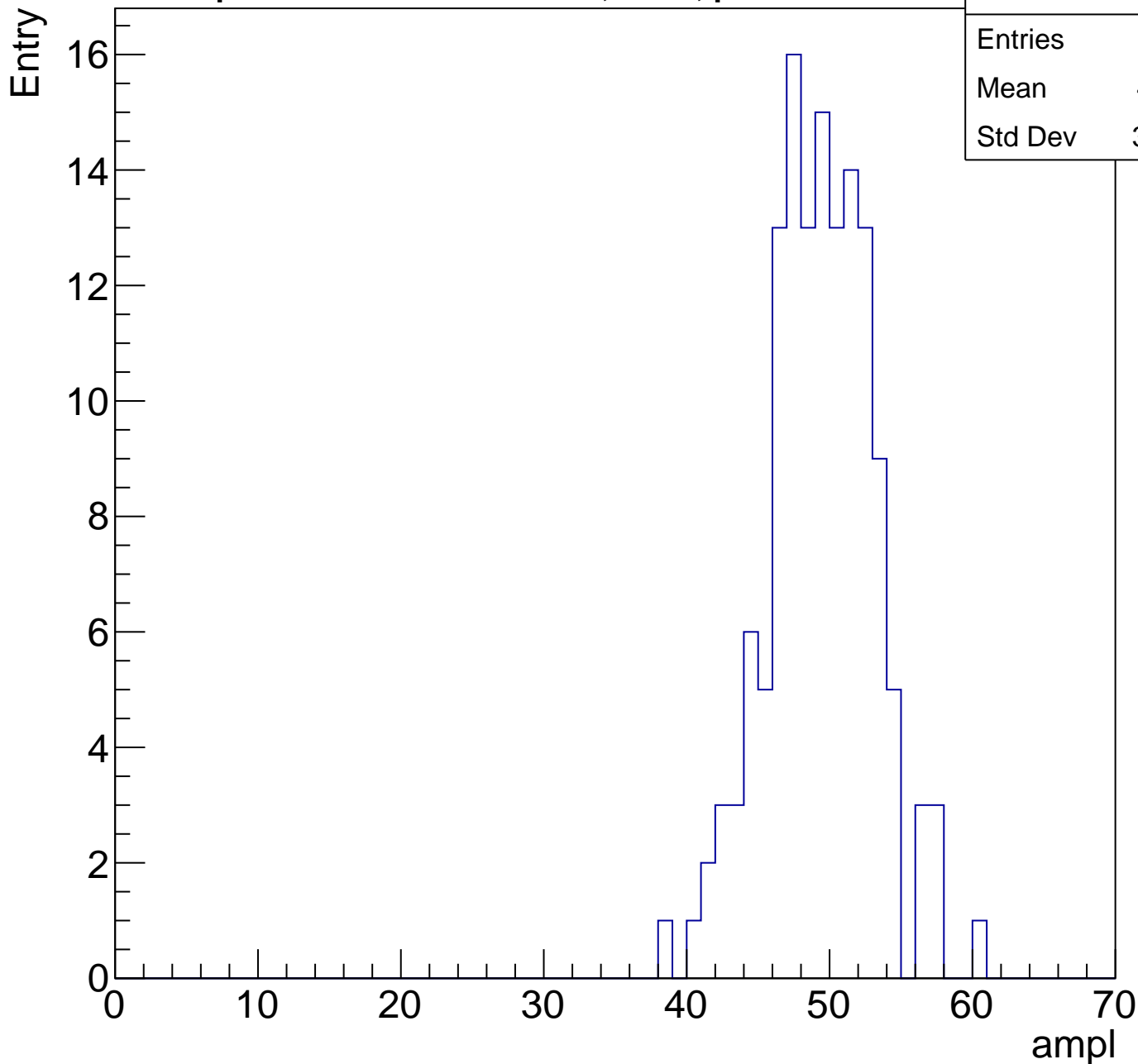
Entry



B0L101S, U15-ch95, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	48.91
Std Dev	3.719



B0L101S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

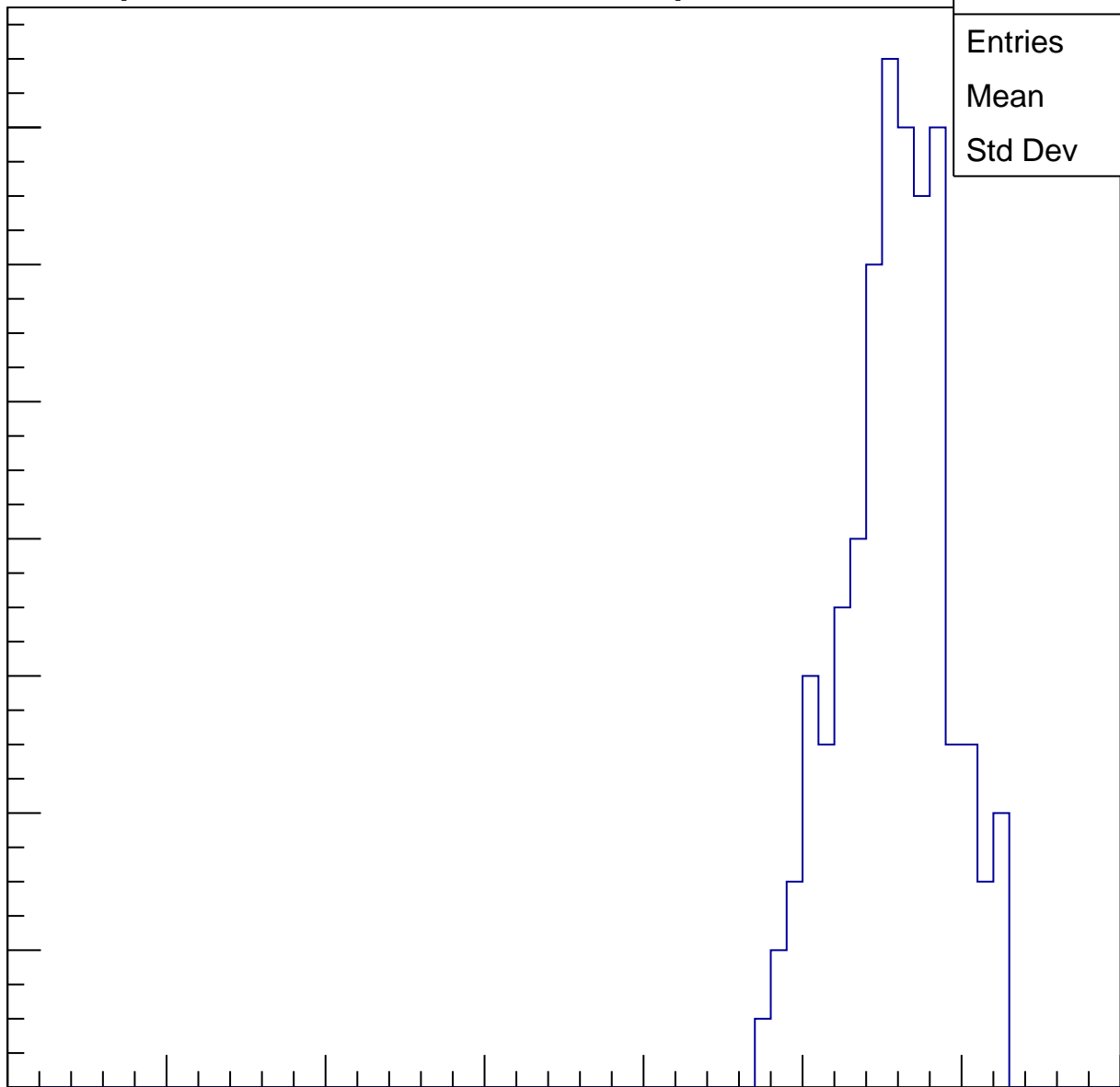
117

Mean

55.29

Std Dev

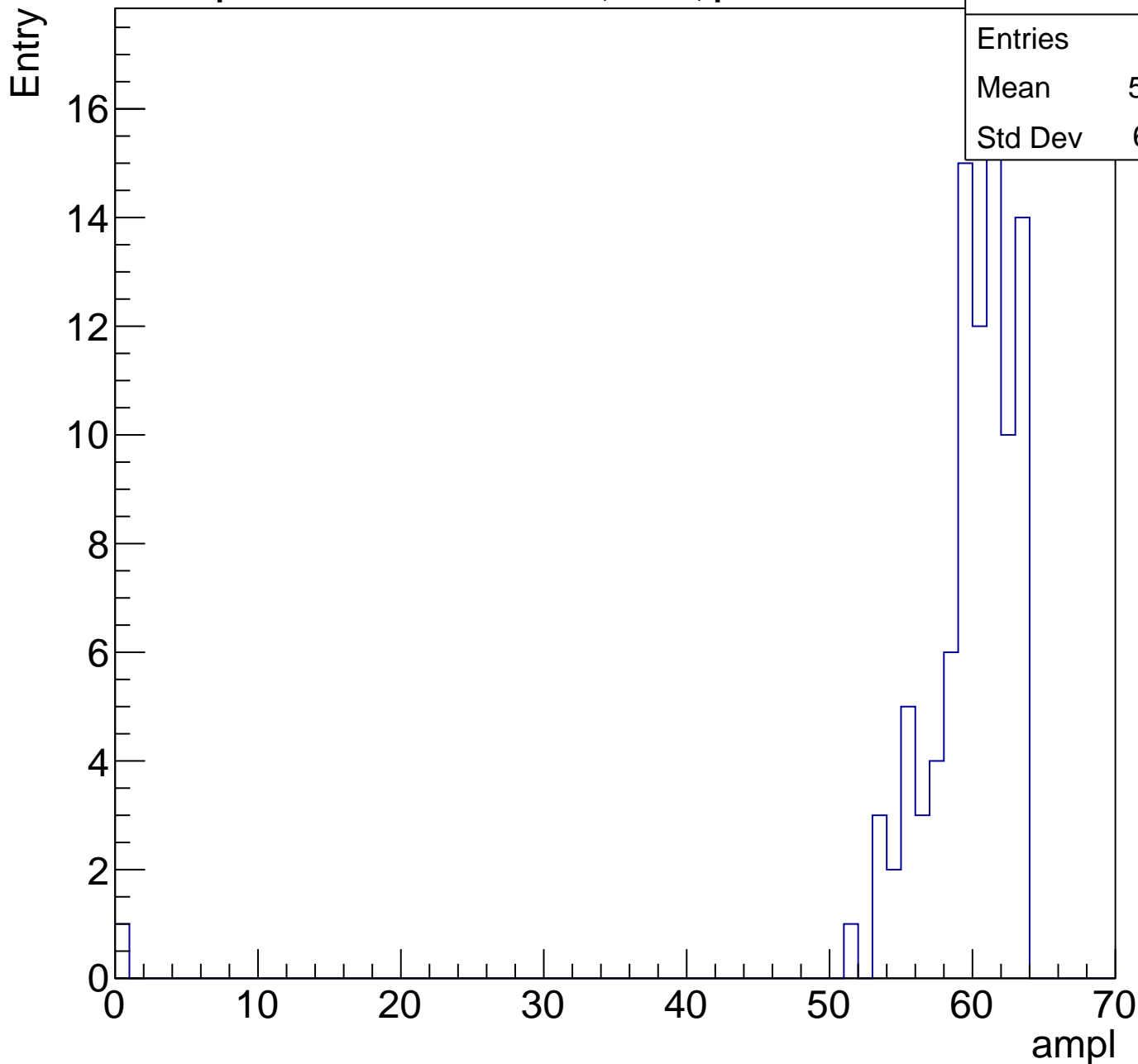
3.331



B0L101S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	93
Mean	58.94
Std Dev	6.741

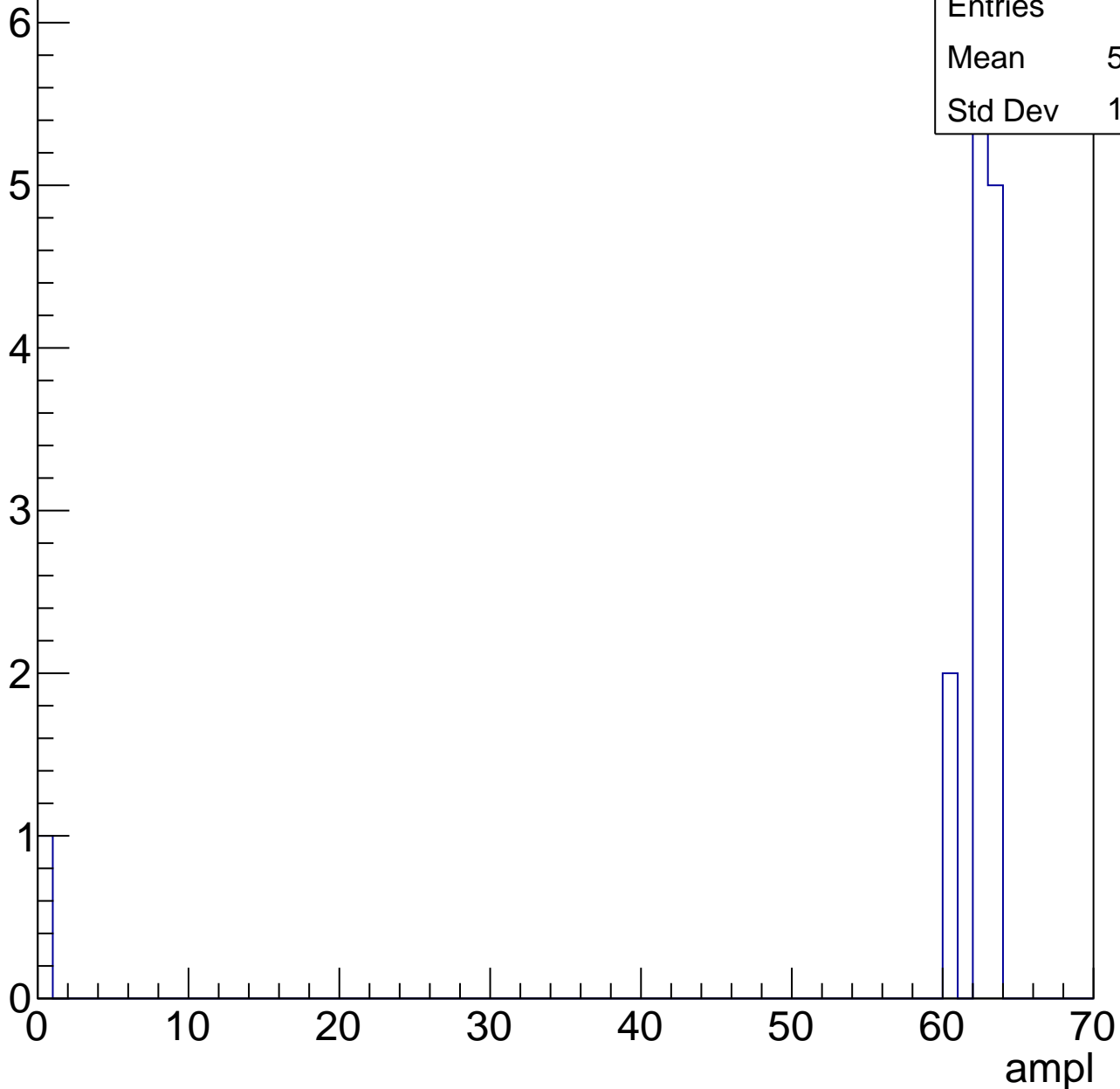


B0L101S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	14
Mean	57.64
Std Dev	16.02



B0L101S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch96, adc0

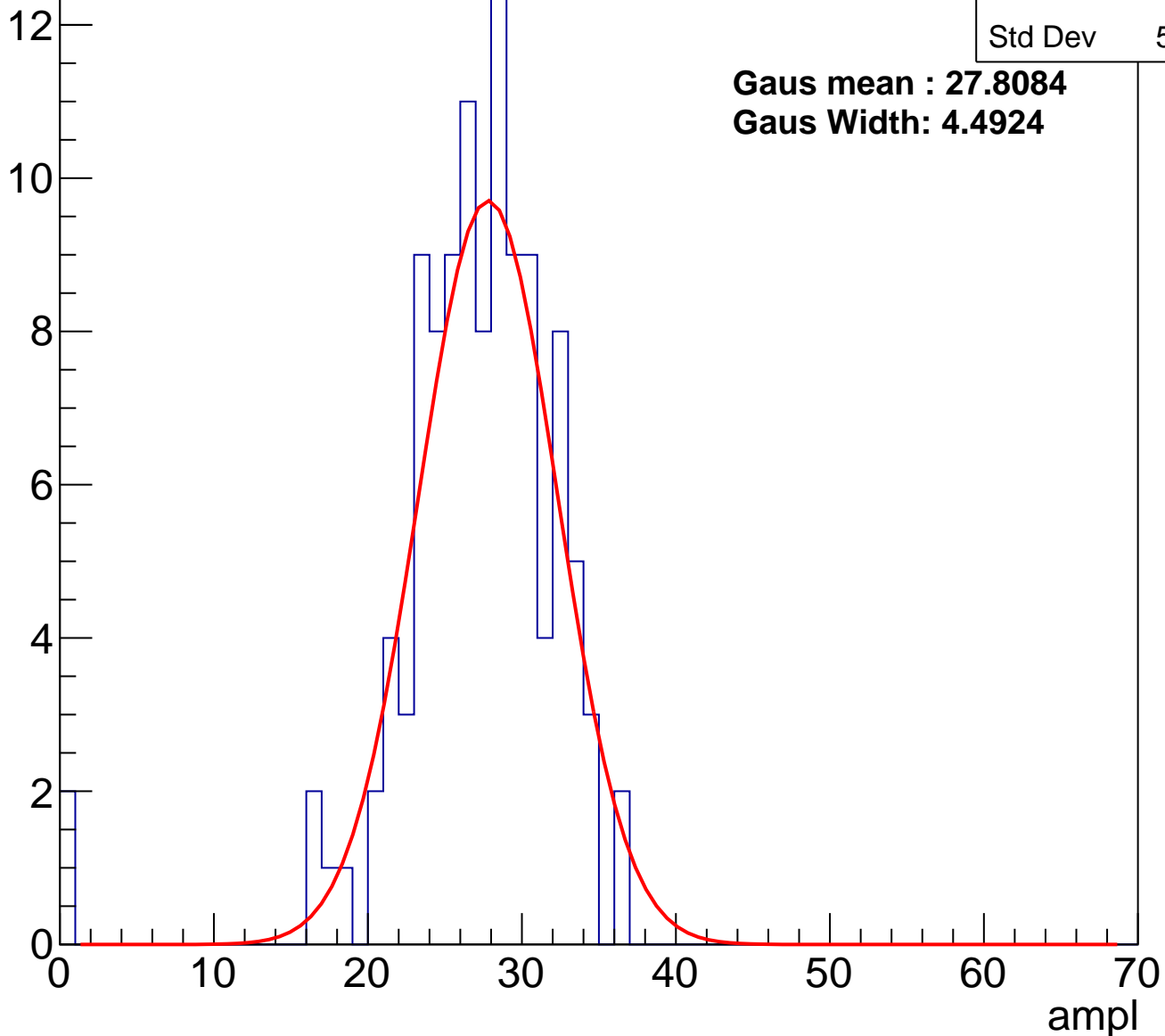
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	26.52
Std Dev	5.419

Gaus mean : 27.8084

Gaus Width: 4.4924

Entry



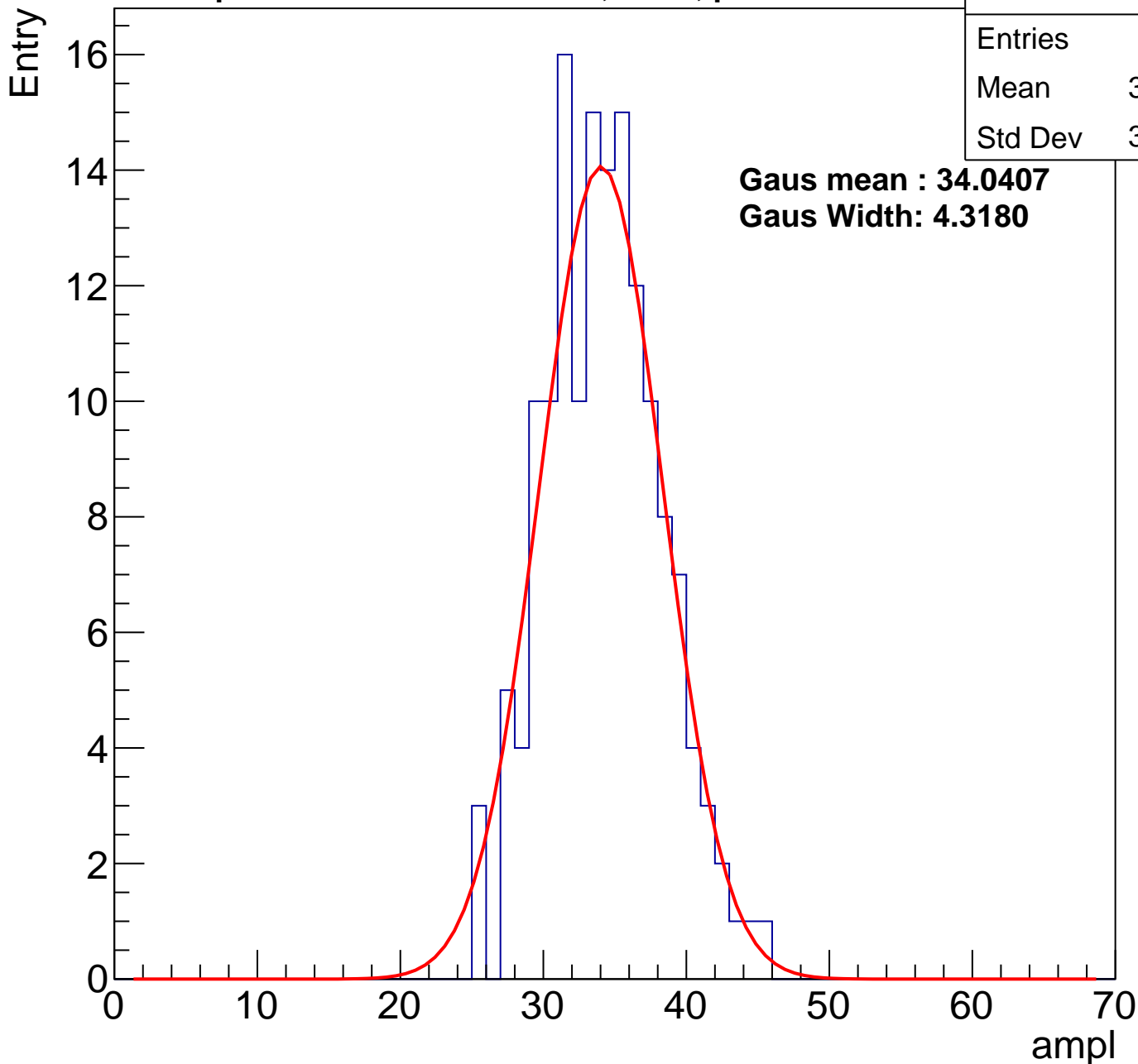
B0L101S, U15-ch96, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	33.79
Std Dev	3.996

Gaus mean : 34.0407

Gaus Width: 4.3180



B0L101S, U15-ch96, adc2

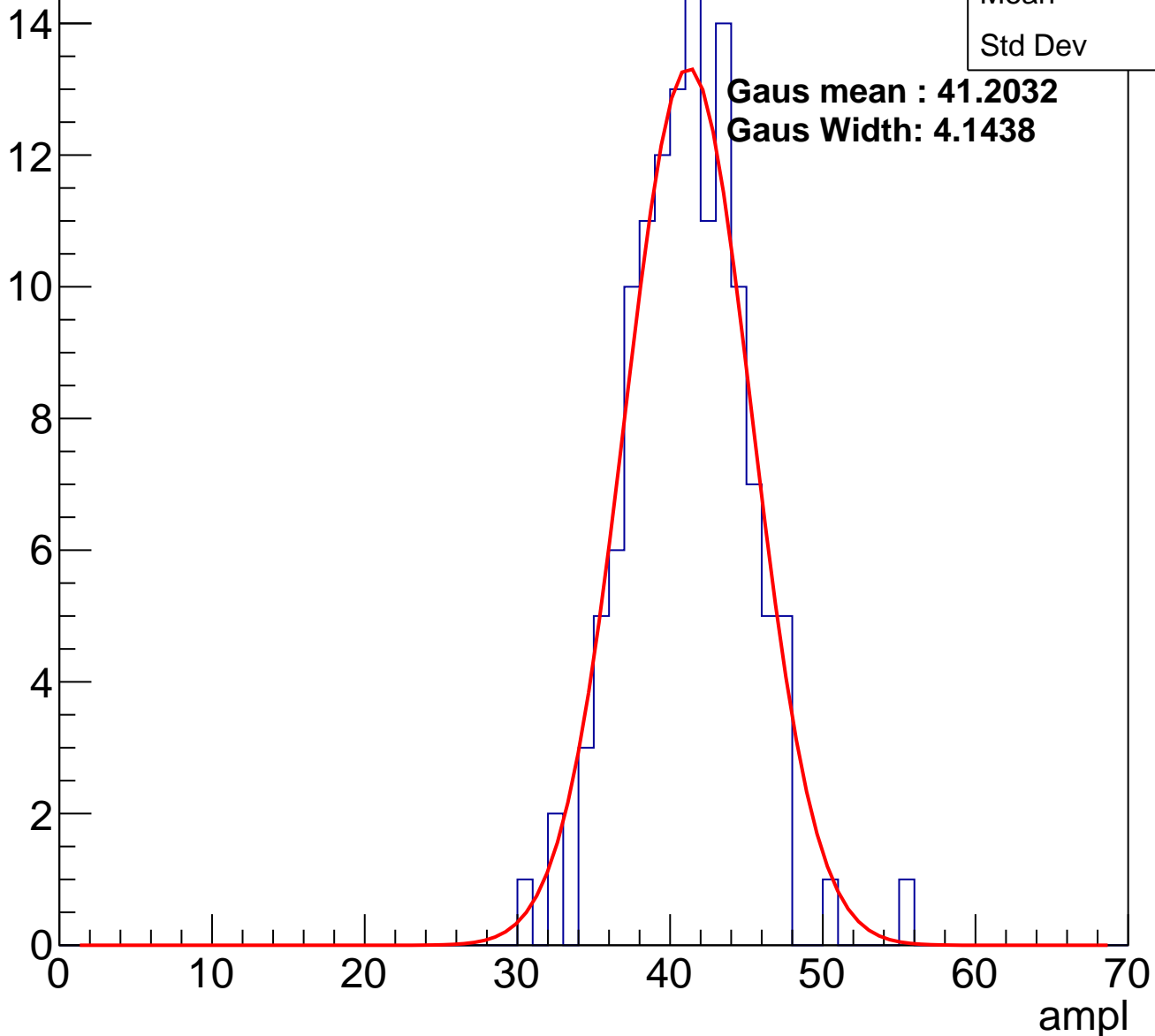
calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	40.66
Std Dev	3.81

Gaus mean : 41.2032

Gaus Width: 4.1438

Entry



B0L101S, U15-ch96, adc3

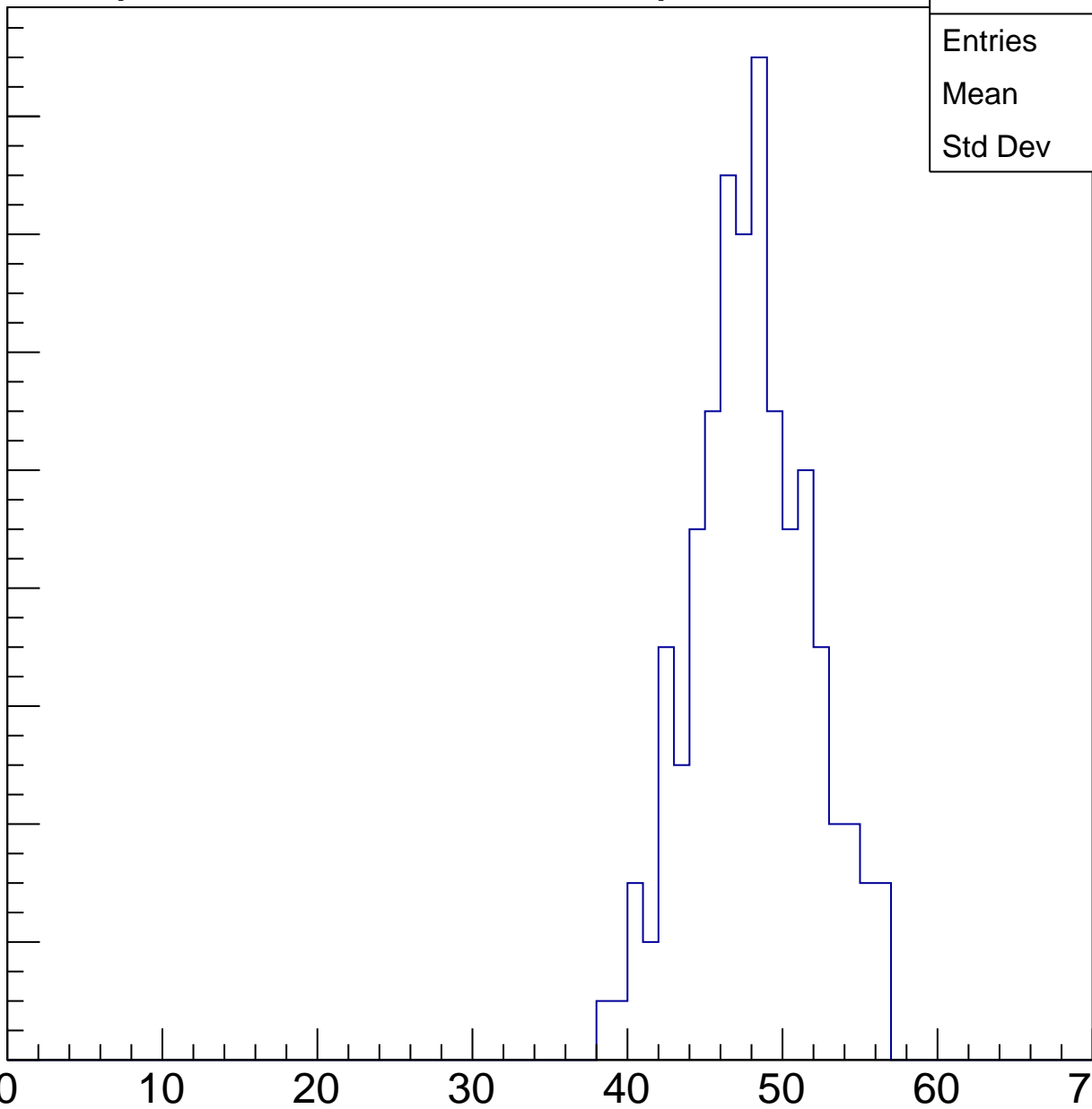
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	136
Mean	47.55
Std Dev	3.806

ampl

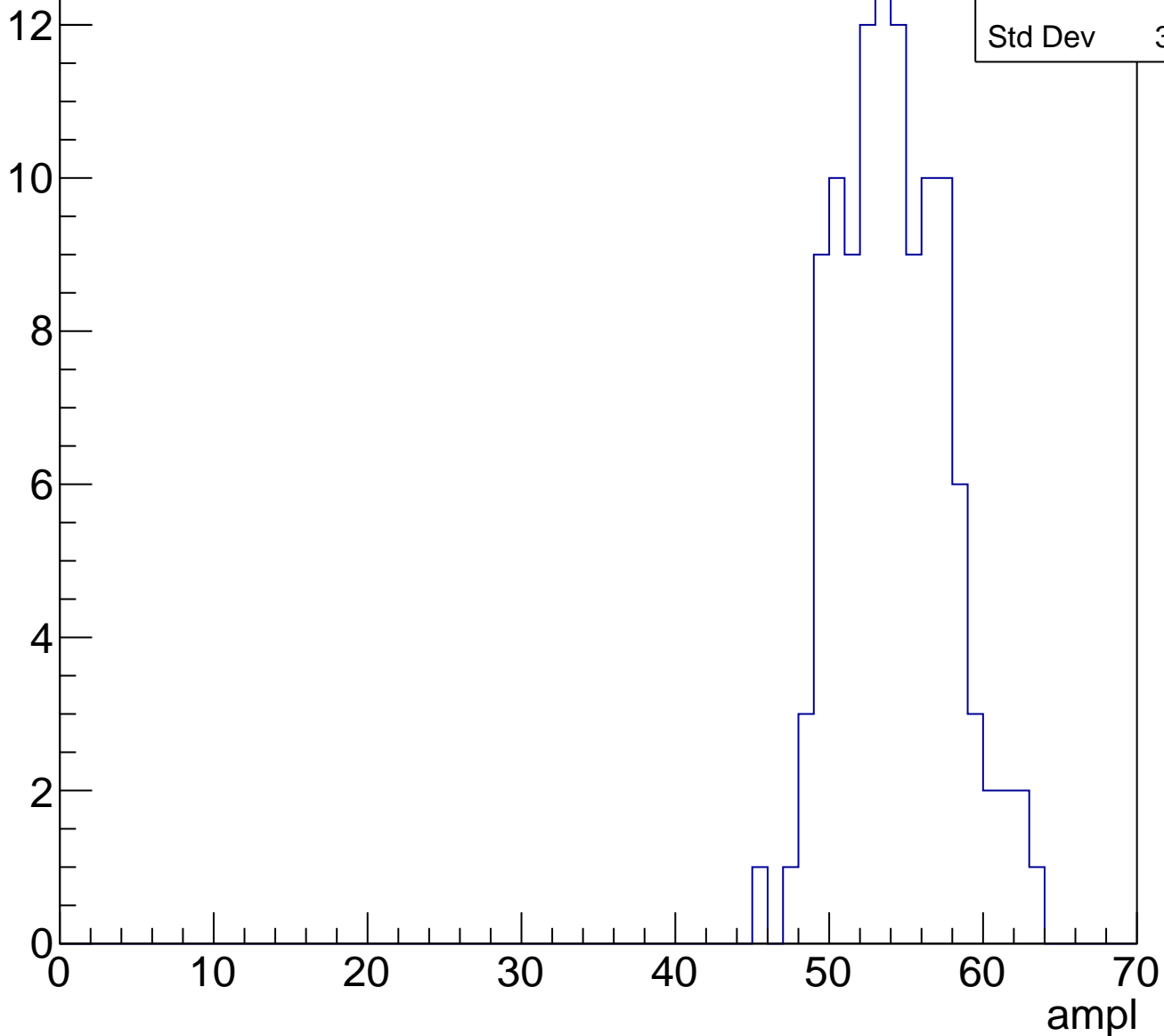


B0L101S, U15-ch96, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	115
Mean	53.7
Std Dev	3.543

Entry



B0L101S, U15-ch96, adc5

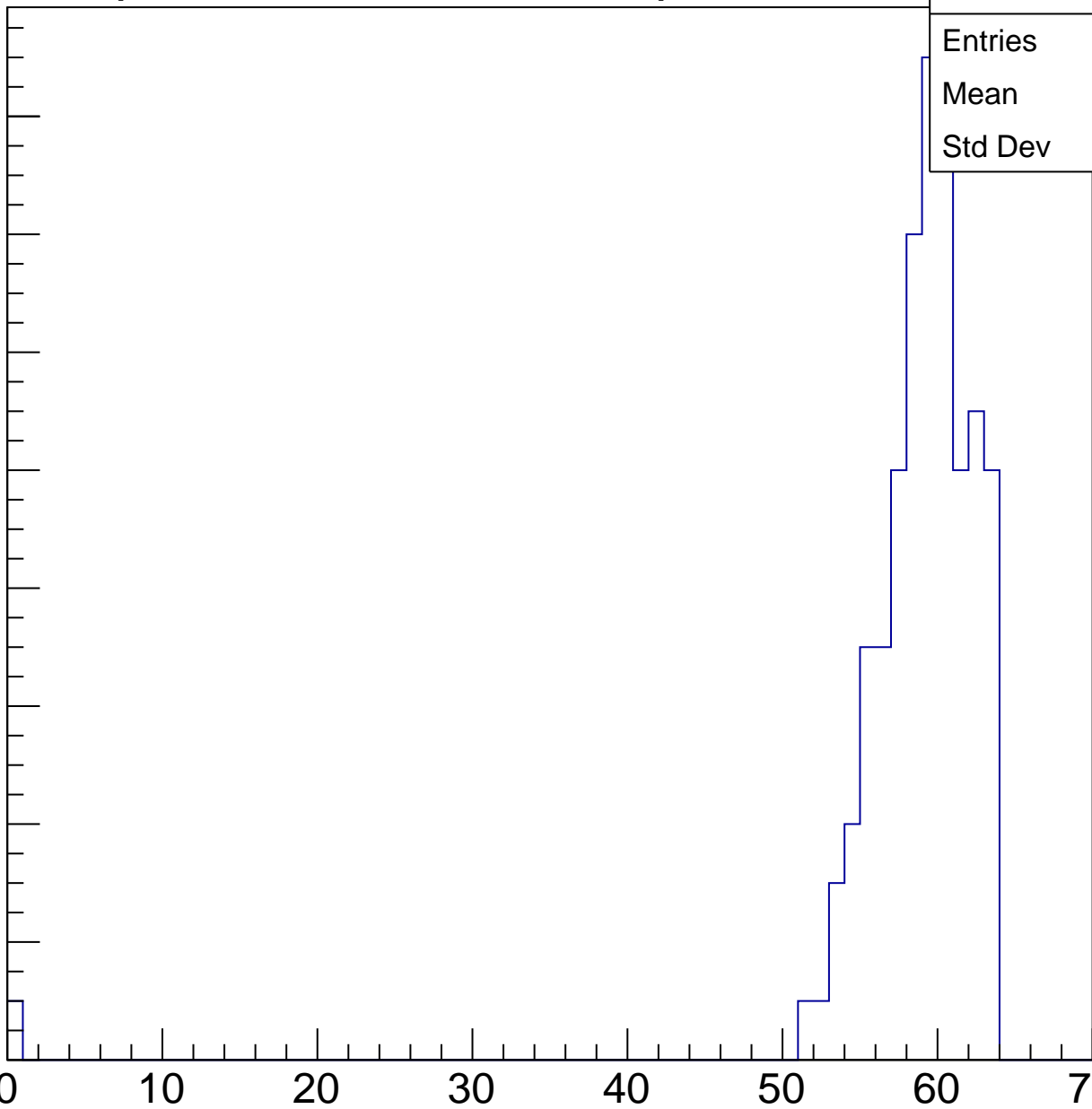
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.23
Std Dev	6.182

ampl

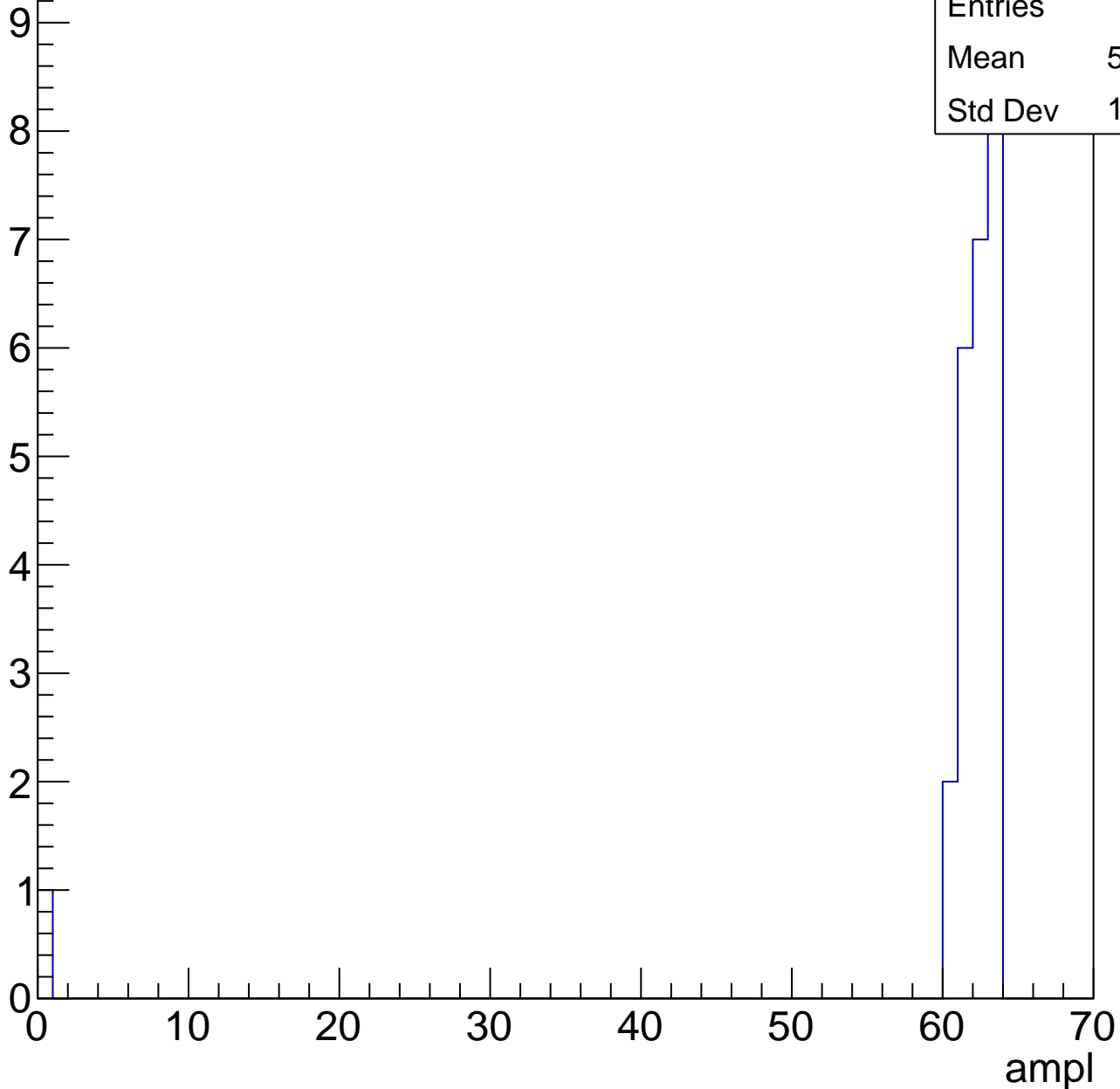


B0L101S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

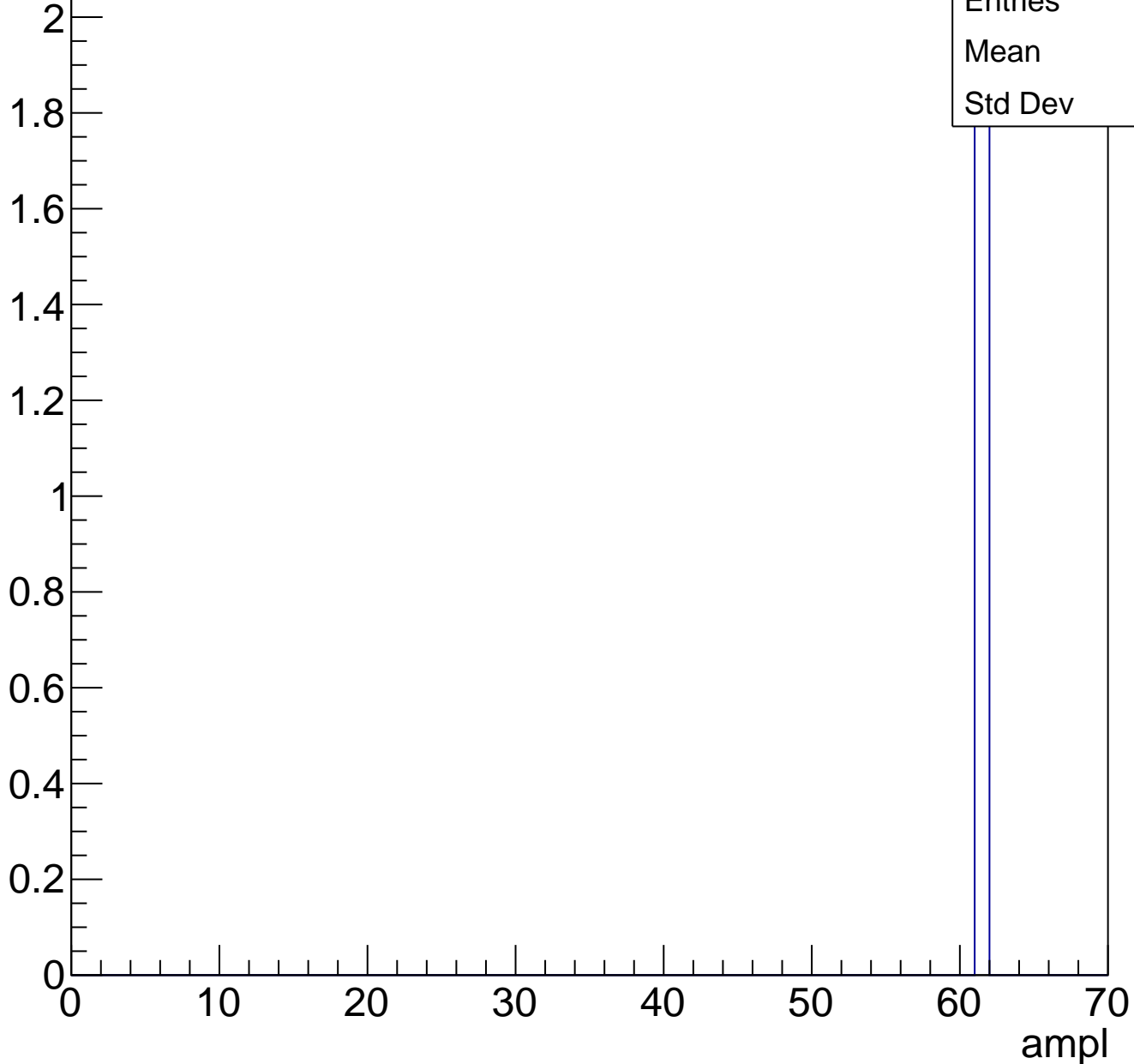
Entries	25
Mean	59.48
Std Dev	12.18



B0L101S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch97, adc0

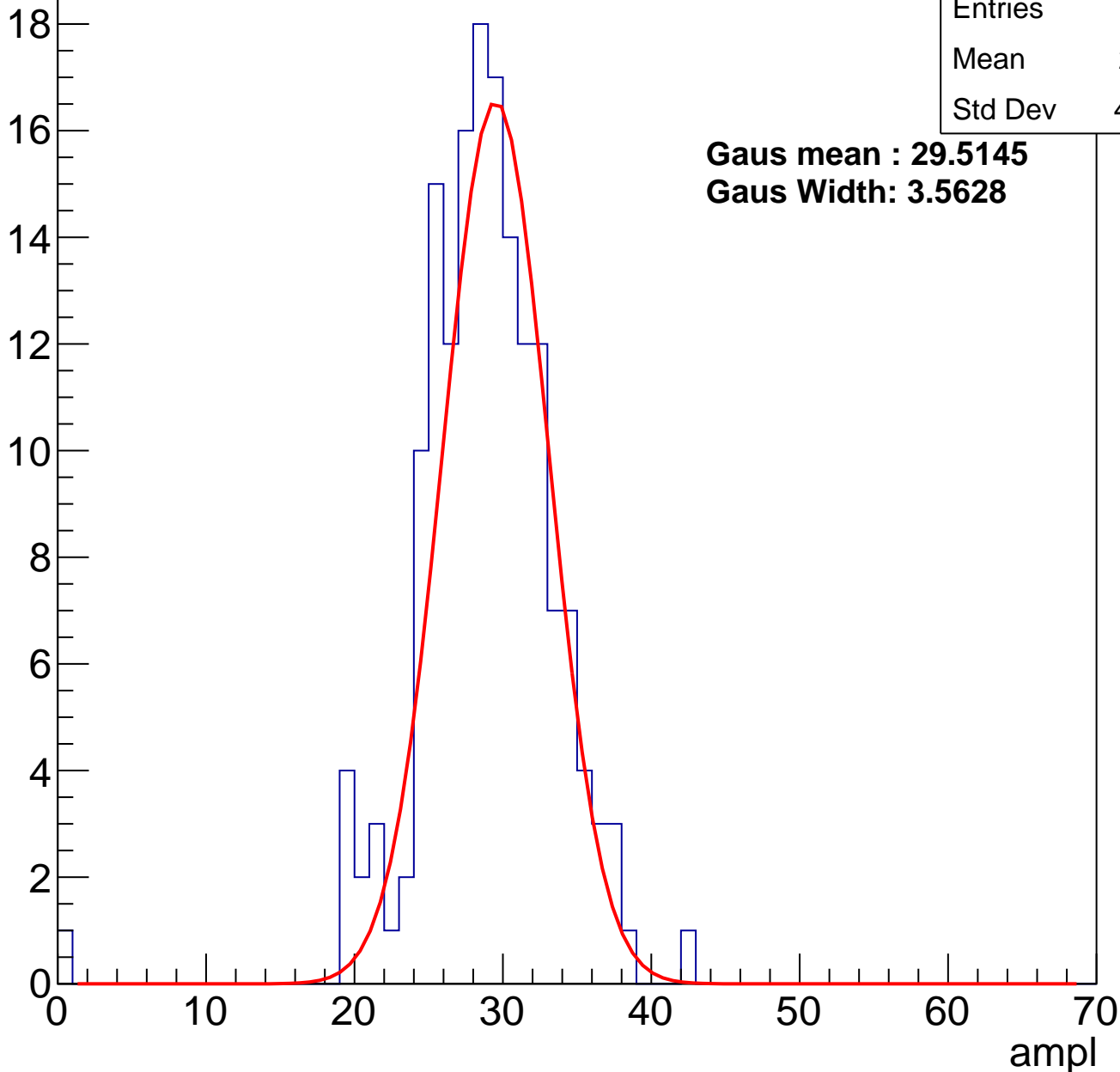
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	28.41
Std Dev	4.624

Gaus mean : 29.5145

Gaus Width: 3.5628

Entry



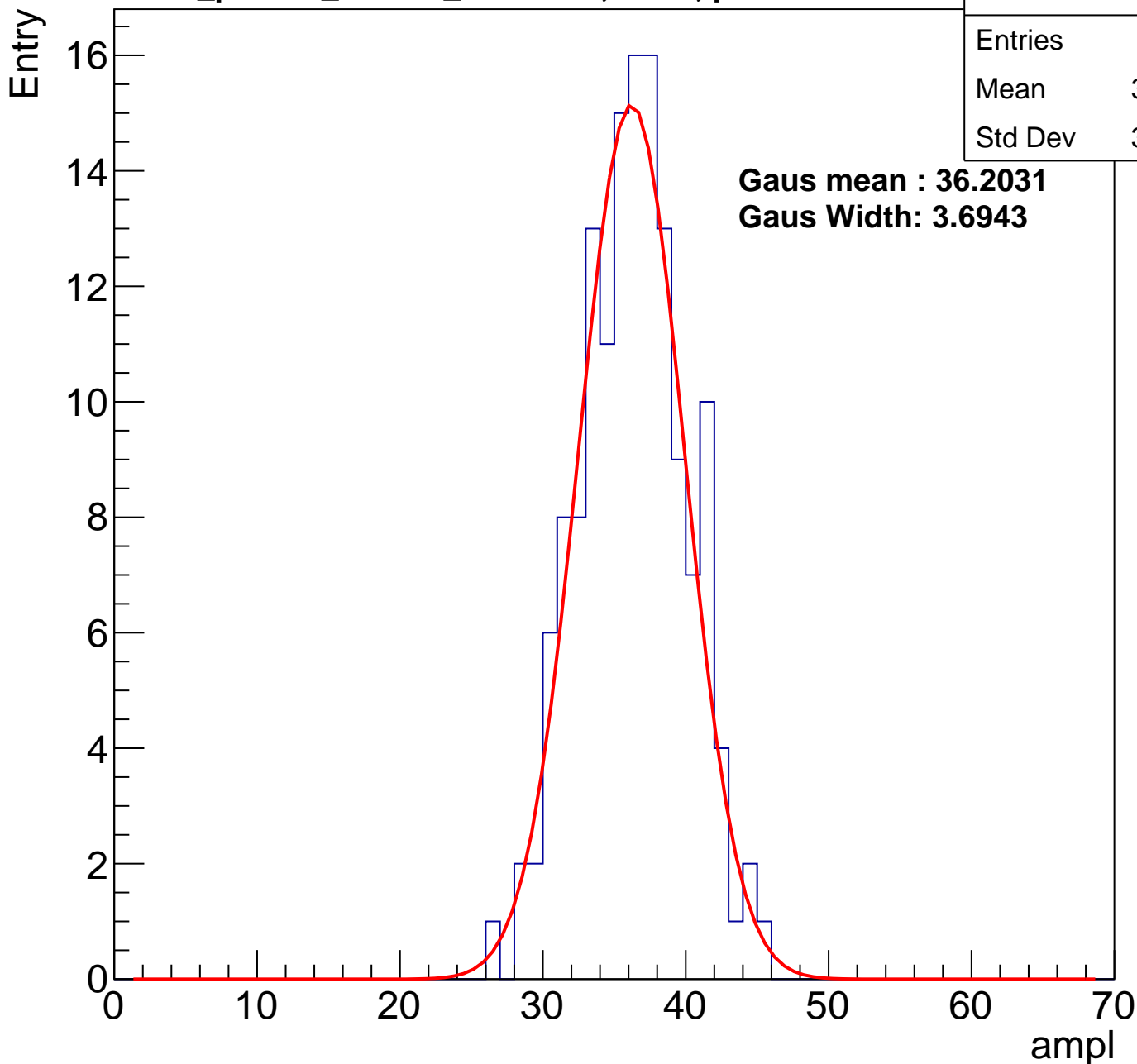
B0L101S, U15-ch97, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	35.86
Std Dev	3.673

Gaus mean : 36.2031

Gaus Width: 3.6943



B0L101S, U15-ch97, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

116

Mean

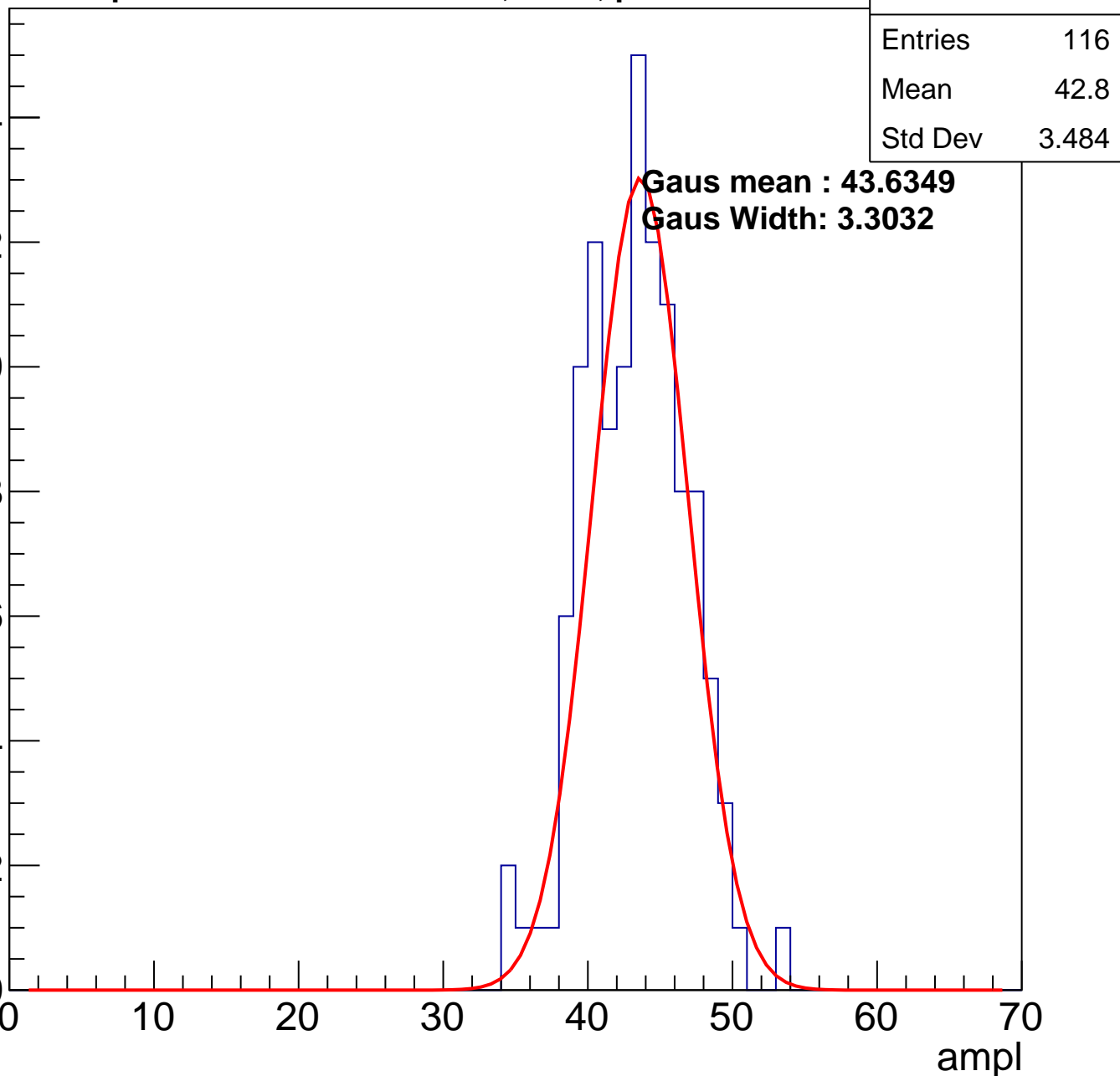
42.8

Std Dev

3.484

Gaus mean : 43.6349

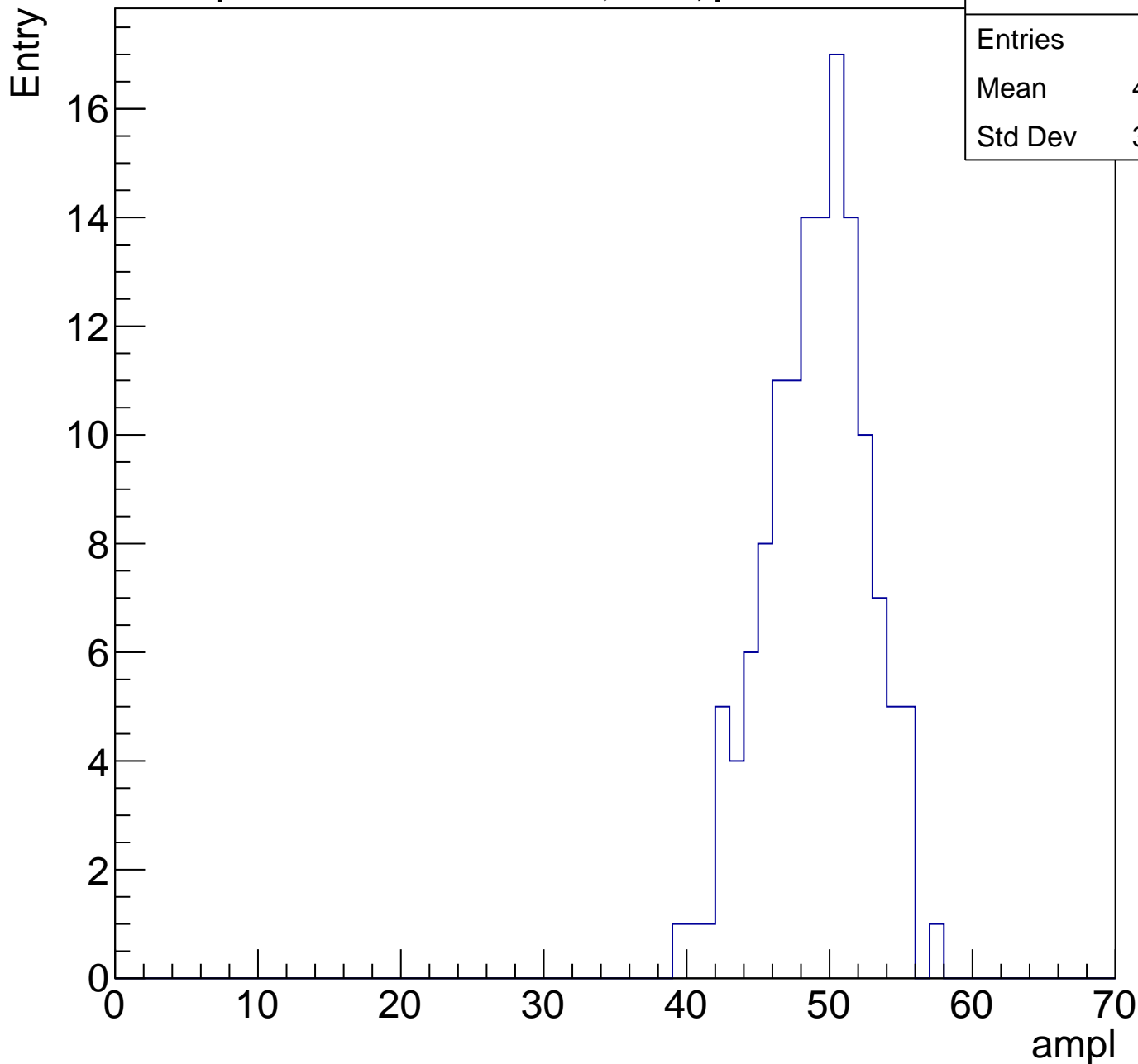
Gaus Width: 3.3032



B0L101S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	48.62
Std Dev	3.542

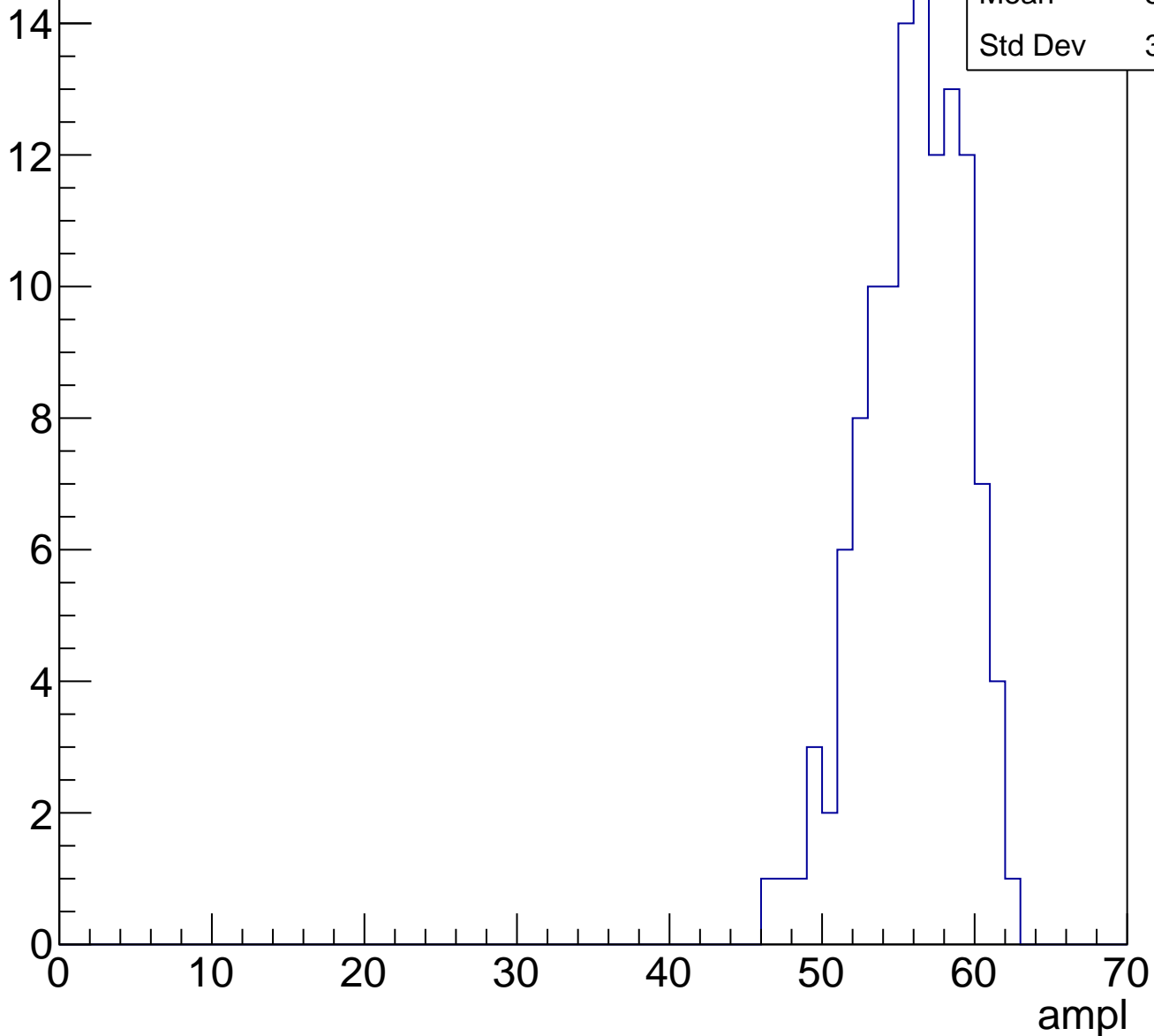


B0L101S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	55.52
Std Dev	3.263

Entry



B0L101S, U15-ch97, adc5

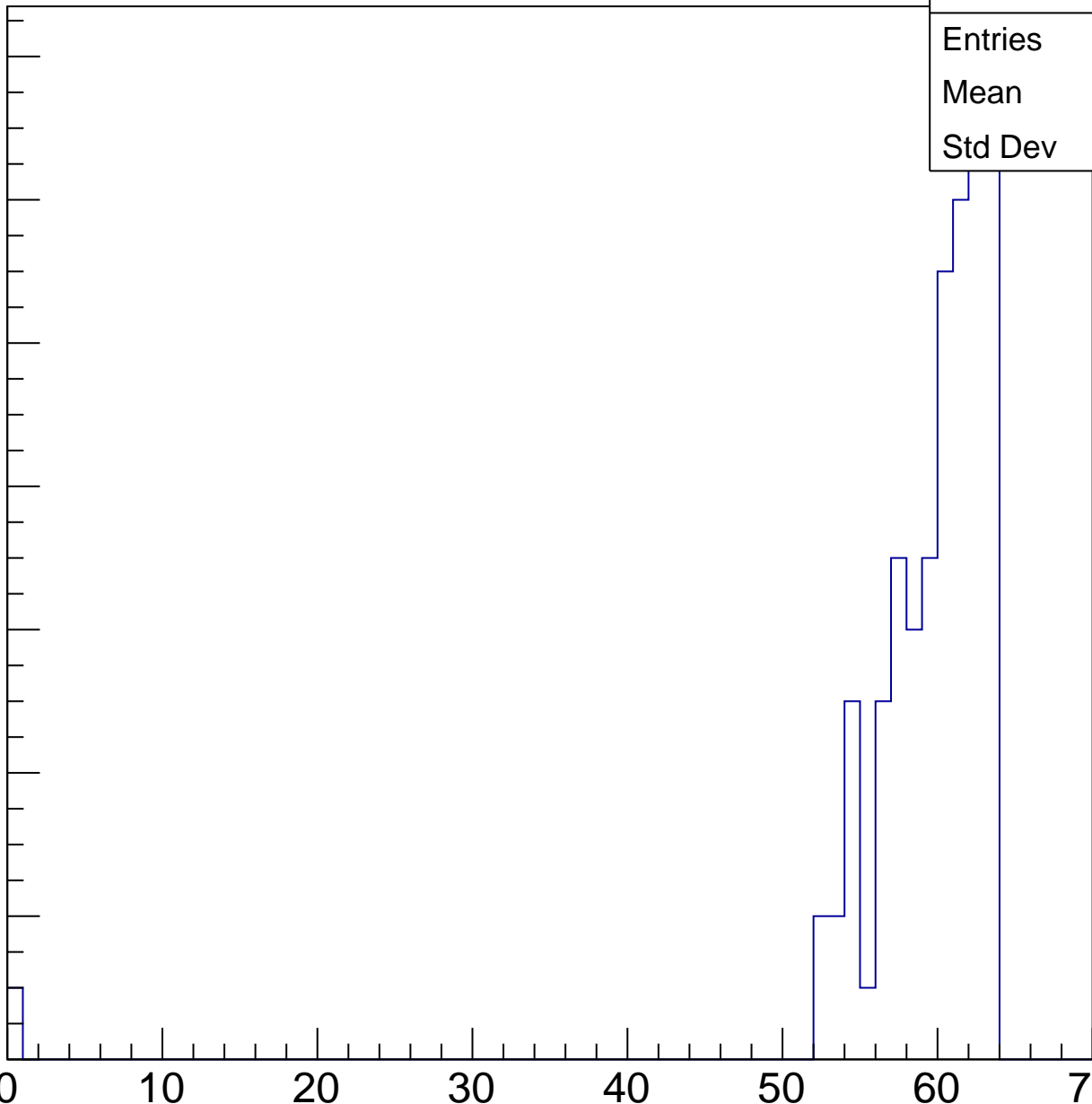
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	58.78
Std Dev	7.044

ampl

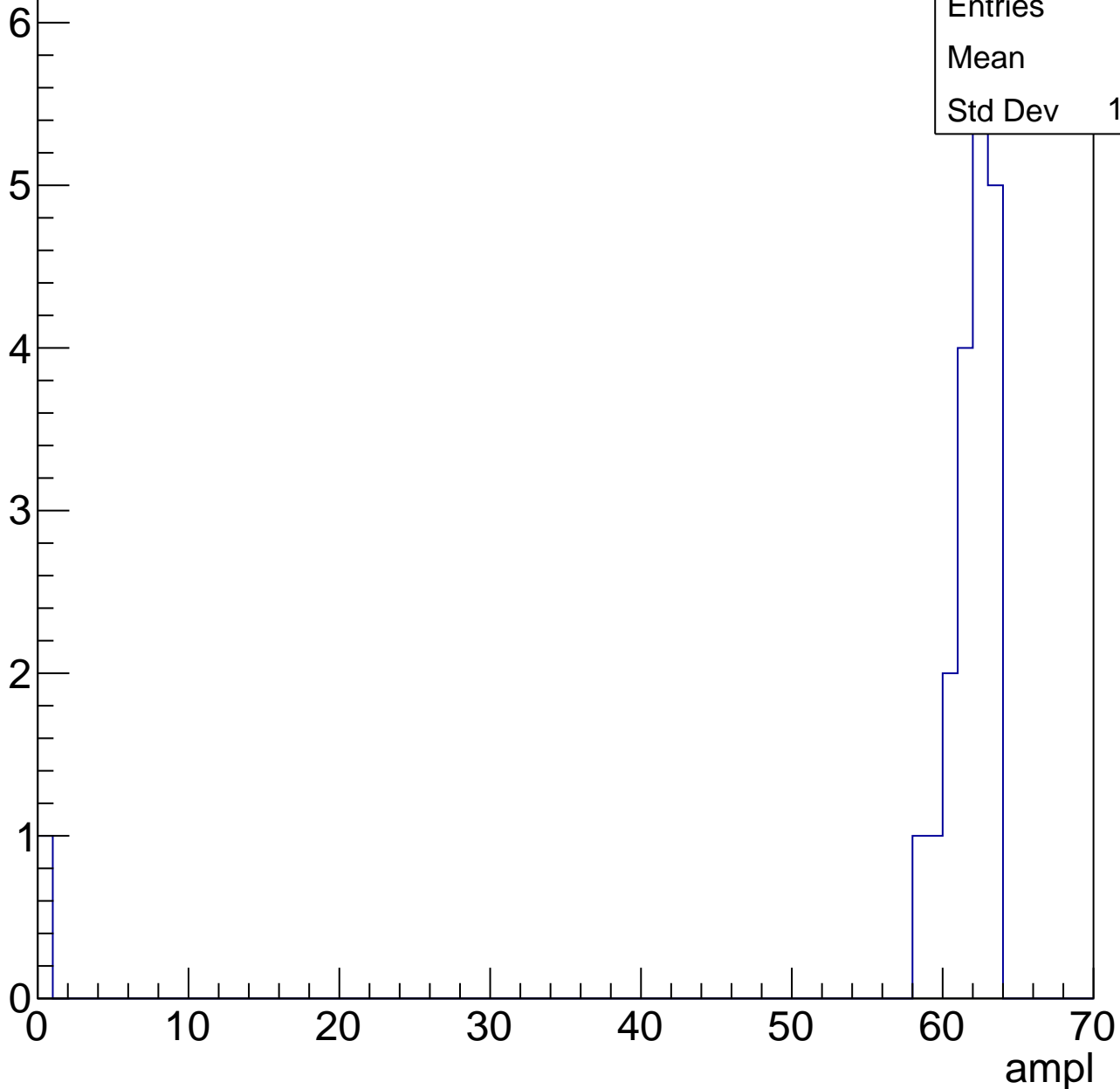


B0L101S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	20
Mean	58.4
Std Dev	13.47



B0L101S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

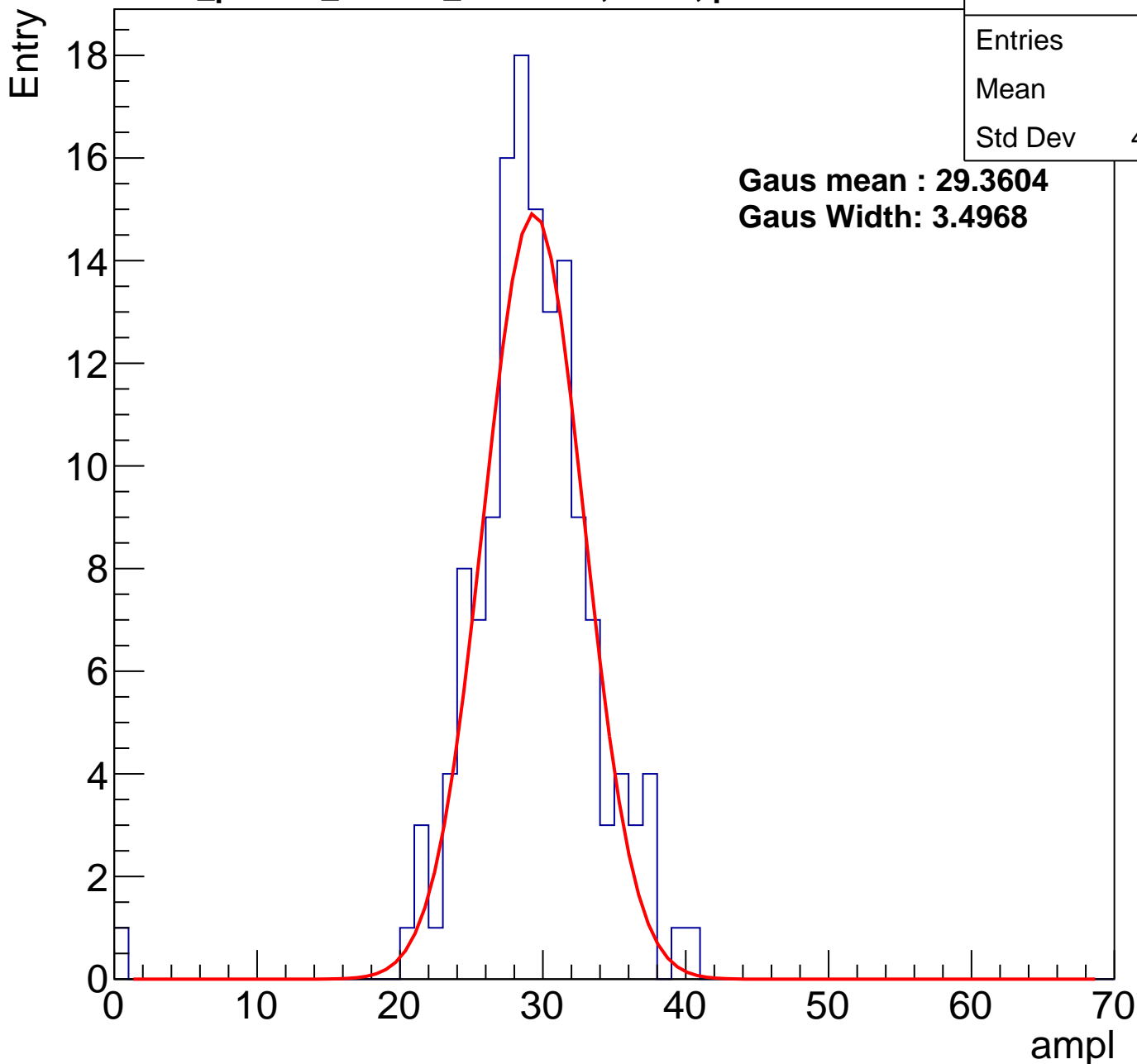
B0L101S, U15-ch98, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	28.8
Std Dev	4.504

Gaus mean : 29.3604

Gaus Width: 3.4968

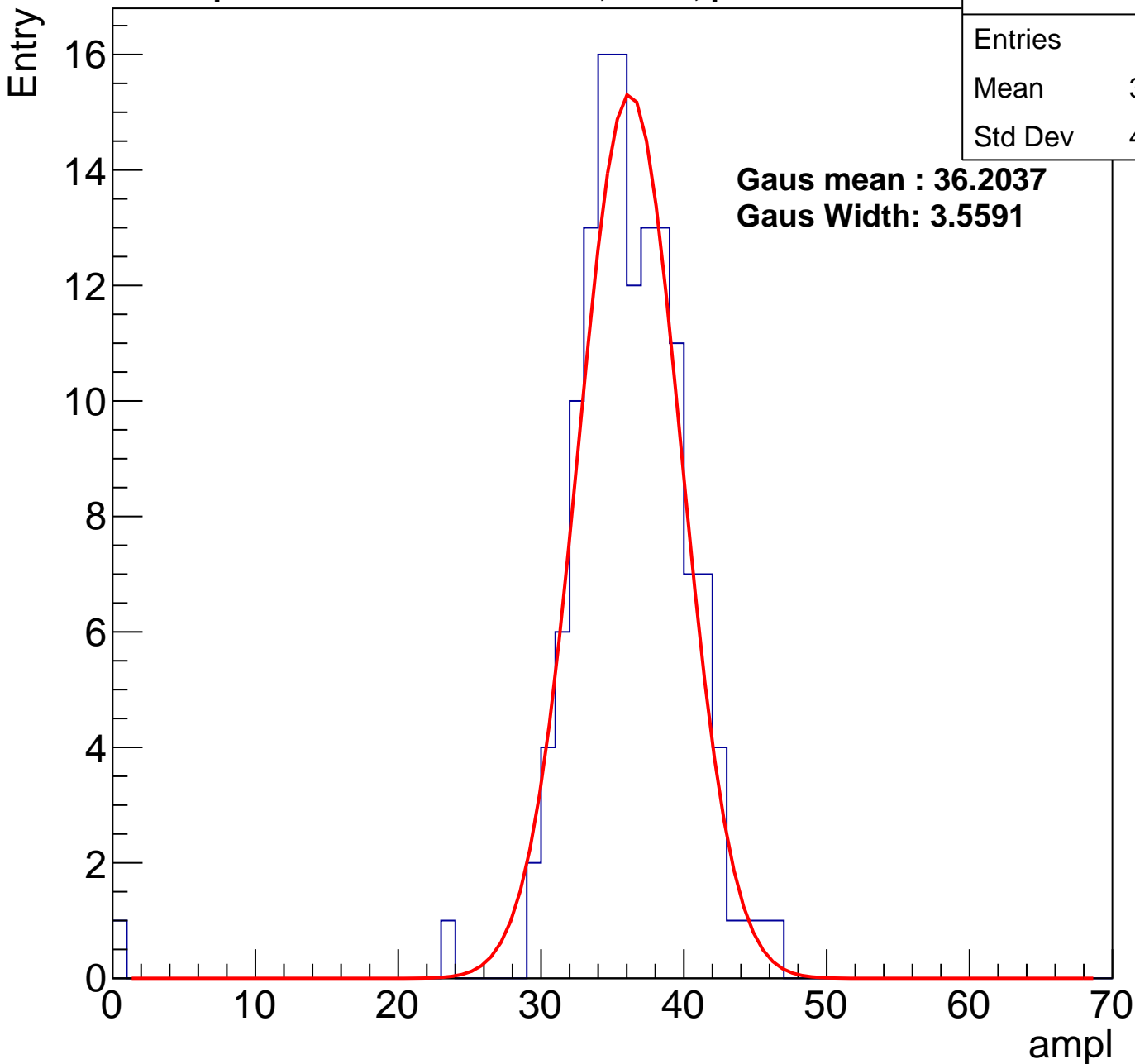


B0L101S, U15-ch98, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	35.64
Std Dev	4.694

Gaus mean : 36.2037
Gaus Width: 3.5591



B0L101S, U15-ch98, adc2

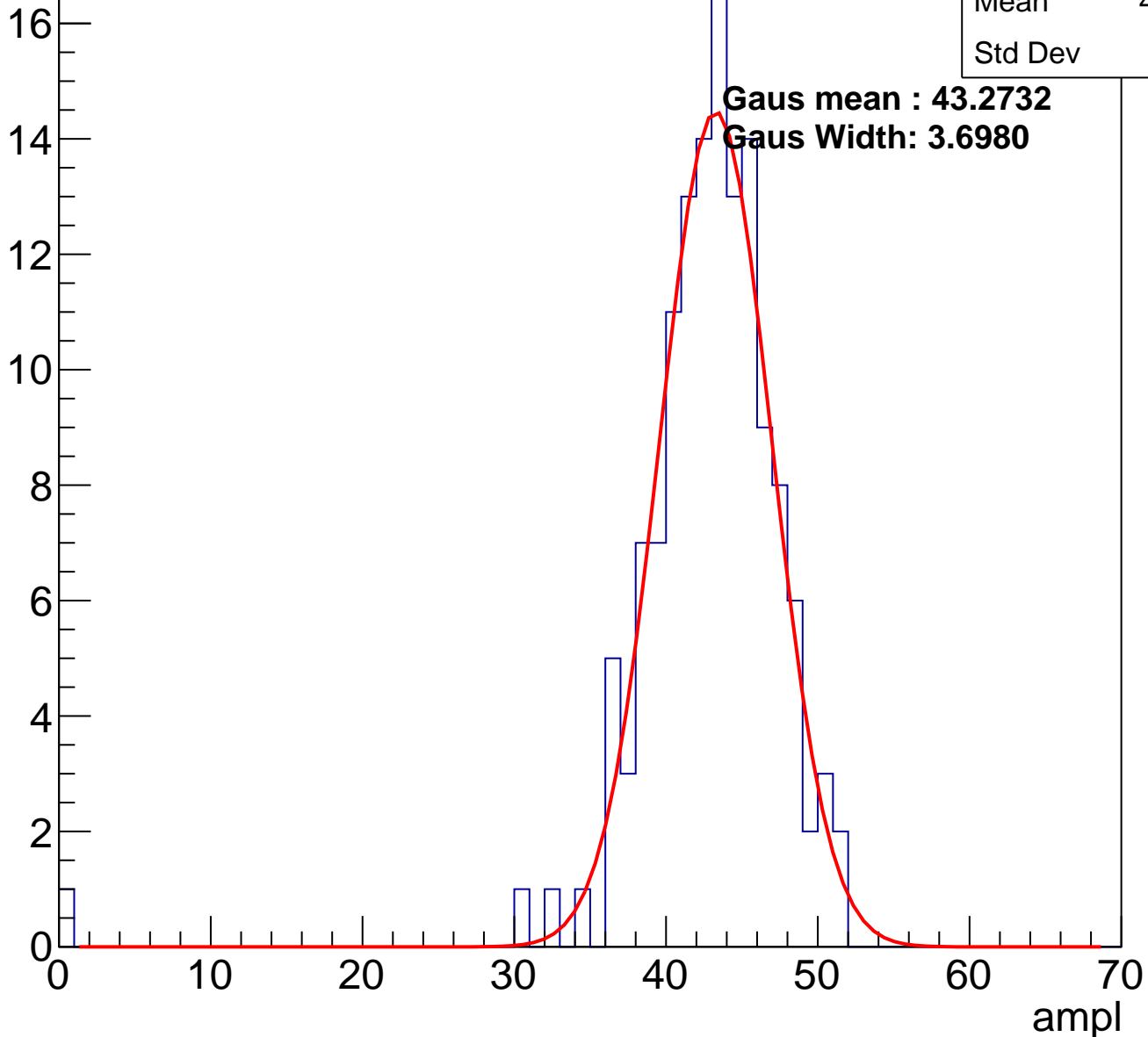
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	138
Mean	42.38
Std Dev	5.21

Gaus mean : 43.2732

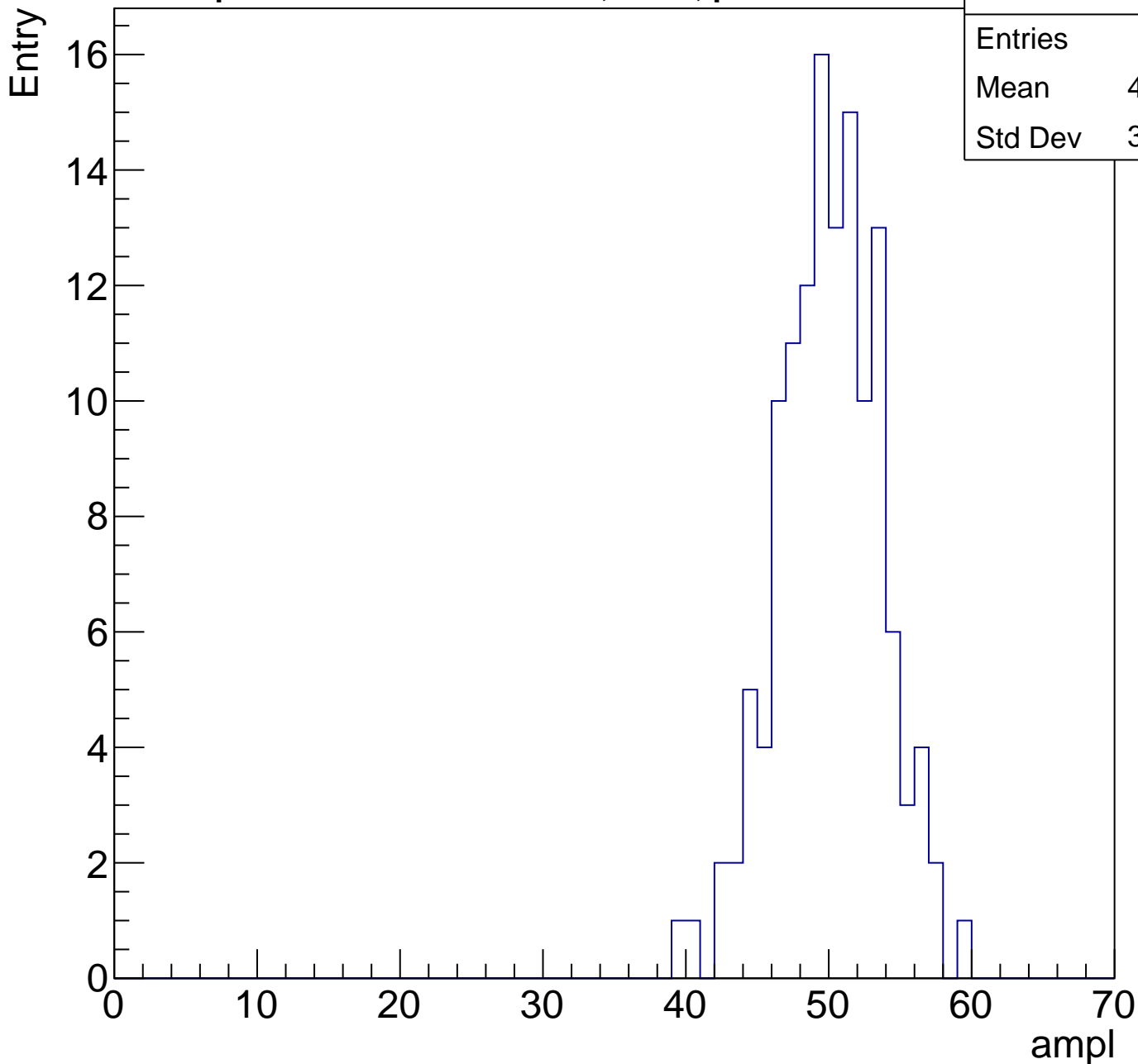
Gaus Width: 3.6980



B0L101S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	49.59
Std Dev	3.596



B0L101S, U15-ch98, adc4

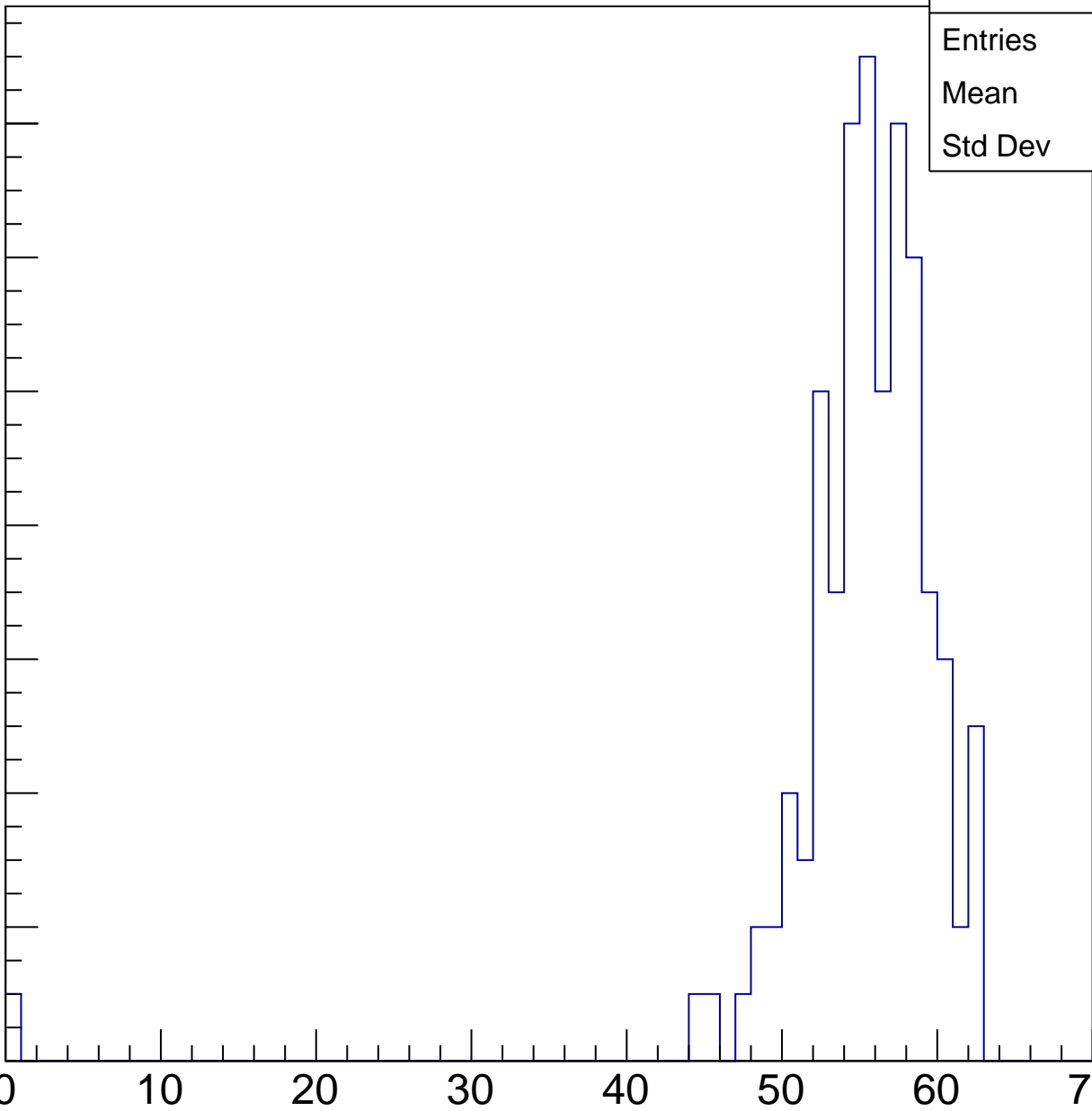
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	54.82
Std Dev	6.208

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch98, adc5

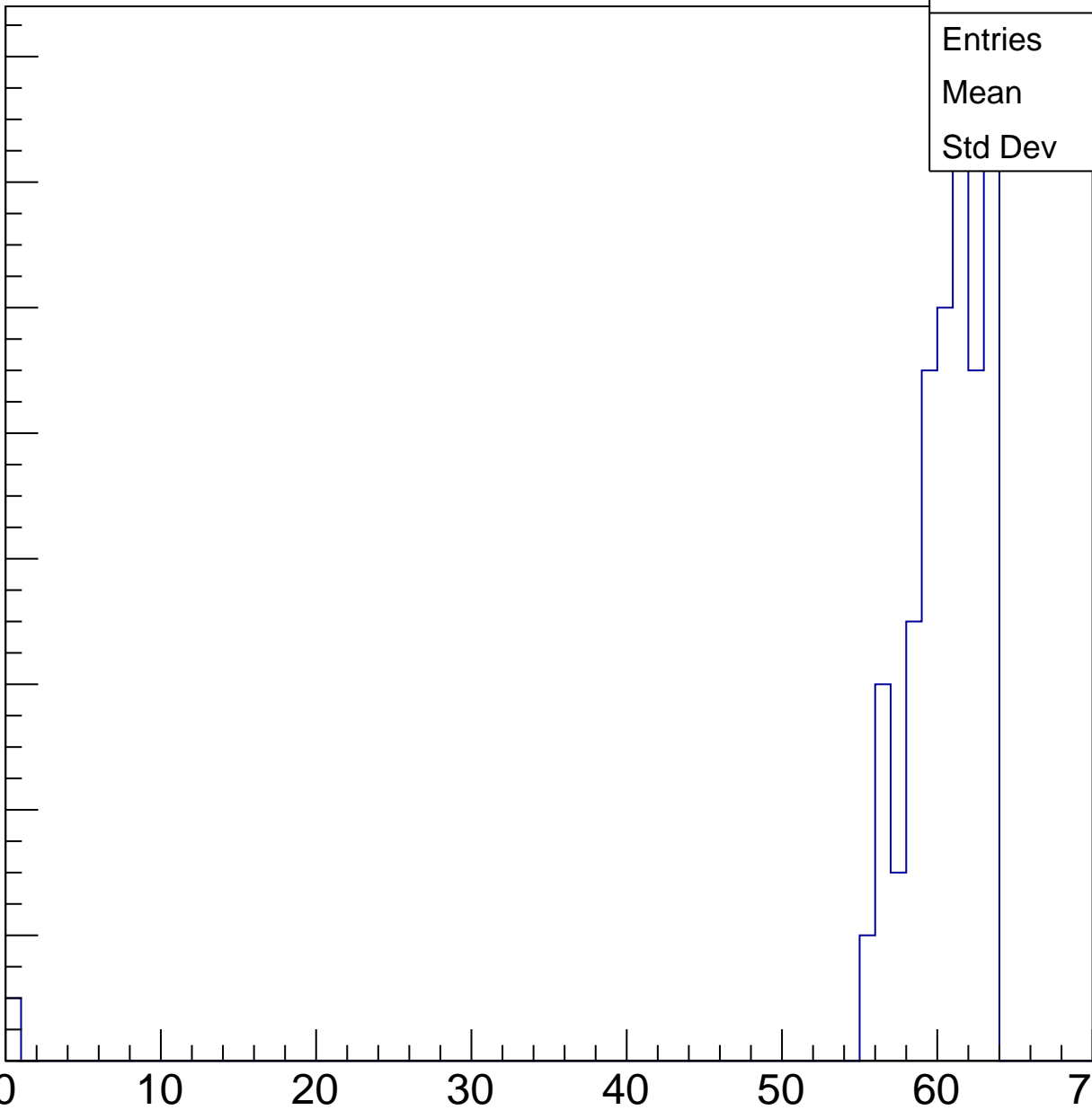
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	59.49
Std Dev	6.895

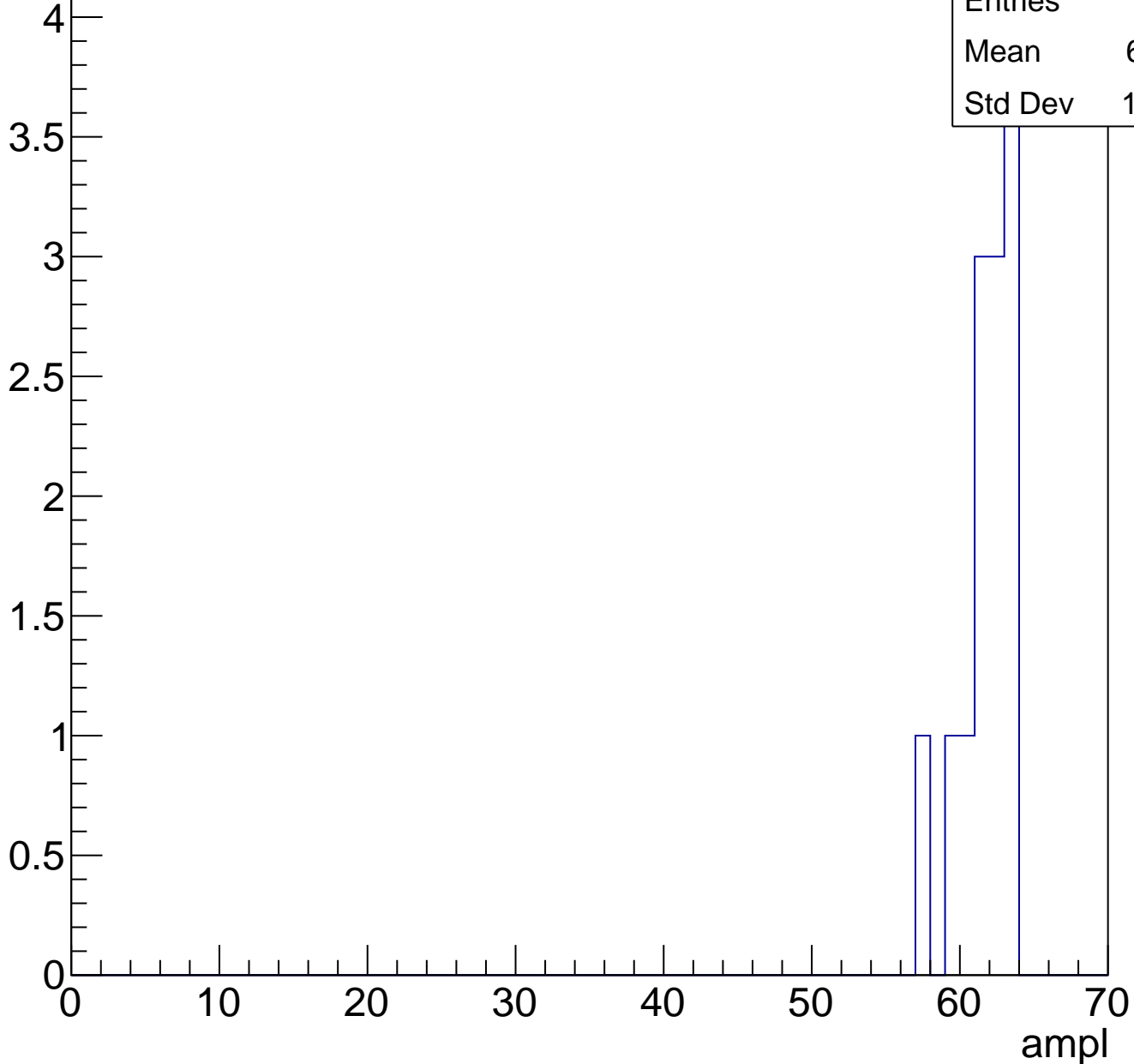
ampl



B0L101S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#1, port C1

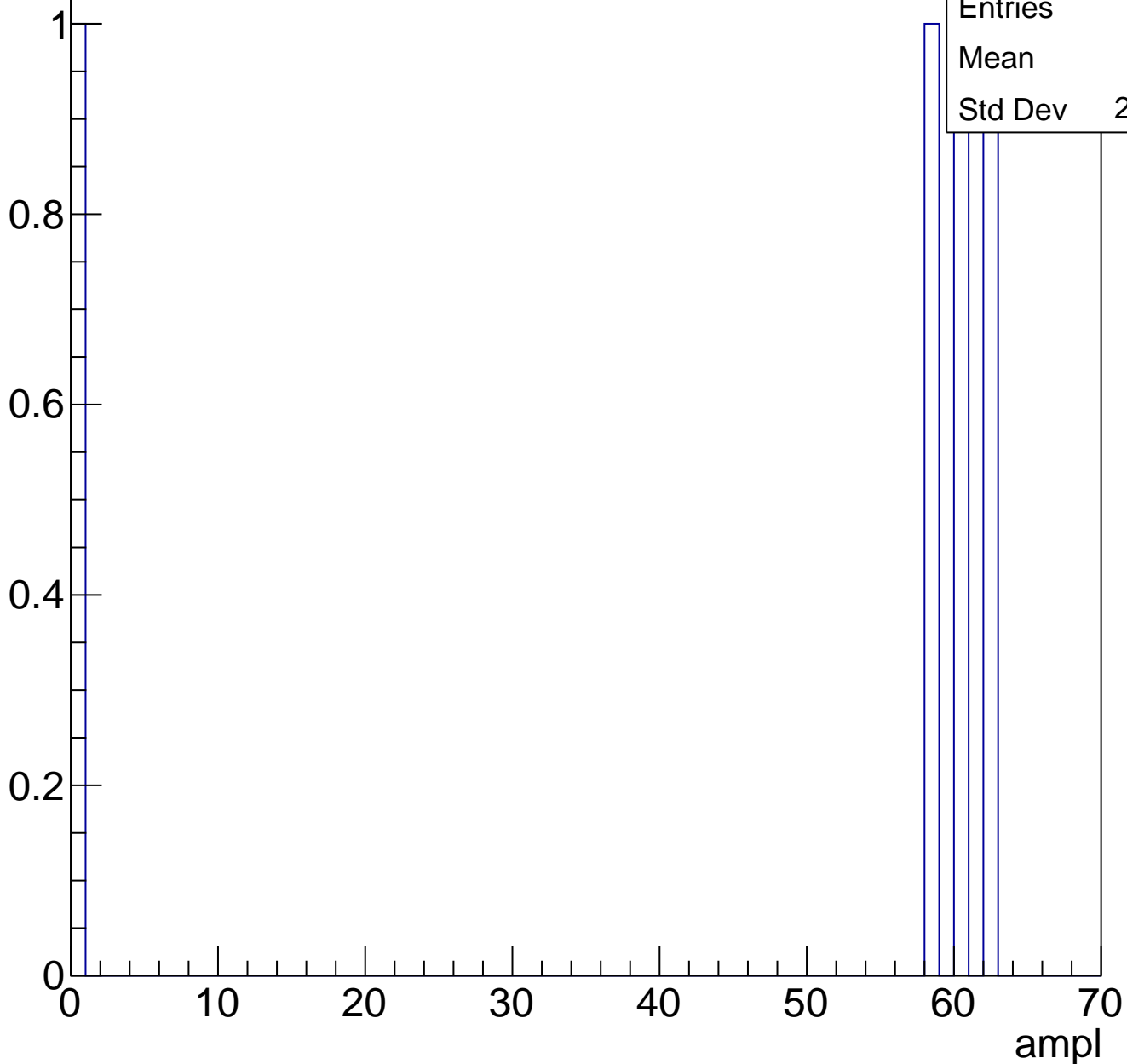
Entry



B0L101S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch99, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	27.83
Std Dev	5.262

Gaus mean : 28.7587

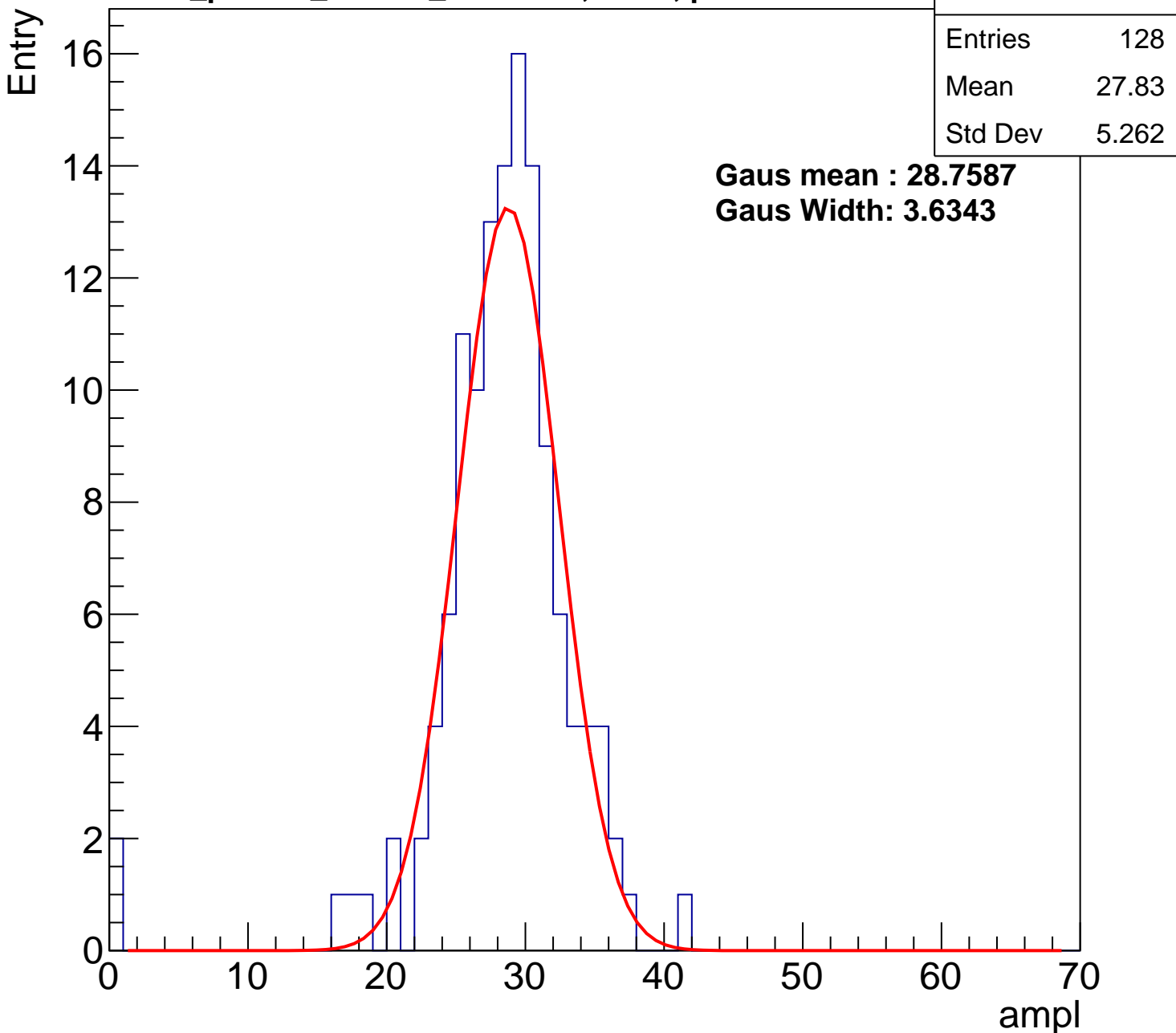
Gaus Width: 3.6343

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



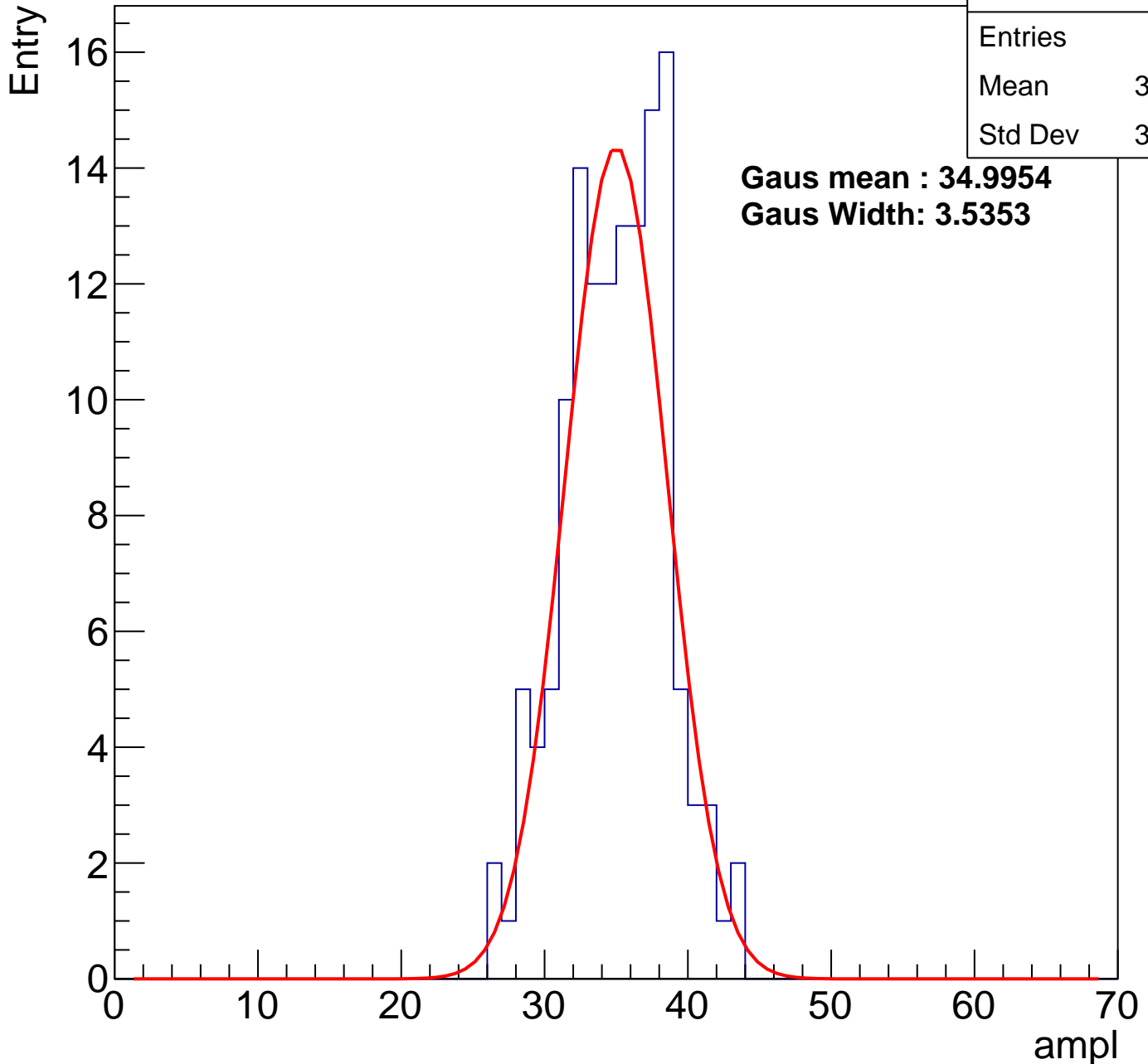
B0L101S, U15-ch99, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	34.55
Std Dev	3.552

Gaus mean : 34.9954

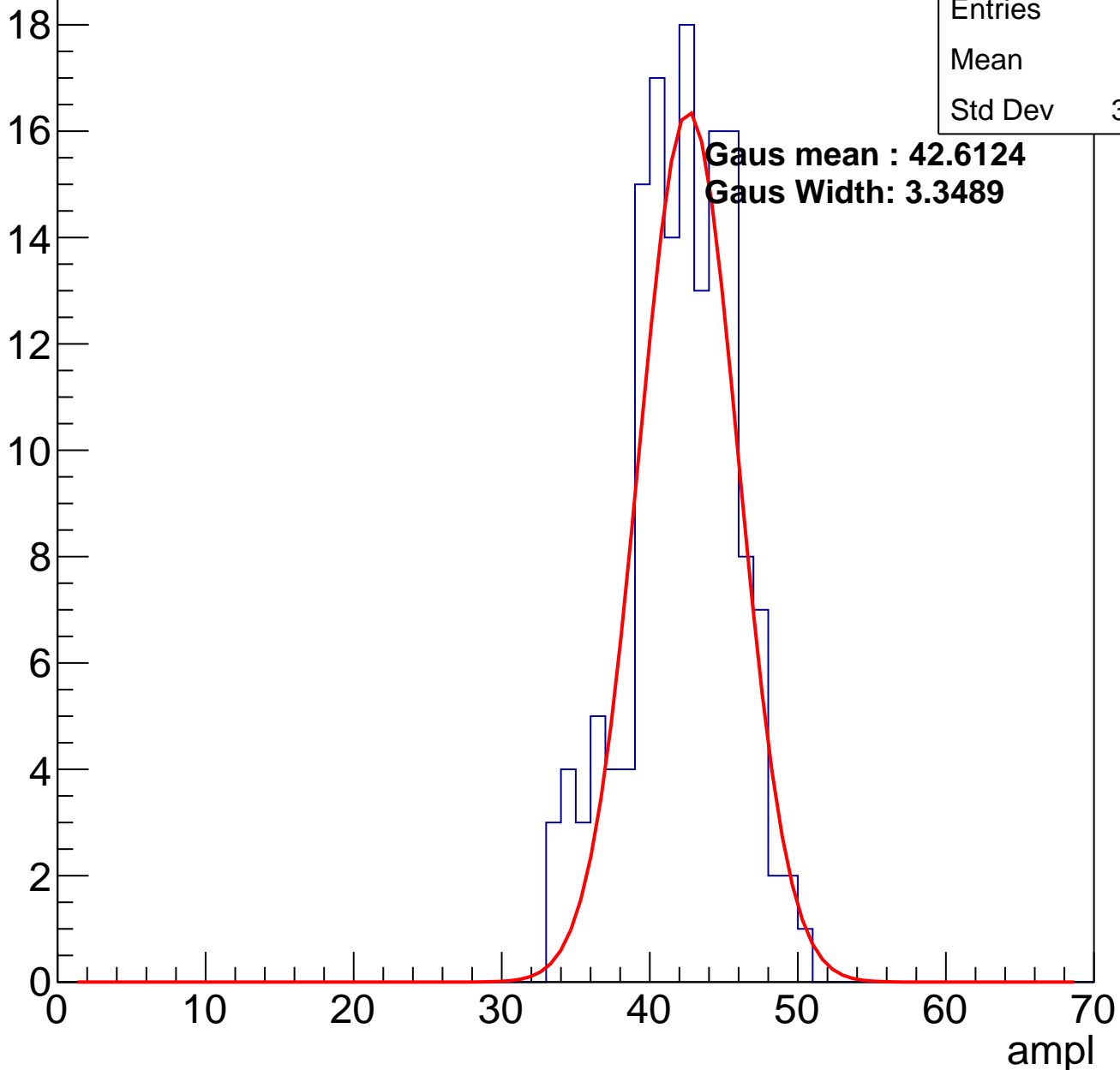
Gaus Width: 3.5353



B0L101S, U15-ch99, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	49.39
Std Dev	3.607

Entry

16
14
12
10
8
6
4
2
0

0

10

20

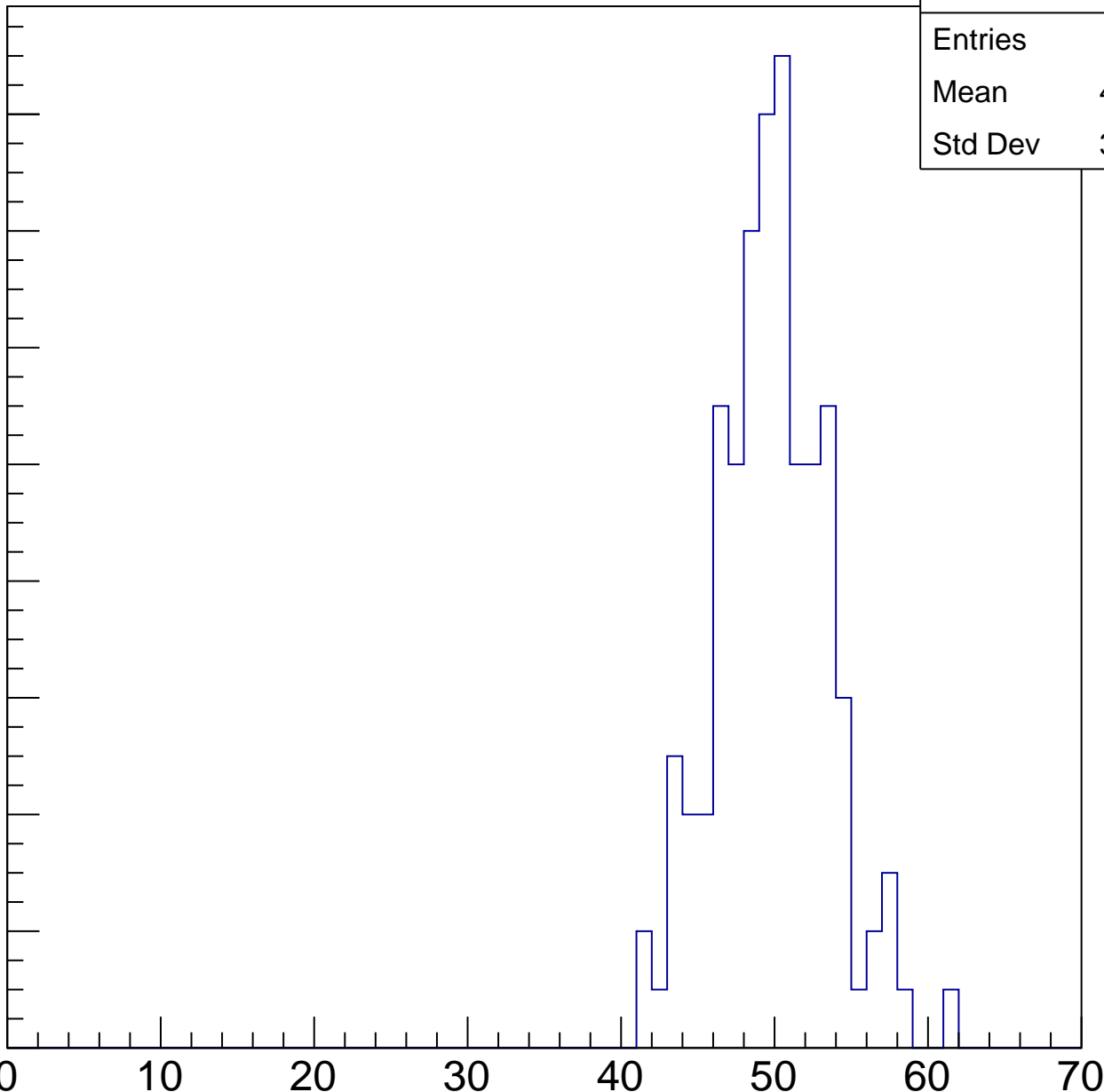
30

40

50

60

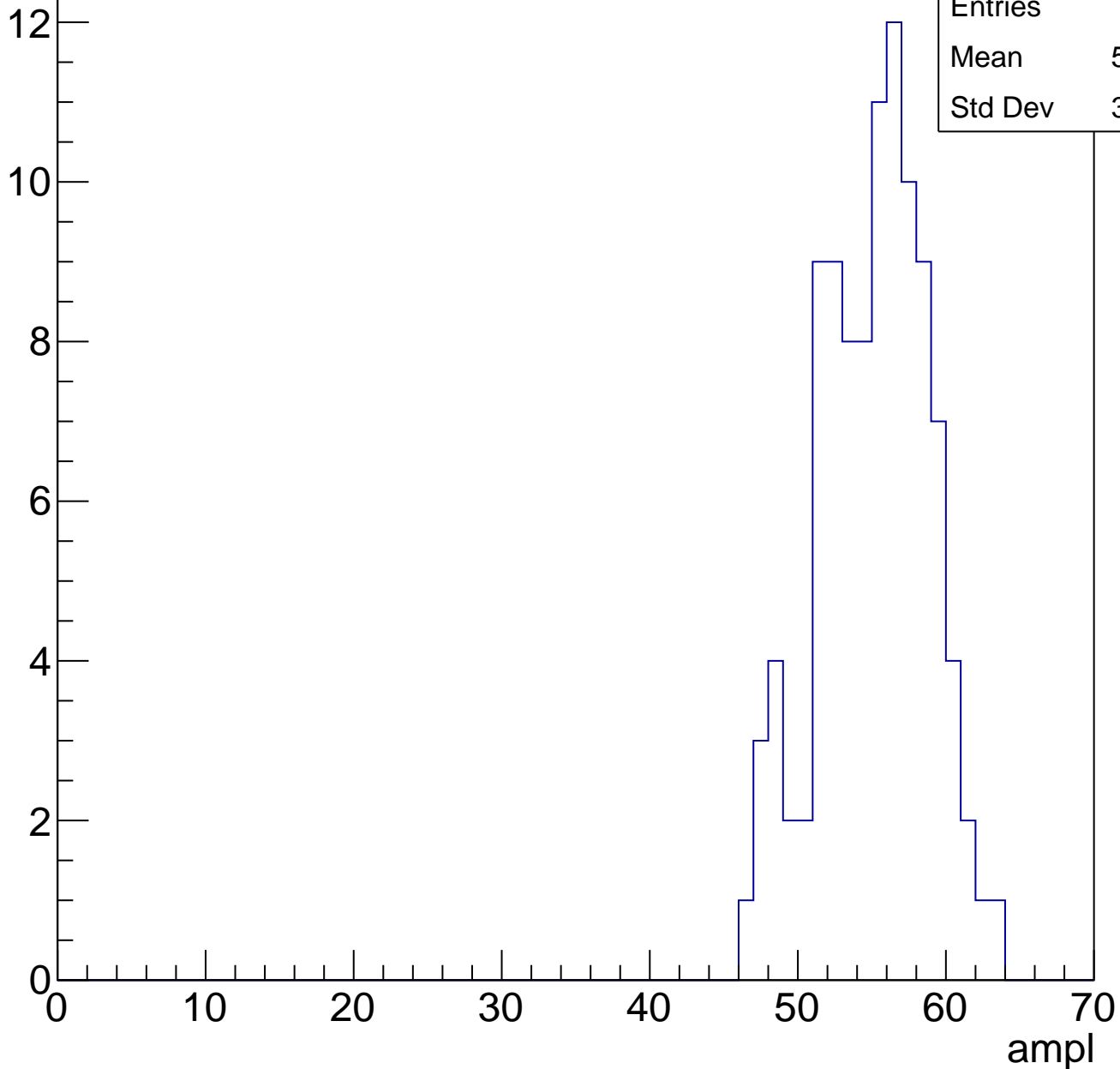
ampl



B0L101S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch99, adc5

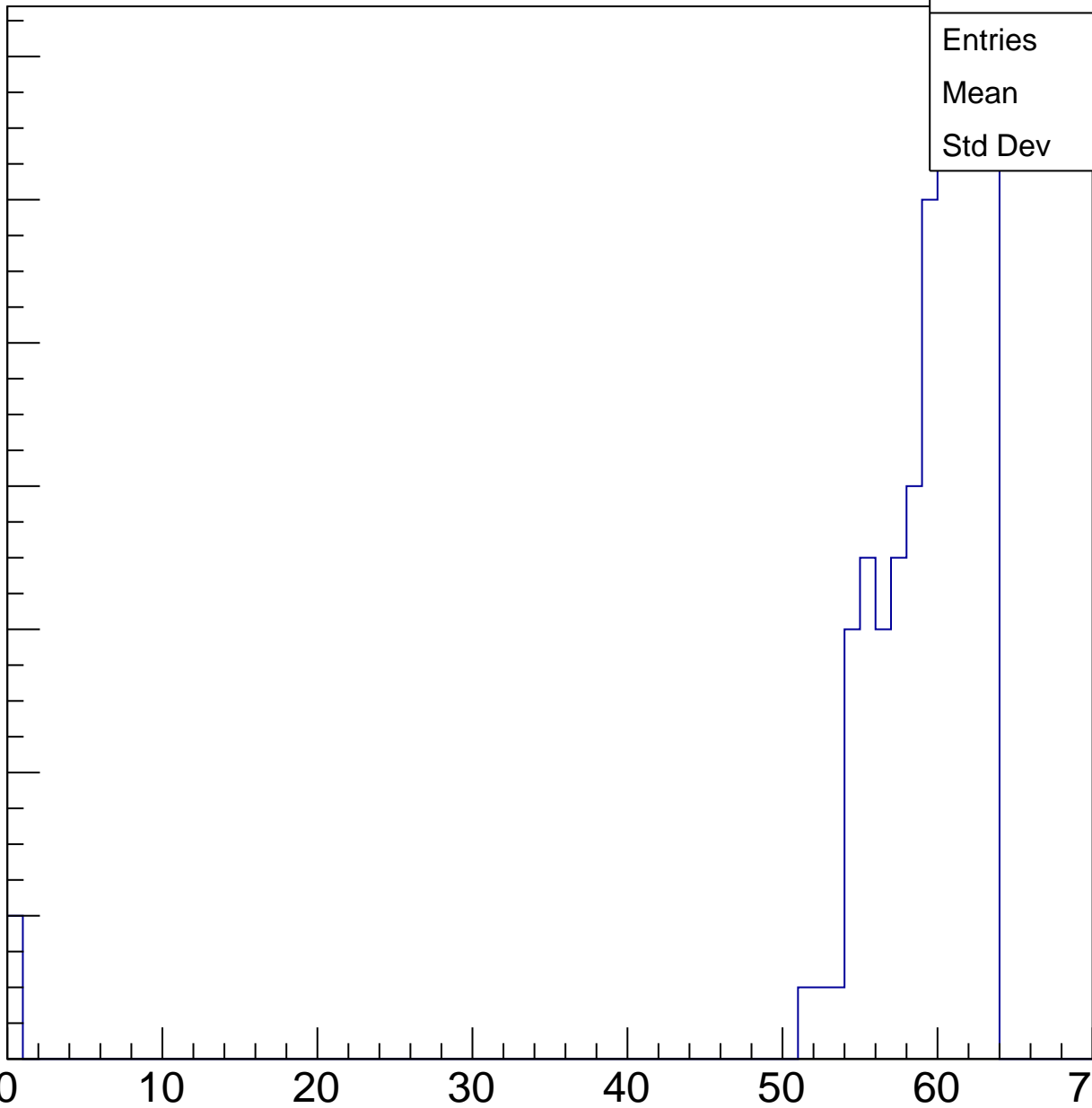
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	105
Mean	58.03
Std Dev	8.603

ampl

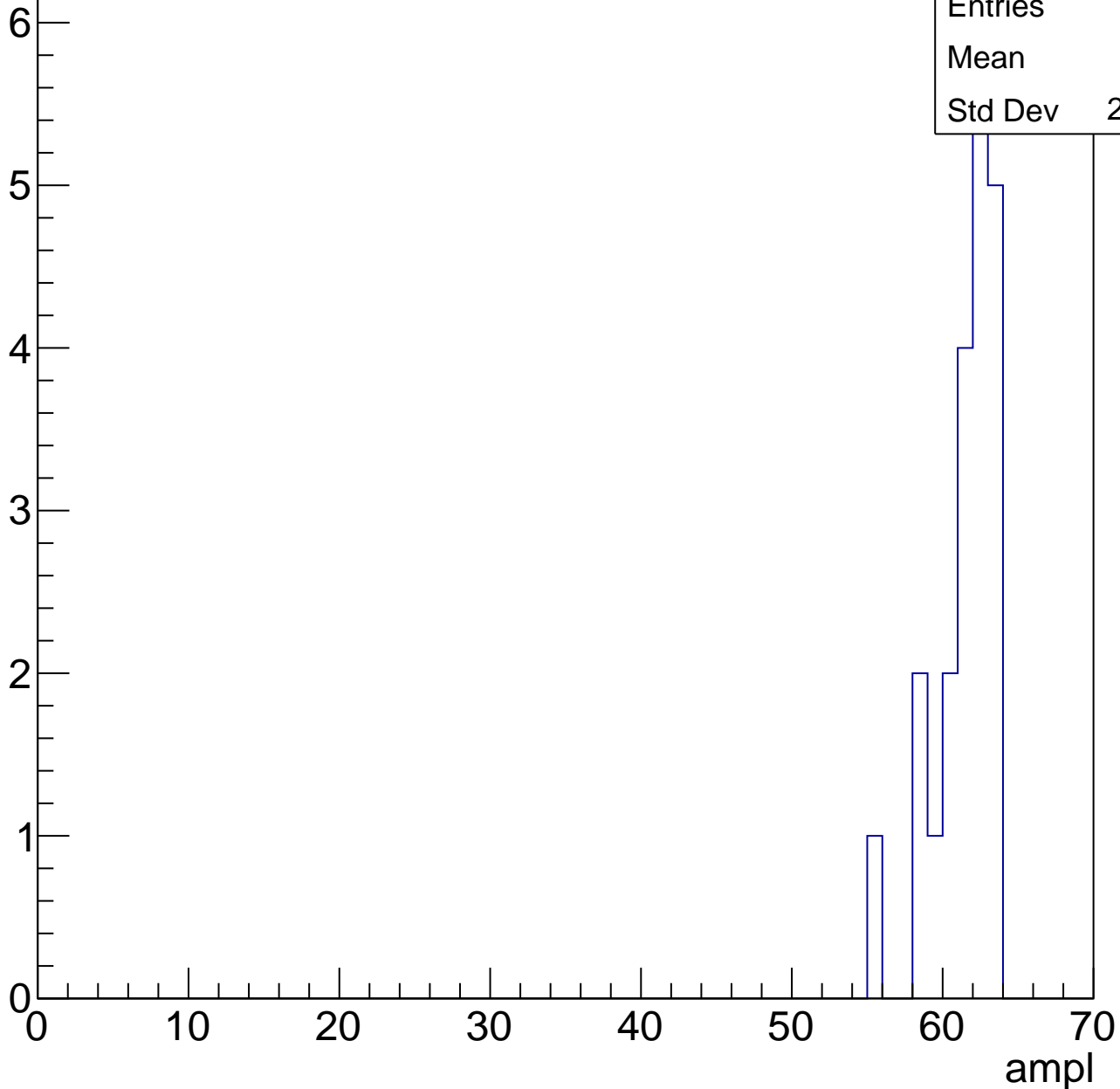


B0L101S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61
Std Dev	2.024



B0L101S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch100, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	27.37
Std Dev	3.921

Gaus mean : 27.9475

Gaus Width: 3.5699

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch100, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16

14

12

10

8

6

4

2

0

Entries

126

Mean

34.11

Std Dev

5.026

Gaus mean : 34.8983

Gaus Width: 4.0161

0

10

20

30

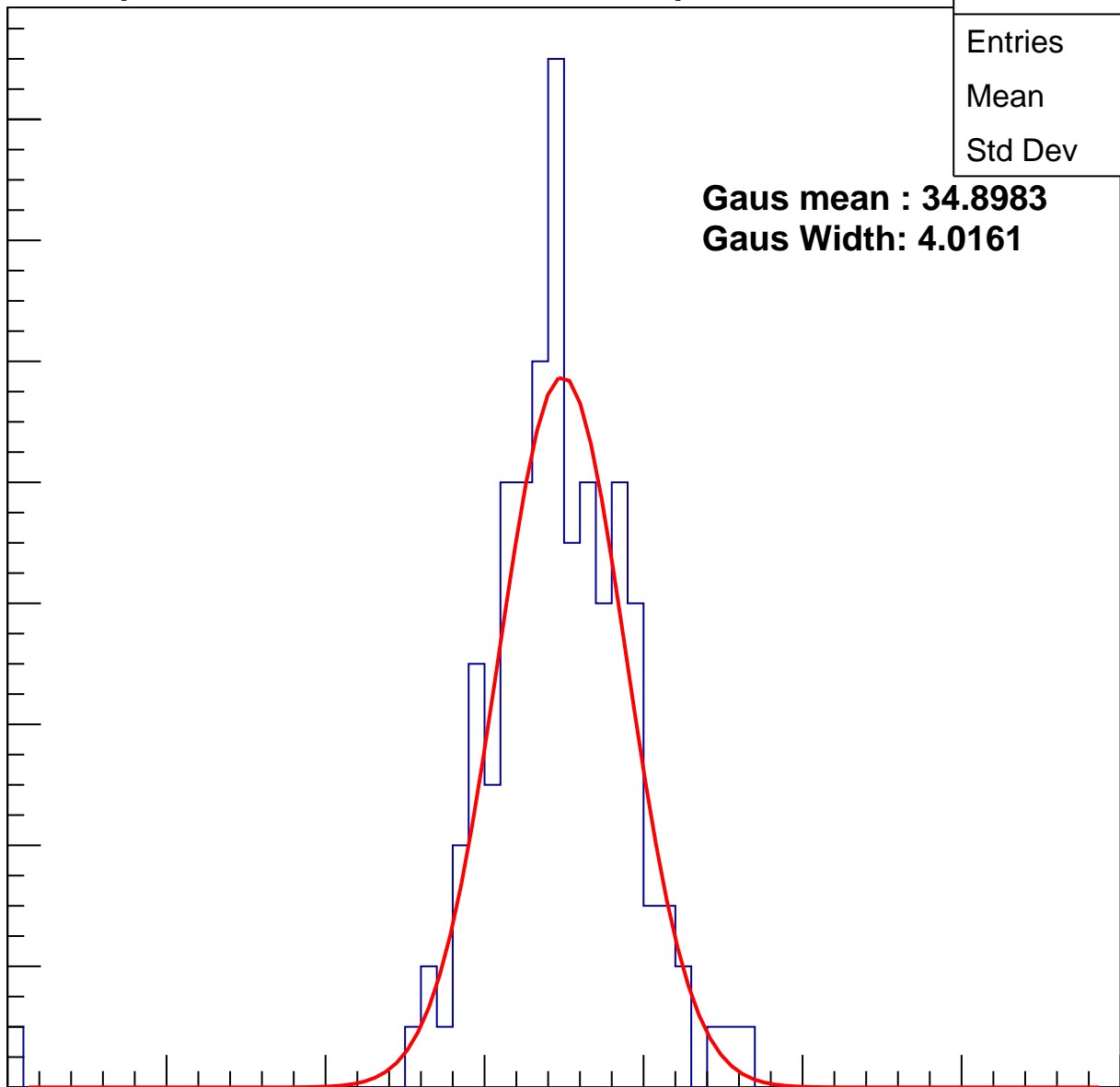
40

50

60

70

ampl



B0L101S, U15-ch100, adc2

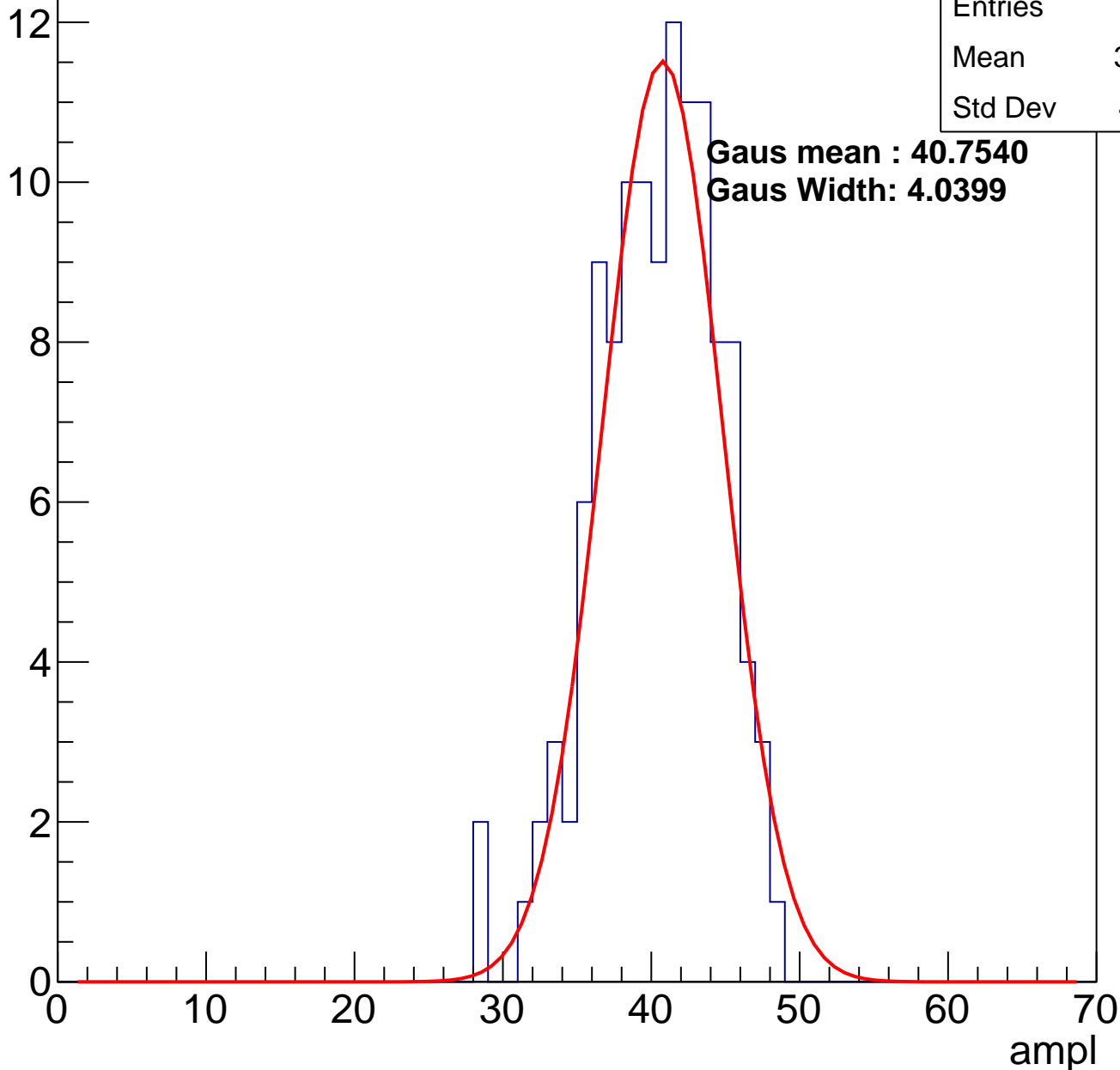
calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	39.92
Std Dev	4.051

Gaus mean : 40.7540

Gaus Width: 4.0399

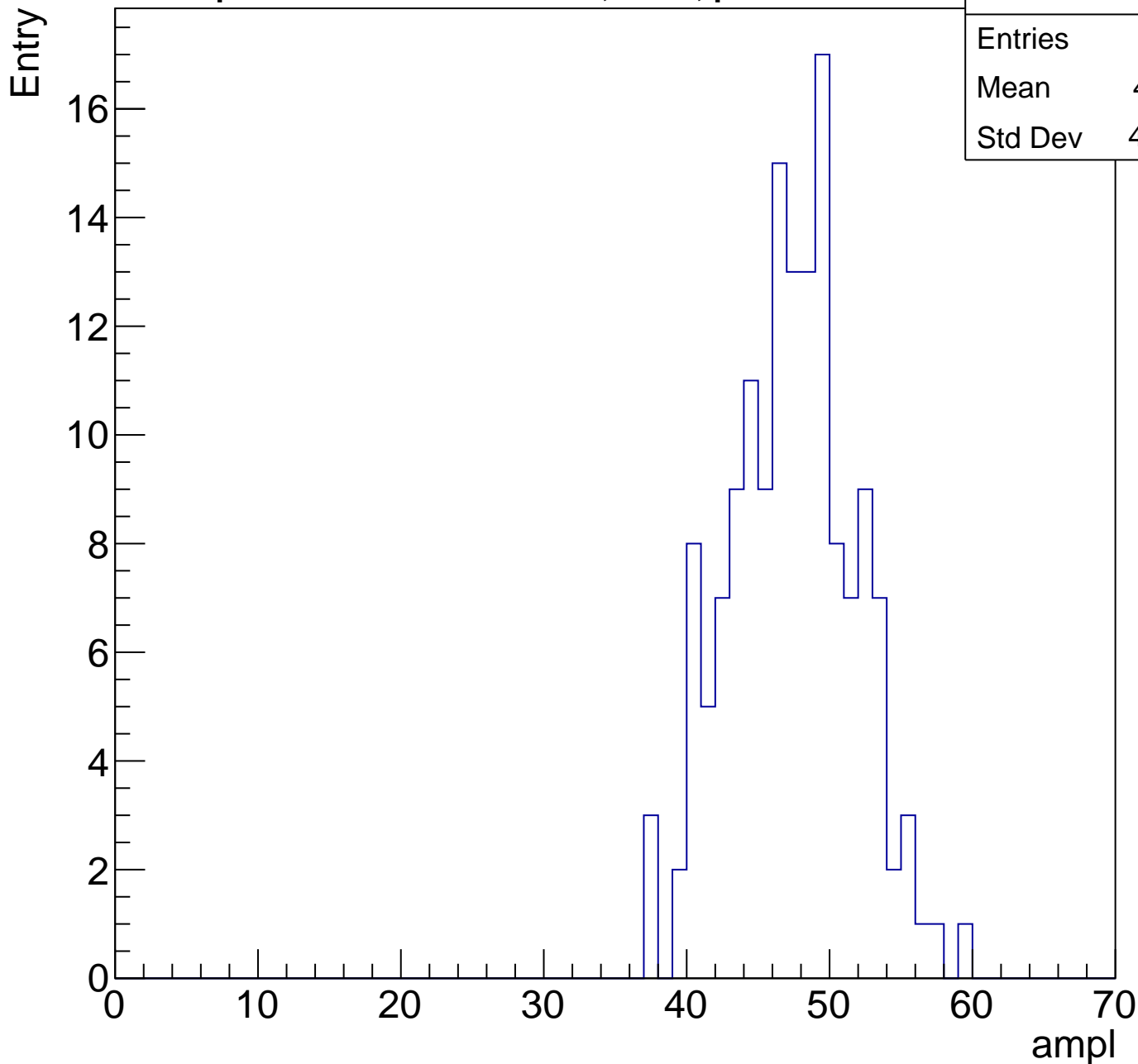
Entry



B0L101S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	151
Mean	46.91
Std Dev	4.325



B0L101S, U15-ch100, adc4

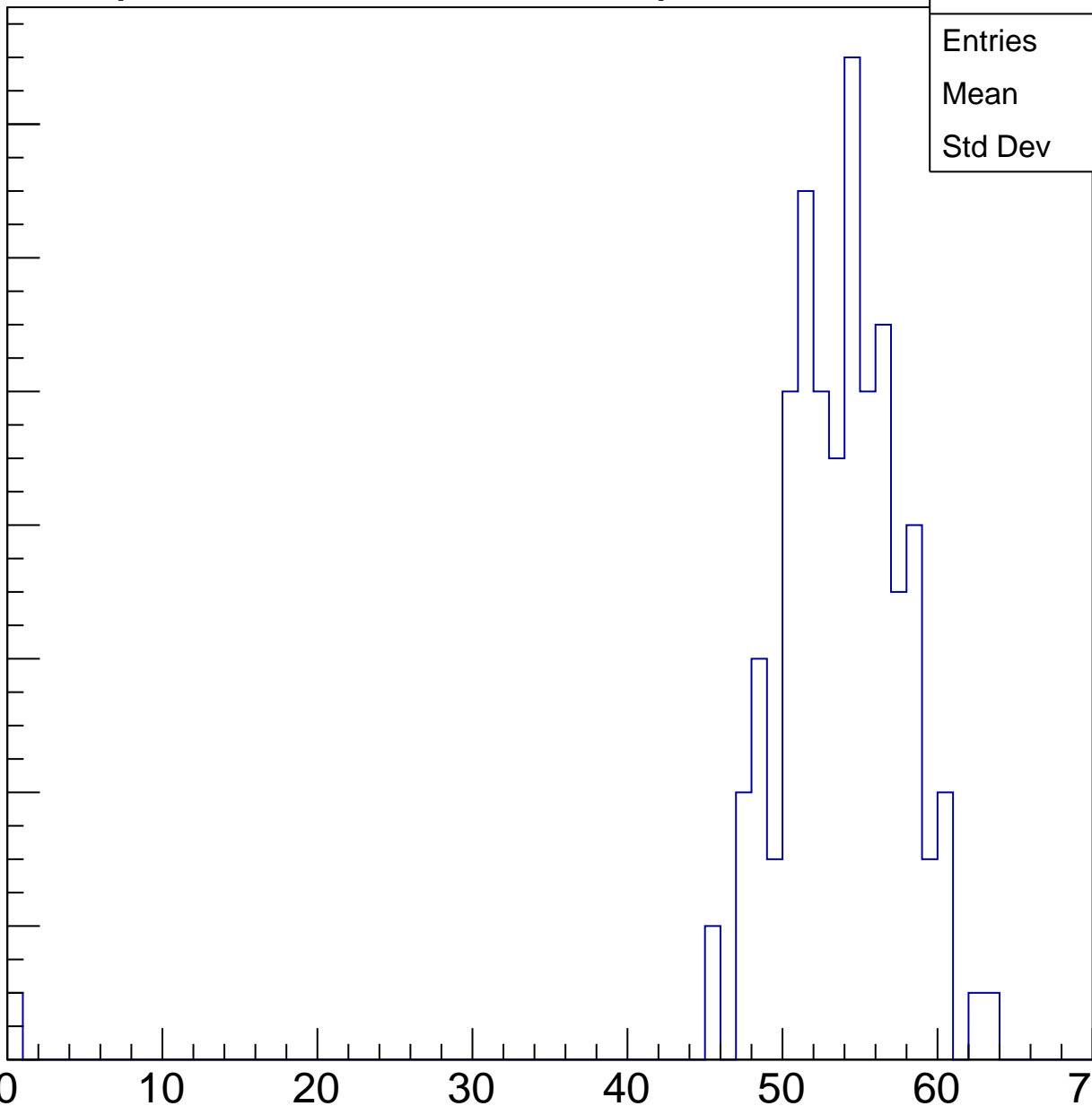
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	53
Std Dev	6.094

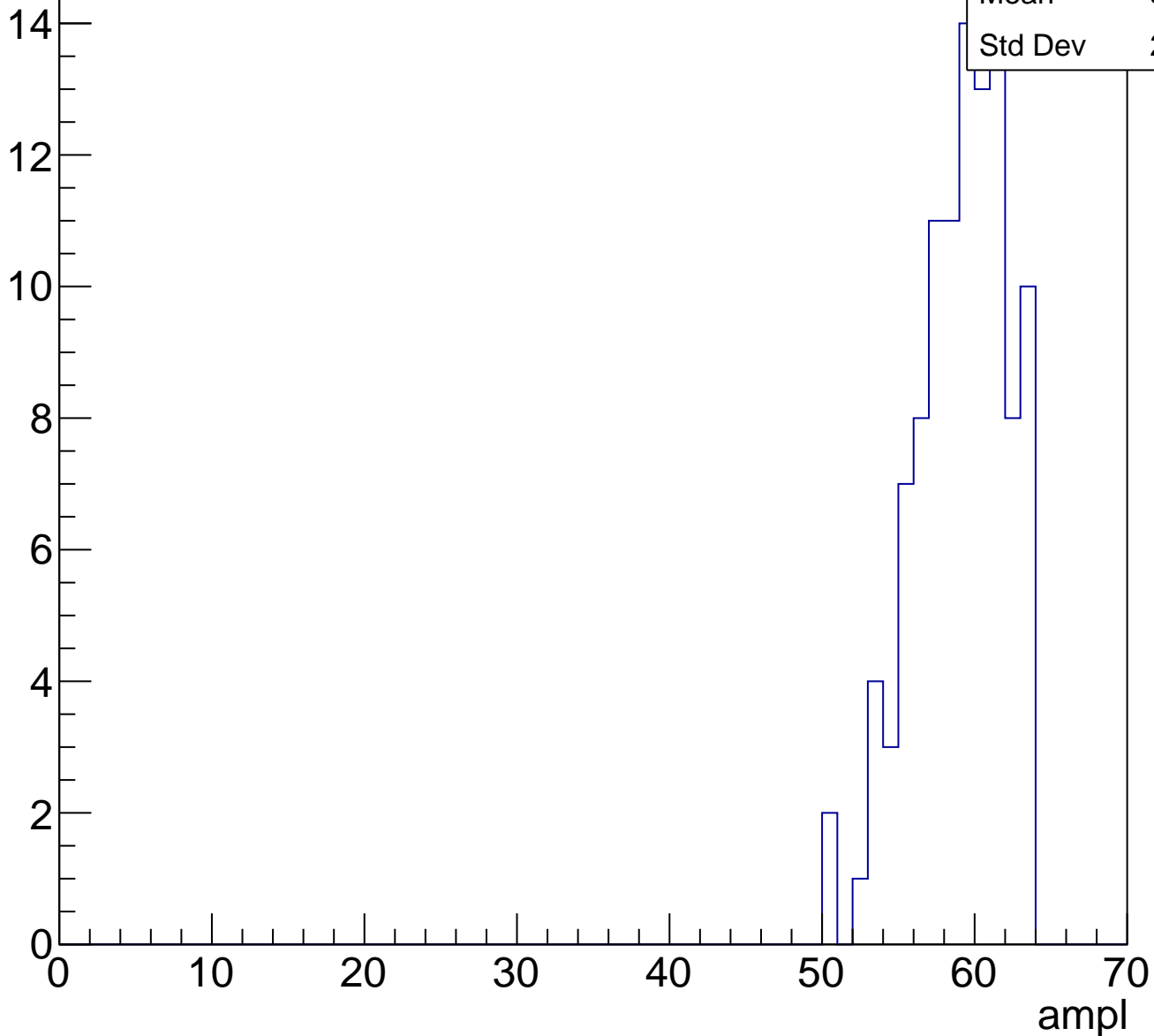
ampl



B0L101S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry



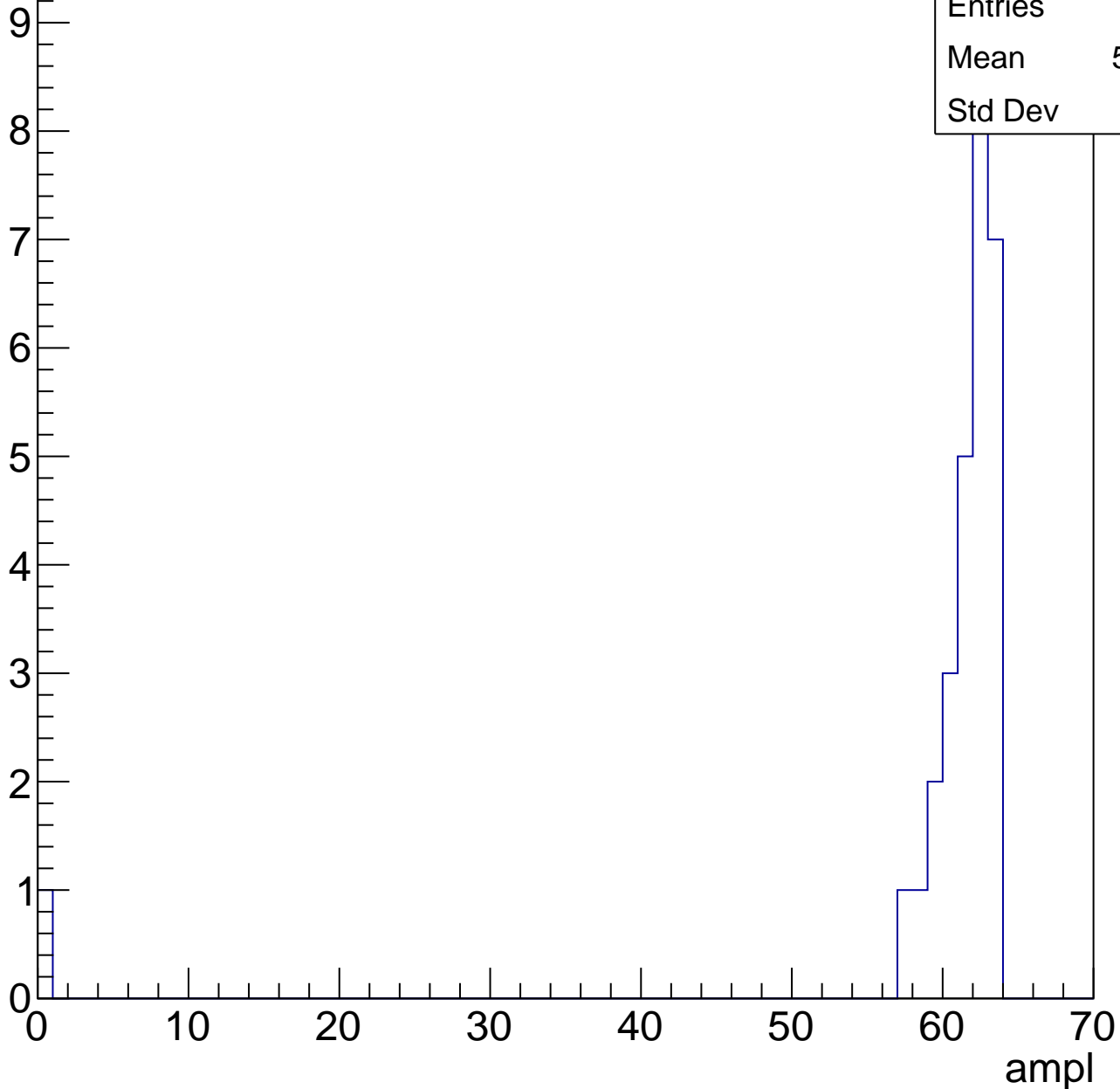
Entries	107
Mean	58.61
Std Dev	2.991

B0L101S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

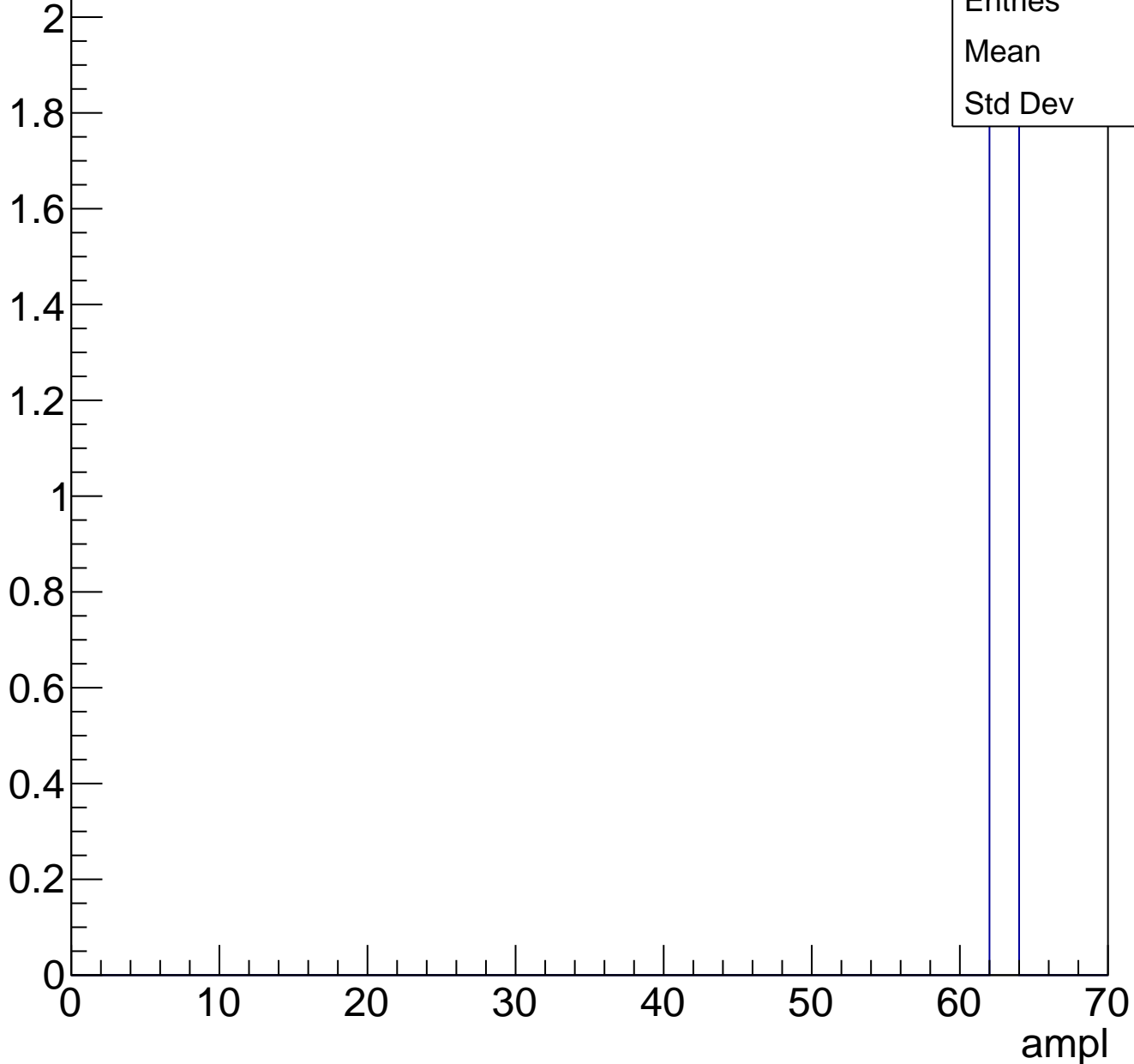
Entries	29
Mean	59.21
Std Dev	11.3



B0L101S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch101, adc0

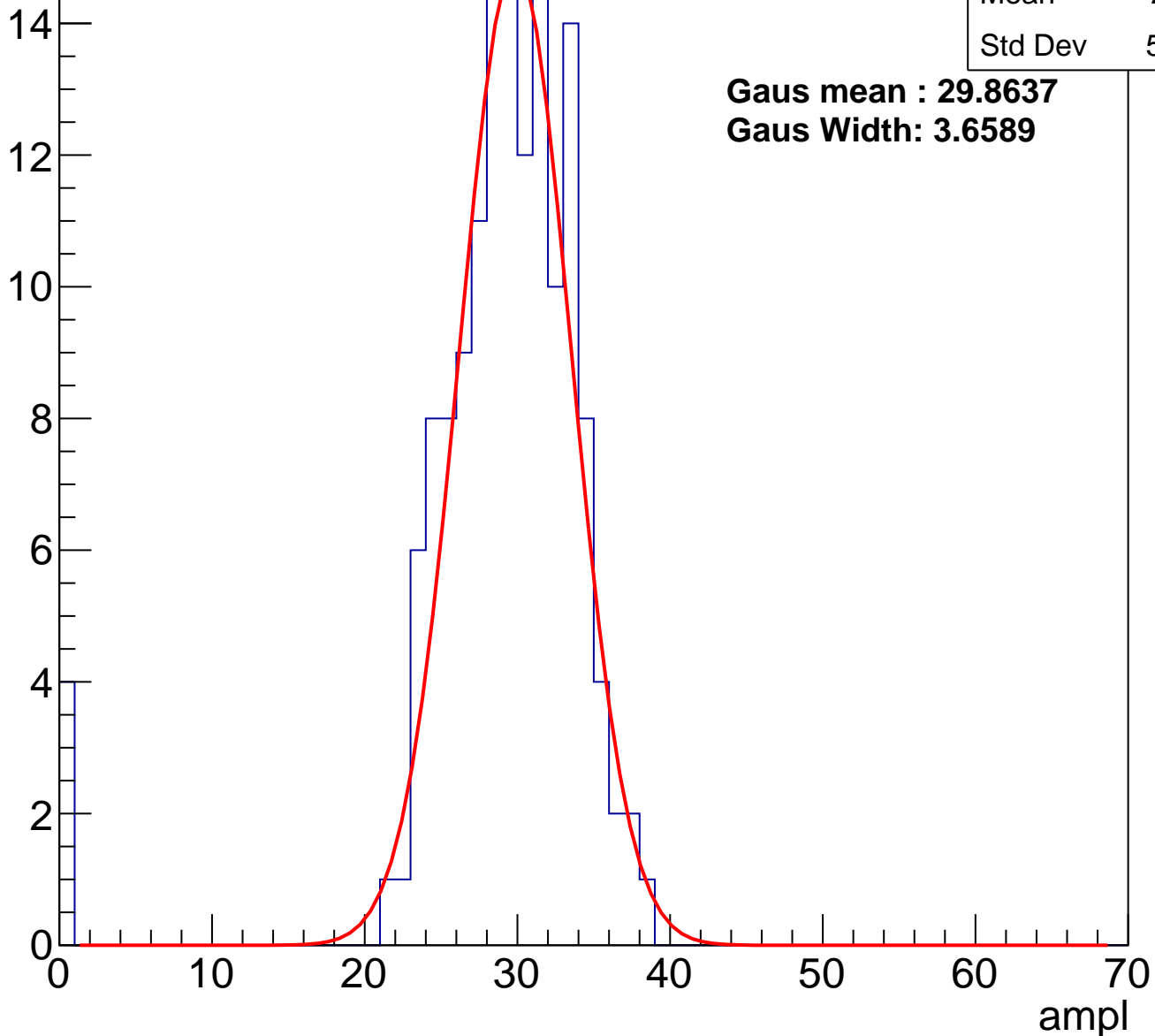
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	28.51
Std Dev	5.946

Gaus mean : 29.8637

Gaus Width: 3.6589

Entry



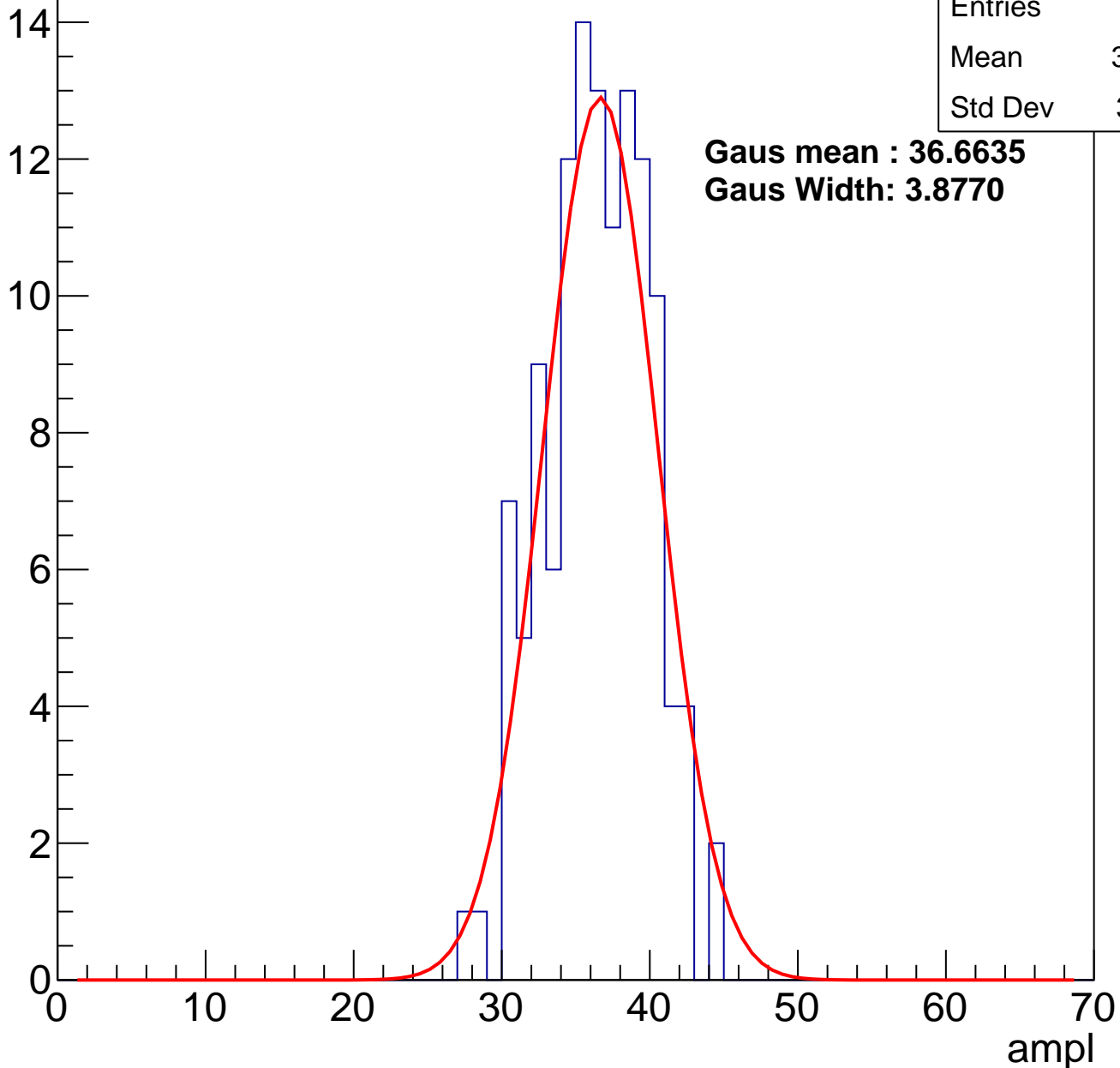
B0L101S, U15-ch101, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	35.98
Std Dev	3.461

Gaus mean : 36.6635
Gaus Width: 3.8770

Entry



B0L101S, U15-ch101, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

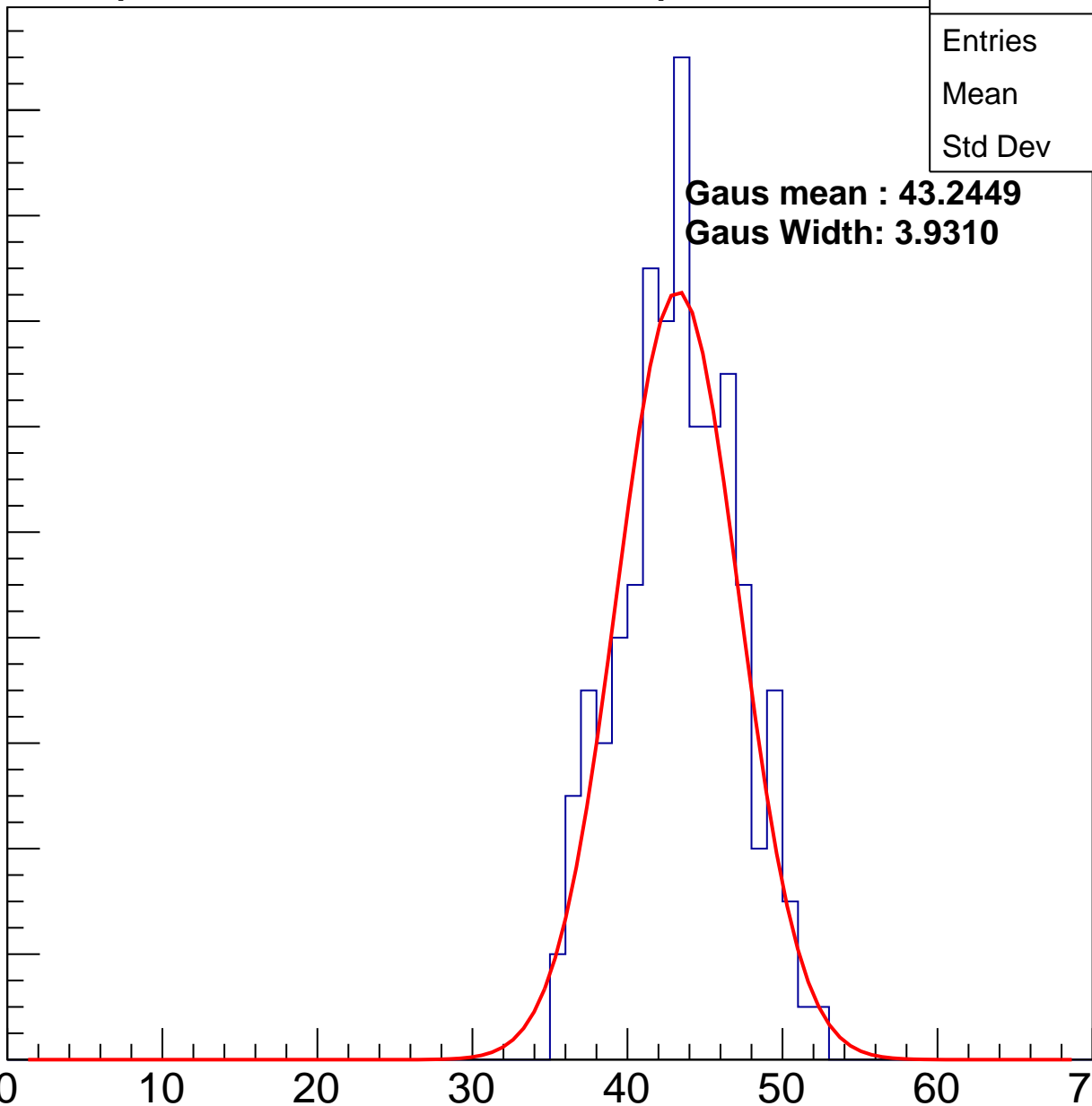
18
16
14
12
10
8
6
4
2
0

Entries	147
Mean	42.9
Std Dev	3.708

Gaus mean : 43.2449

Gaus Width: 3.9310

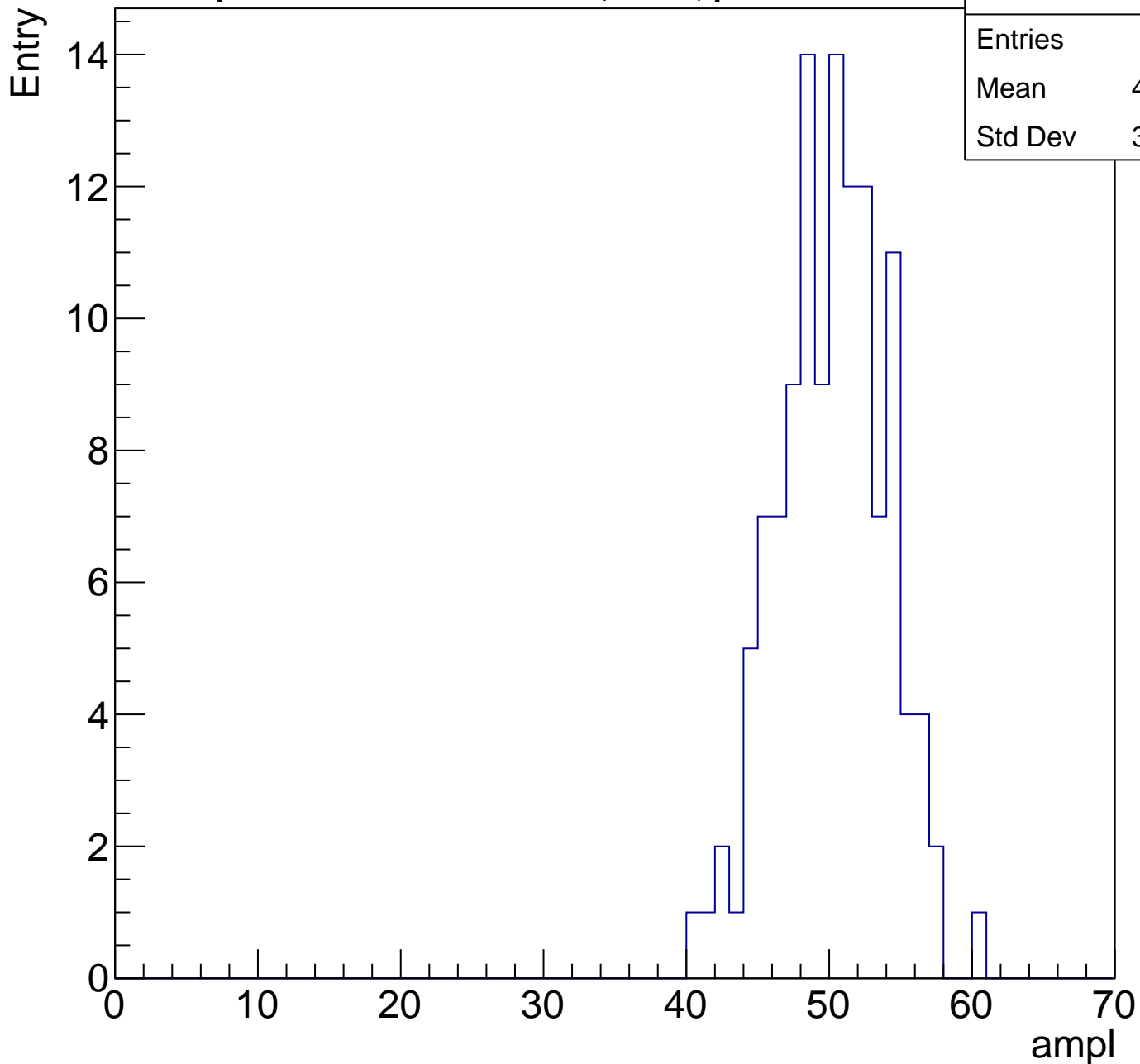
ampl



B0L101S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	123
Mean	49.76
Std Dev	3.723



B0L101S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

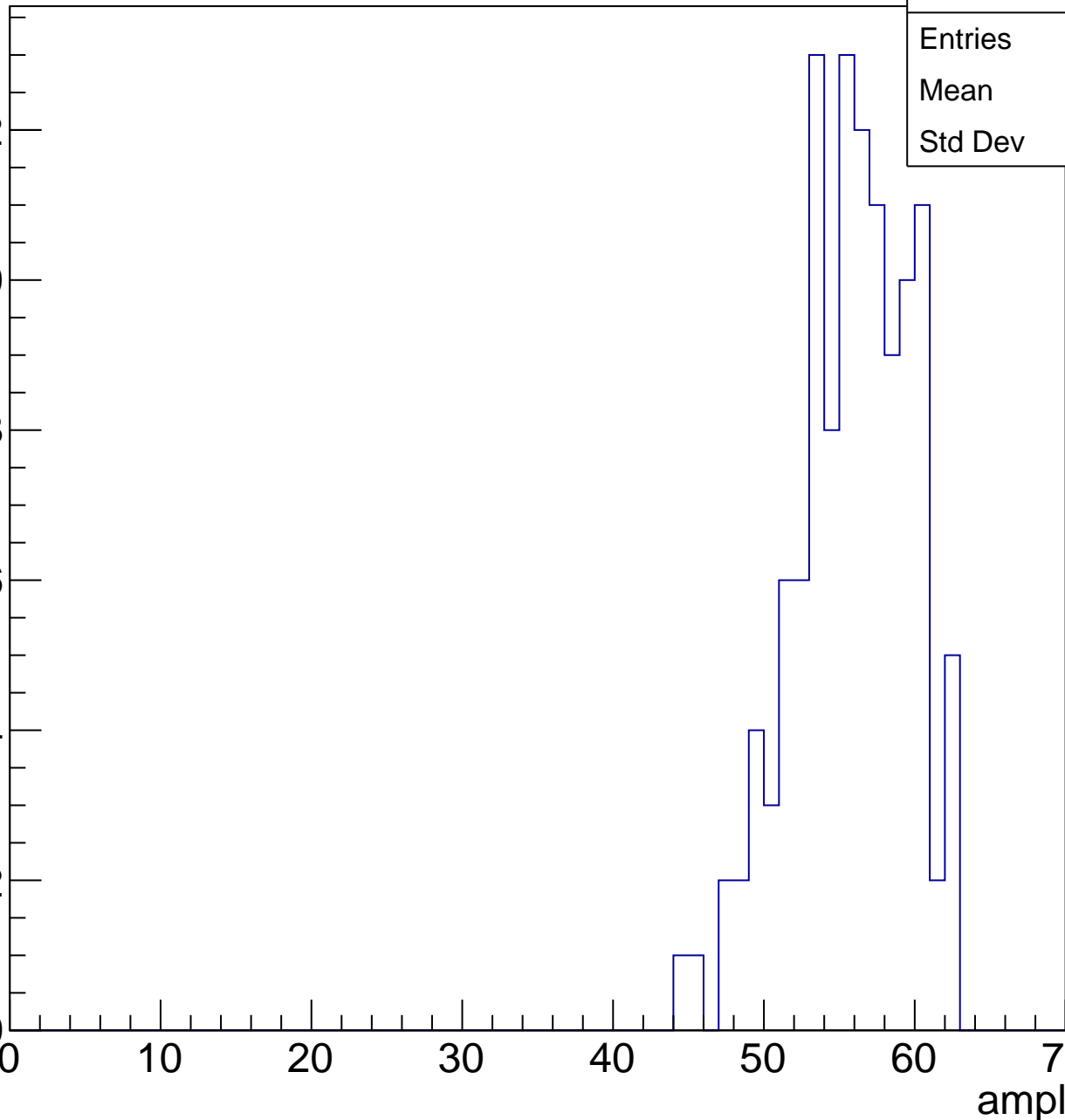
40

50

60

ampl

Entries	119
Mean	55.31
Std Dev	3.839



B0L101S, U15-ch101, adc5

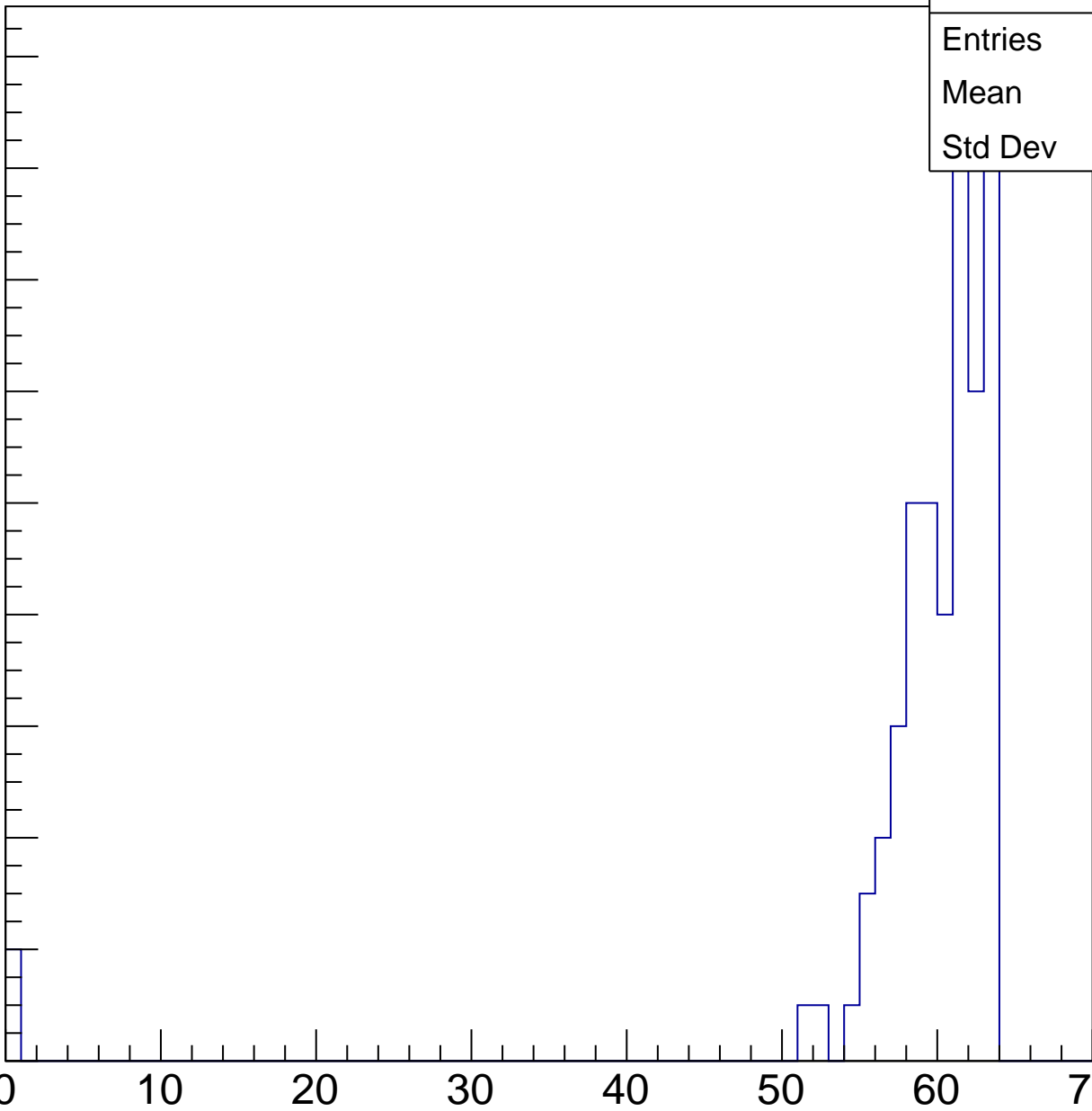
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.61
Std Dev	9.132

ampl



B0L101S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	60.88
Std Dev	2.088

0 10 20 30 40 50 60 70

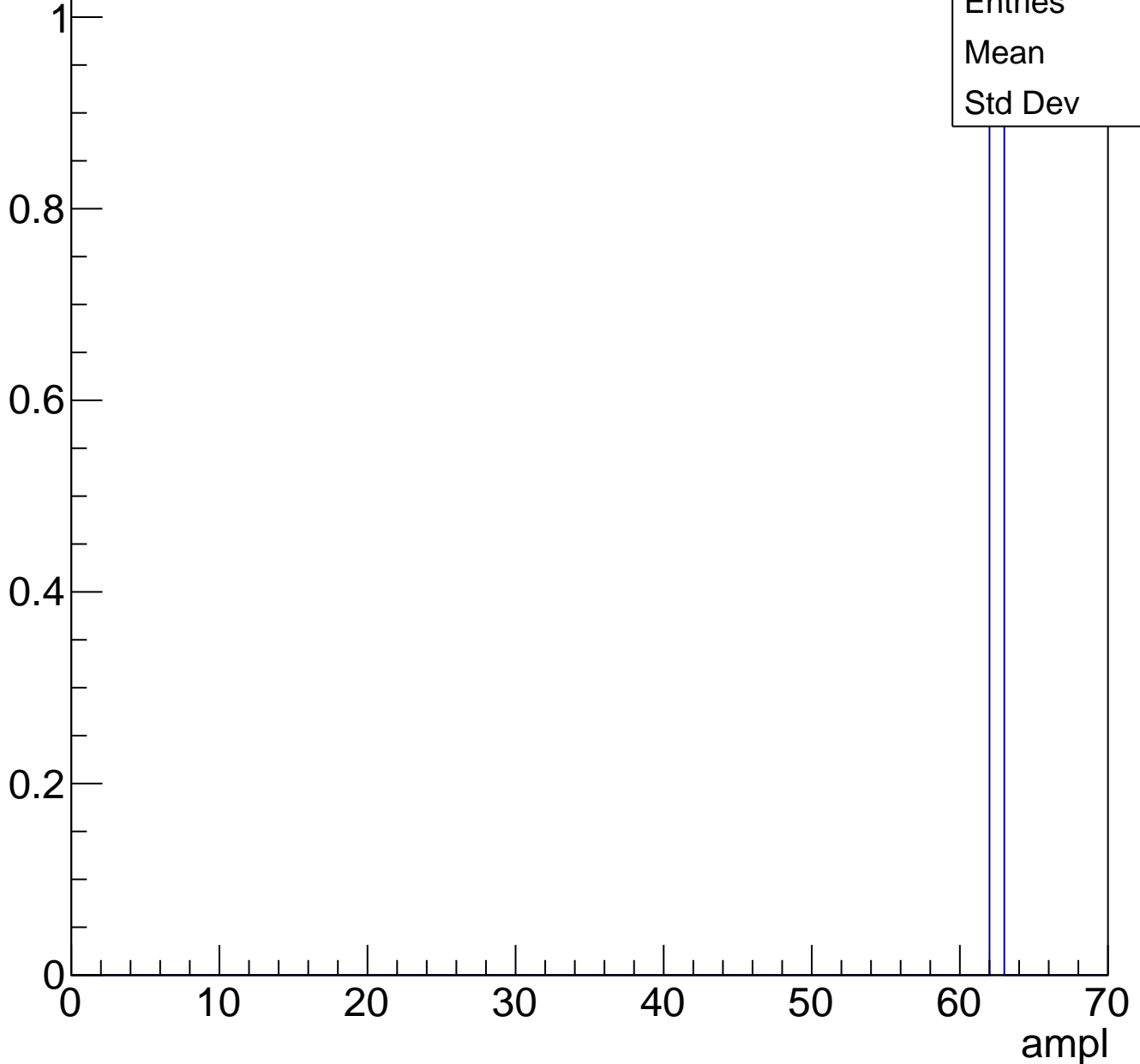
ampl

1

B0L101S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



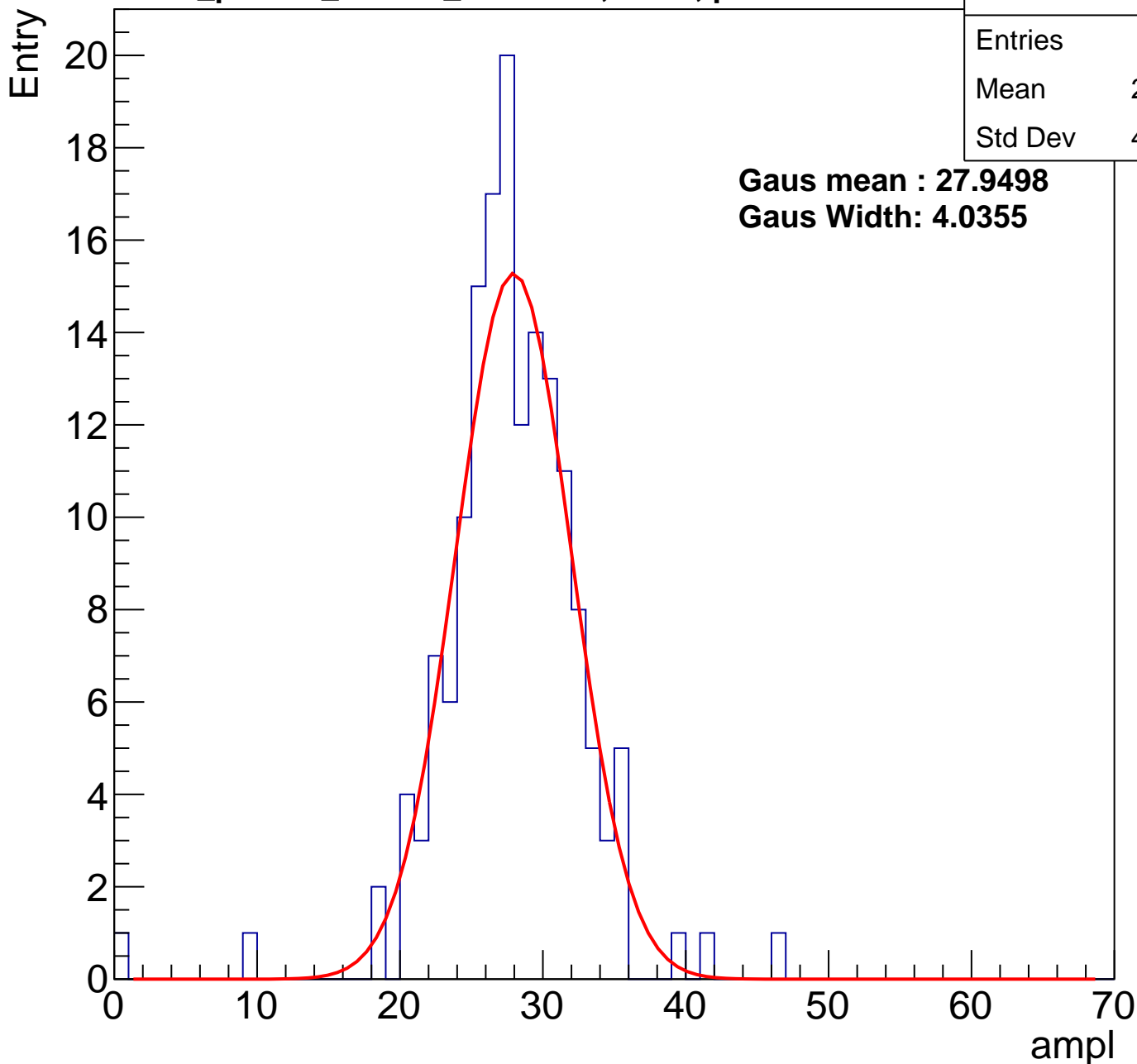
B0L101S, U15-ch102, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	160
Mean	27.34
Std Dev	4.904

Gaus mean : 27.9498

Gaus Width: 4.0355



B0L101S, U15-ch102, adc1

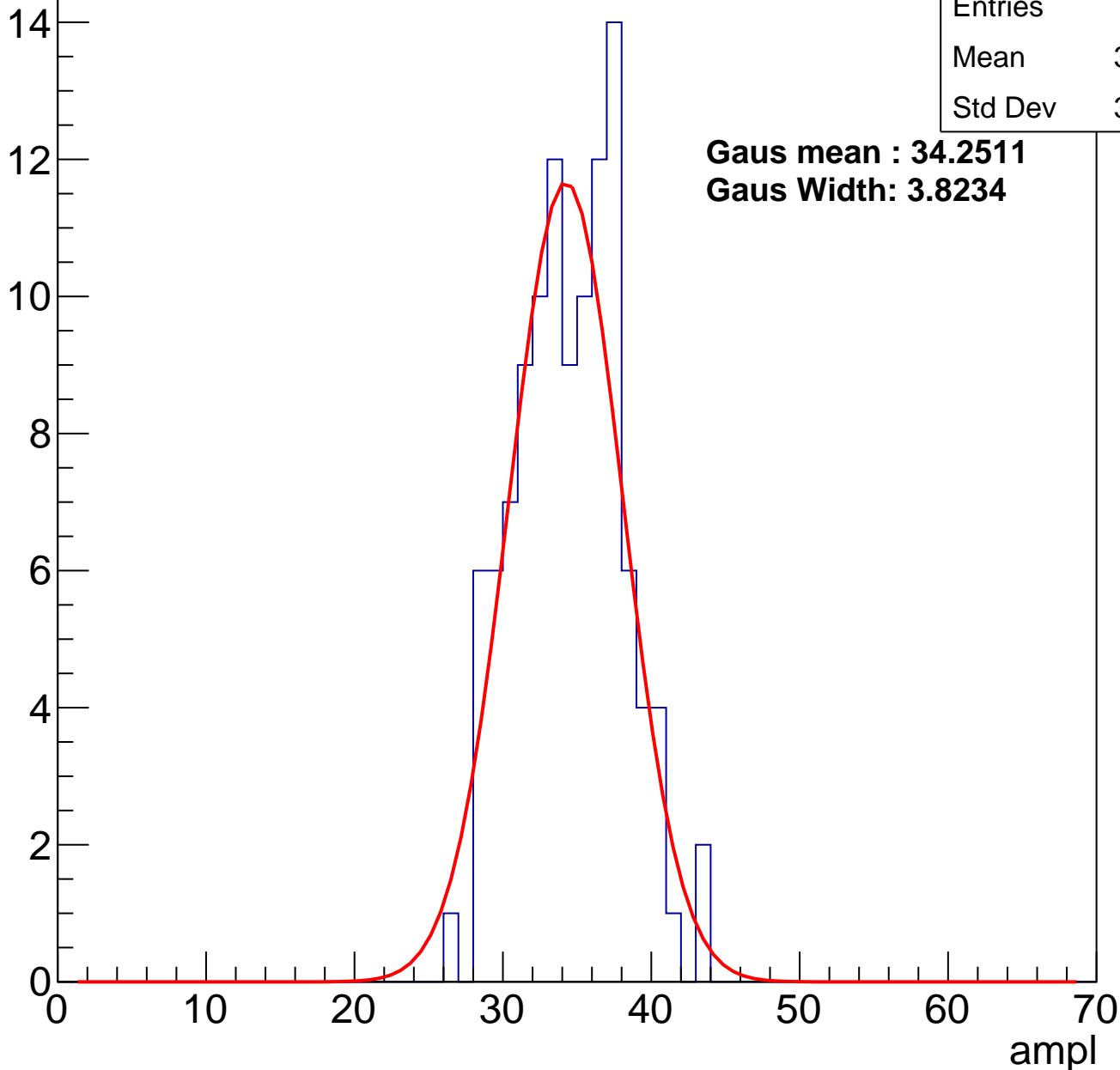
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	34.07
Std Dev	3.532

Gaus mean : 34.2511

Gaus Width: 3.8234

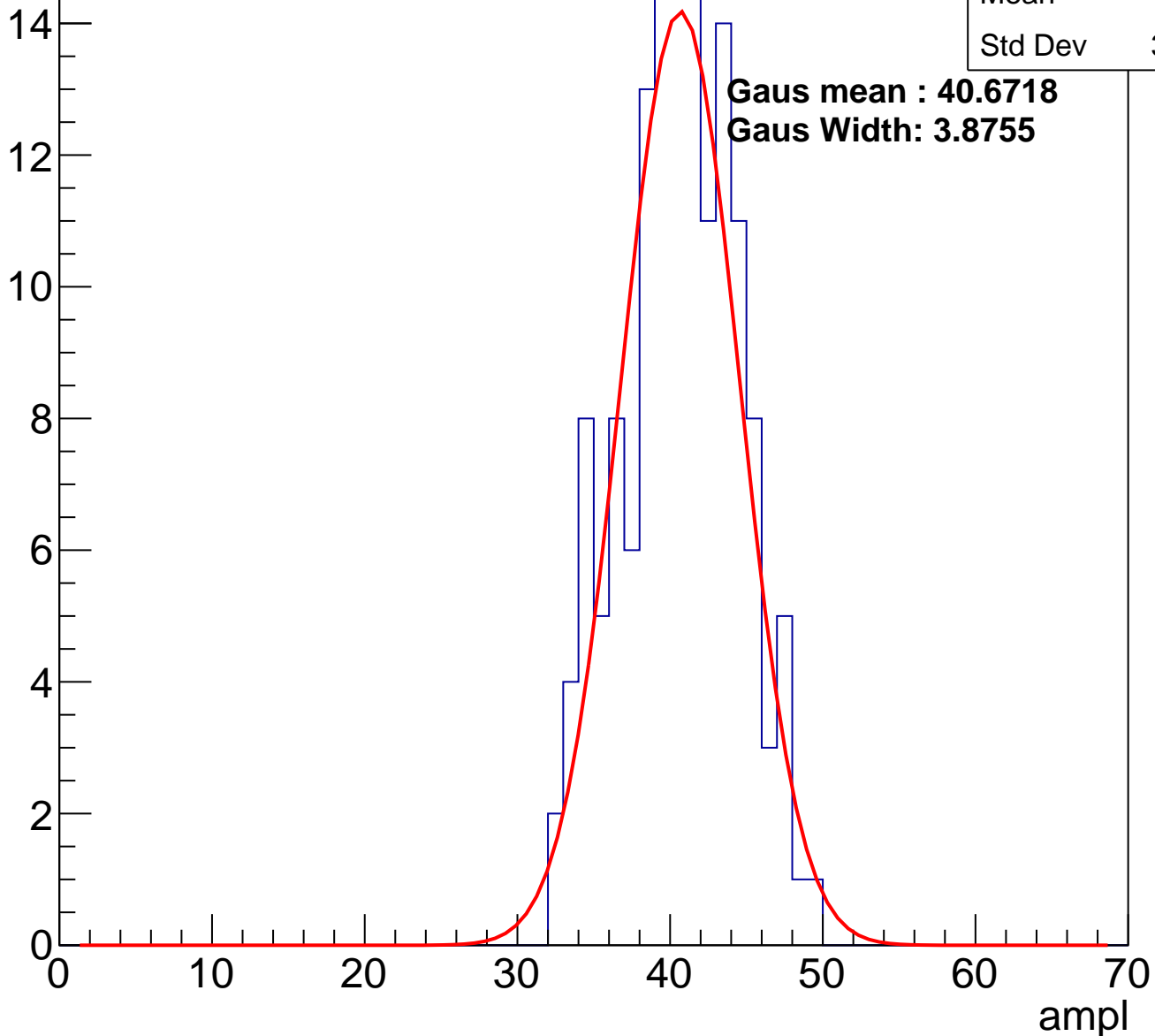
Entry



B0L101S, U15-ch102, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#1, port C1

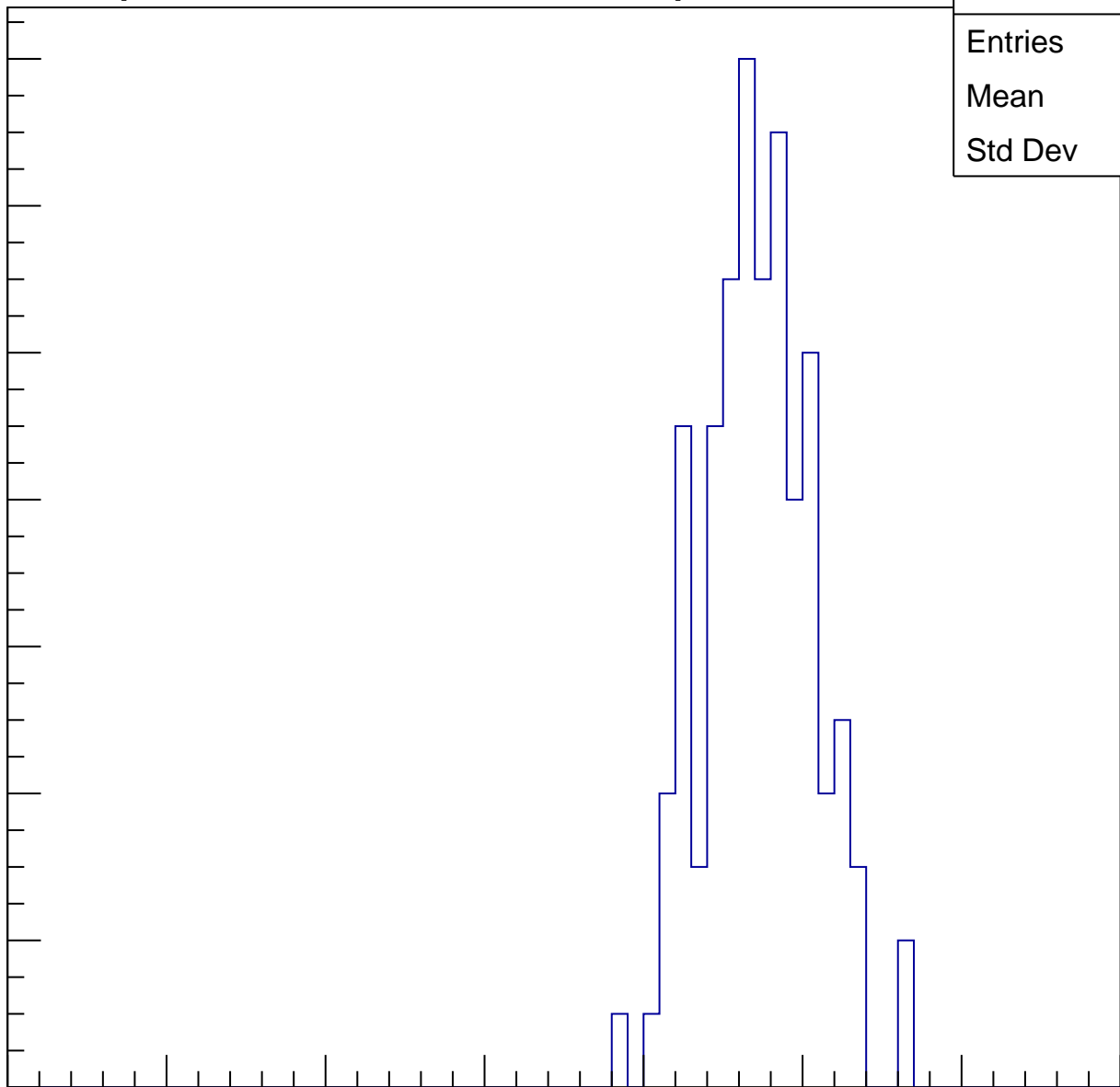
Entries	108
Mean	46.78
Std Dev	3.435

Entry

14
12
10
8
6
4
2
0

ampl

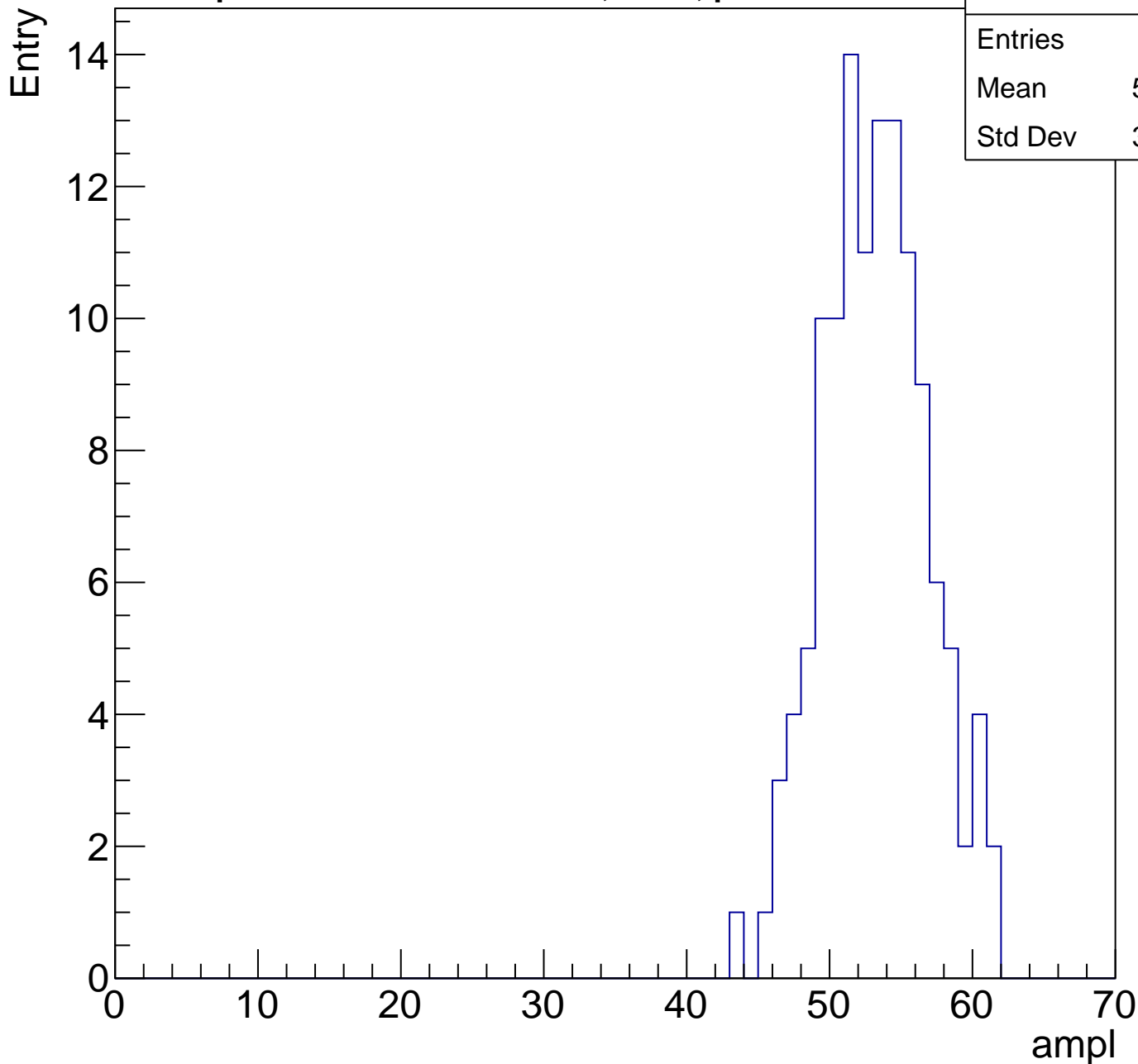
0 10 20 30 40 50 60 70



B0L101S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	52.76
Std Dev	3.642



B0L101S, U15-ch102, adc5

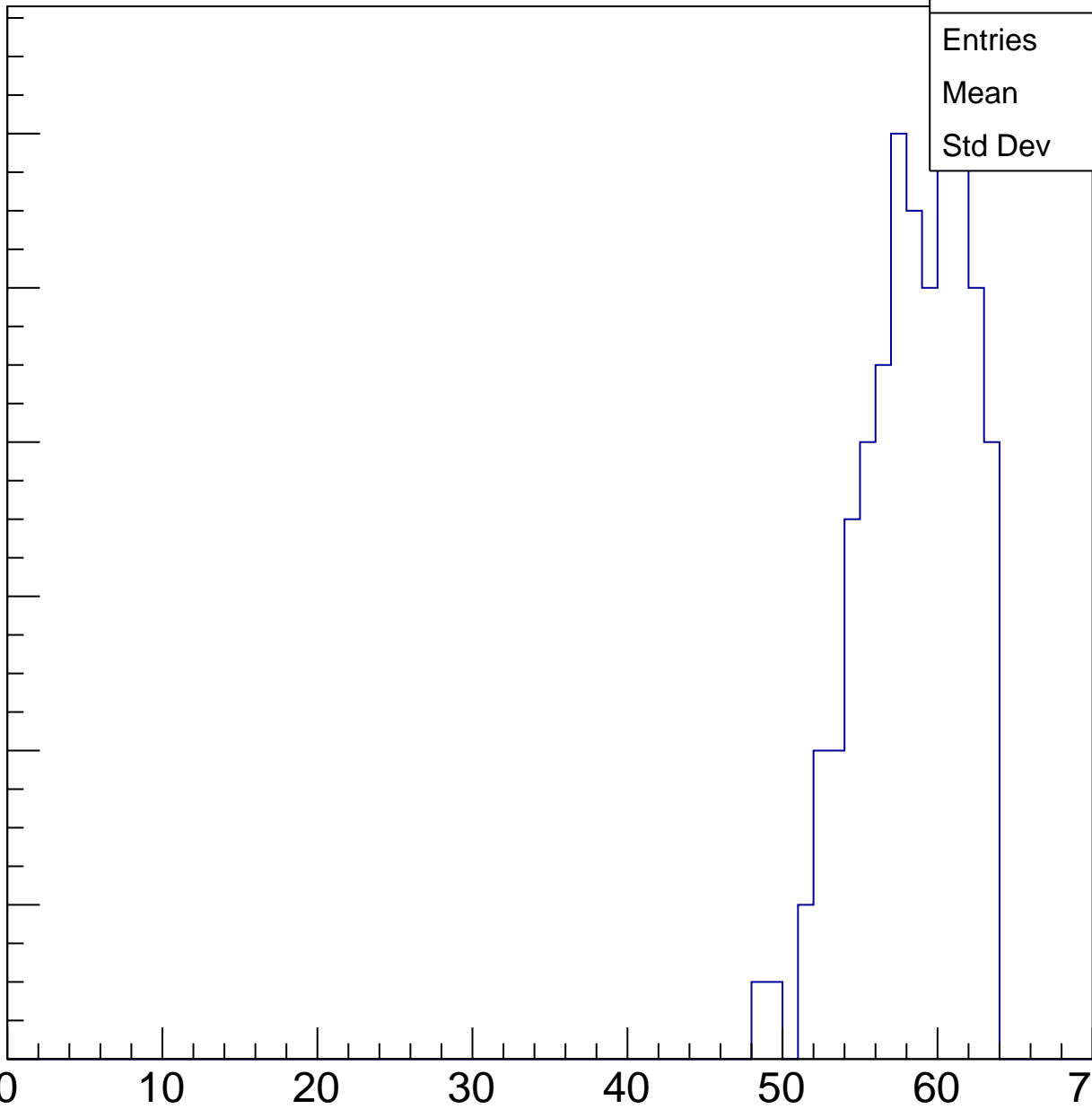
calib_packv5_042523_0143.root, FC#1, port C1

Entry

12
10
8
6
4
2
0

Entries	112
Mean	57.95
Std Dev	3.393

ampl



B0L101S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	44
Mean	59.43
Std Dev	9.279

0

2

4

6

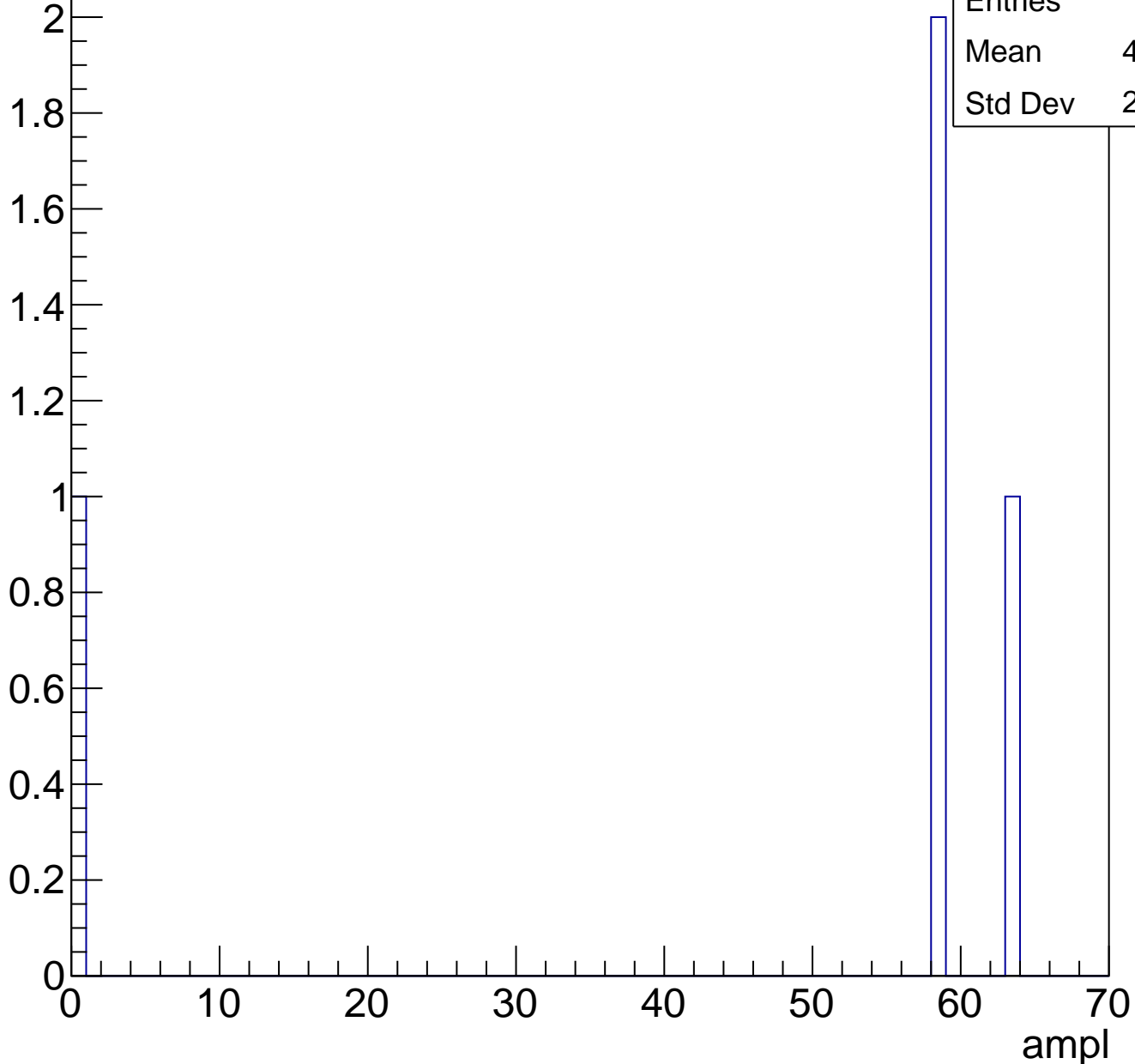
8

10

B0L101S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	4
Mean	44.75
Std Dev	25.92

B0L101S, U15-ch103, adc0

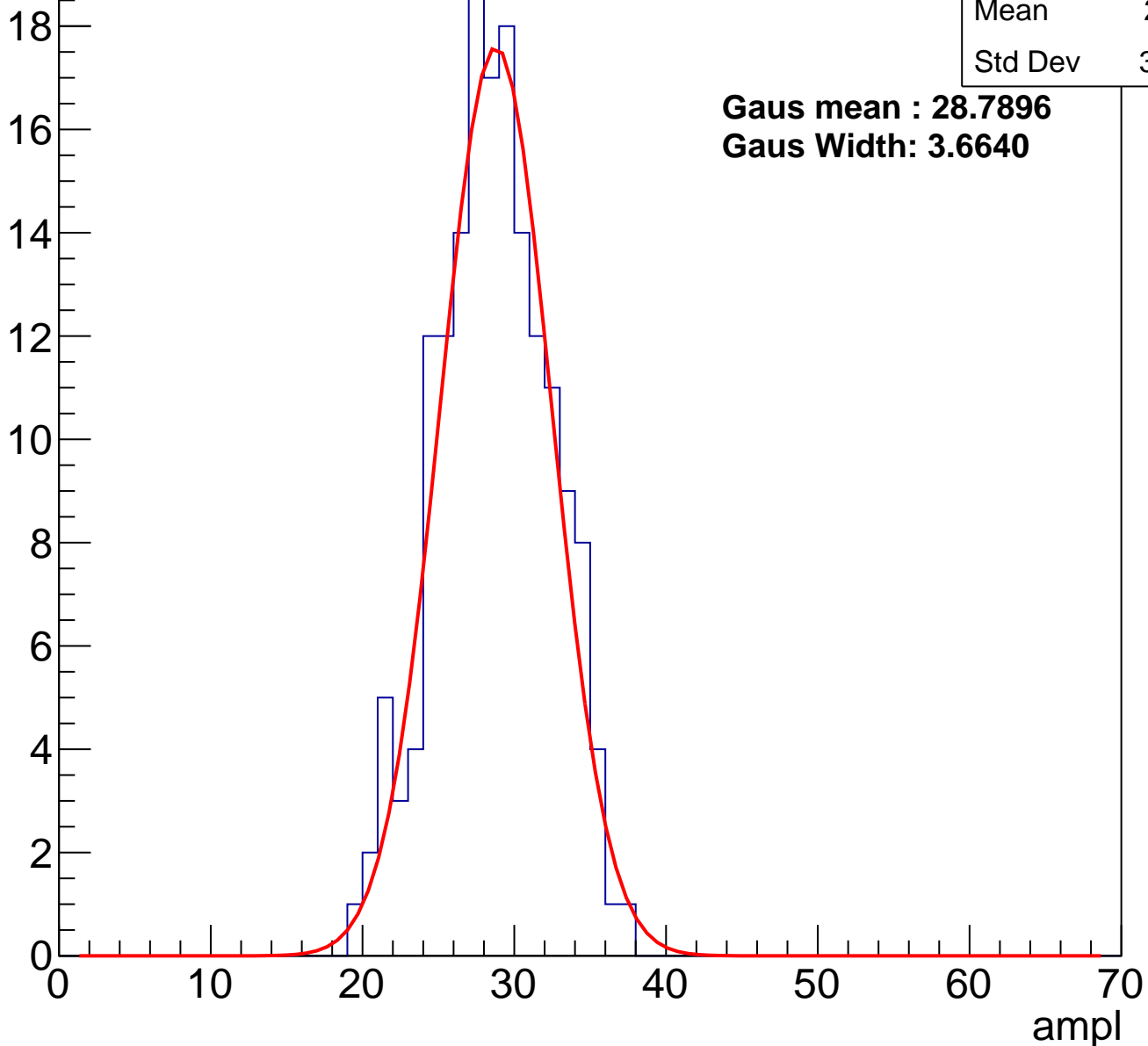
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	28.21
Std Dev	3.664

Gaus mean : 28.7896

Gaus Width: 3.6640

Entry



B0L101S, U15-ch103, adc1

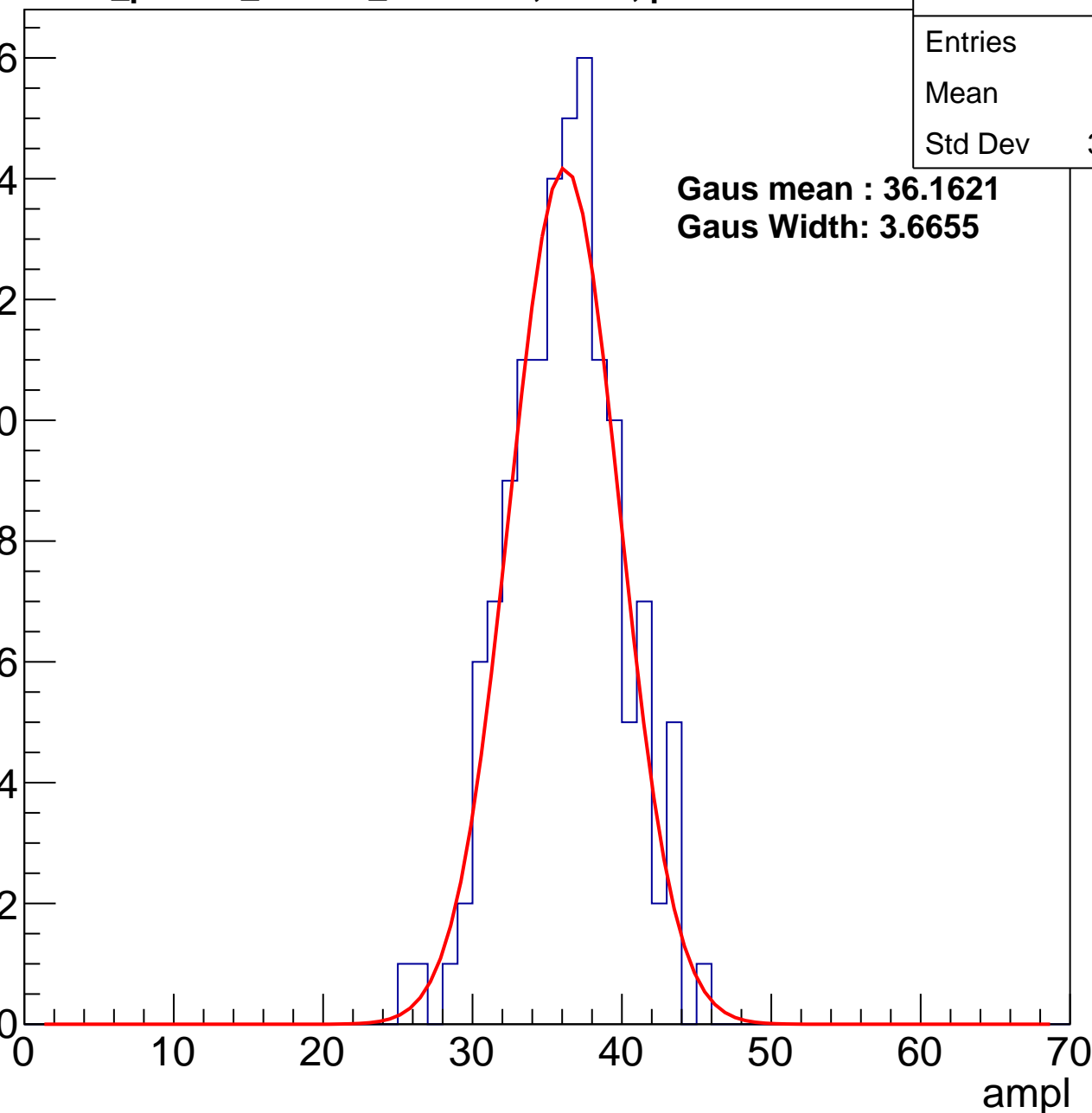
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	135
Mean	35.7
Std Dev	3.718

Gaus mean : 36.1621
Gaus Width: 3.6655



B0L101S, U15-ch103, adc2

calib_packv5_042523_0143.root, FC#1, port C1

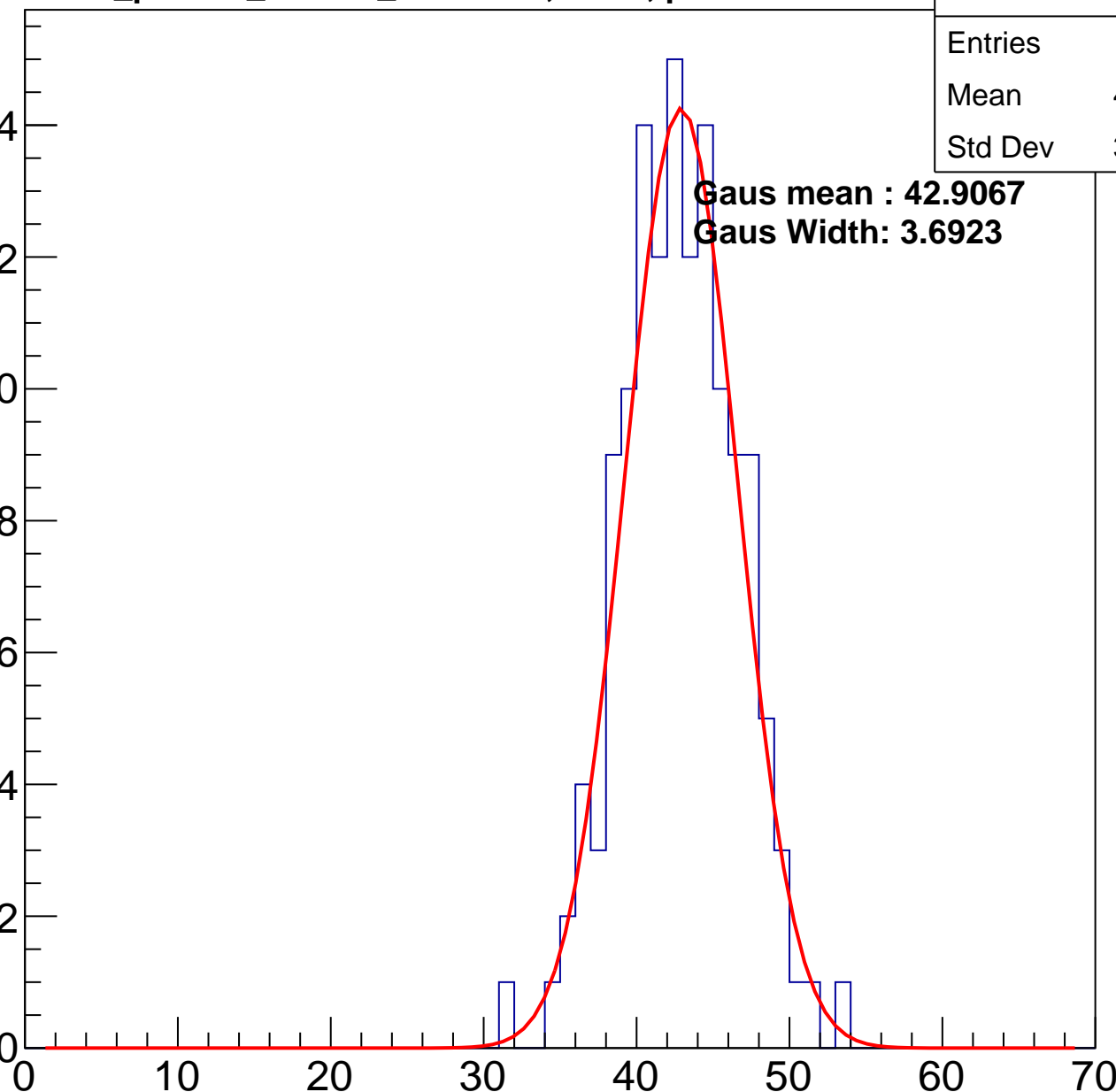
Entry

14
12
10
8
6
4
2
0

Entries	136
Mean	42.38
Std Dev	3.742

Gaus mean : 42.9067

Gaus Width: 3.6923

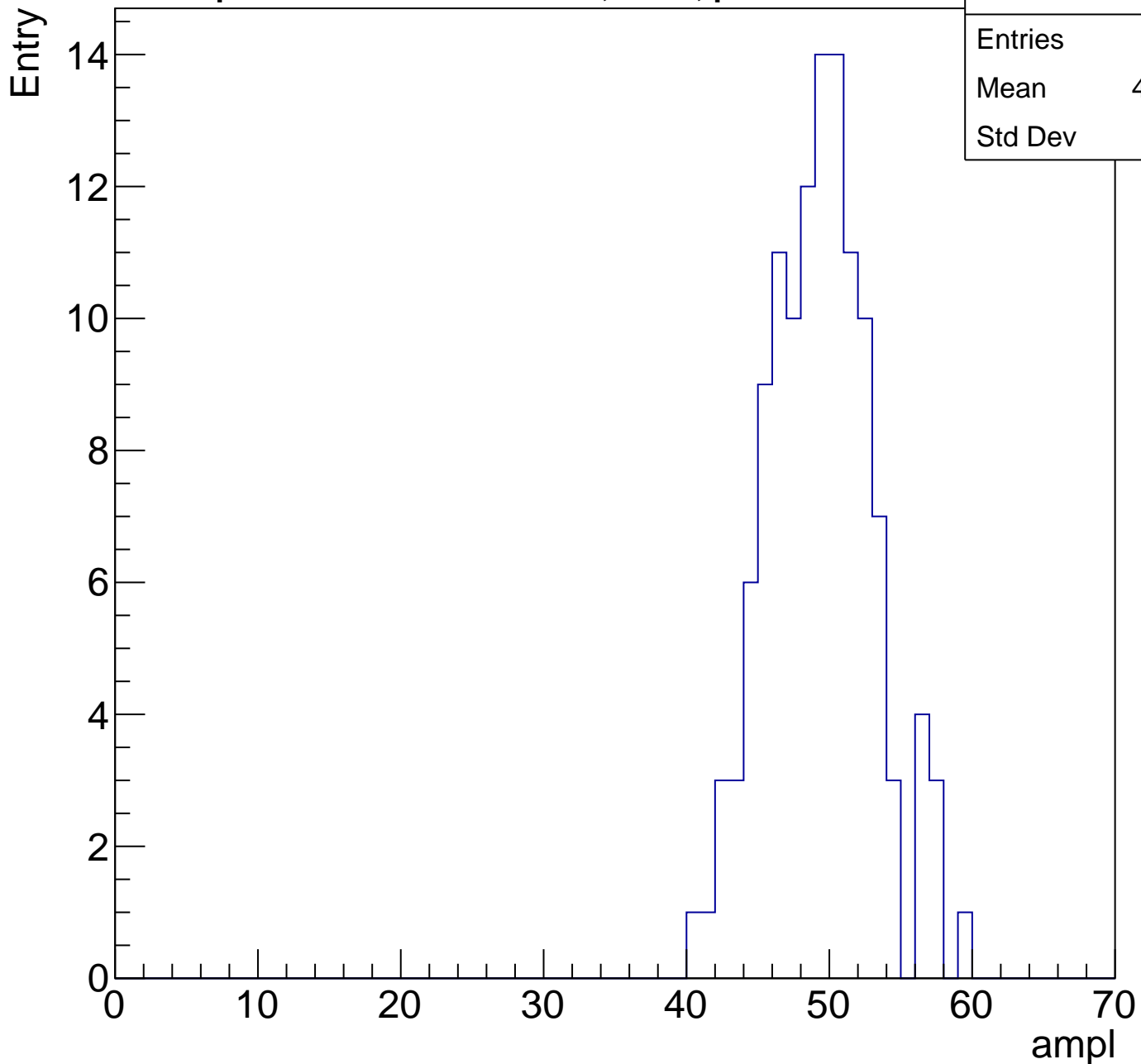


ampl

B0L101S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#1, port C1

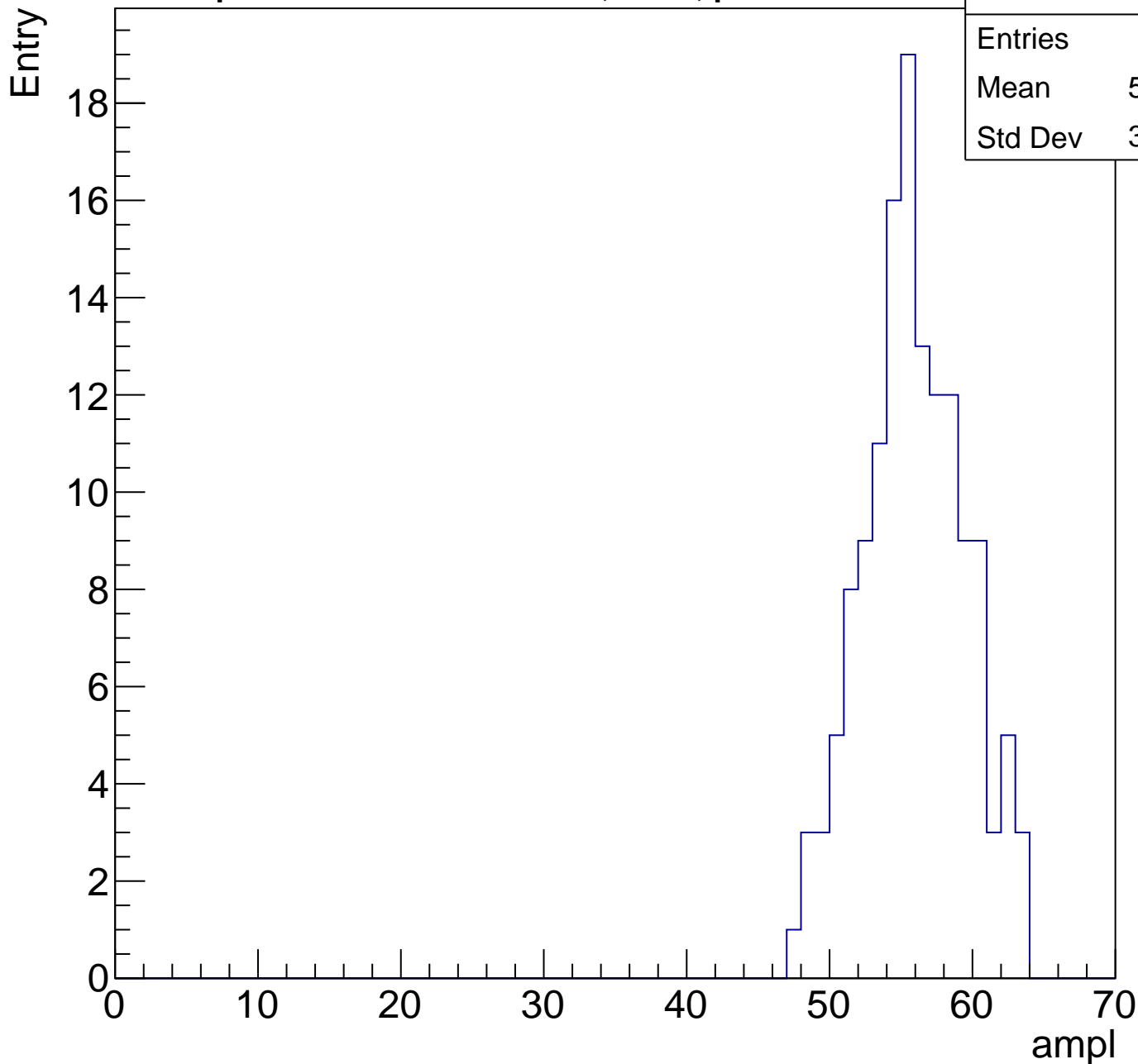
Entries	123
Mean	48.87
Std Dev	3.68



B0L101S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	55.44
Std Dev	3.542



B0L101S, U15-ch103, adc5

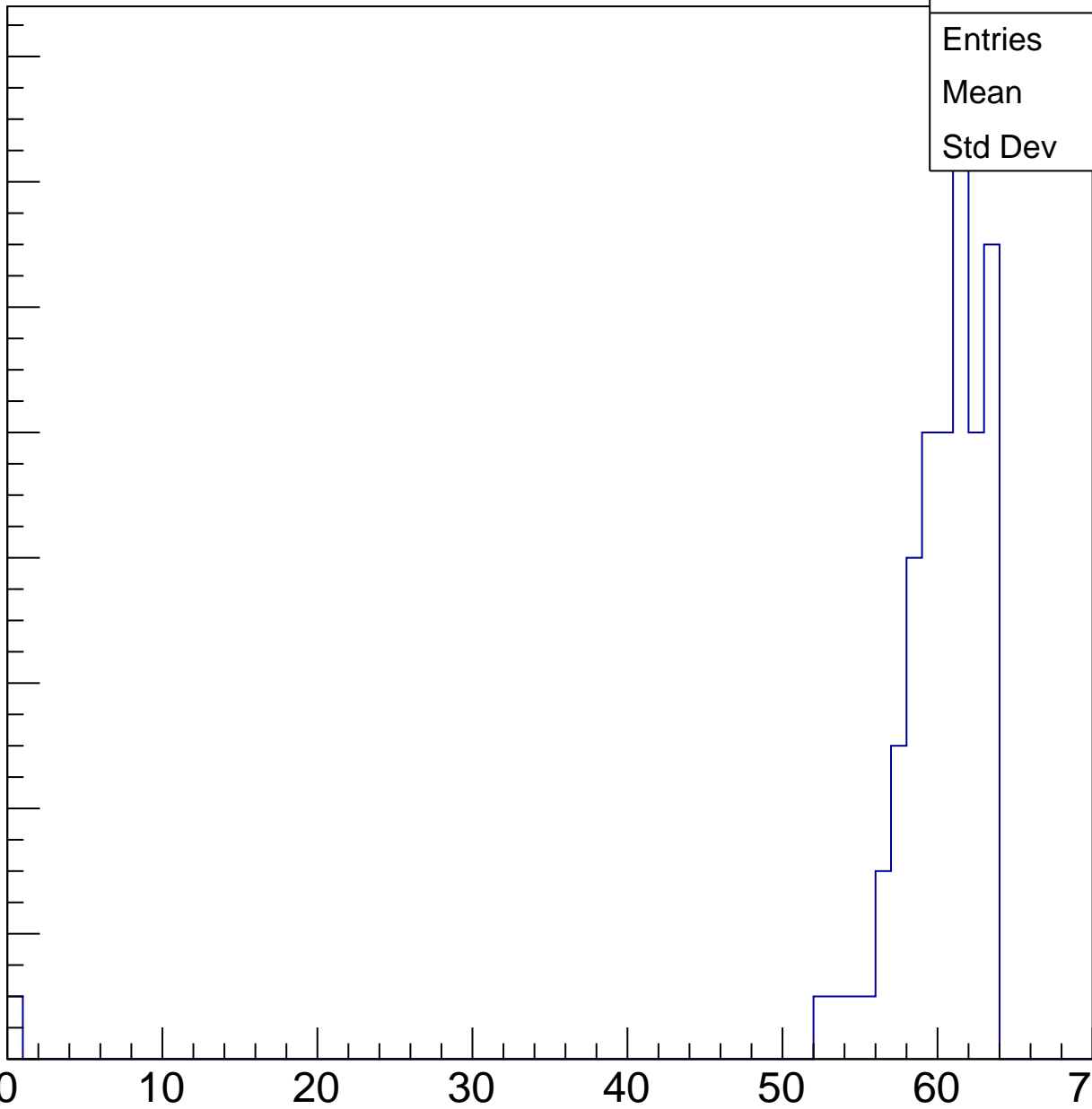
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	80
Mean	59.2
Std Dev	7.098

ampl

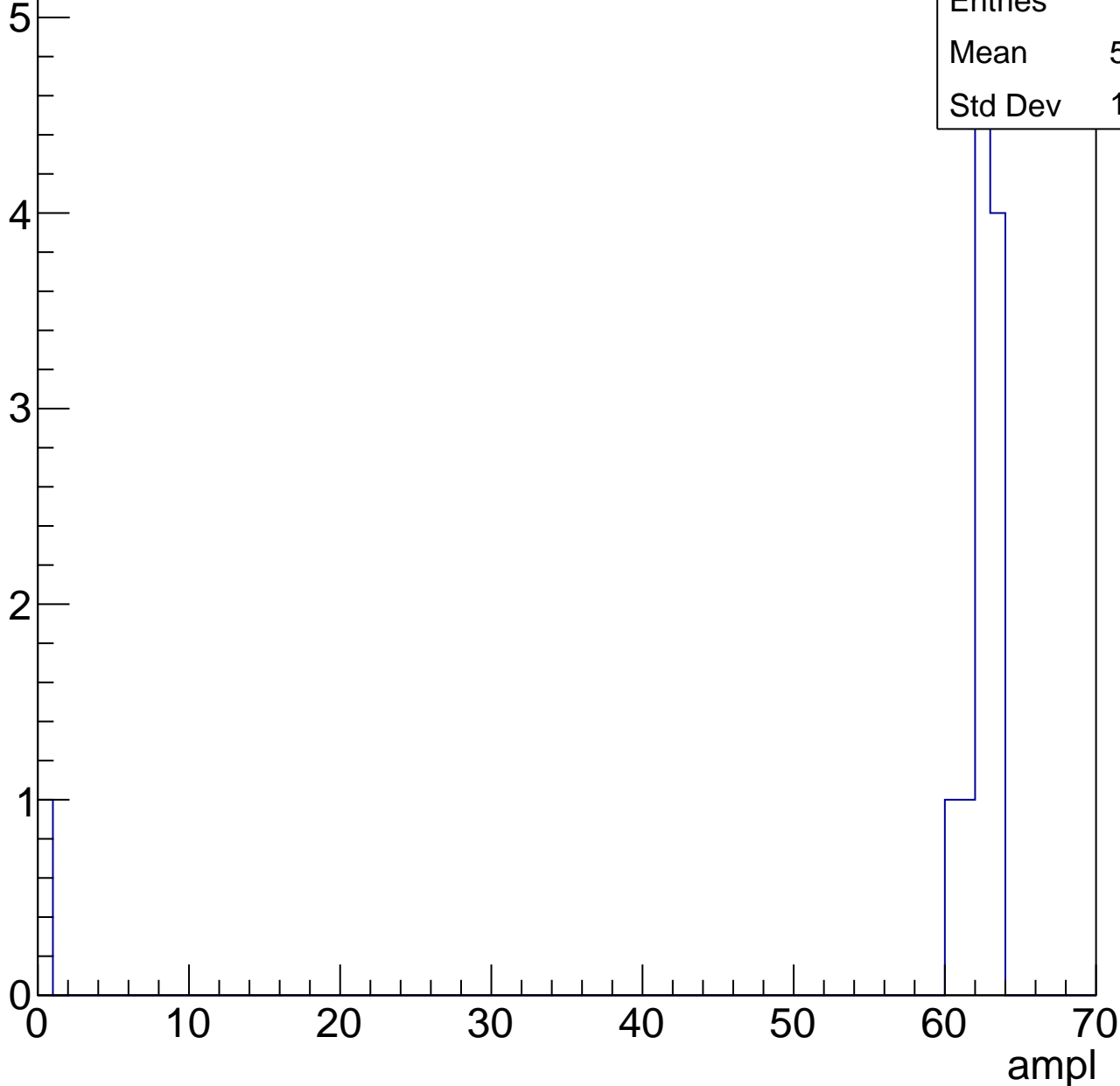


B0L101S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

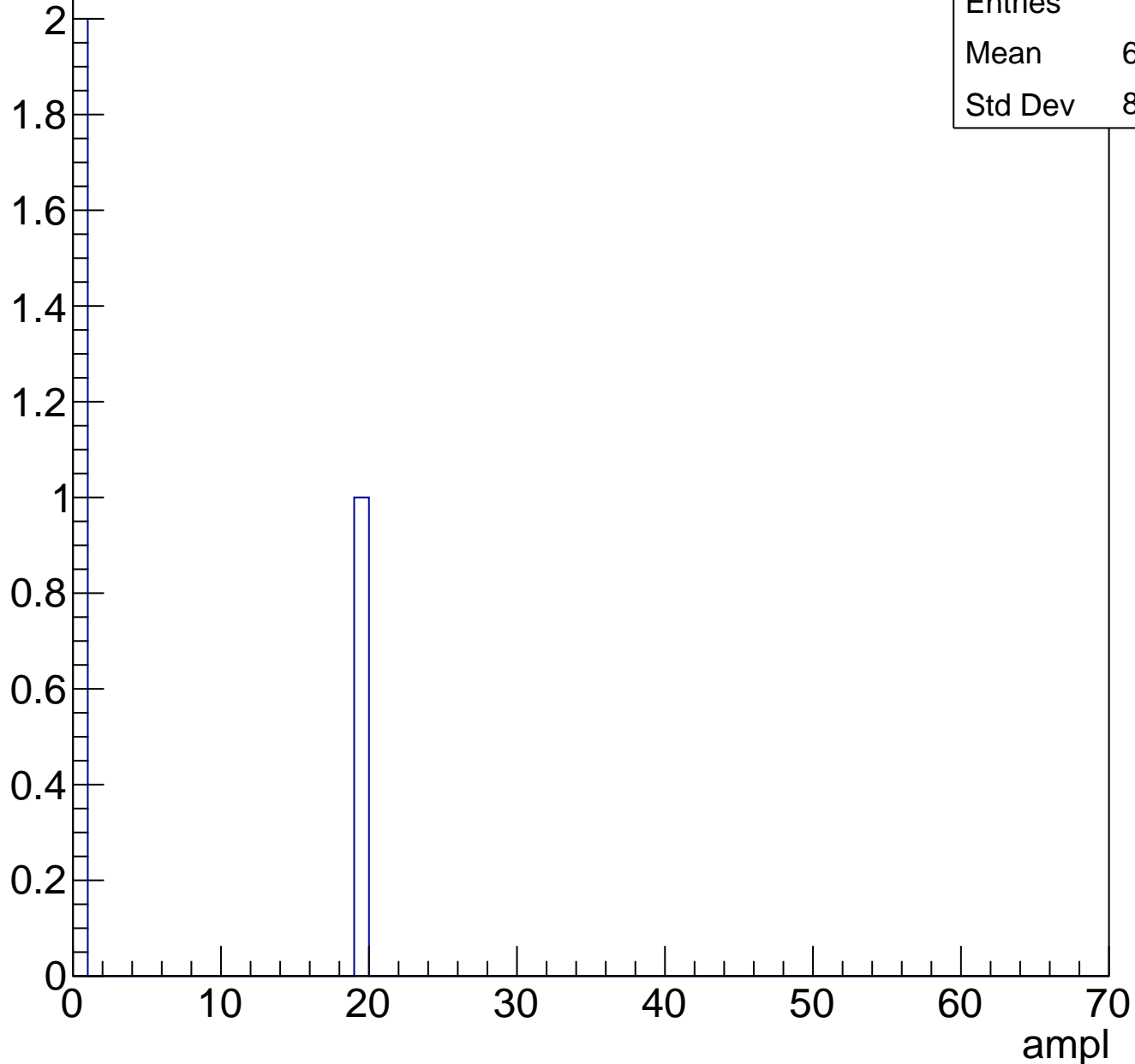
Entries	12
Mean	56.92
Std Dev	17.18



B0L101S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B0L101S, U15-ch104, adc0

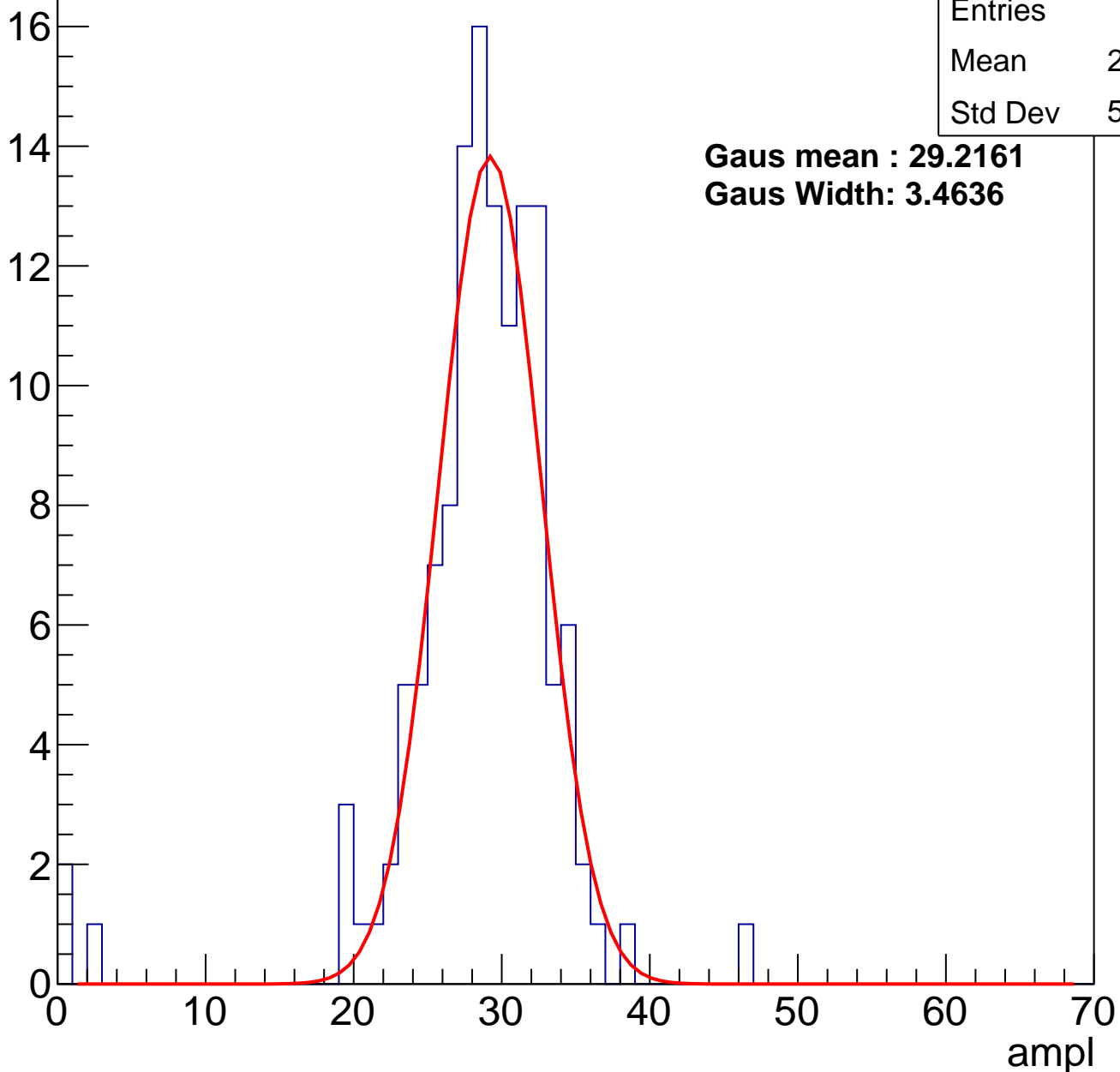
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	28.04
Std Dev	5.726

Gaus mean : 29.2161

Gaus Width: 3.4636

Entry



B0L101S, U15-ch104, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	126
Mean	34.75
Std Dev	3.939

Gaus mean : 35.1980

Gaus Width: 4.2341

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch104, adc2

calib_packv5_042523_0143.root, FC#1, port C1

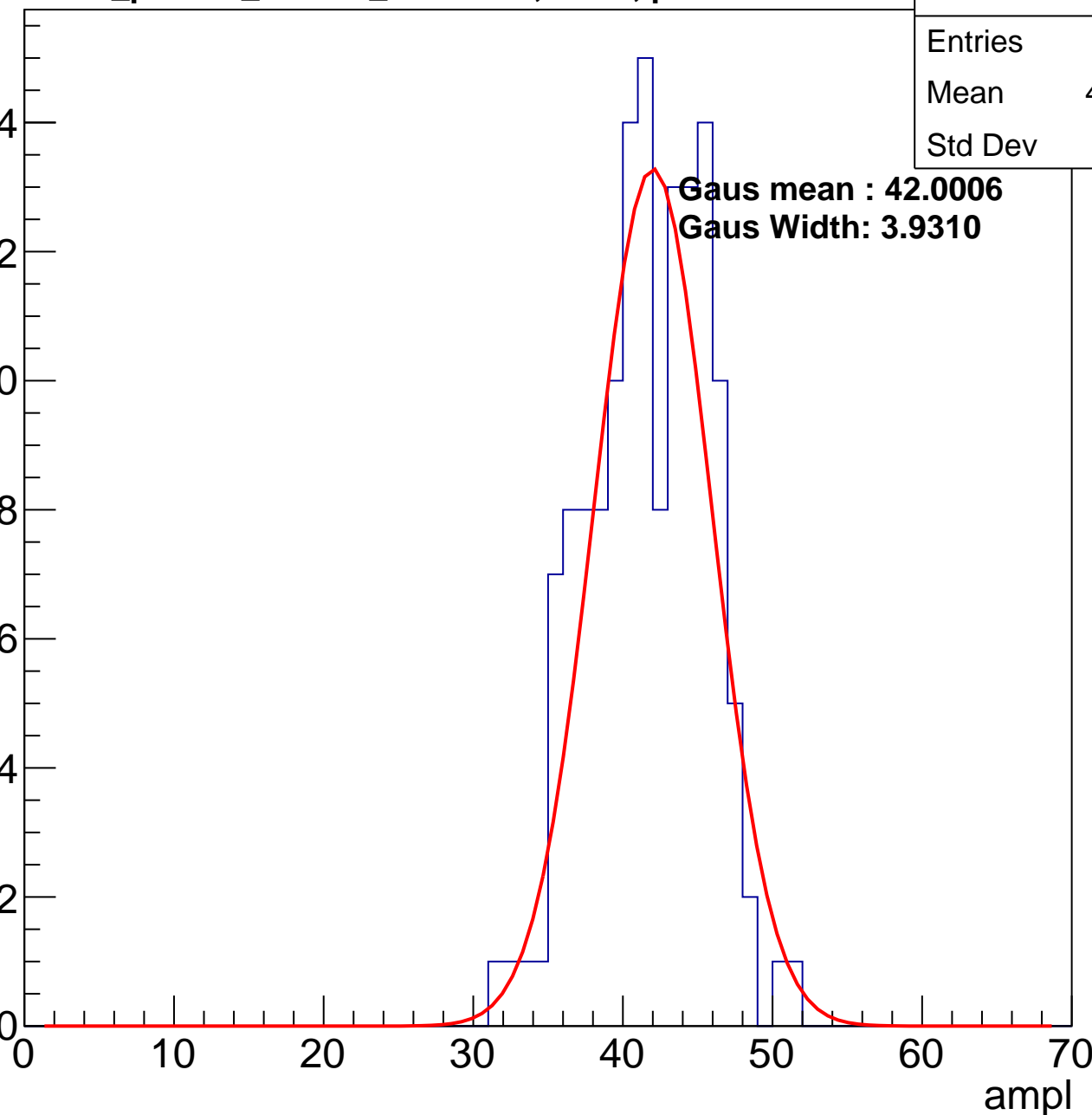
Entry

14
12
10
8
6
4
2
0

Entries	141
Mean	41.26
Std Dev	3.86

Gaus mean : 42.0006

Gaus Width: 3.9310

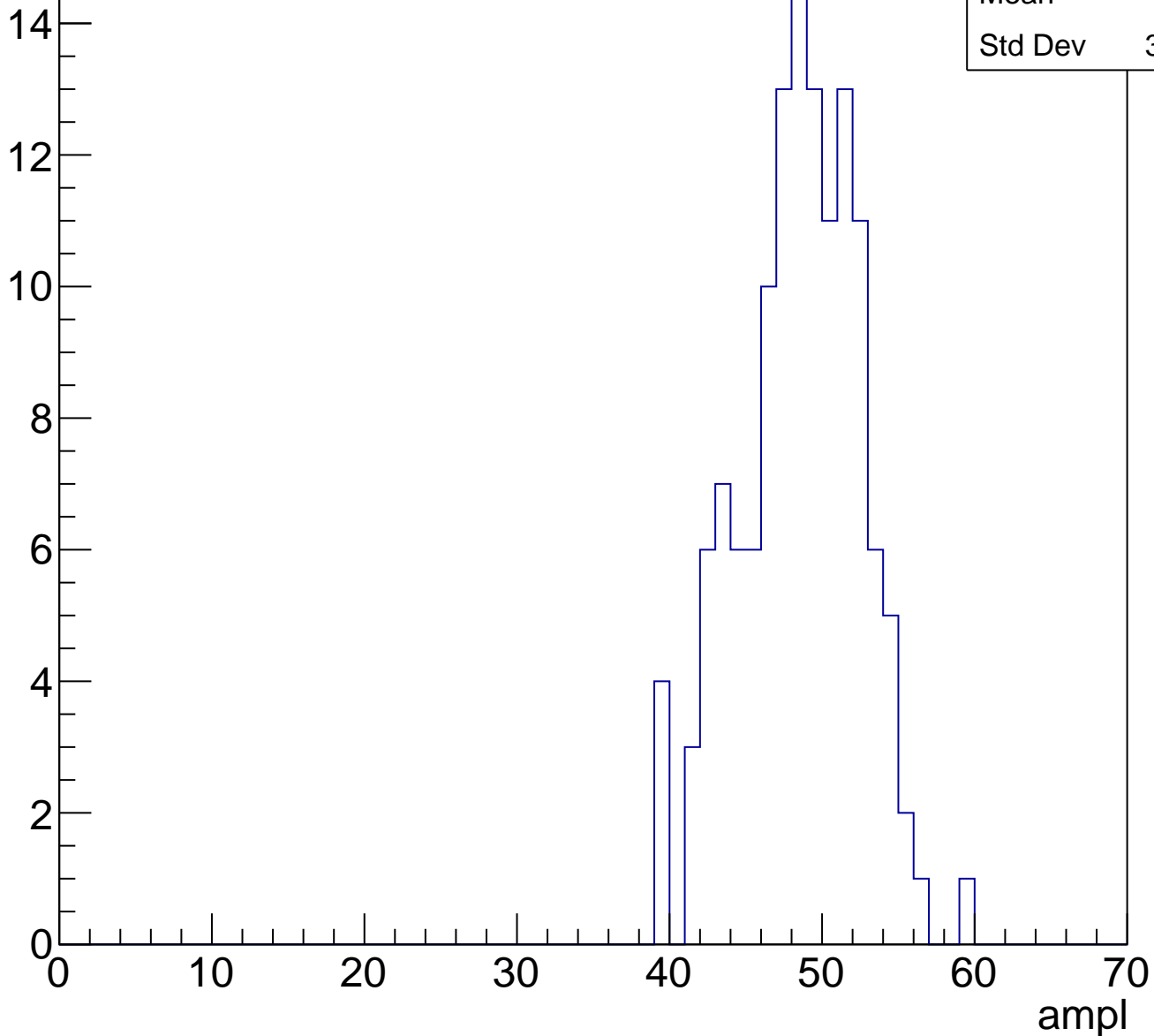


B0L101S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	133
Mean	48.06
Std Dev	3.904

Entry



B0L101S, U15-ch104, adc4

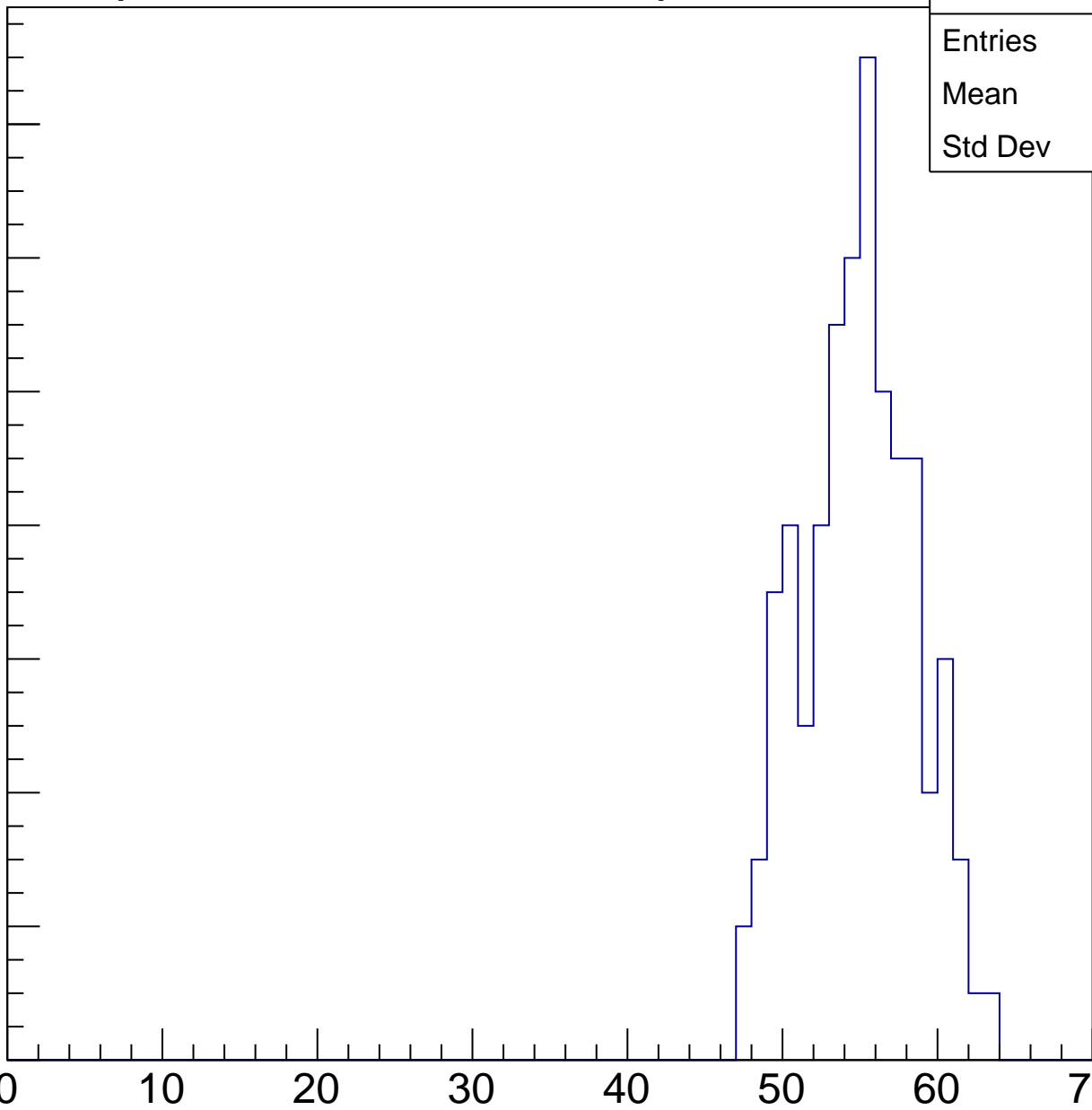
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	54.45
Std Dev	3.581

ampl



B0L101S, U15-ch104, adc5

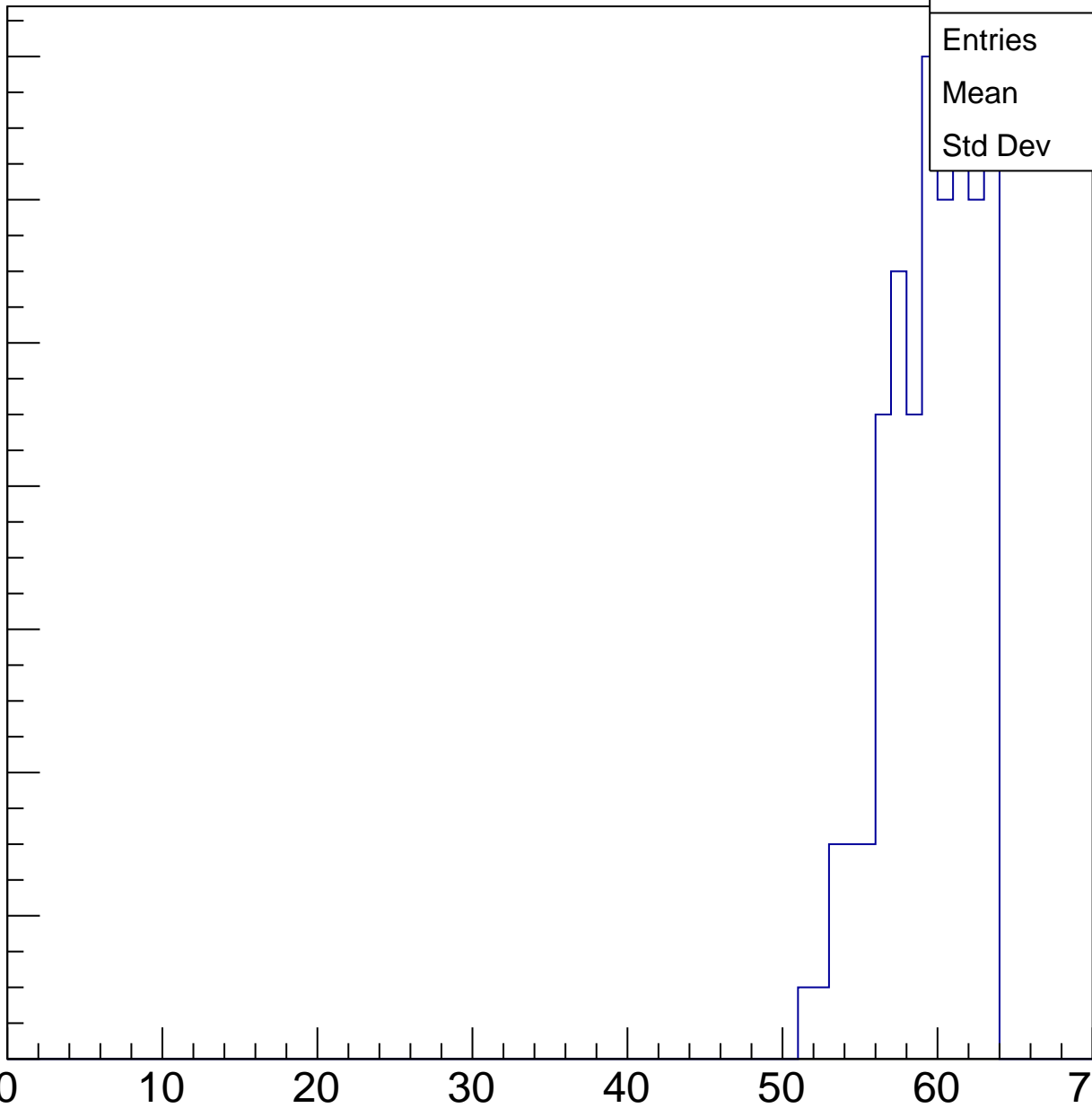
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	59.13
Std Dev	2.875

ampl

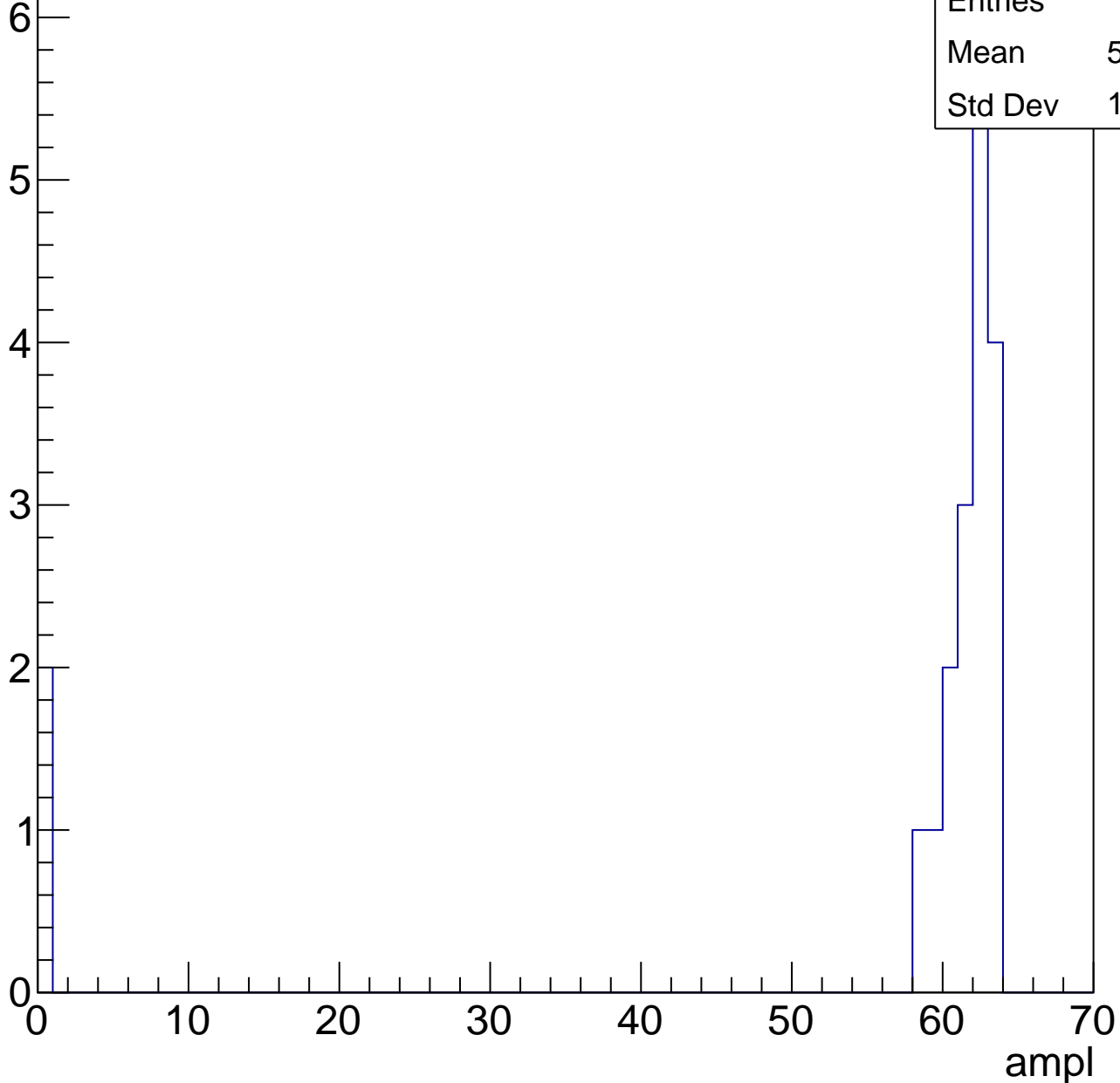


B0L101S, U15-ch104, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

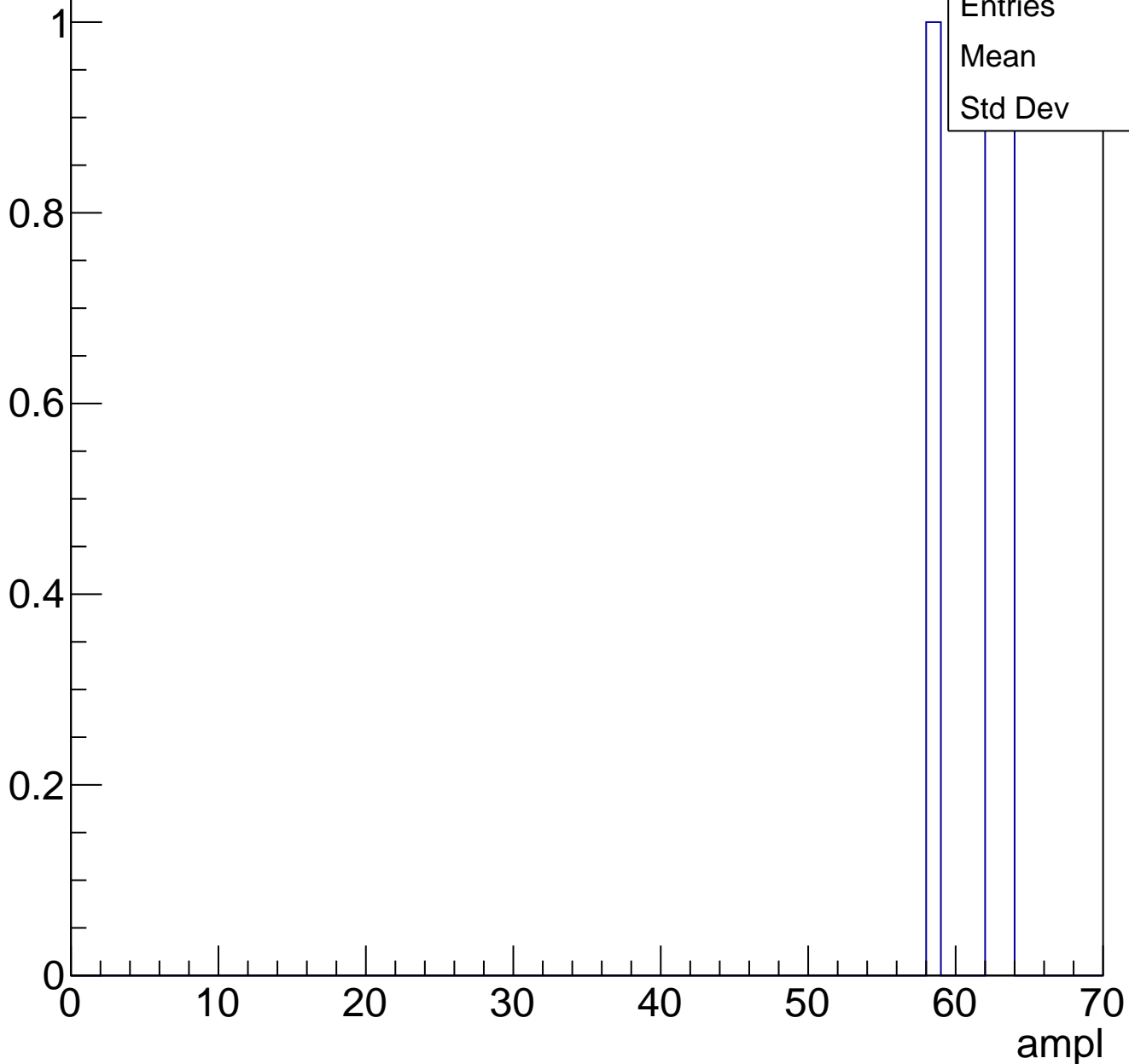
Entries	19
Mean	54.95
Std Dev	18.89



B0L101S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch105, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	28.72
Std Dev	4.511

Gaus mean : 29.2913

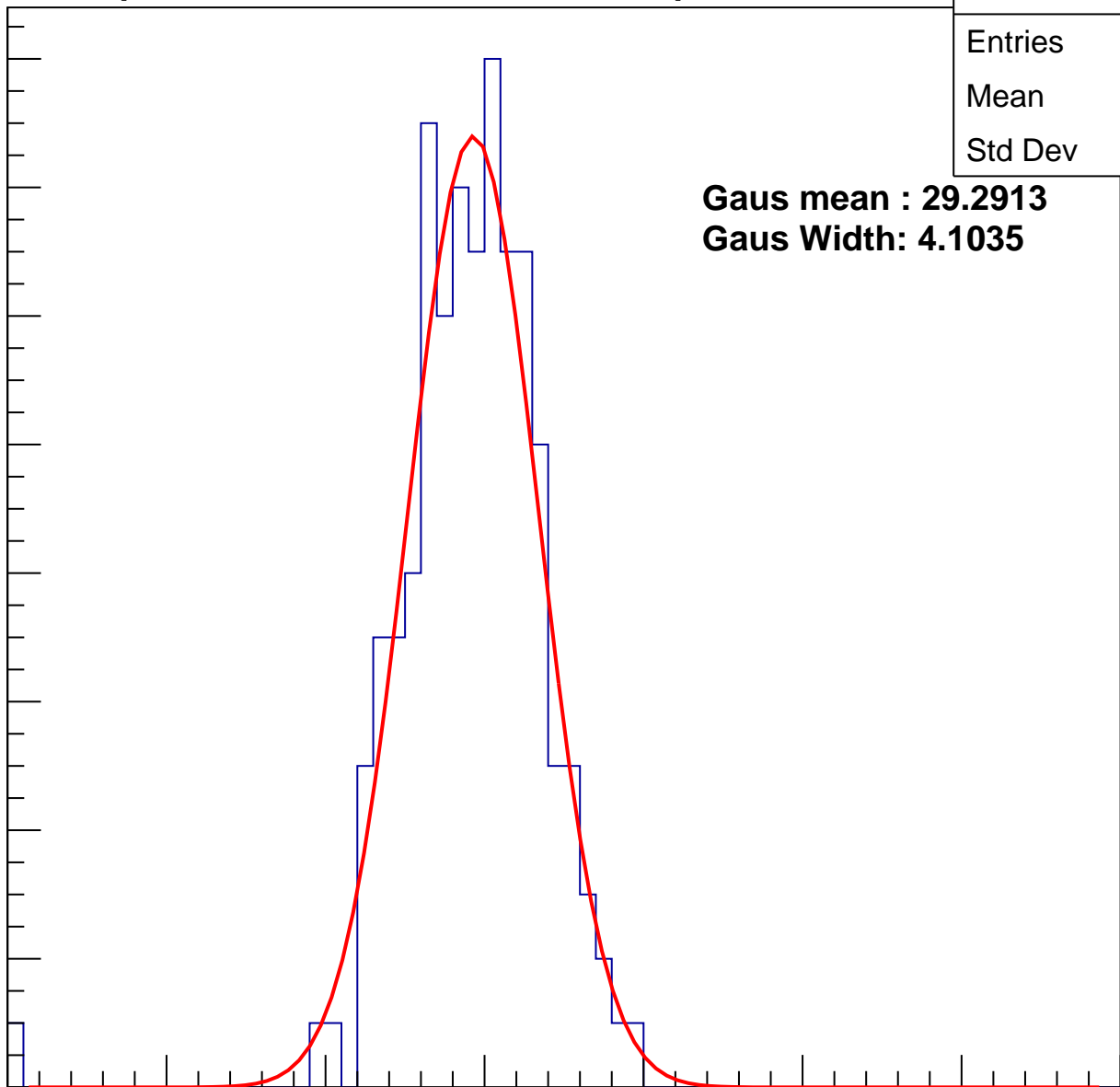
Gaus Width: 4.1035

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch105, adc1

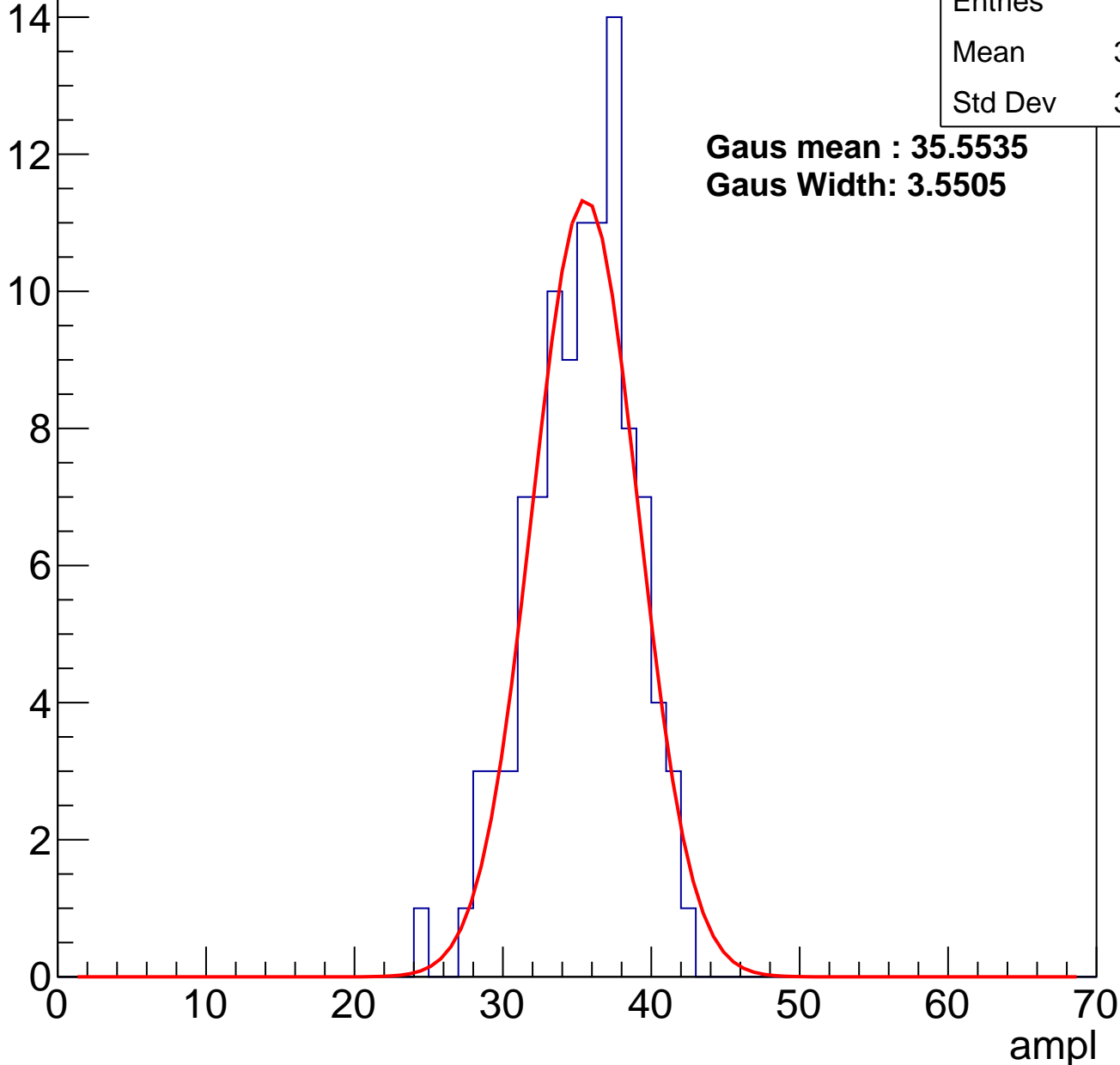
calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	34.85
Std Dev	3.476

Gaus mean : 35.5535

Gaus Width: 3.5505

Entry



B0L101S, U15-ch105, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

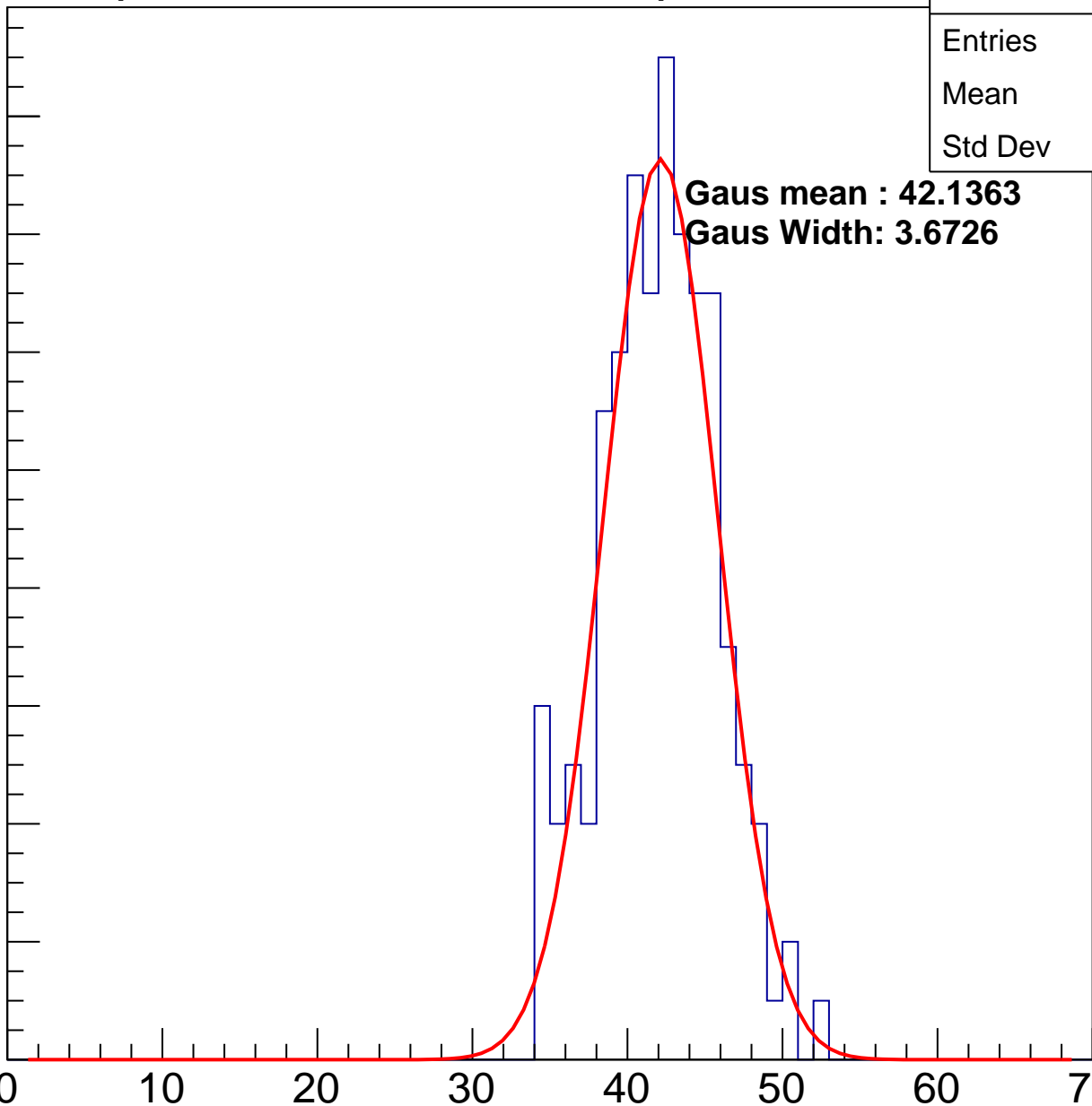
16
14
12
10
8
6
4
2
0

Entries	147
Mean	41.59
Std Dev	3.719

Gaus mean : 42.1363

Gaus Width: 3.6726

ampl

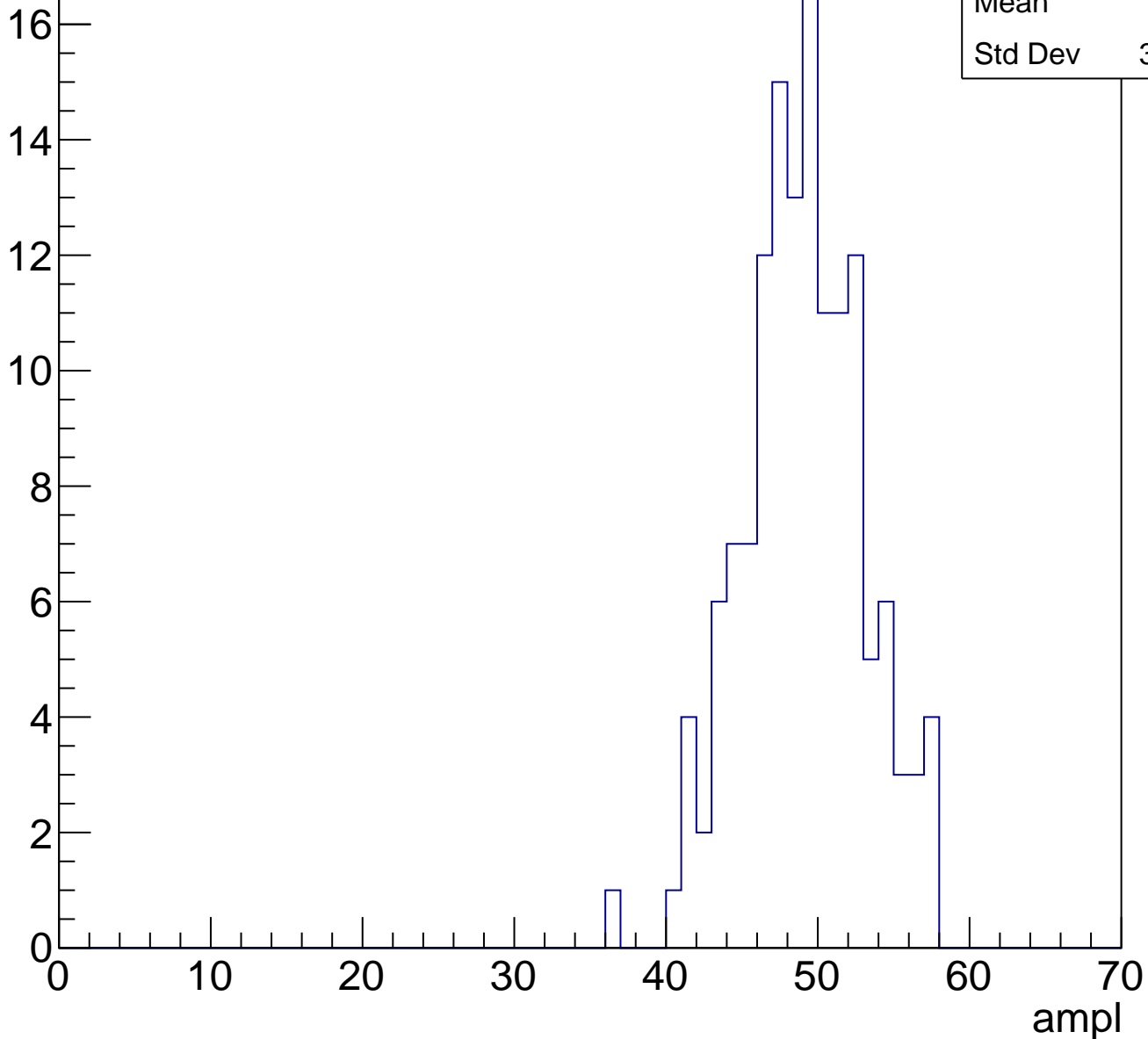


B0L101S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	140
Mean	48.6
Std Dev	3.937



B0L101S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#1, port C1

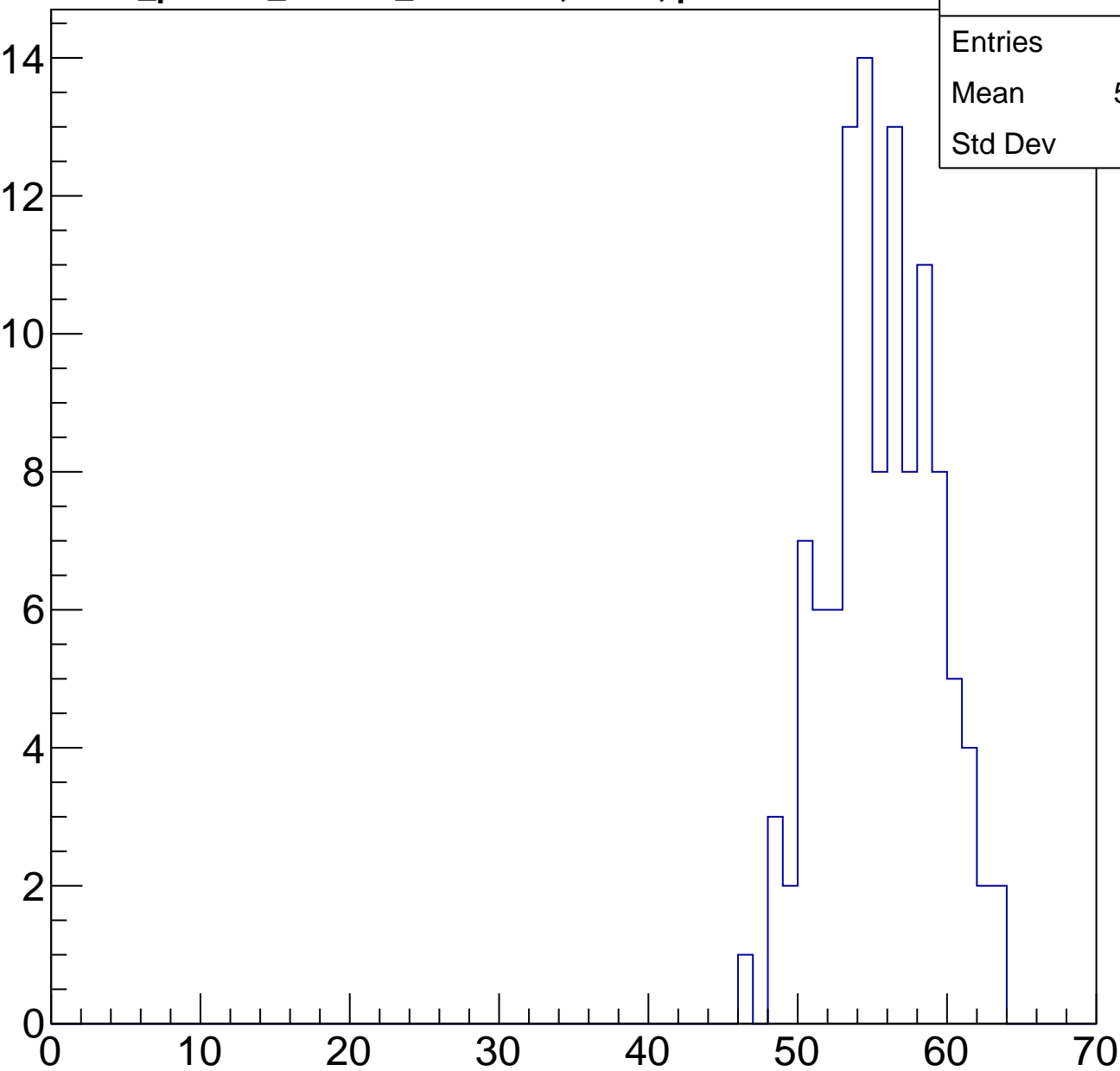
Entries	113
Mean	55.12
Std Dev	3.59

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch105, adc5

calib_packv5_042523_0143.root, FC#1, port C1

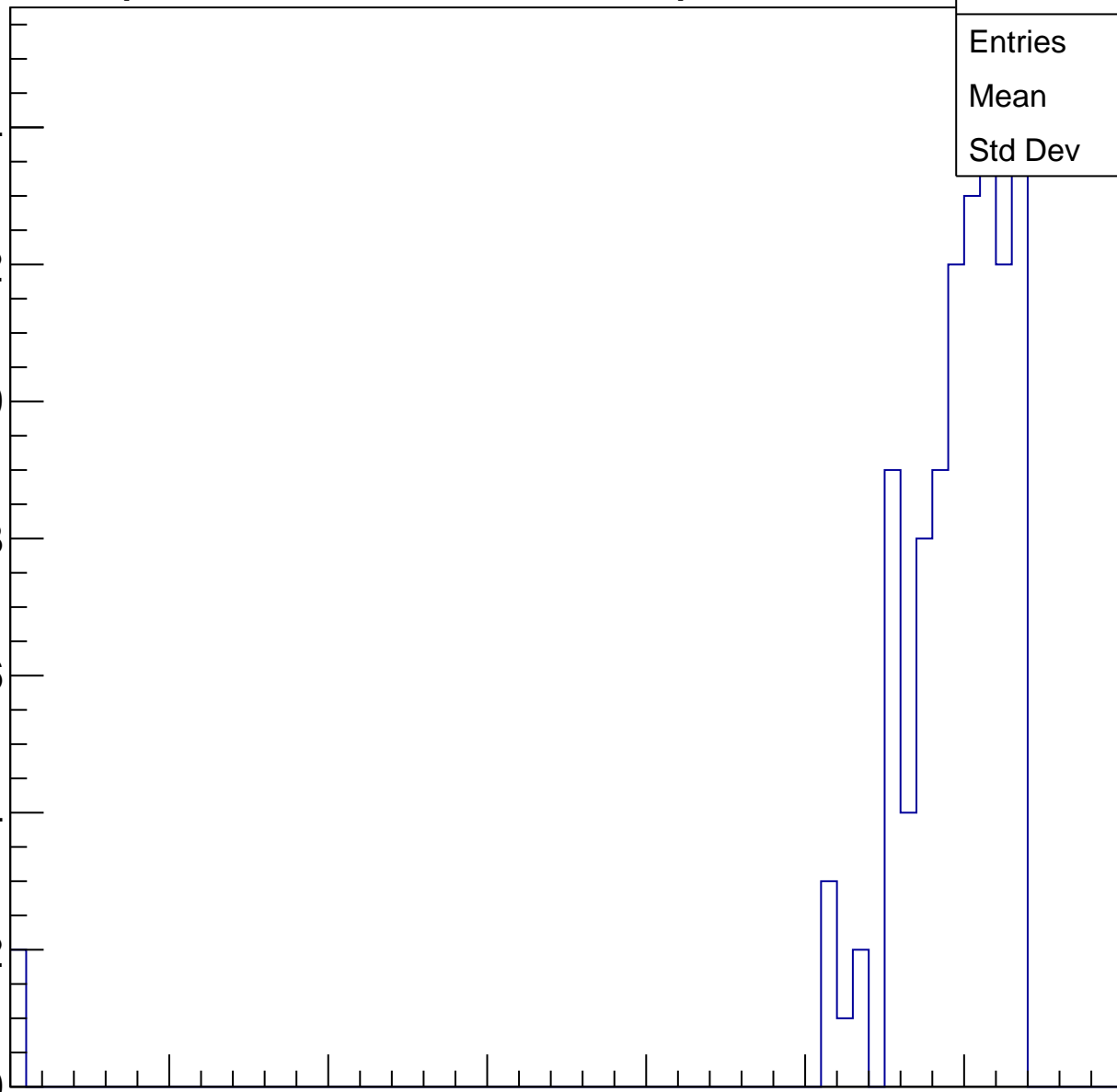
Entries	104
Mean	58.07
Std Dev	8.673

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

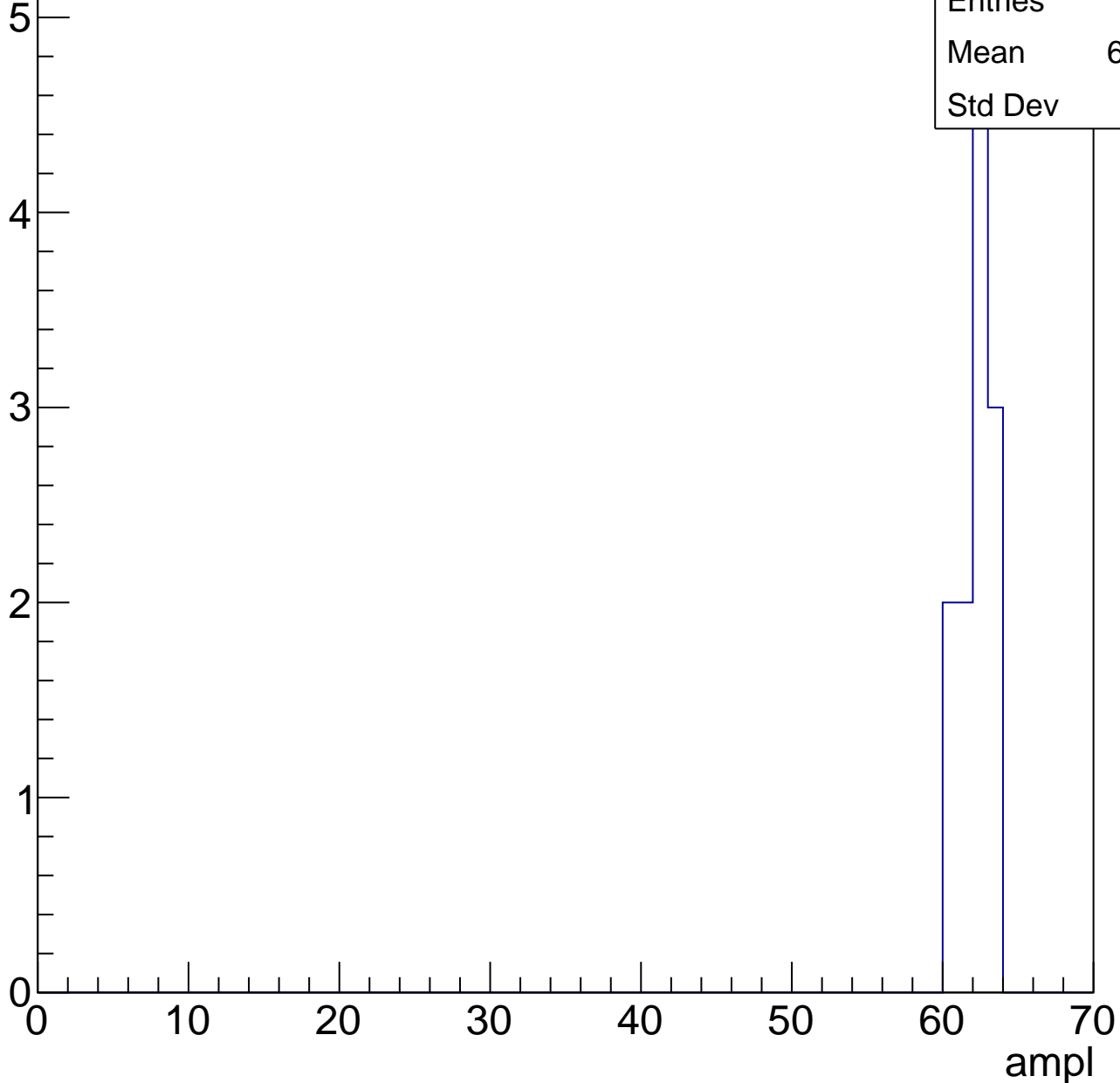


B0L101S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

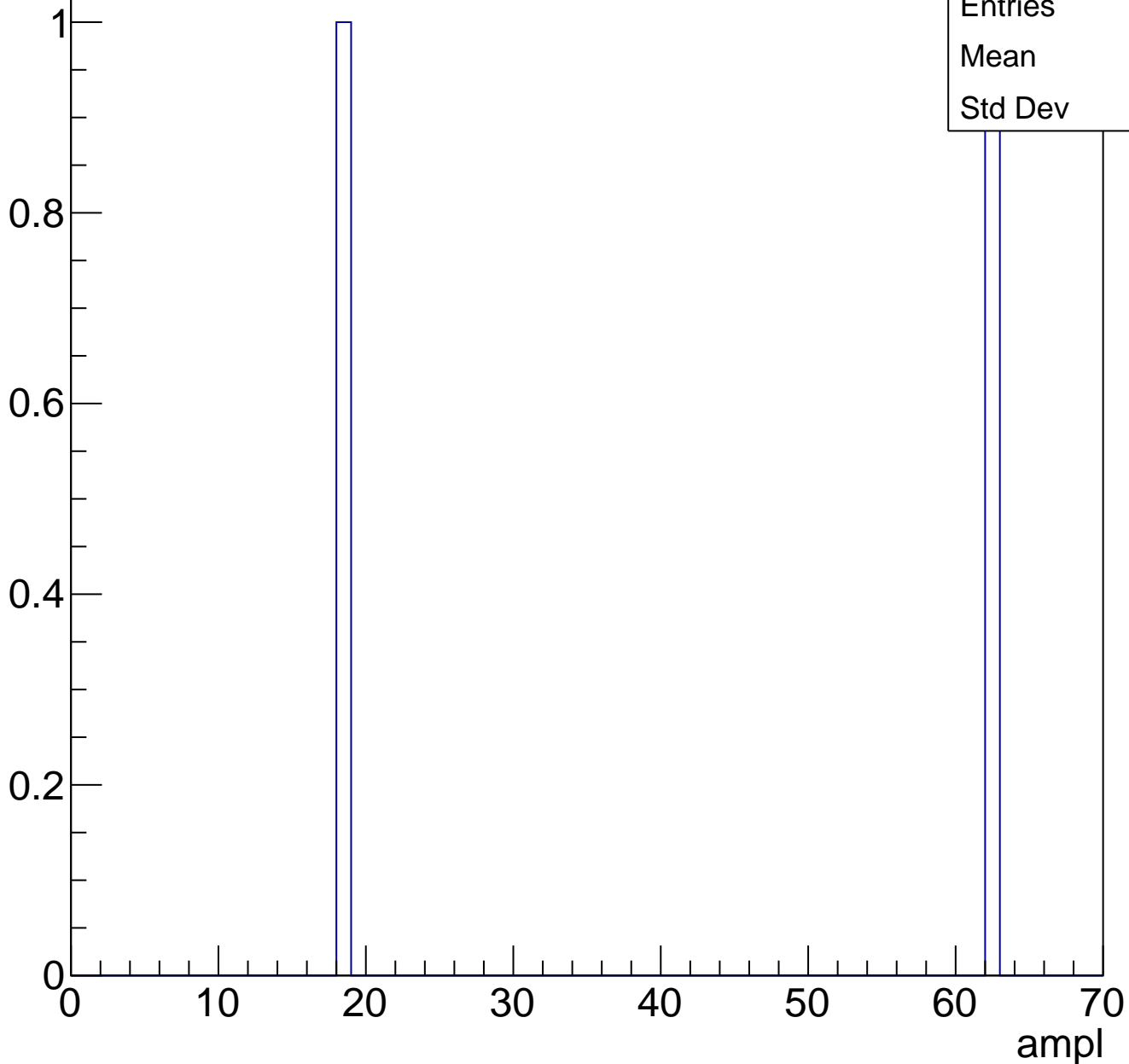
Entries	12
Mean	61.75
Std Dev	1.01



B0L101S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch106, adc0

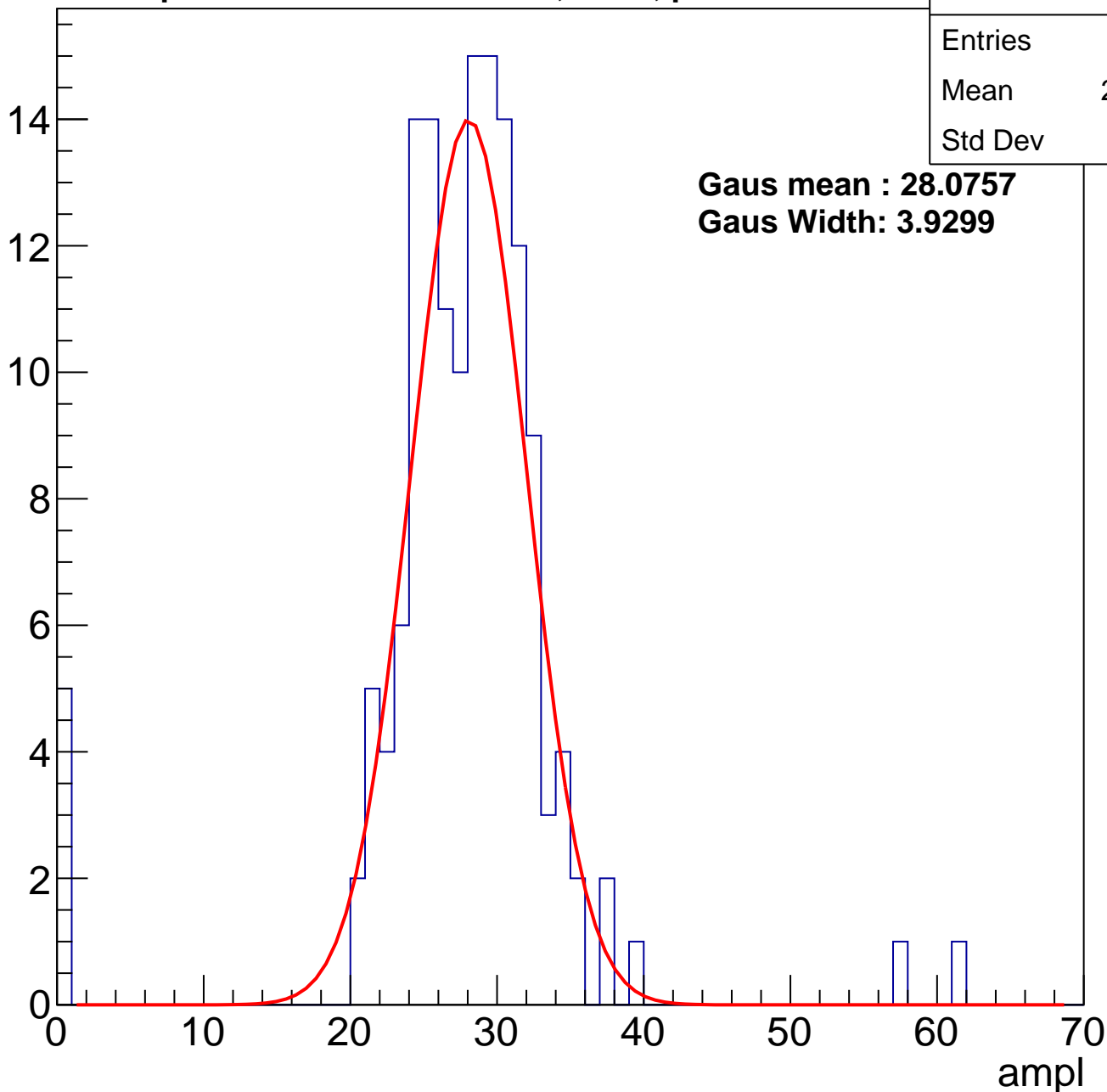
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	27.23
Std Dev	7.19

Gaus mean : 28.0757

Gaus Width: 3.9299

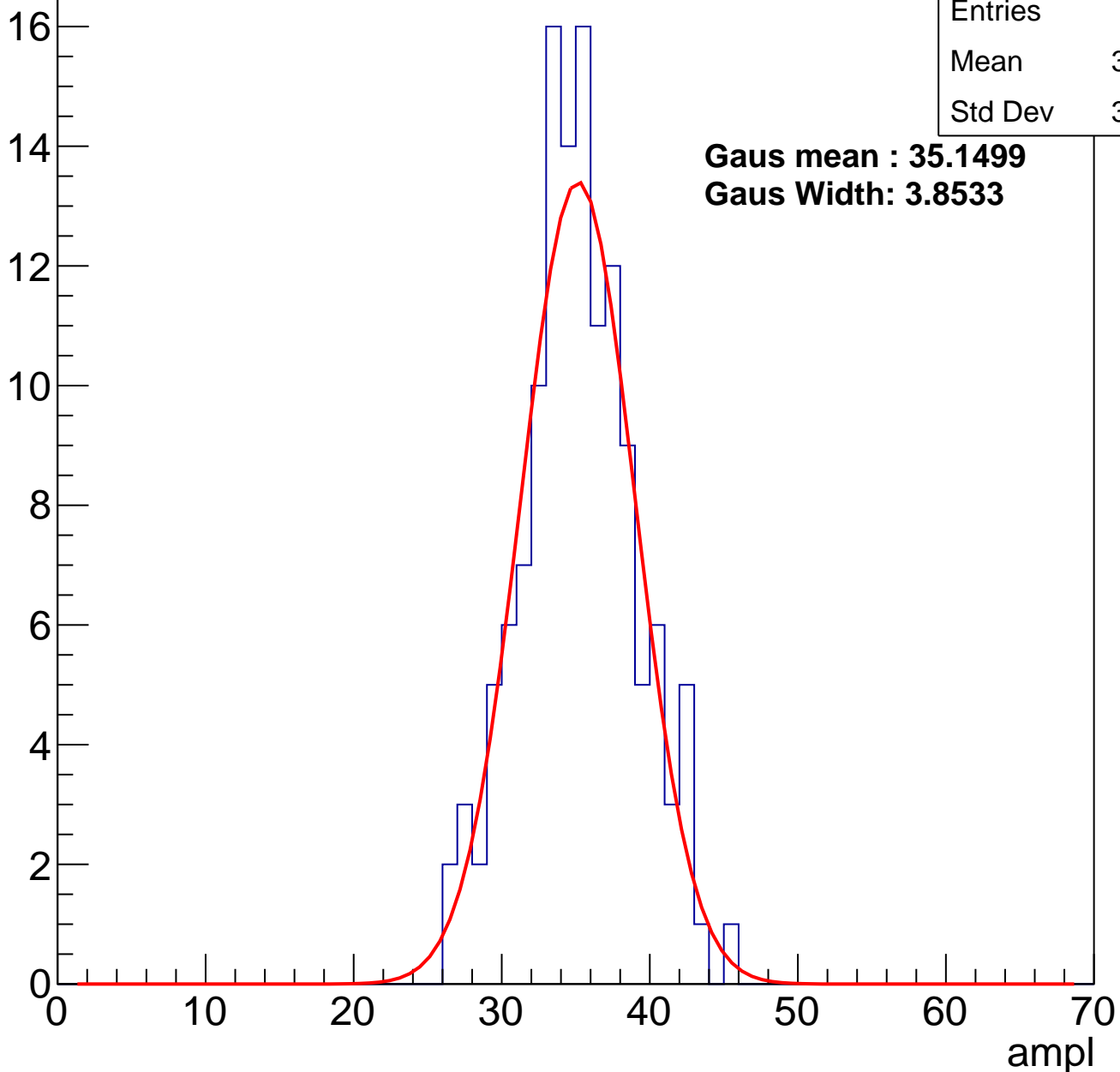
Entry



B0L101S, U15-ch106, adc1

calib_packv5_042523_0143.root, FC#1, port C1

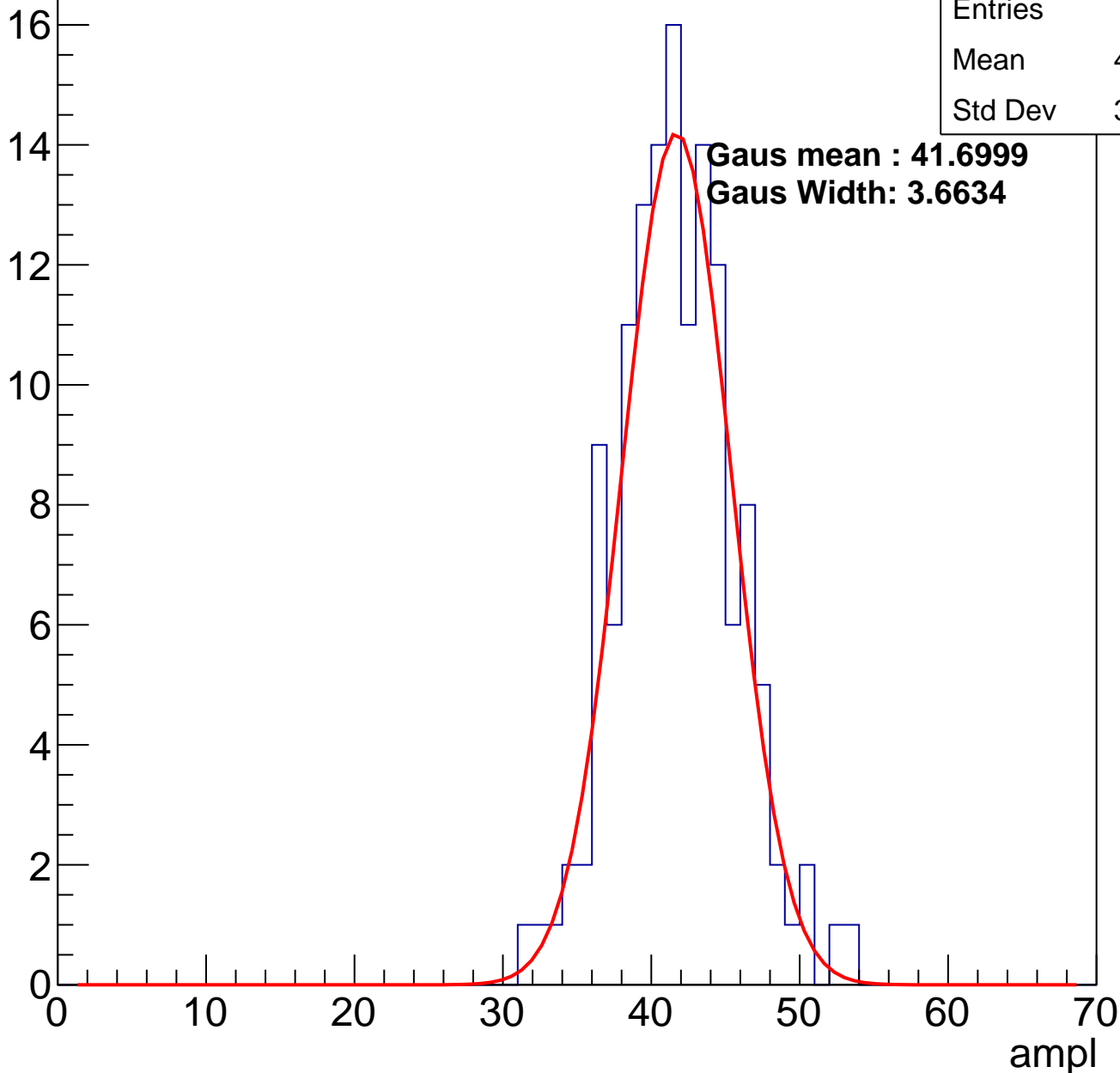
Entry



B0L101S, U15-ch106, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch106, adc3

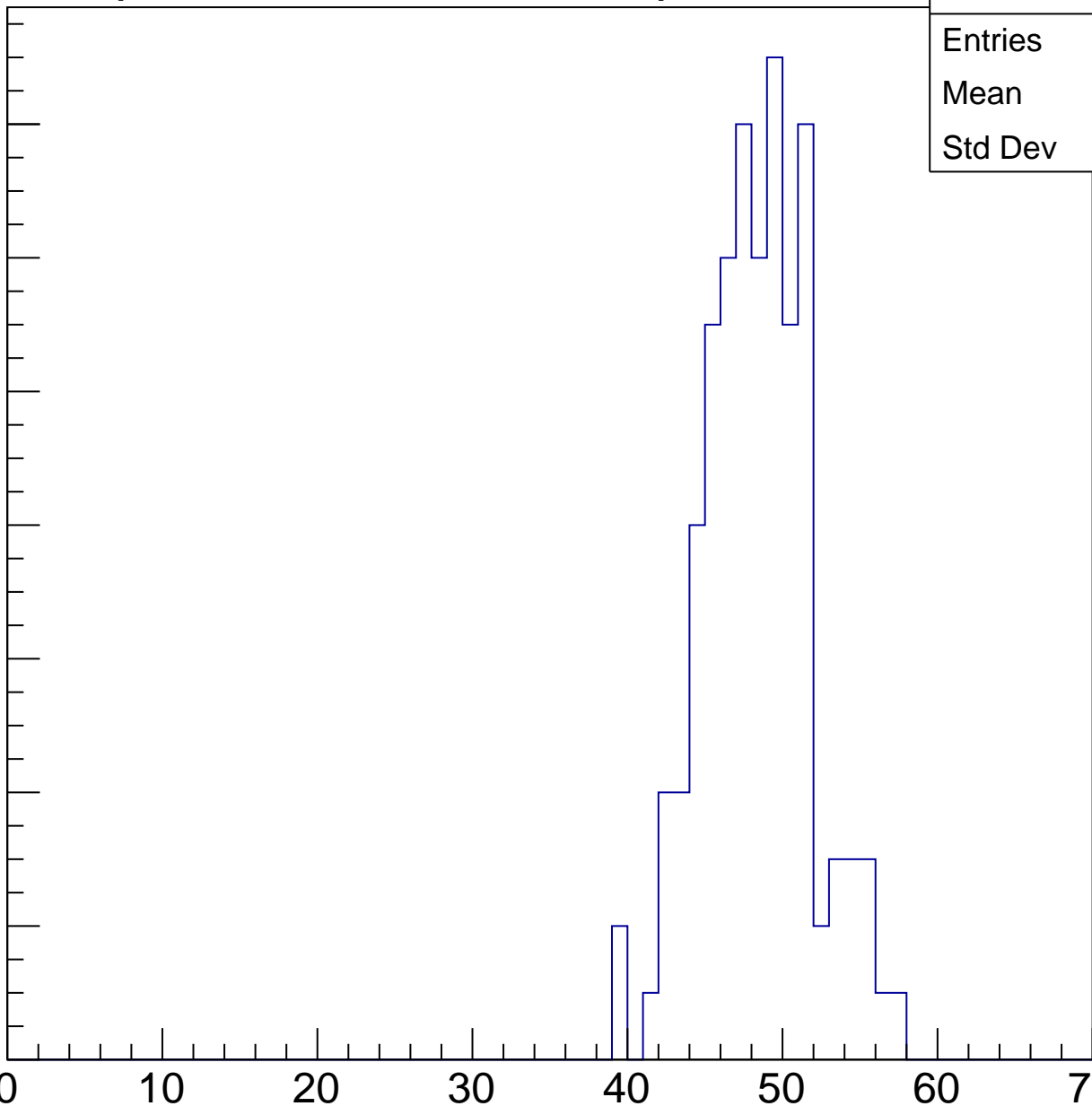
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	47.88
Std Dev	3.443

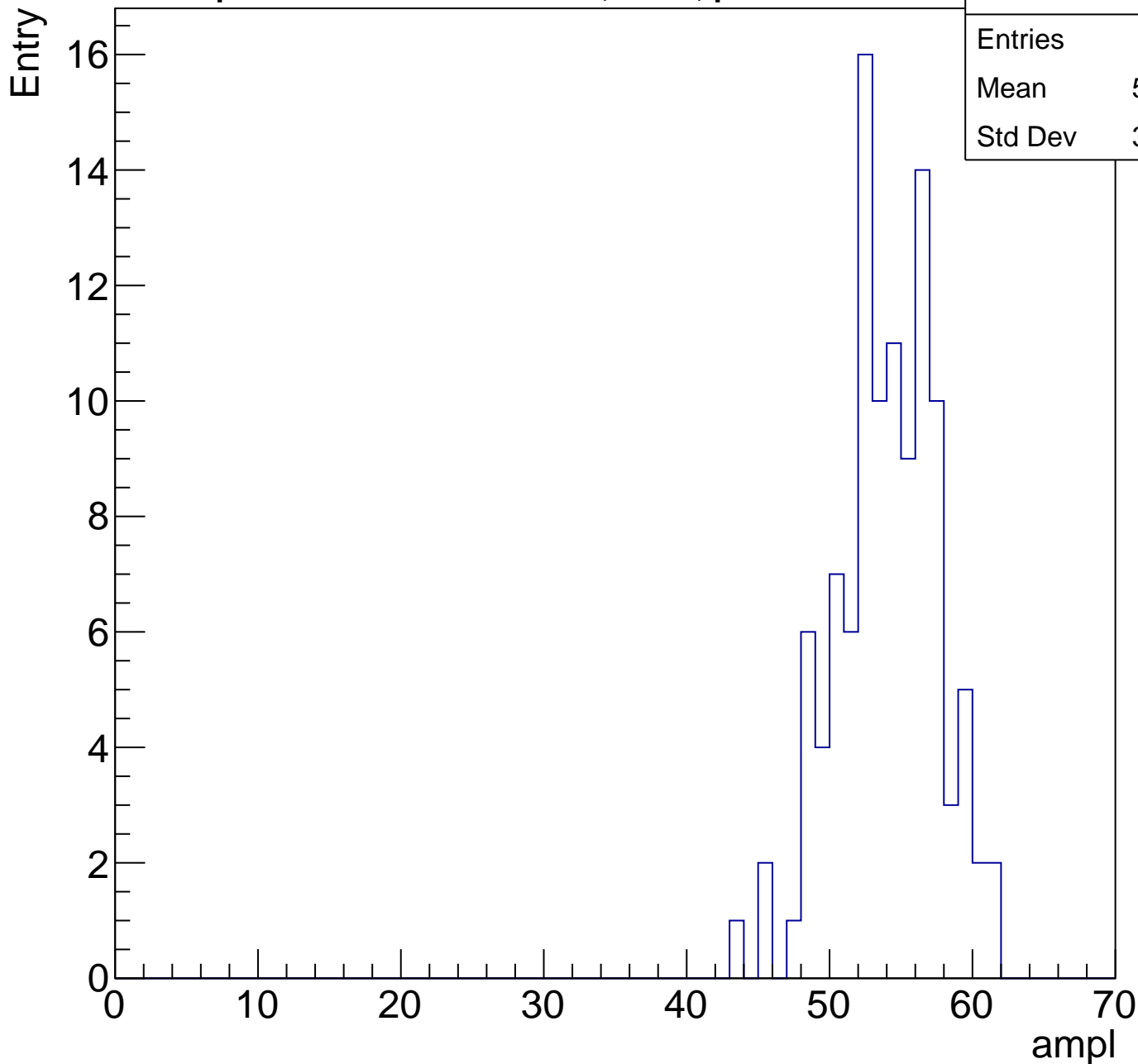
ampl



B0L101S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	53.54
Std Dev	3.536



B0L101S, U15-ch106, adc5

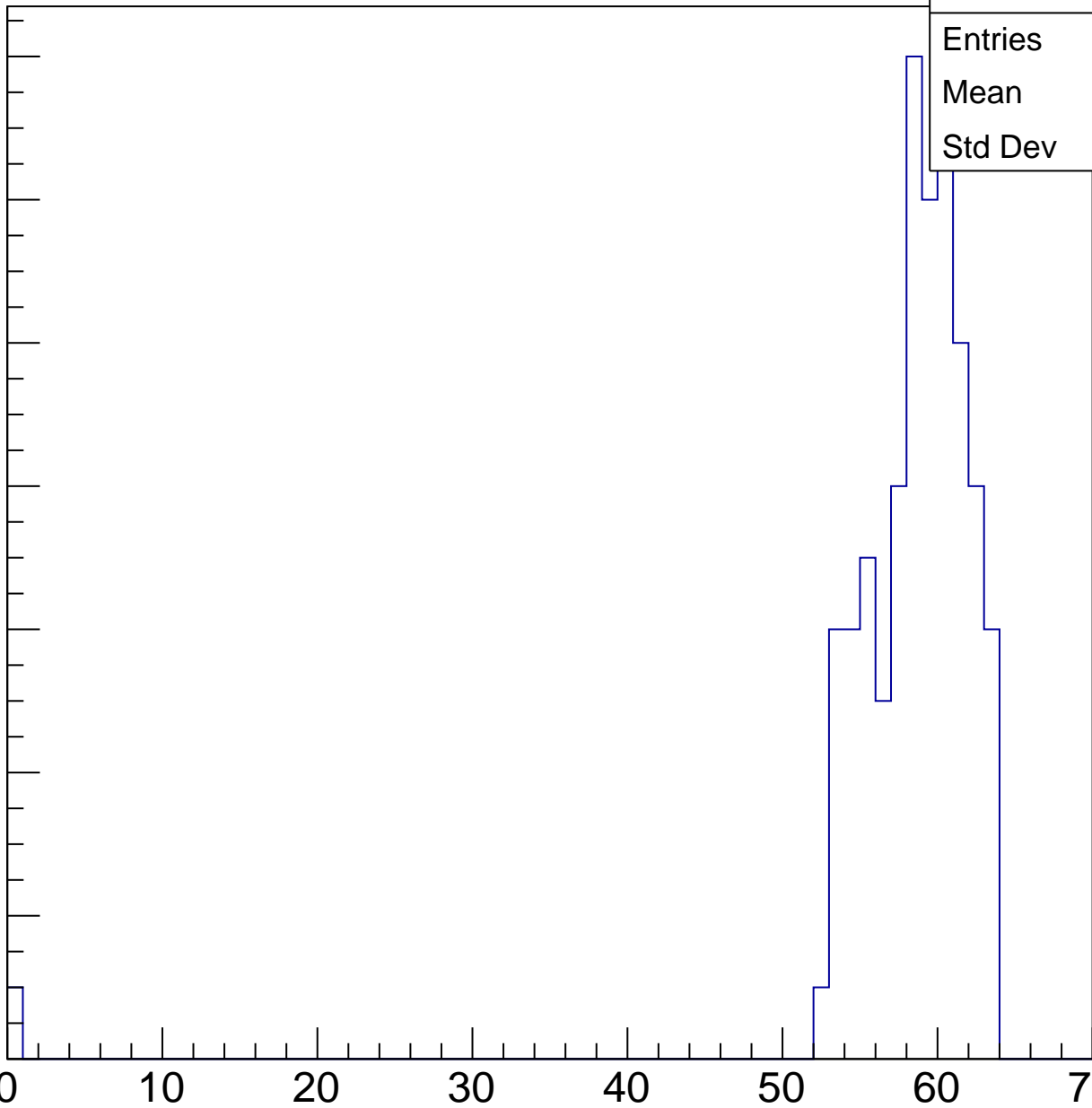
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	57.72
Std Dev	6.55

ampl



B0L101S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

Entries	42
Mean	59.57
Std Dev	9.492

ampl

0

10

20

30

40

50

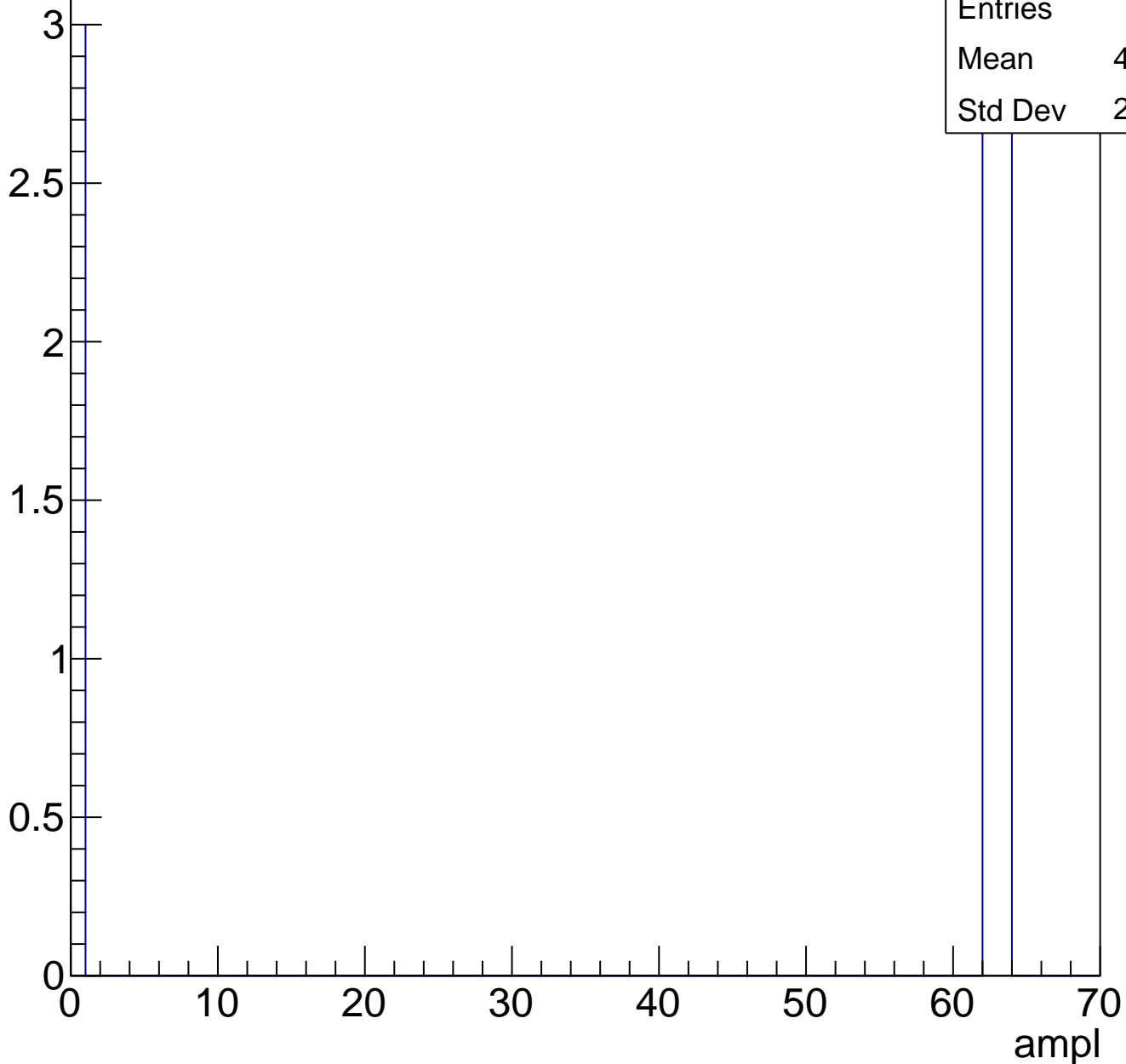
60

70

B0L101S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch107, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	29.12
Std Dev	4.531

Gaus mean : 29.7899

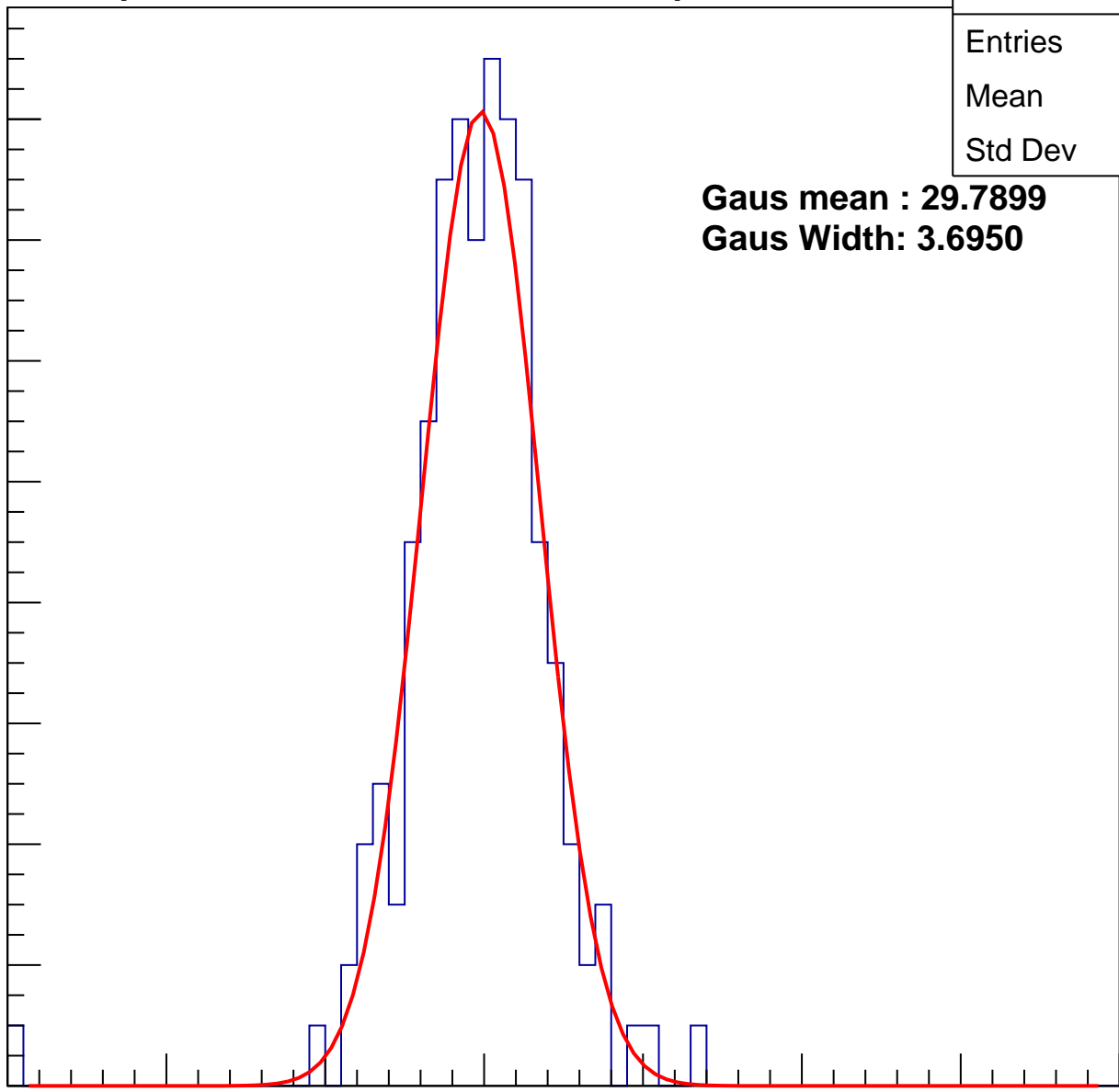
Gaus Width: 3.6950

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch107, adc1

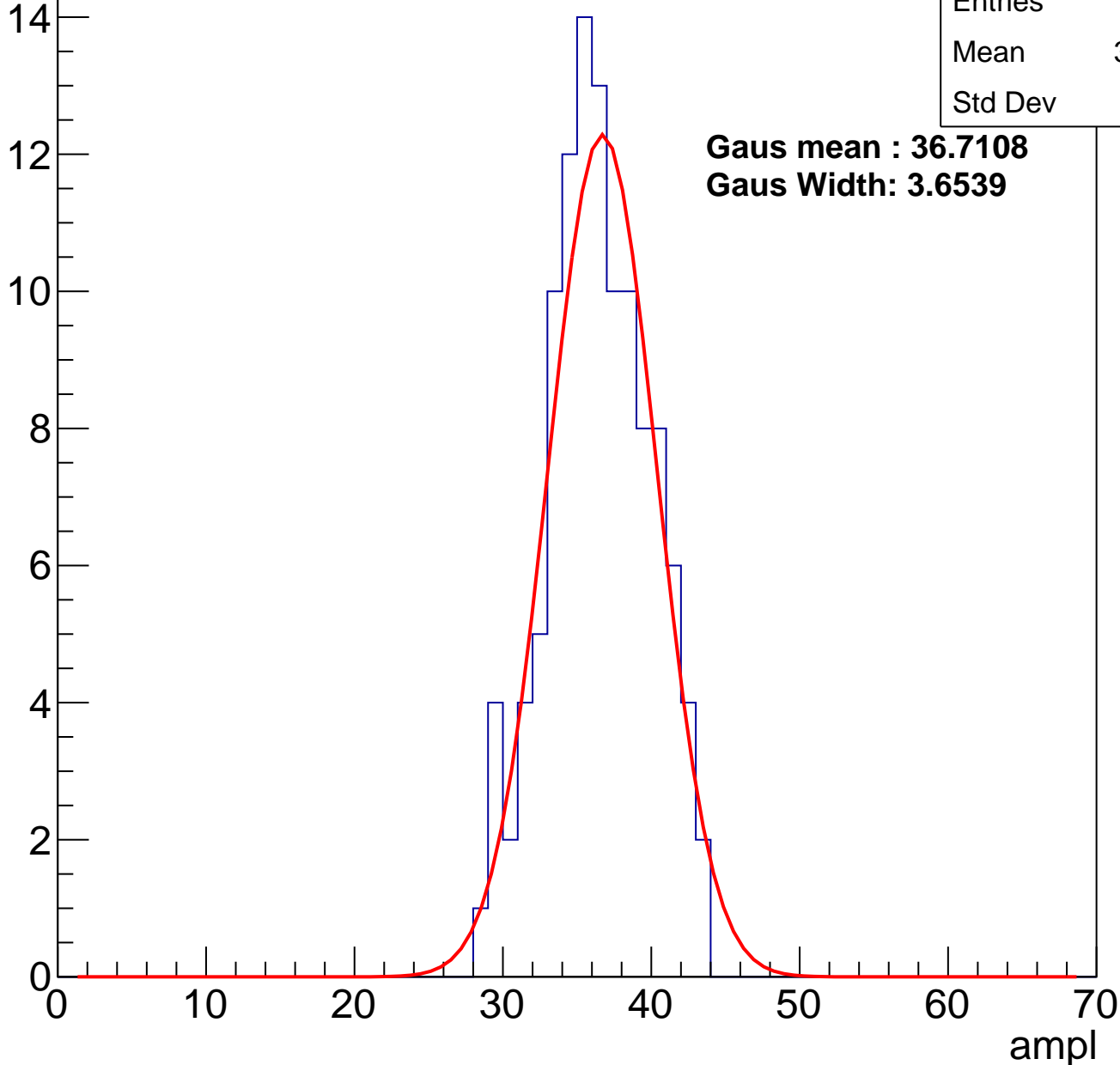
calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	35.98
Std Dev	3.4

Gaus mean : 36.7108

Gaus Width: 3.6539

Entry



B0L101S, U15-ch107, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	41.59
Std Dev	3.377

Gaus mean : 41.9726

Gaus Width: 3.3913

0

0

10

20

30

40

50

60

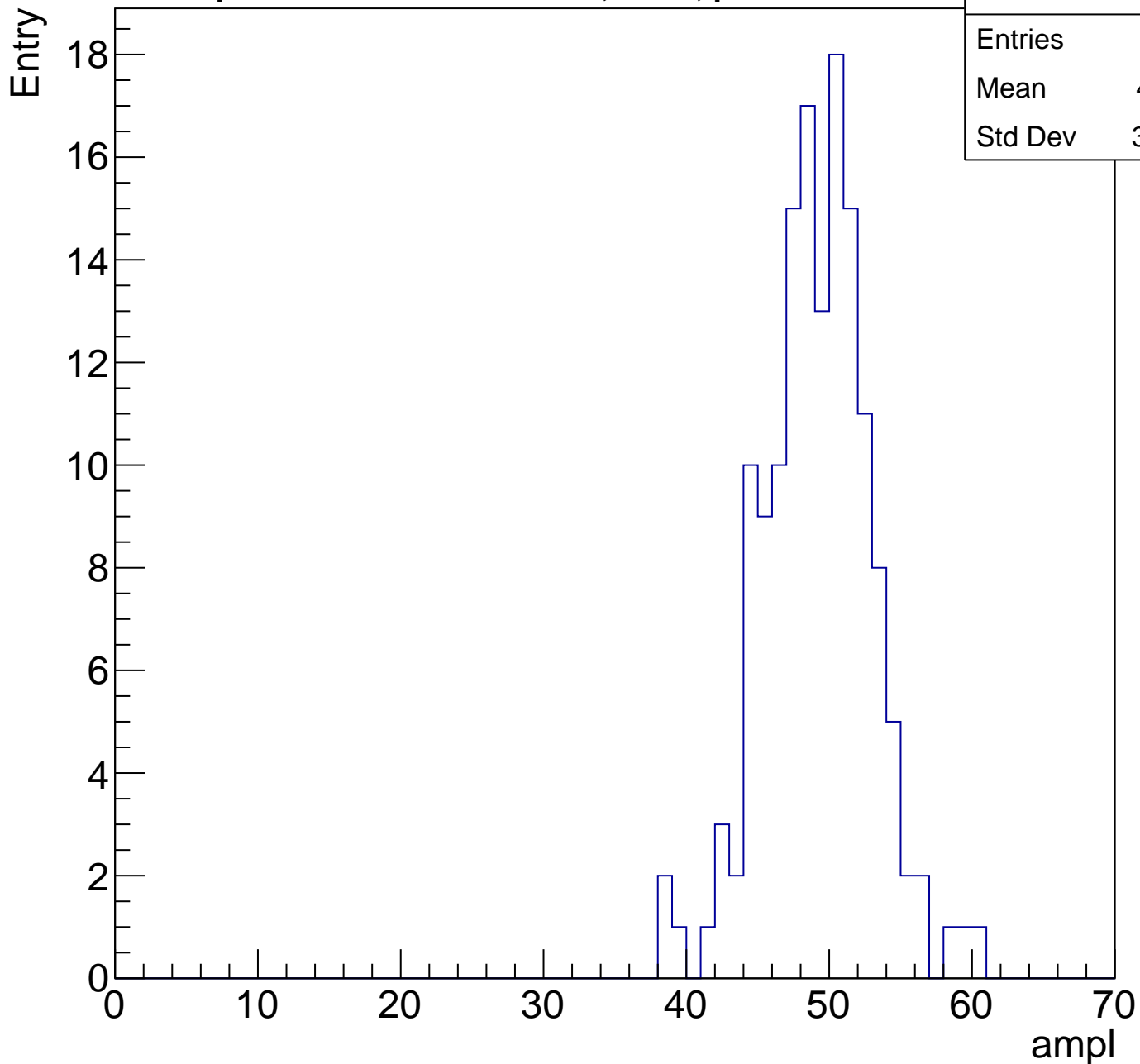
70

ampl

B0L101S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	48.71
Std Dev	3.758



B0L101S, U15-ch107, adc4

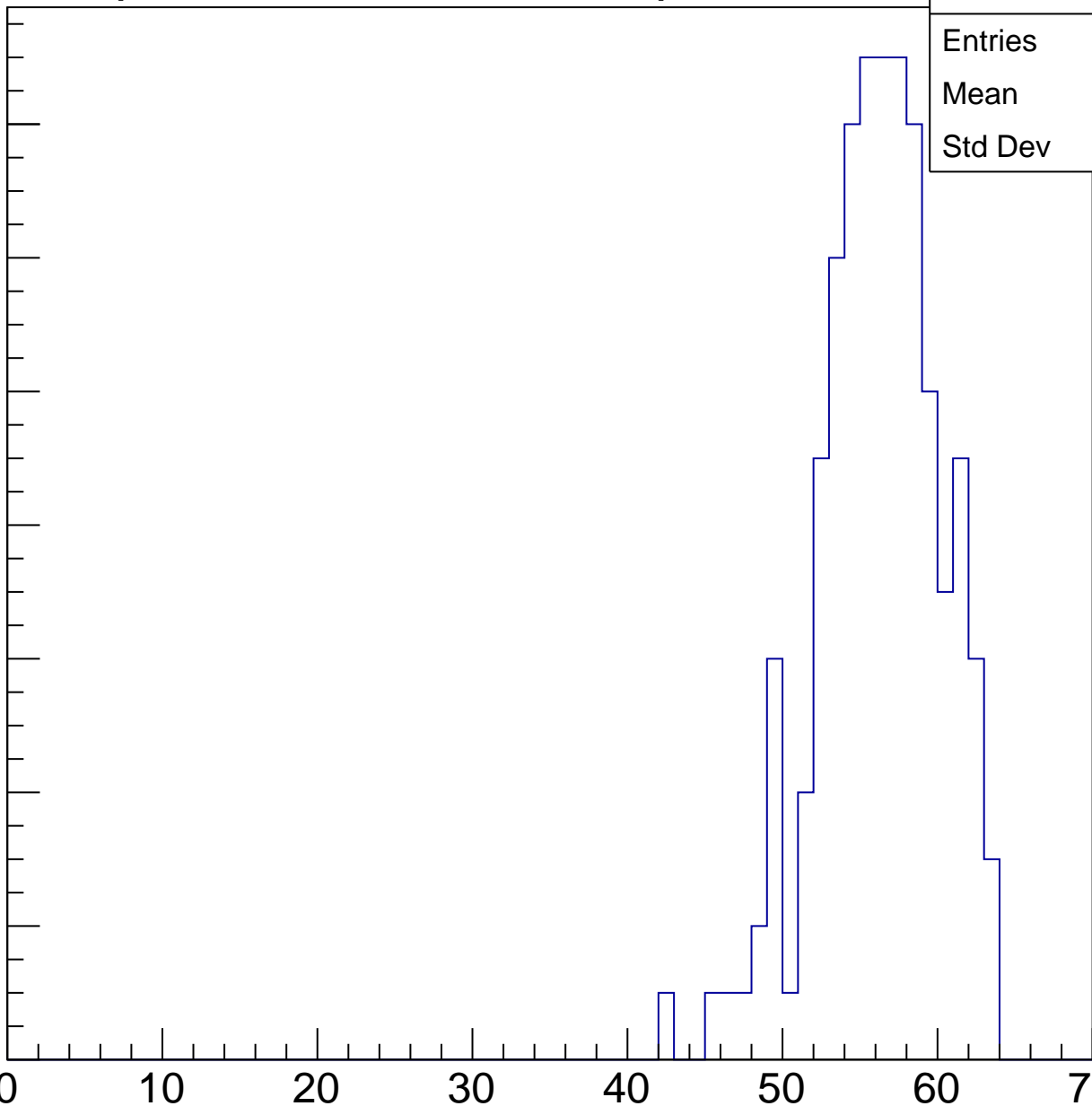
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	146
Mean	55.73
Std Dev	3.907

ampl



B0L101S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries

67

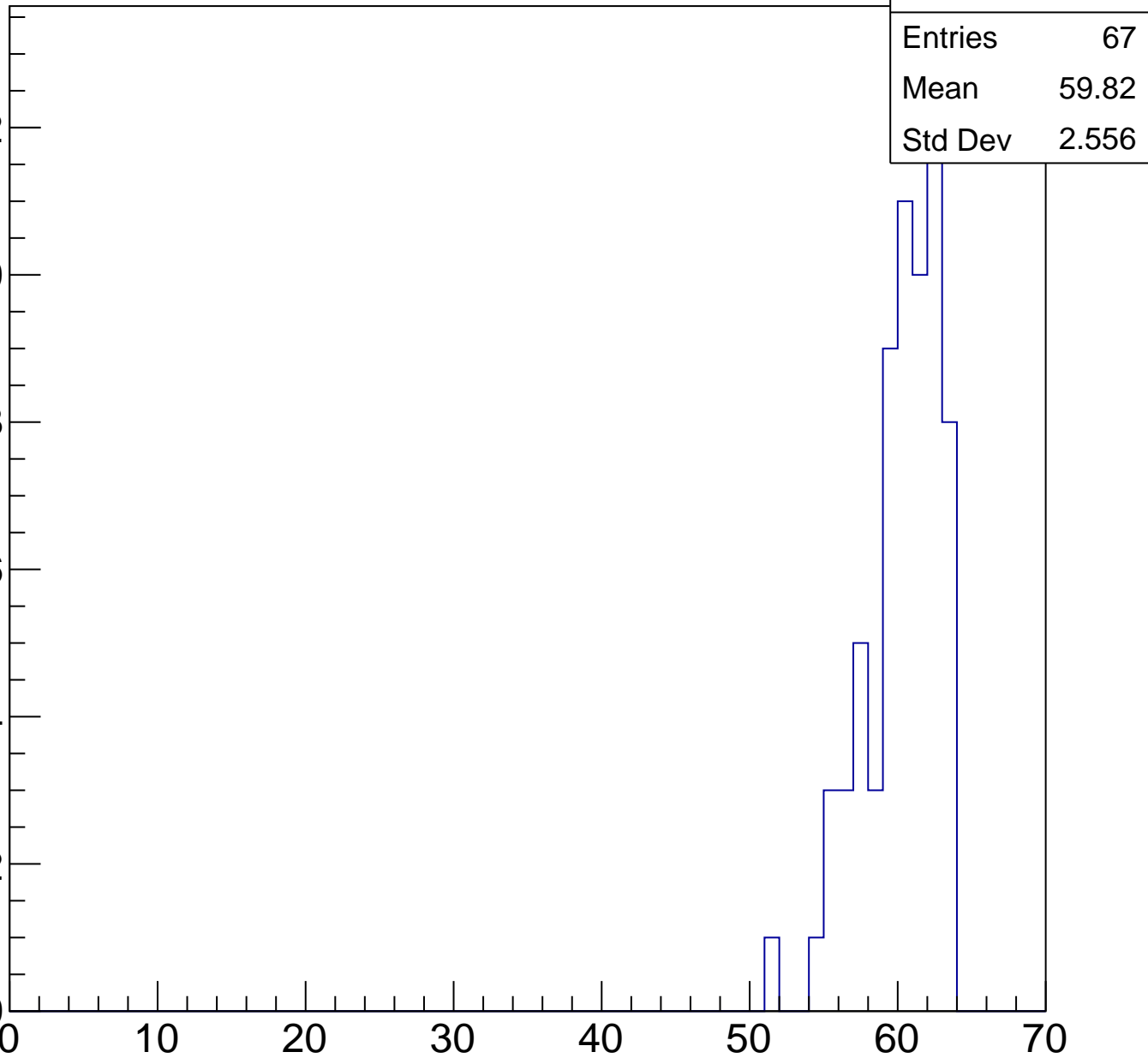
Mean

59.82

Std Dev

2.556

ampl

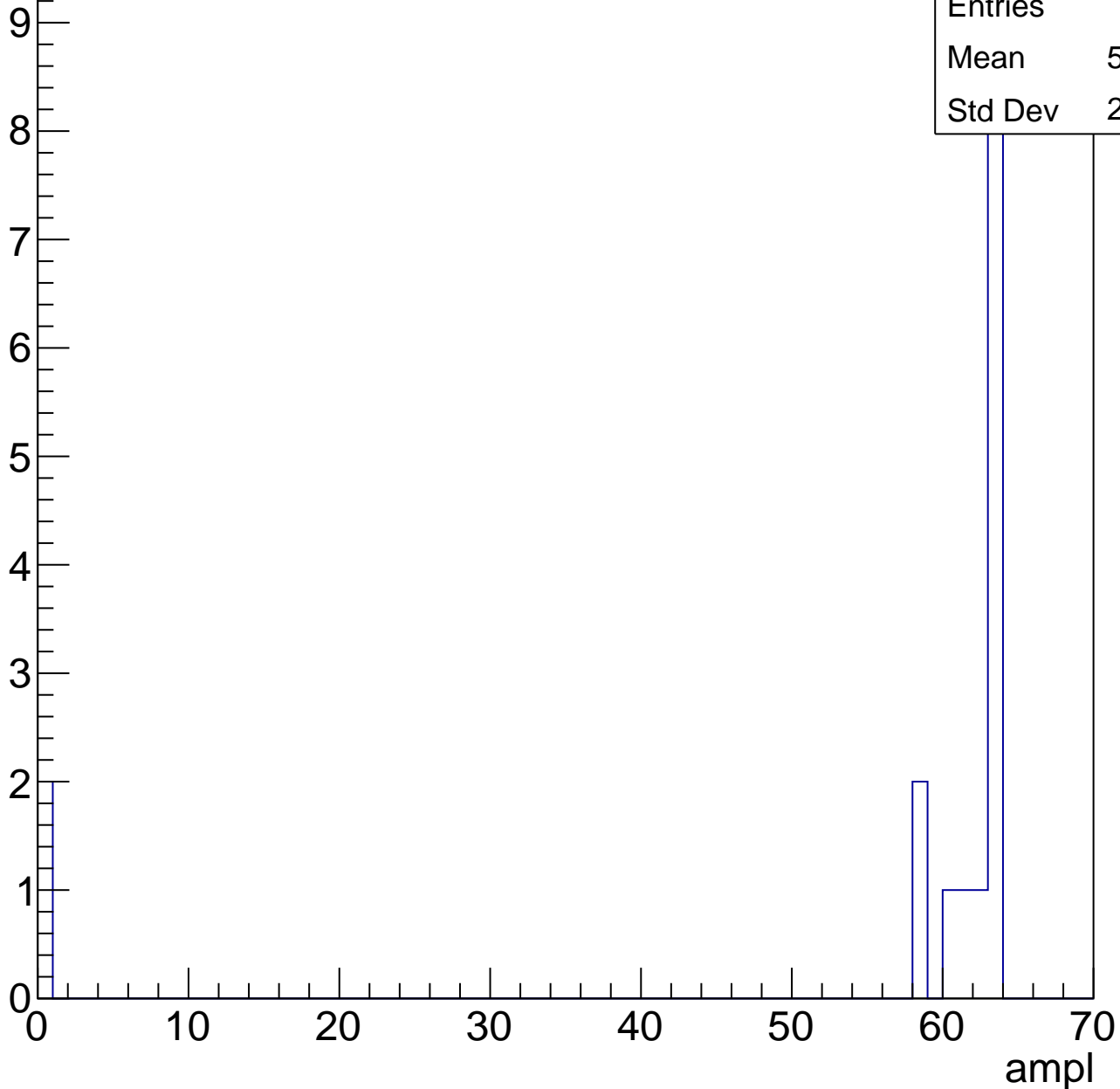


B0L101S, U15-ch107, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	16
Mean	54.12
Std Dev	20.53



B0L101S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



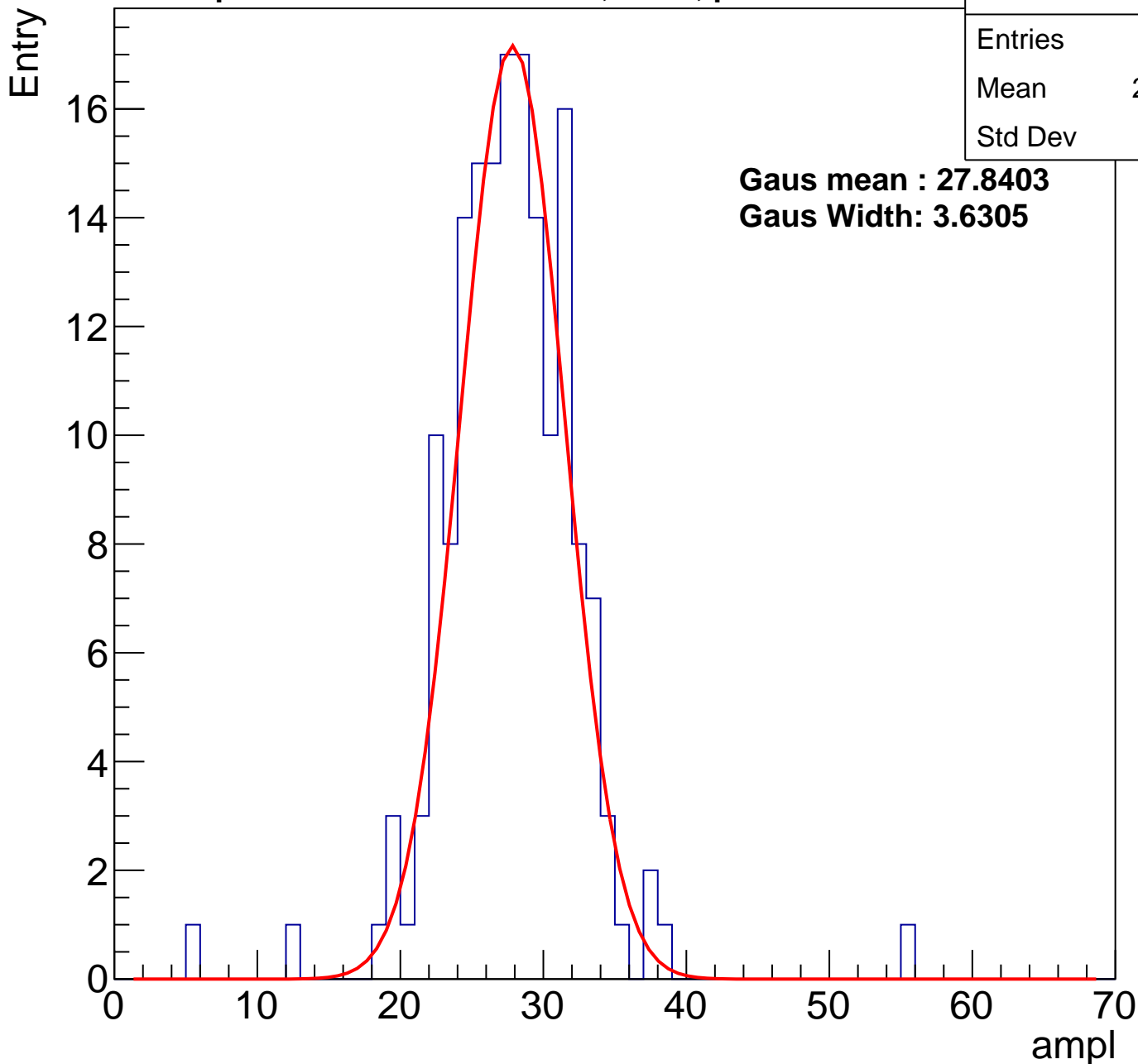
B0L101S, U15-ch108, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	169
Mean	27.27
Std Dev	4.8

Gaus mean : 27.8403

Gaus Width: 3.6305



B0L101S, U15-ch108, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	34.43
Std Dev	3.426

Gaus mean : 34.9380

Gaus Width: 3.4288

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

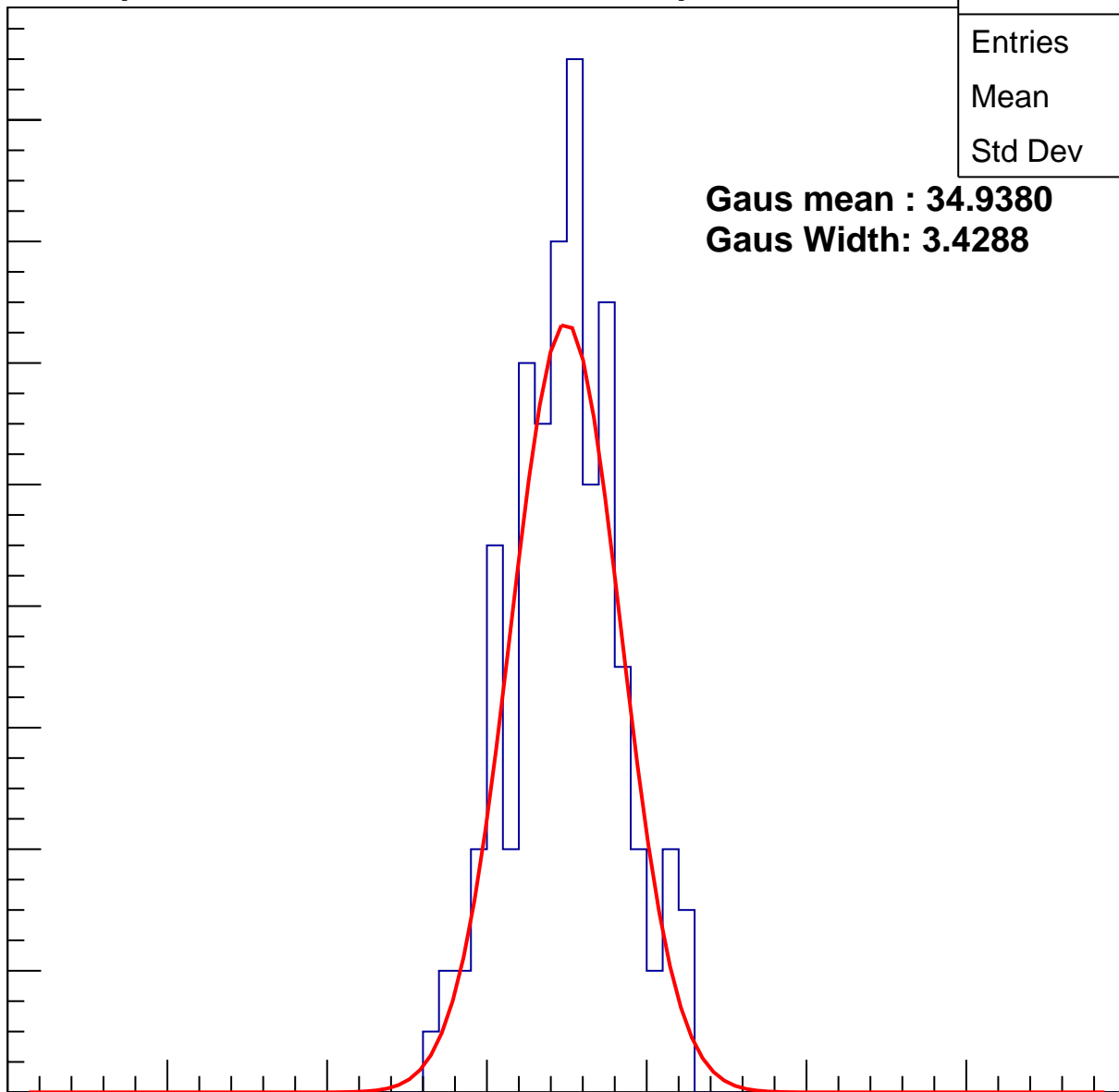
40

50

60

70

ampl



B0L101S, U15-ch108, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	40.83
Std Dev	3.493

Gaus mean : 41.2036

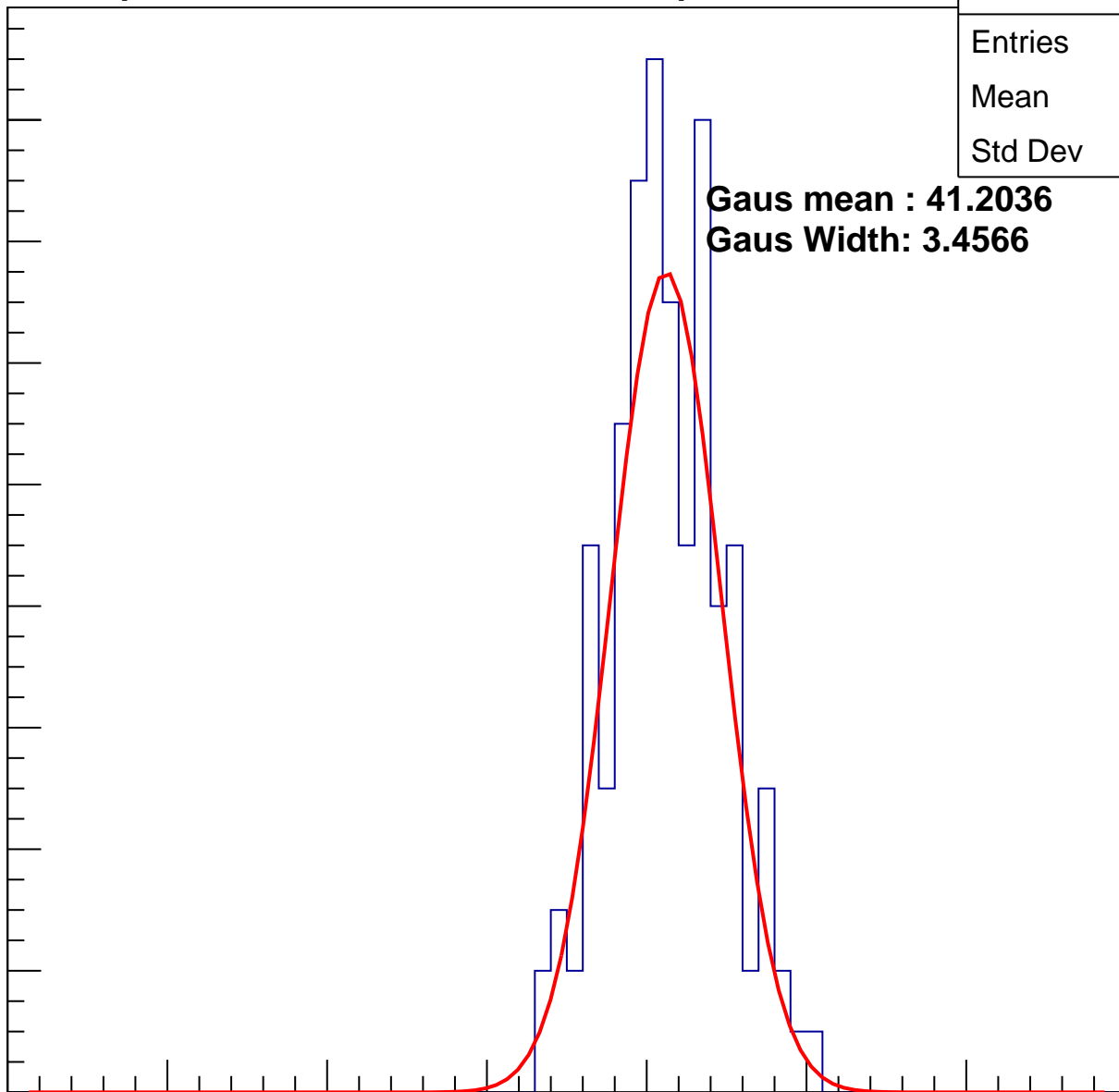
Gaus Width: 3.4566

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

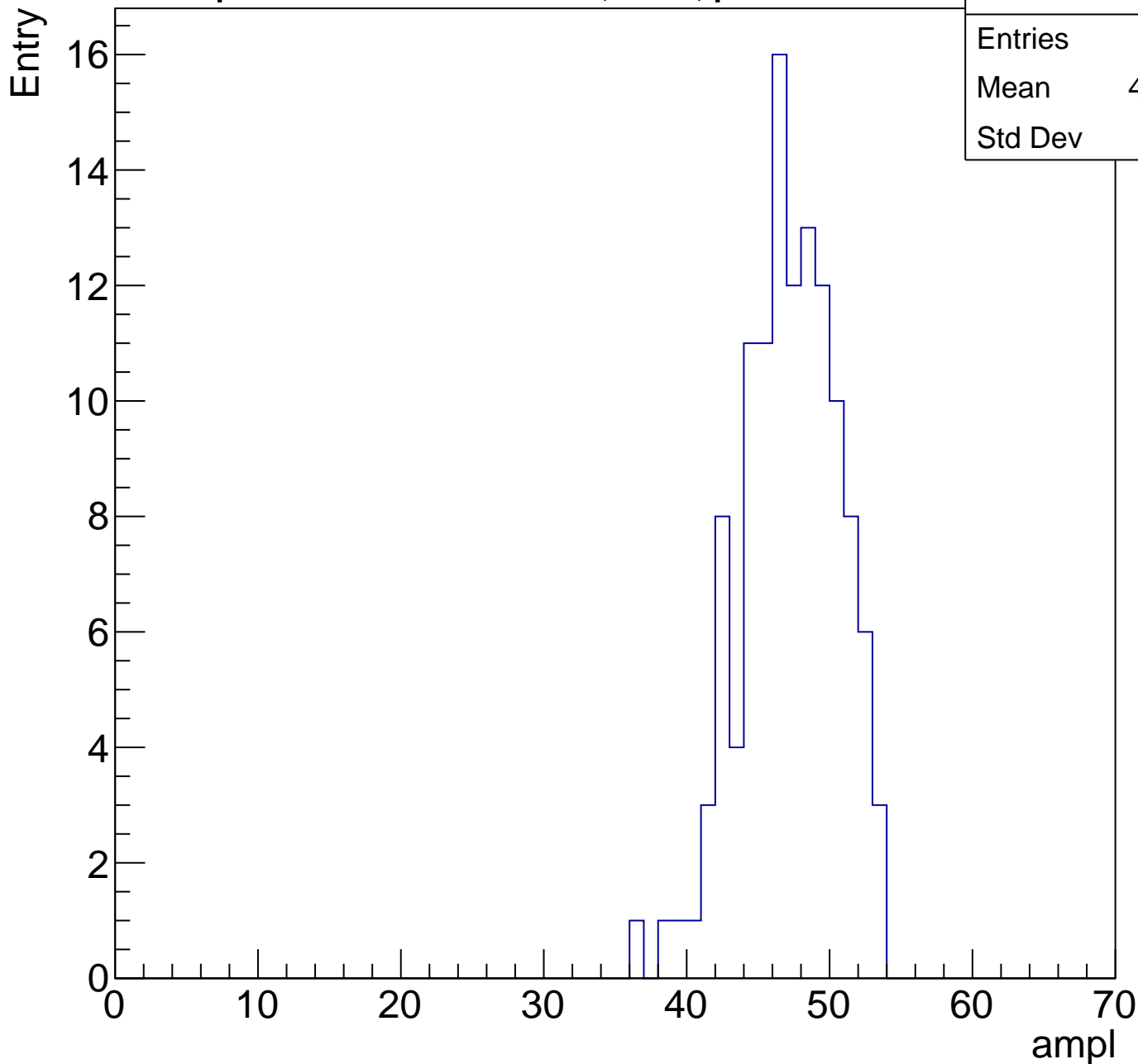
ampl



B0L101S, U15-ch108, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	46.73
Std Dev	3.37



B0L101S, U15-ch108, adc4

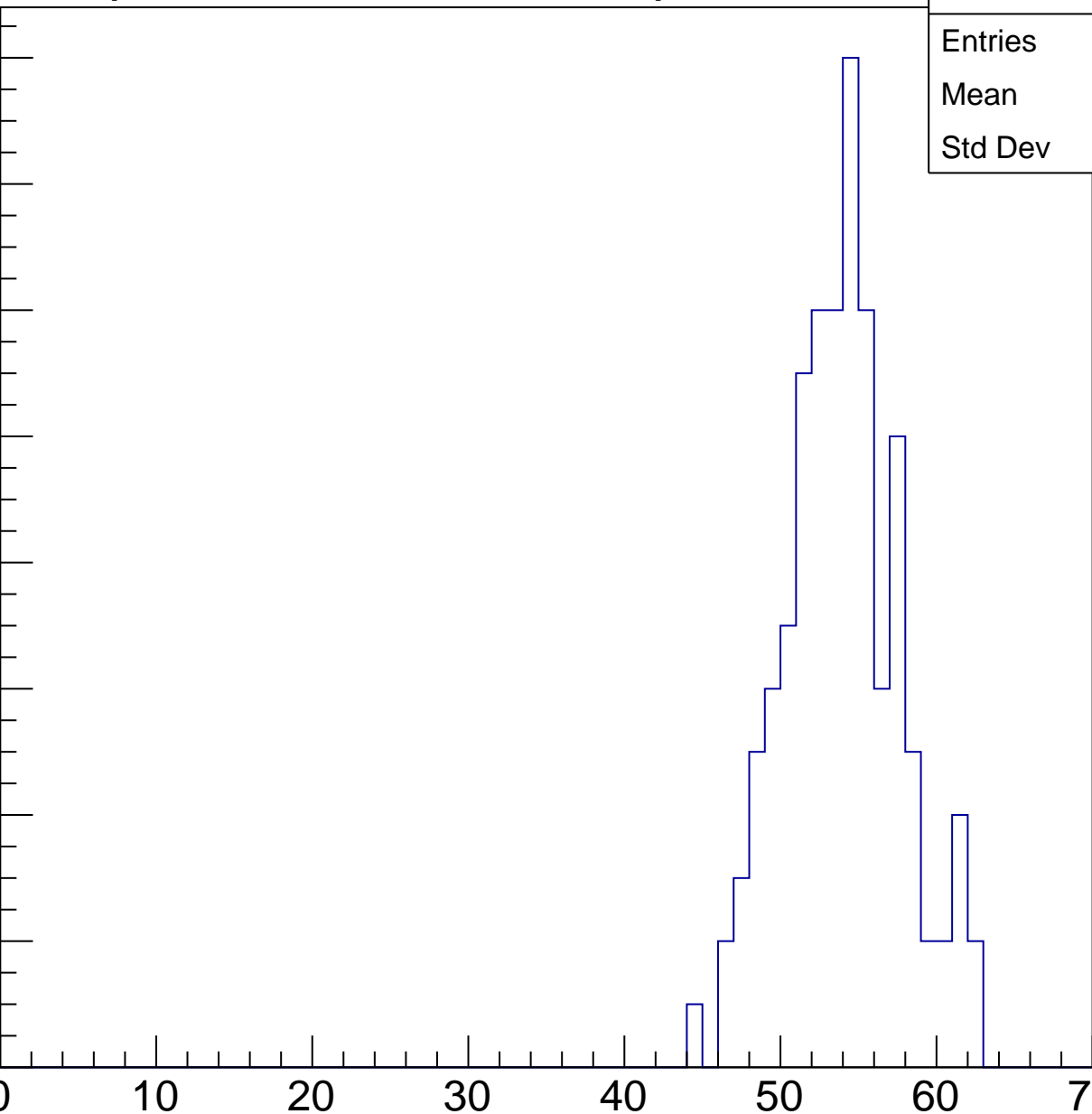
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	53.46
Std Dev	3.675

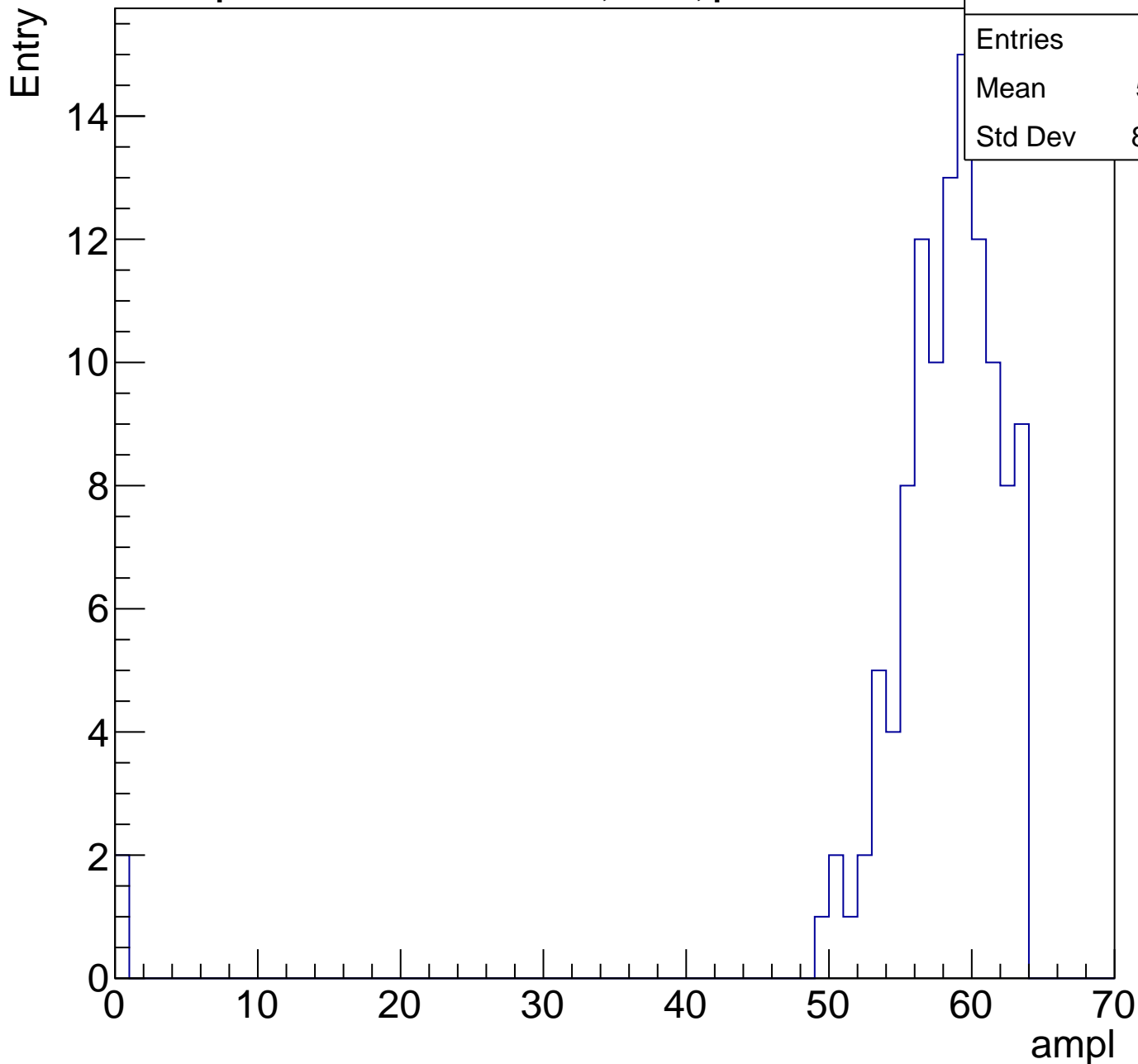
ampl



B0L101S, U15-ch108, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	114
Mean	57.01
Std Dev	8.256



B0L101S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	40
Mean	61.1
Std Dev	1.868

Entry

10

8

6

4

2

0

0

10

20

30

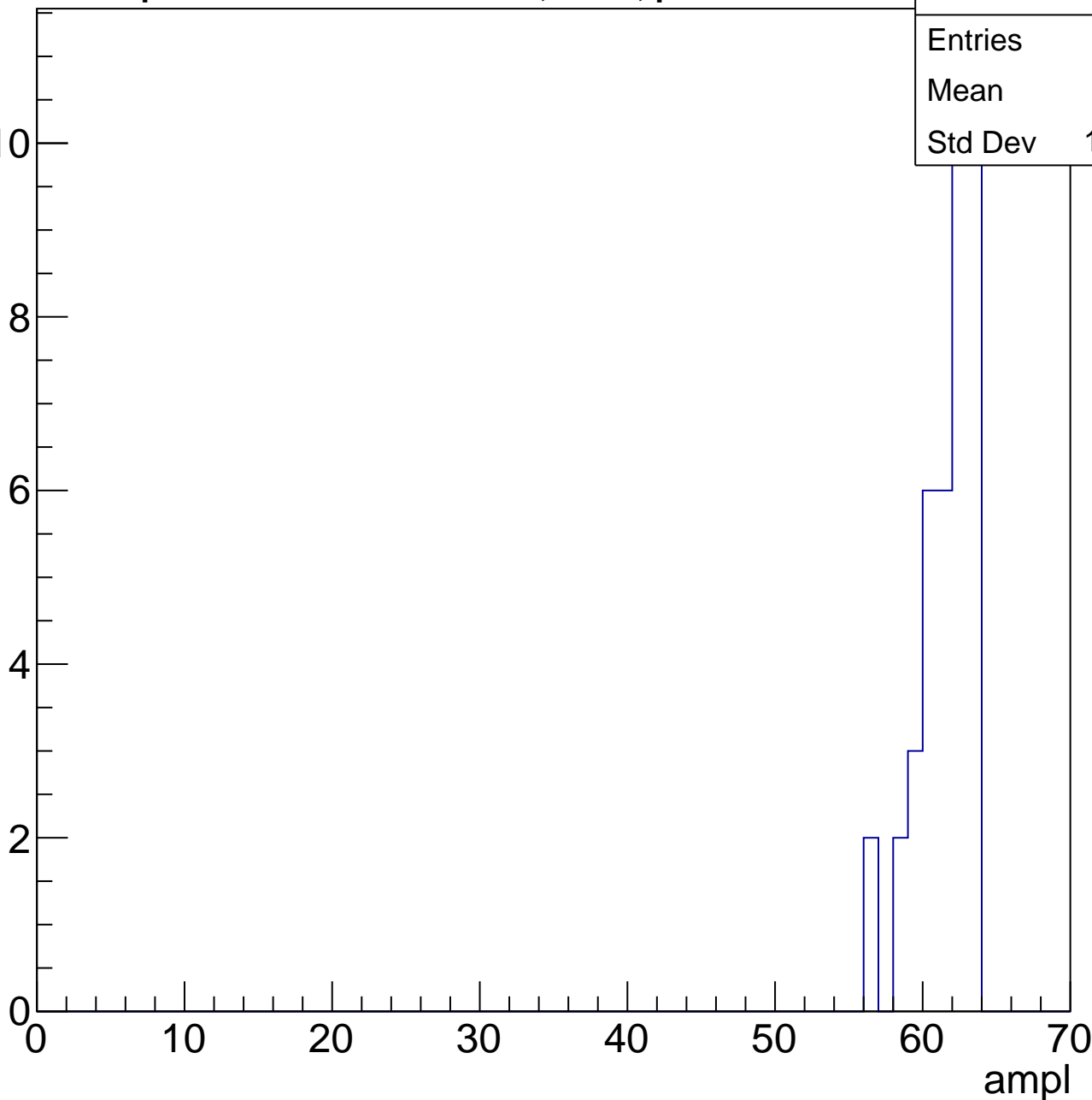
40

50

60

70

ampl



B0L101S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch109, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	26.93
Std Dev	4.232

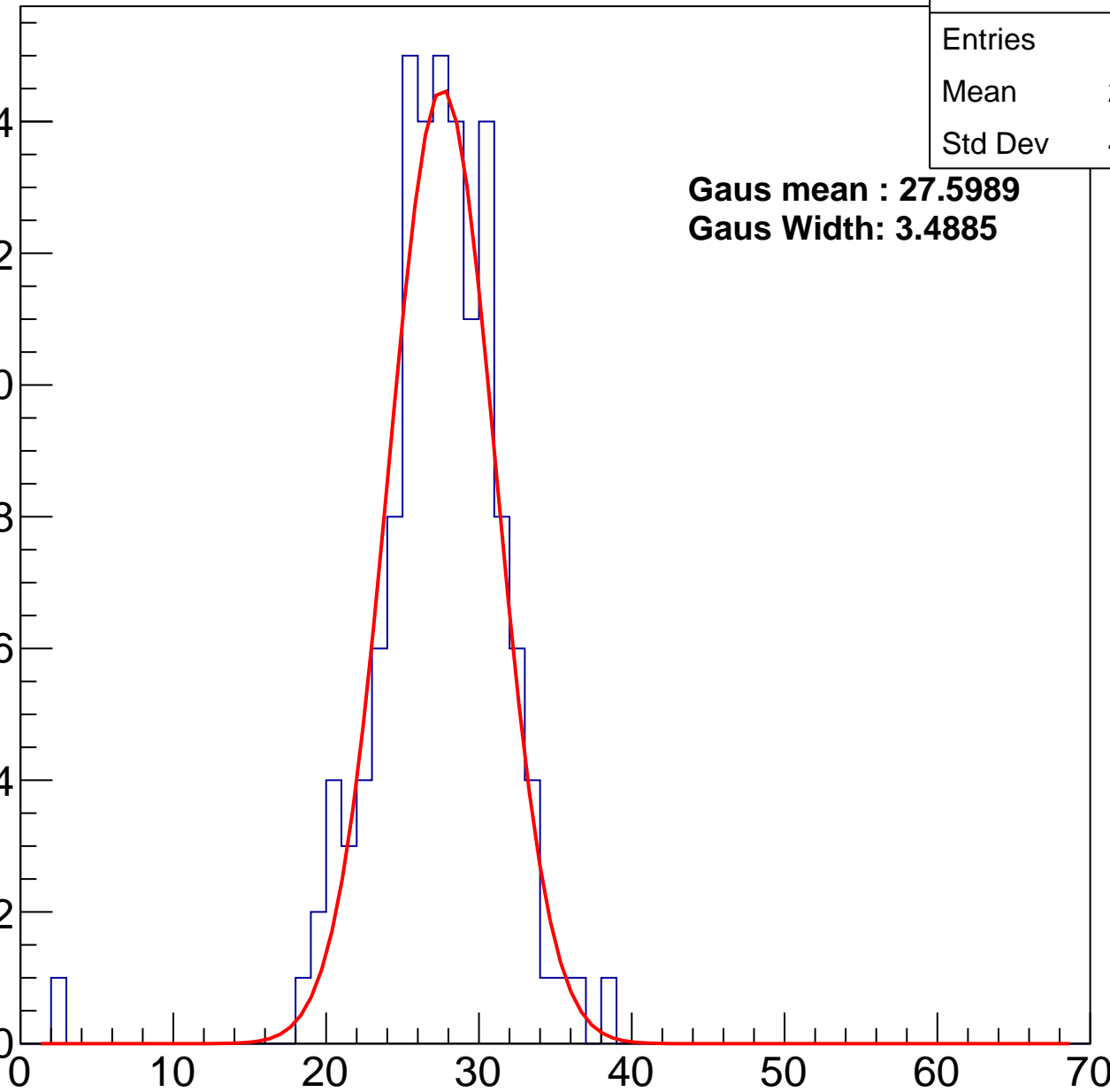
Gaus mean : 27.5989

Gaus Width: 3.4885

Entry

14
12
10
8
6
4
2
0

ampl



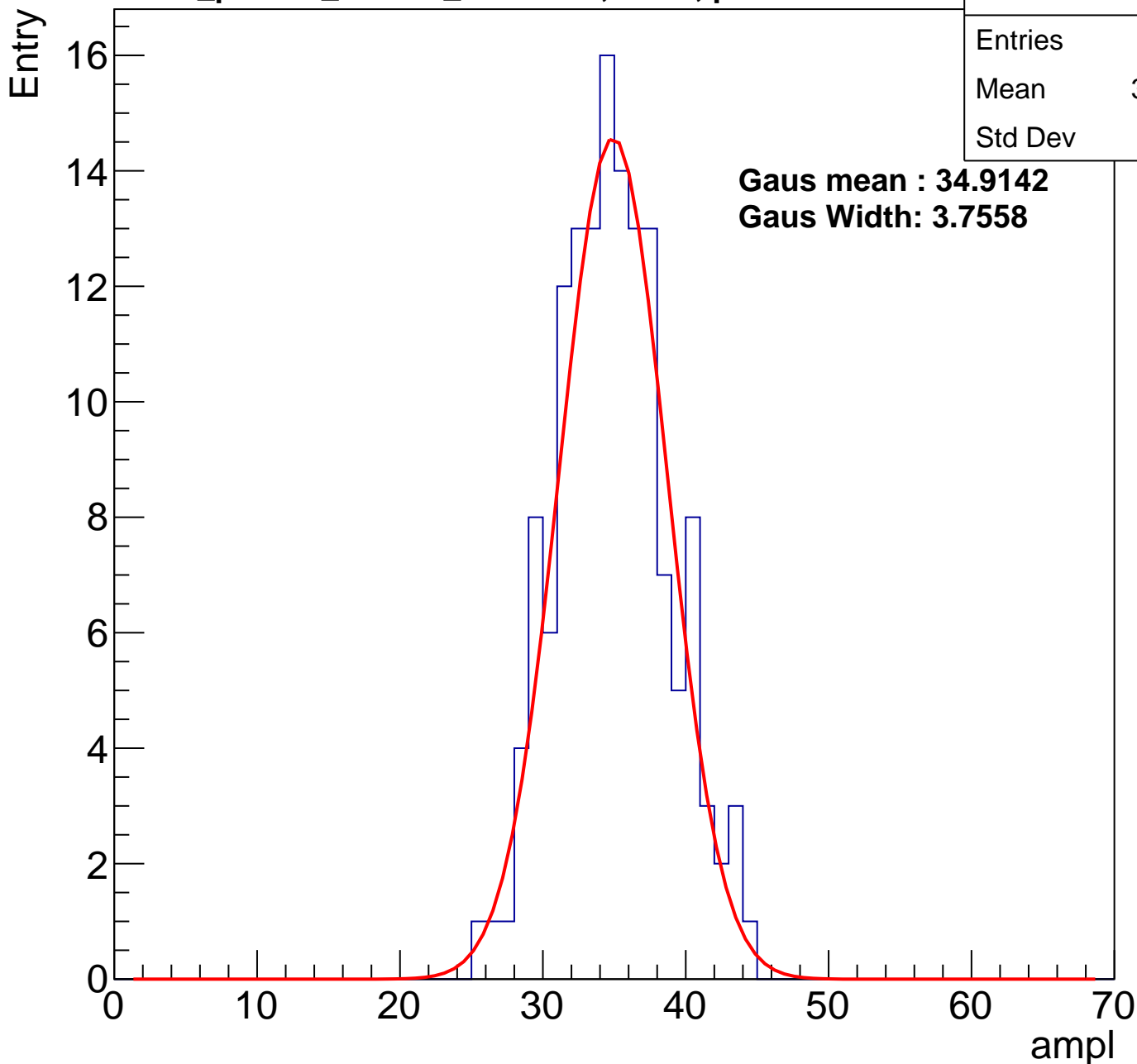
B0L101S, U15-ch109, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	144
Mean	34.47
Std Dev	3.82

Gaus mean : 34.9142

Gaus Width: 3.7558



B0L101S, U15-ch109, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

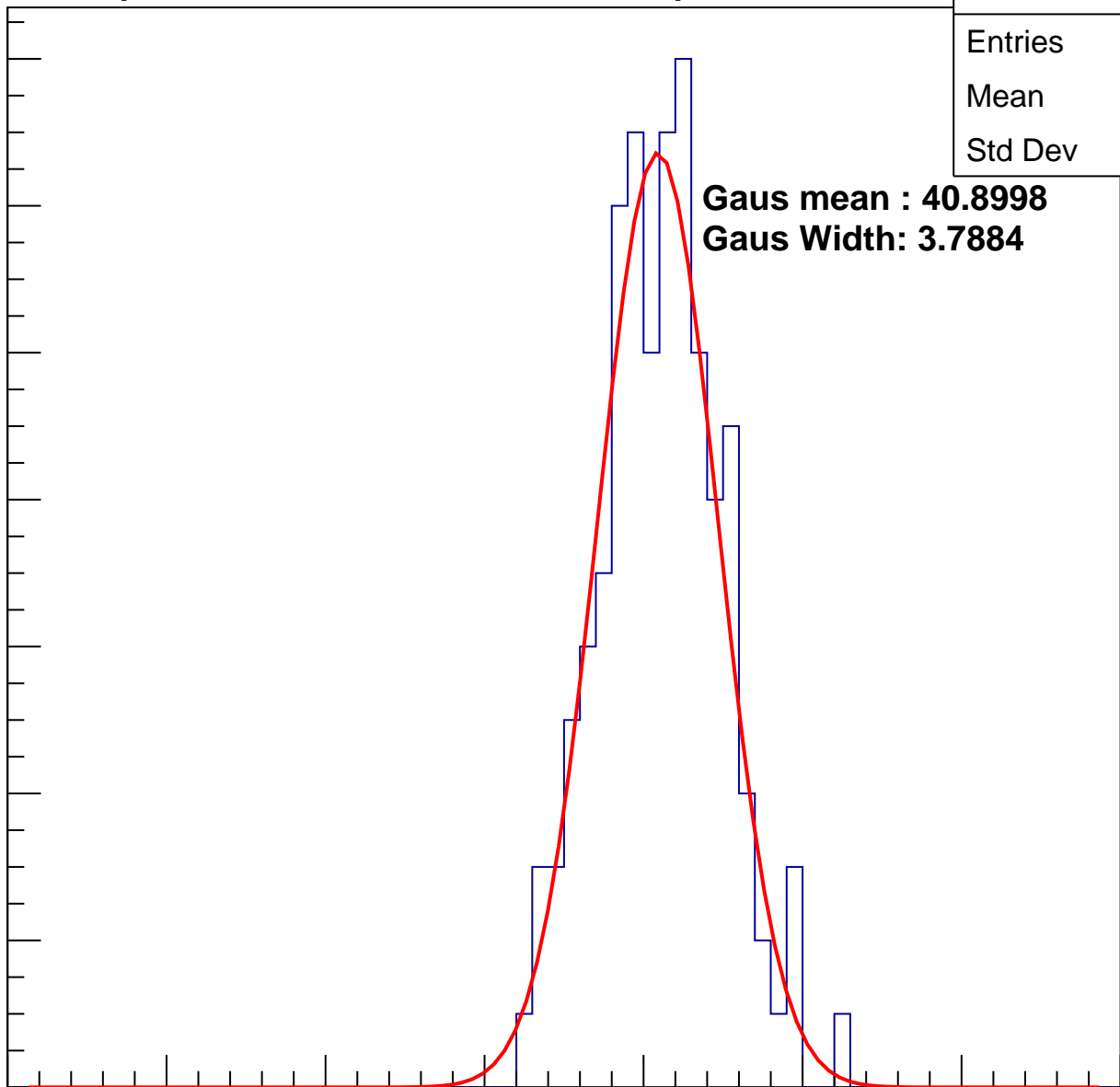
14
12
10
8
6
4
2
0

Entries	125
Mean	40.63
Std Dev	3.815

Gaus mean : 40.8998

Gaus Width: 3.7884

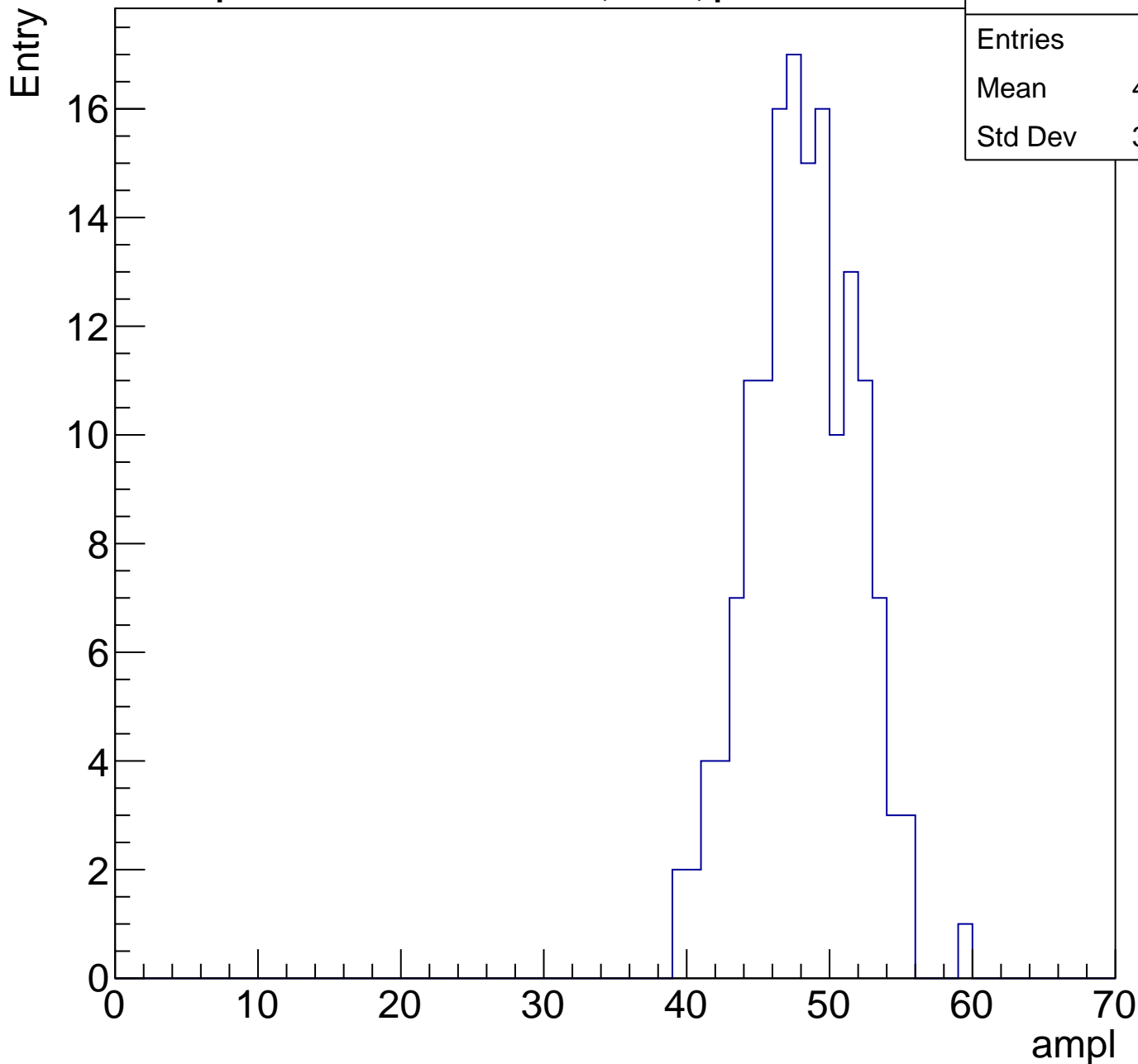
0 10 20 30 40 50 60 70
ampl



B0L101S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#1, port C1

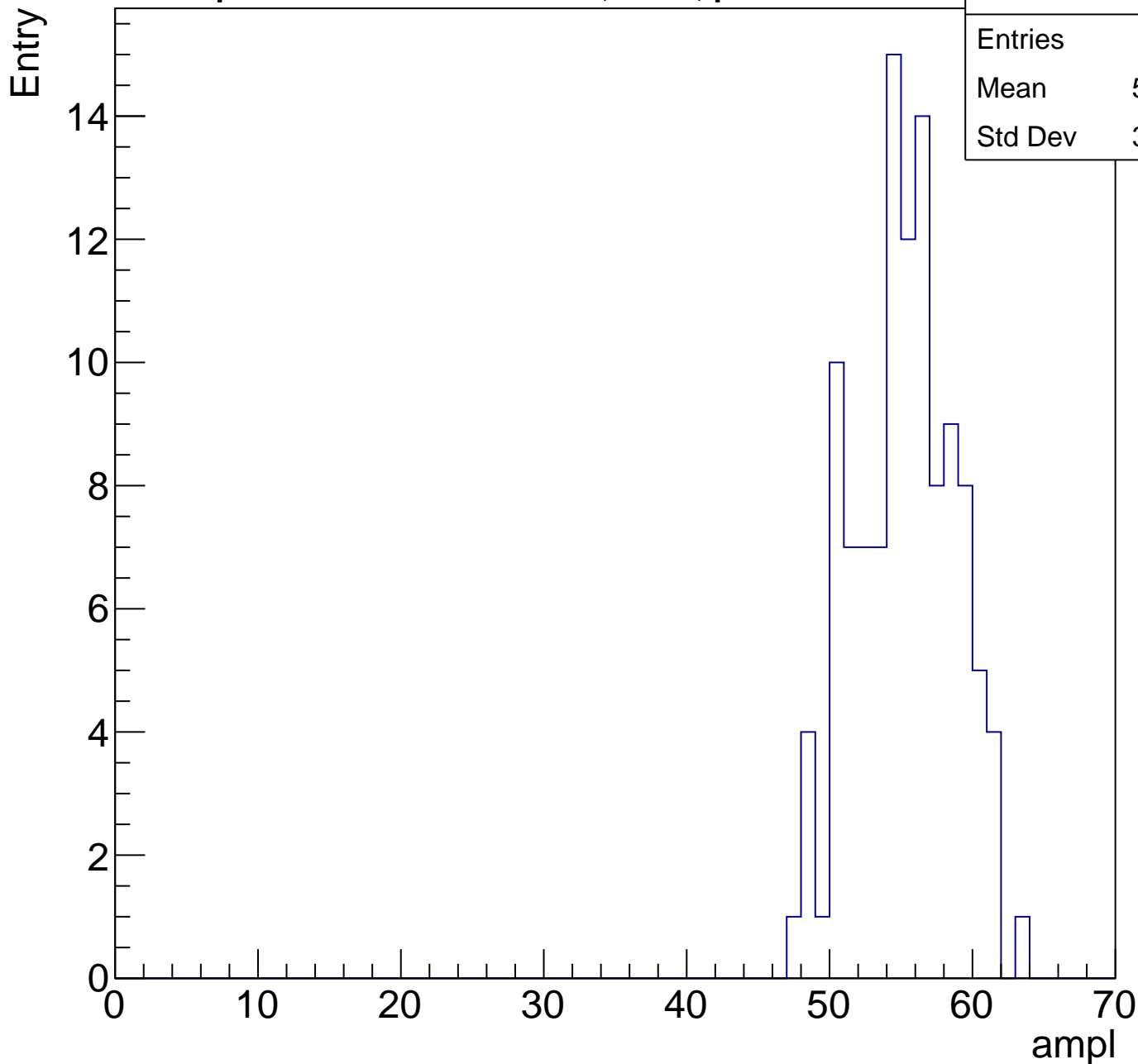
Entries	153
Mean	47.72
Std Dev	3.659



B0L101S, U15-ch109, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	113
Mean	54.79
Std Dev	3.452

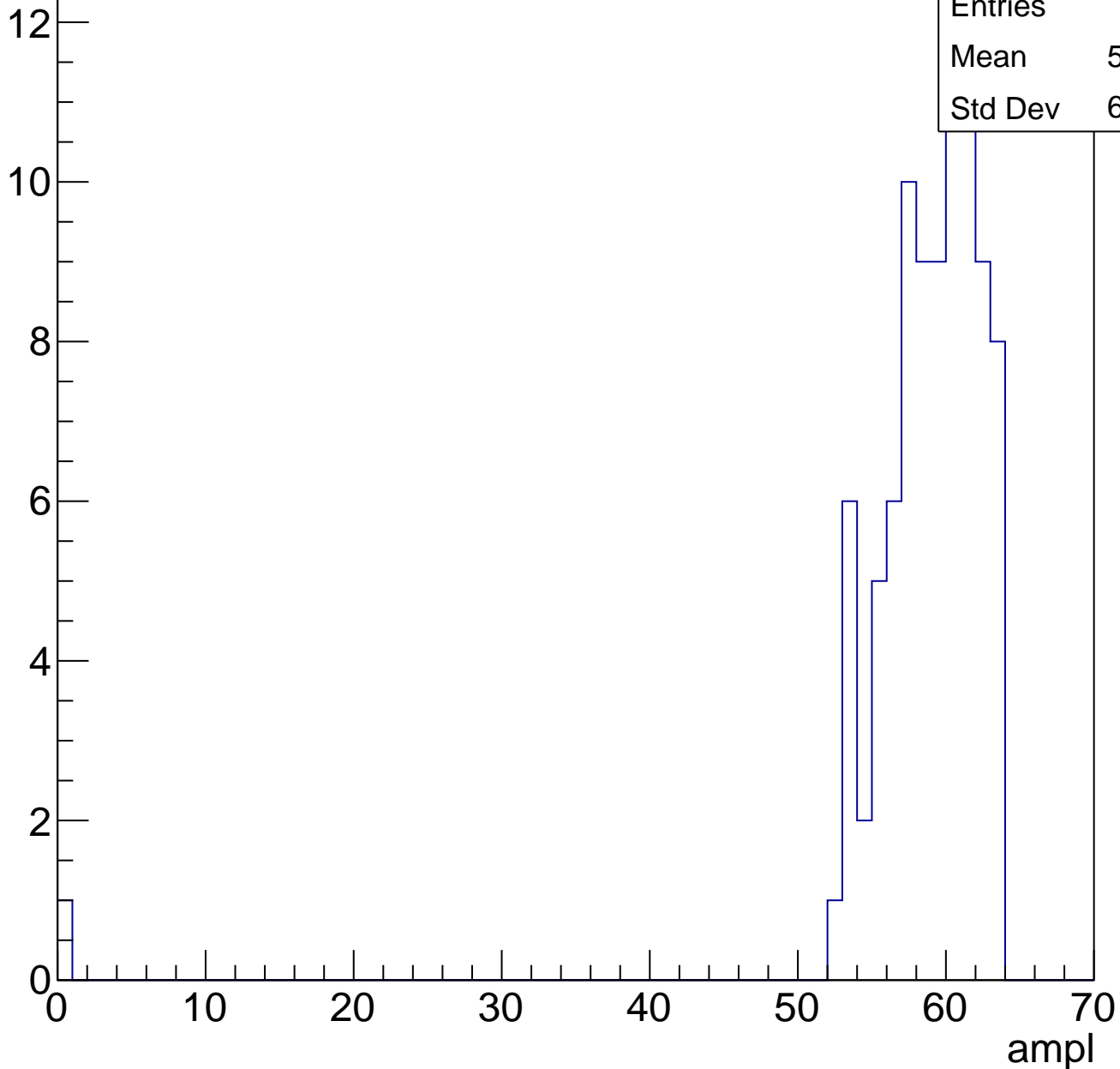


B0L101S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	89
Mean	58.04
Std Dev	6.842

Entry

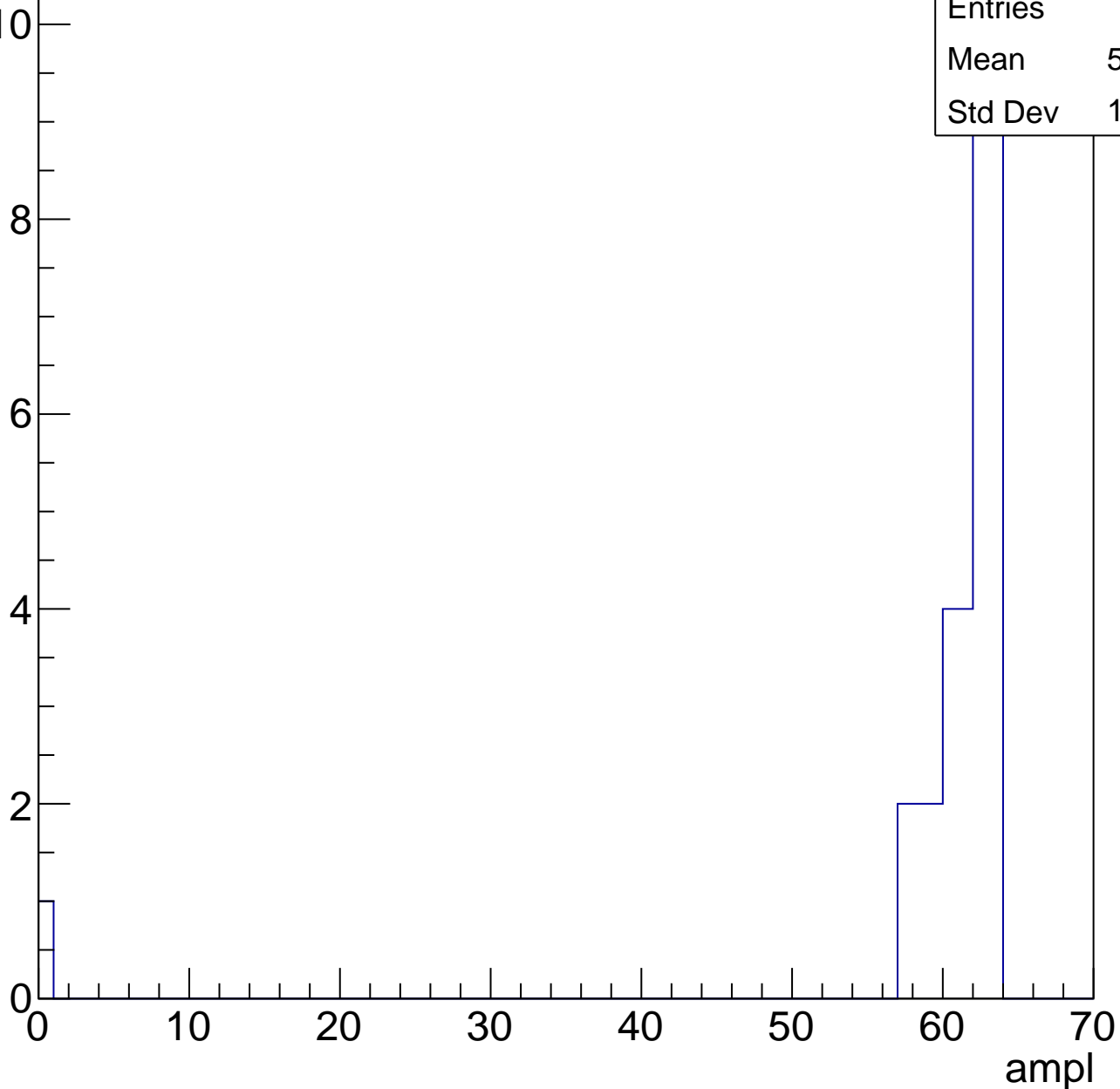


B0L101S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	35
Mean	59.49
Std Dev	10.35



B0L101S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	47.33
Std Dev	21.45

ampl

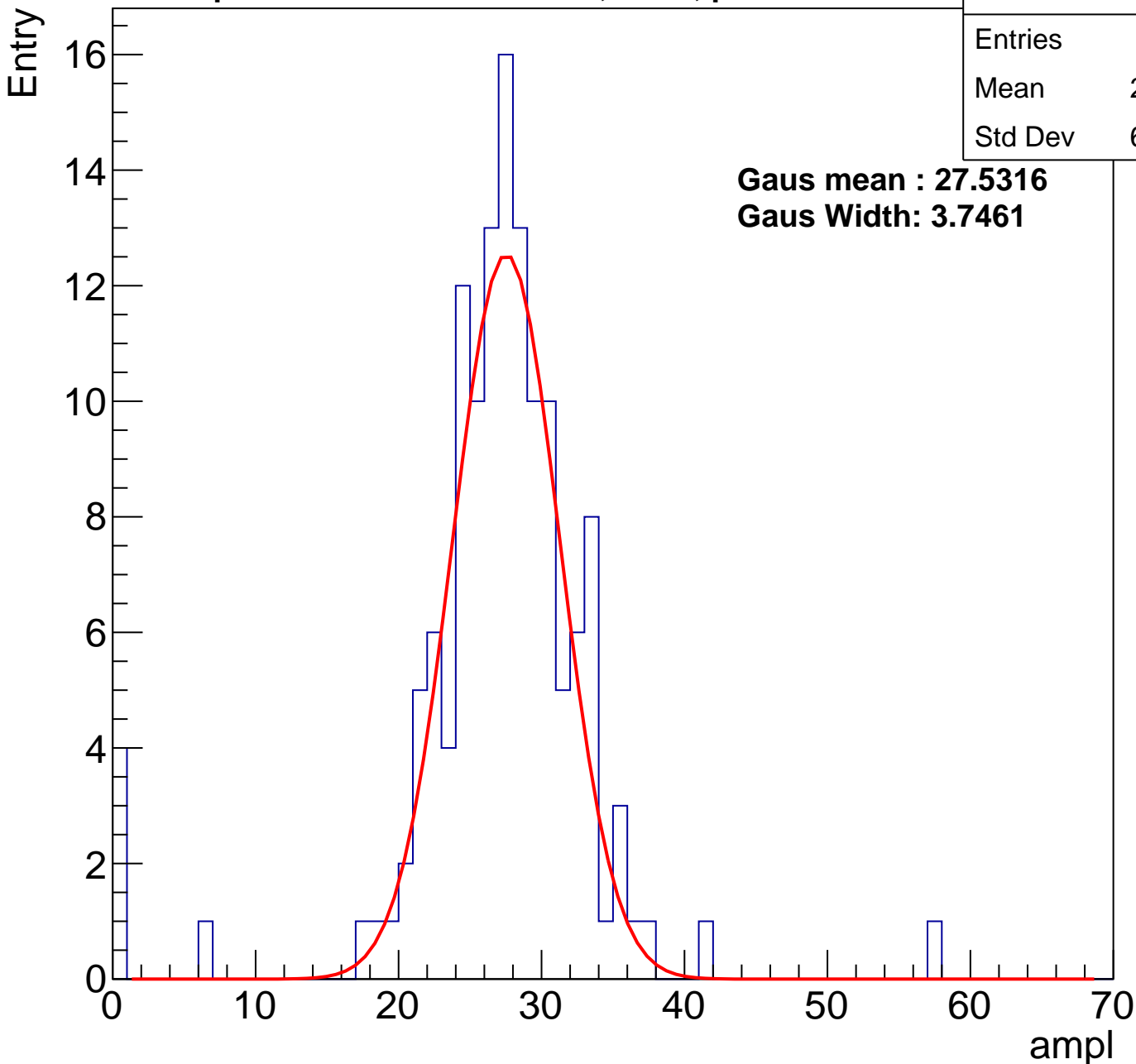
B0L101S, U15-ch110, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	136
Mean	26.56
Std Dev	6.864

Gaus mean : 27.5316

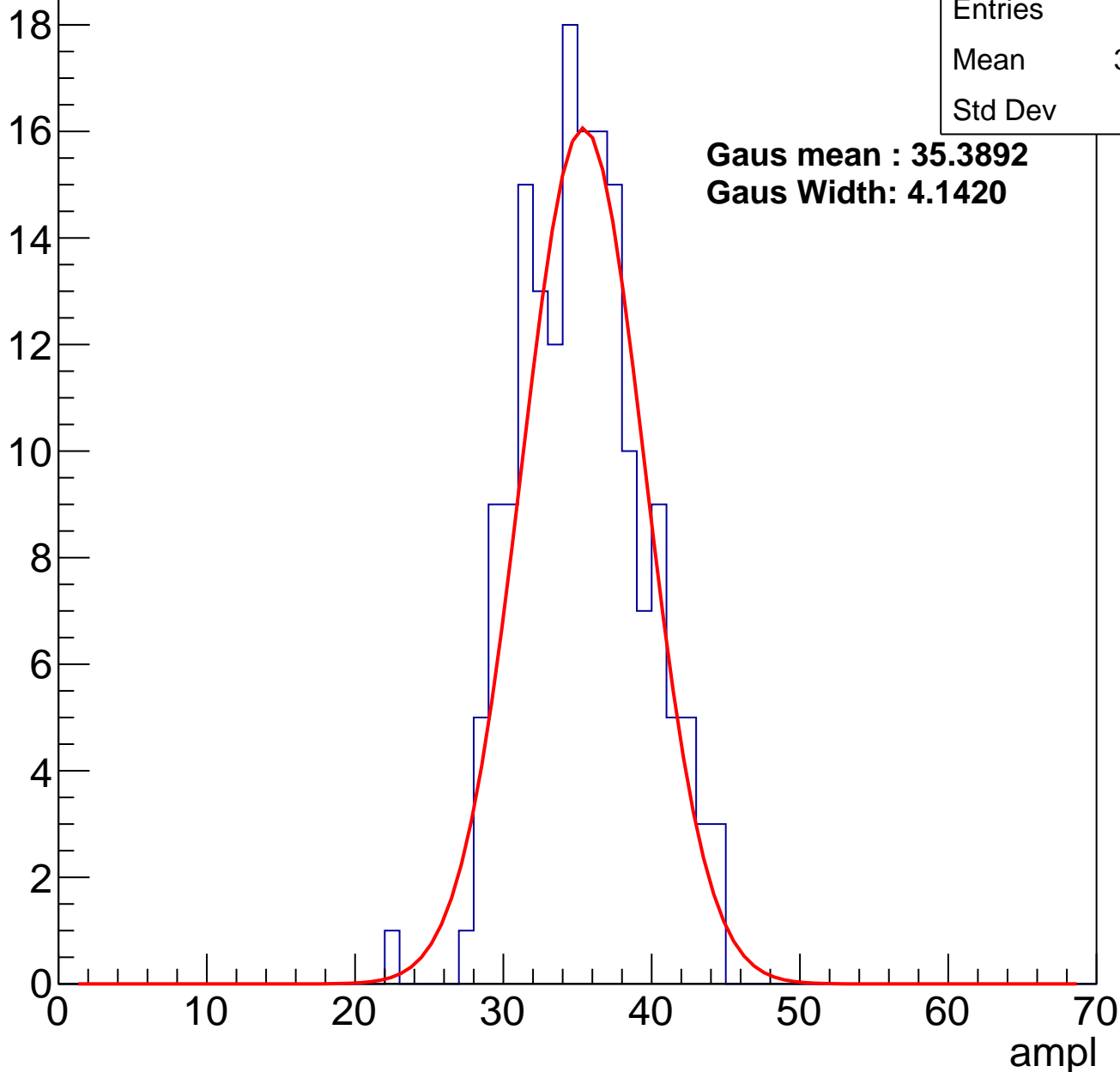
Gaus Width: 3.7461



B0L101S, U15-ch110, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch110, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	41.94
Std Dev	3.695

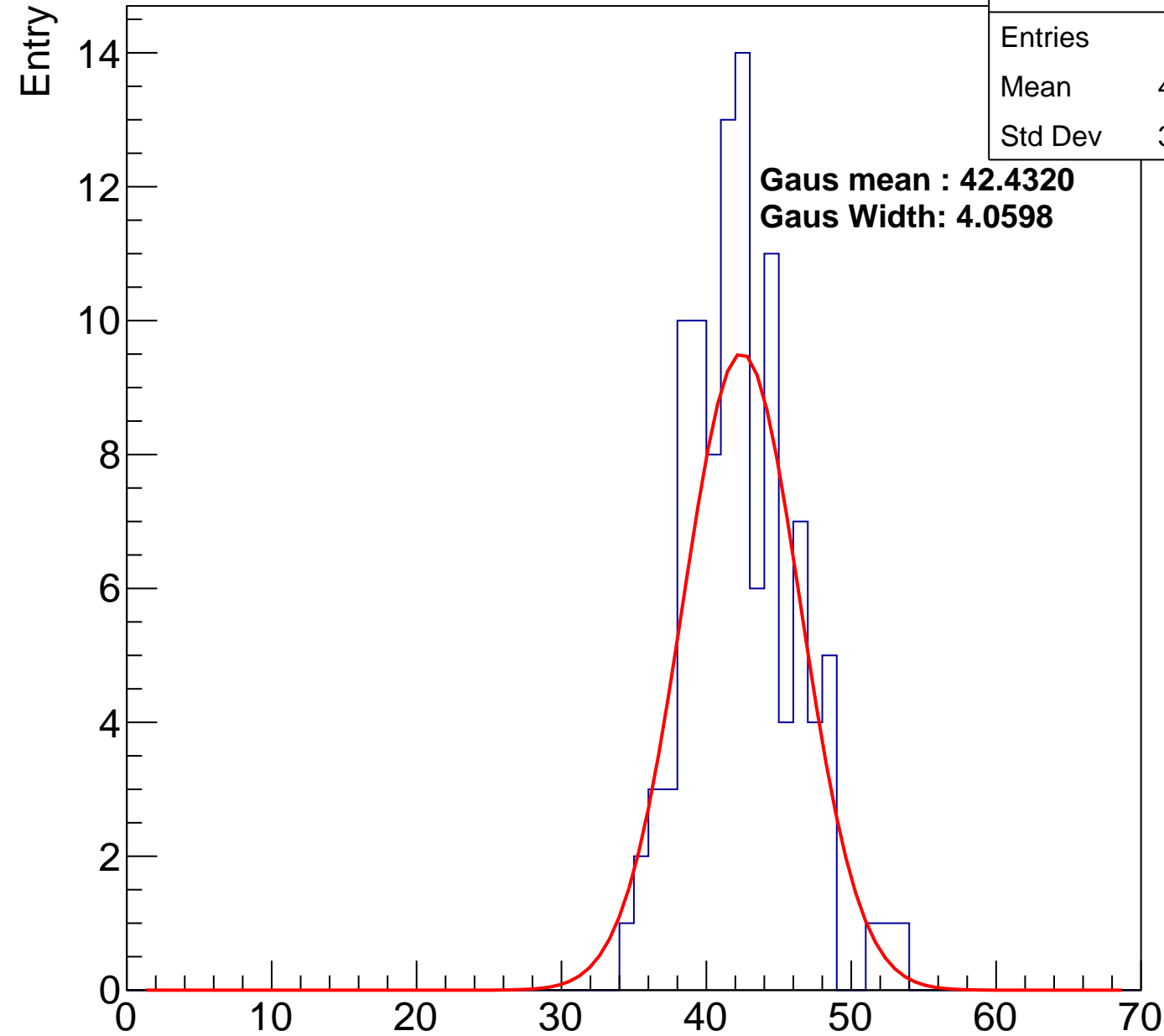
Gaus mean : 42.4320

Gaus Width: 4.0598

Entry

14
12
10
8
6
4
2
0

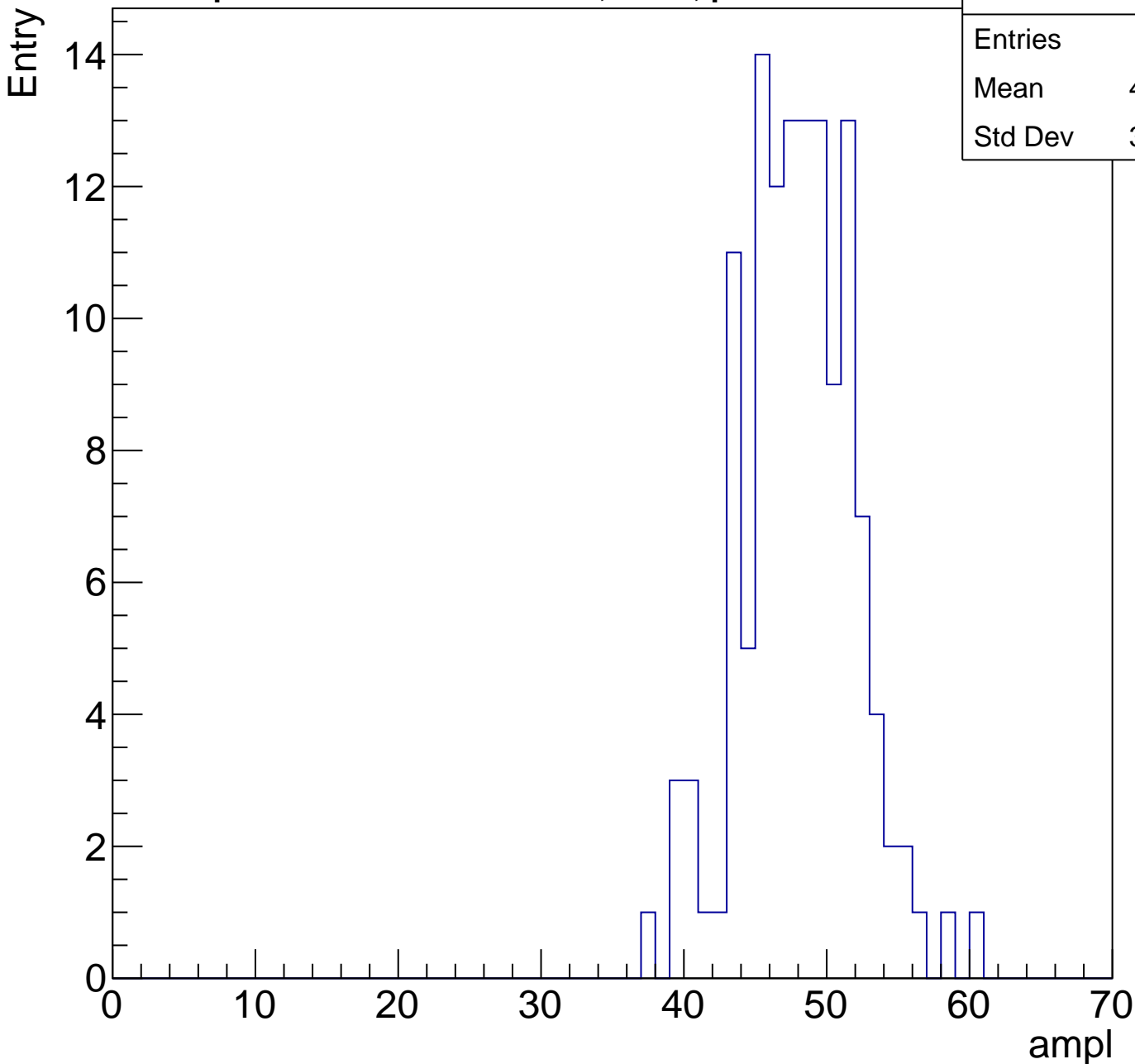
ampl



B0L101S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	130
Mean	47.58
Std Dev	3.938



B0L101S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	153
Mean	54.43
Std Dev	3.878

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

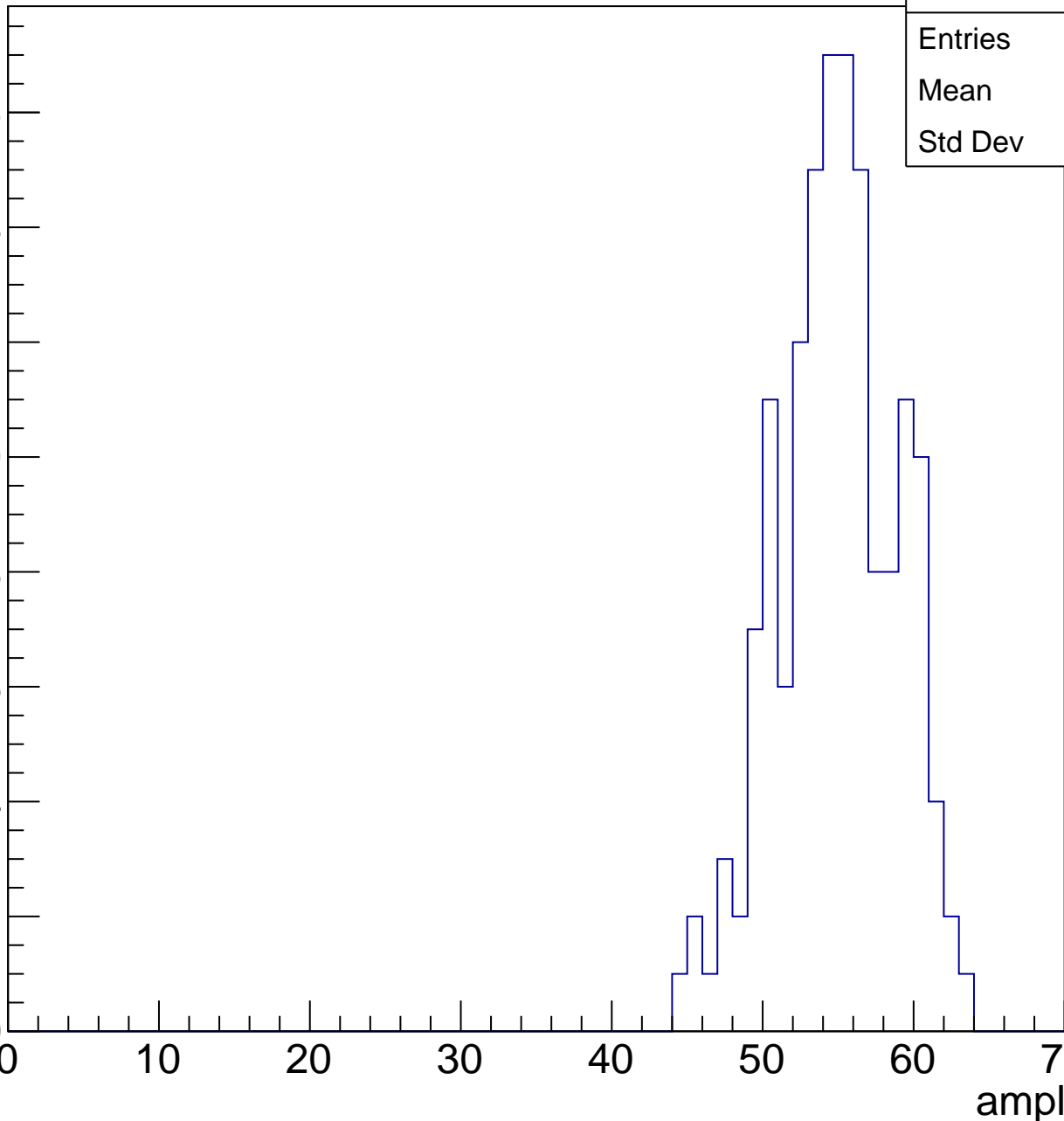
40

50

60

70

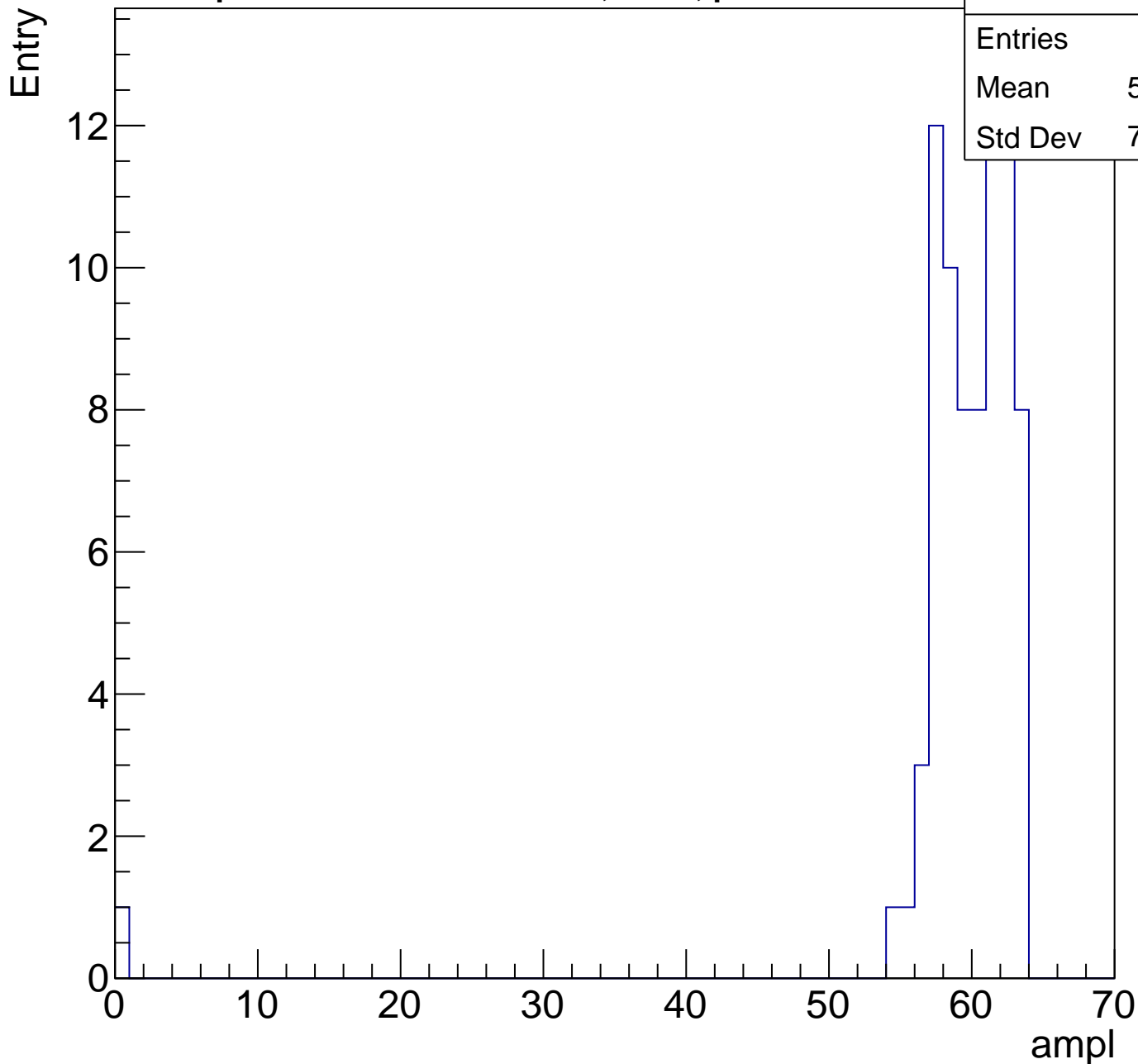
ampl



B0L101S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	77
Mean	58.88
Std Dev	7.119



B0L101S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	24
Mean	59.17
Std Dev	12.47

Entry

10

8

6

4

2

0

0

10

20

30

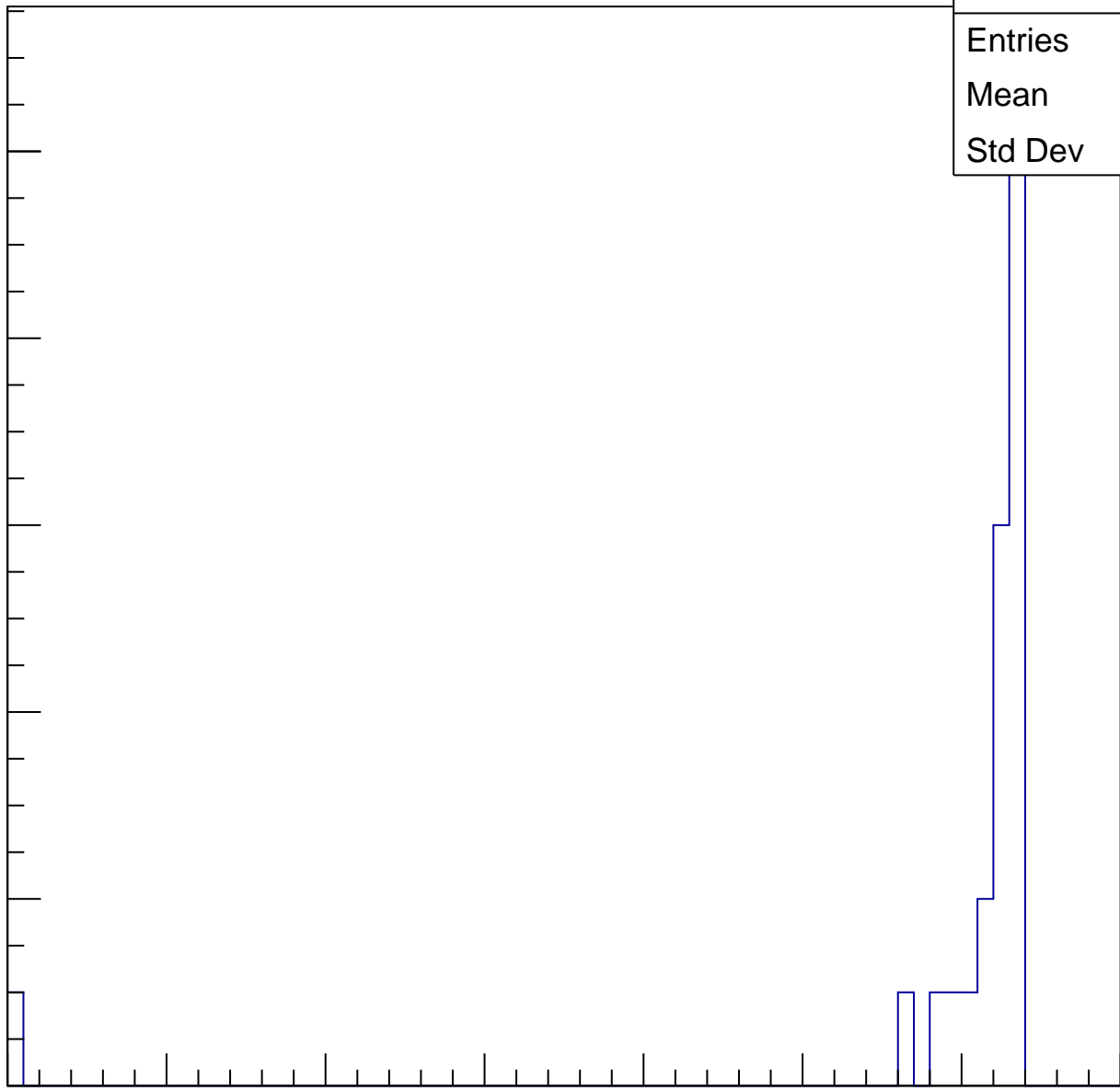
40

50

60

70

ampl



B0L101S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch111, adc0

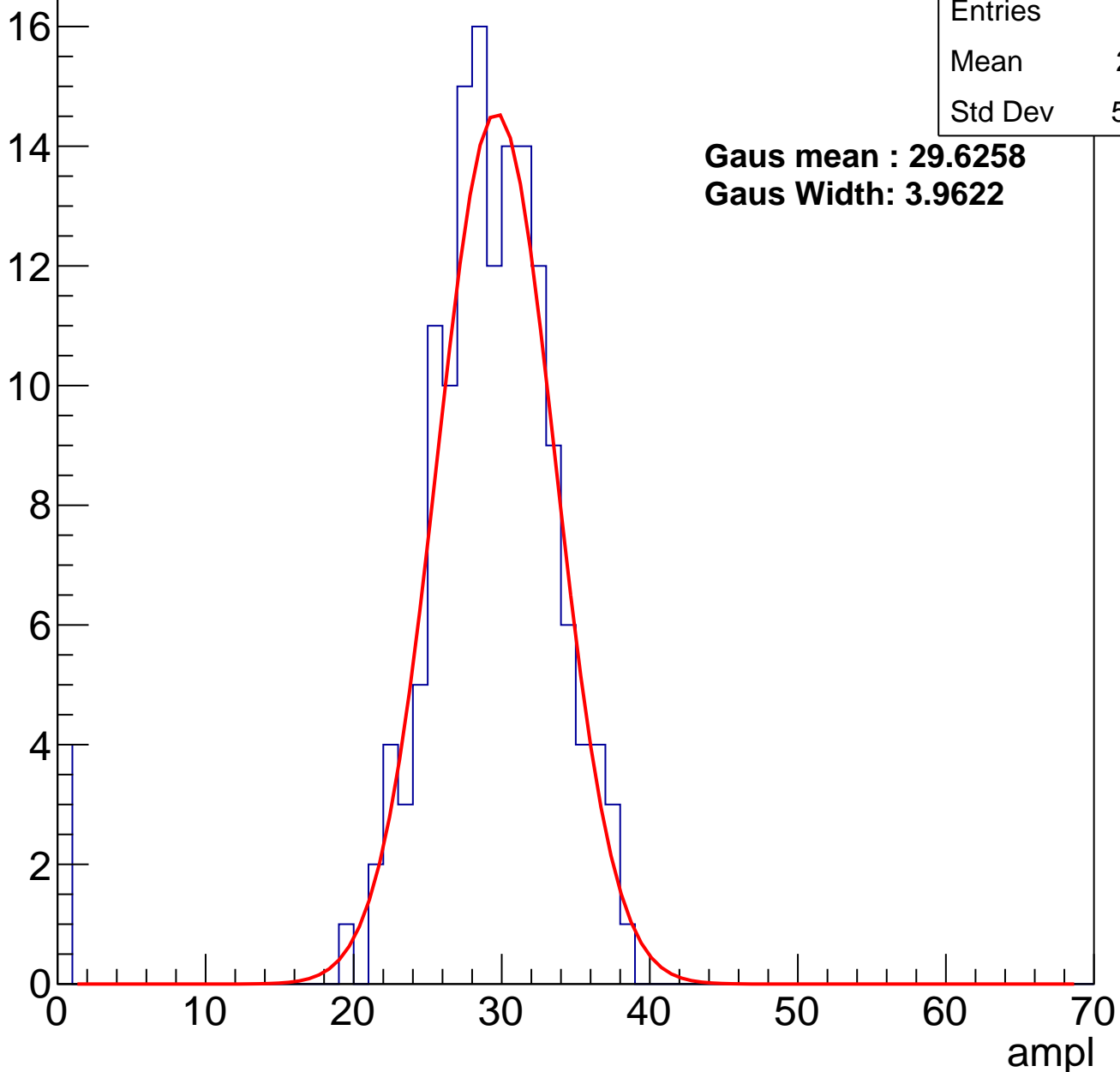
calib_packv5_042523_0143.root, FC#1, port C1

Entries	150
Mean	28.31
Std Dev	5.987

Gaus mean : 29.6258

Gaus Width: 3.9622

Entry



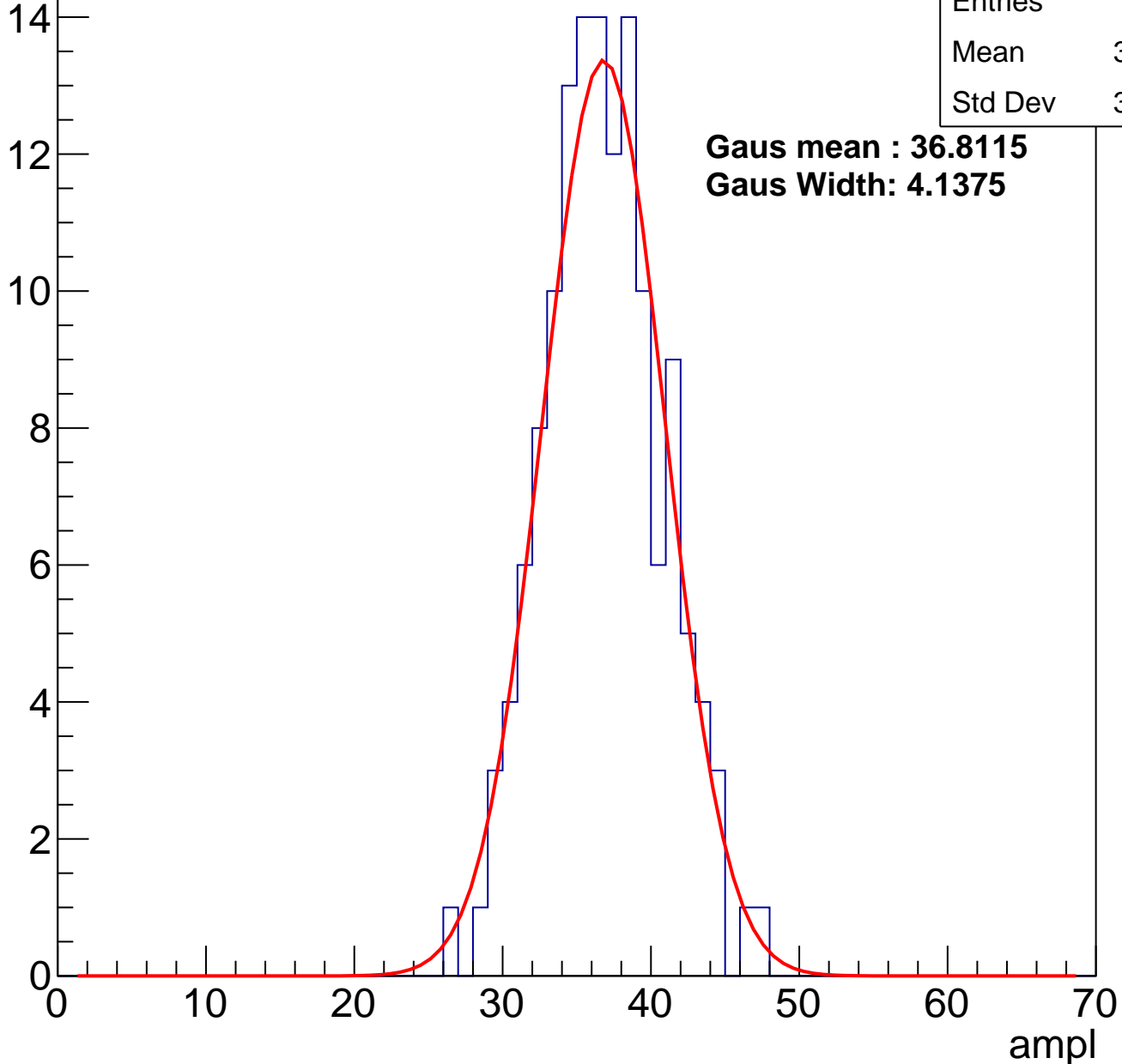
B0L101S, U15-ch111, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	36.34
Std Dev	3.925

Gaus mean : 36.8115
Gaus Width: 4.1375

Entry



B0L101S, U15-ch111, adc2

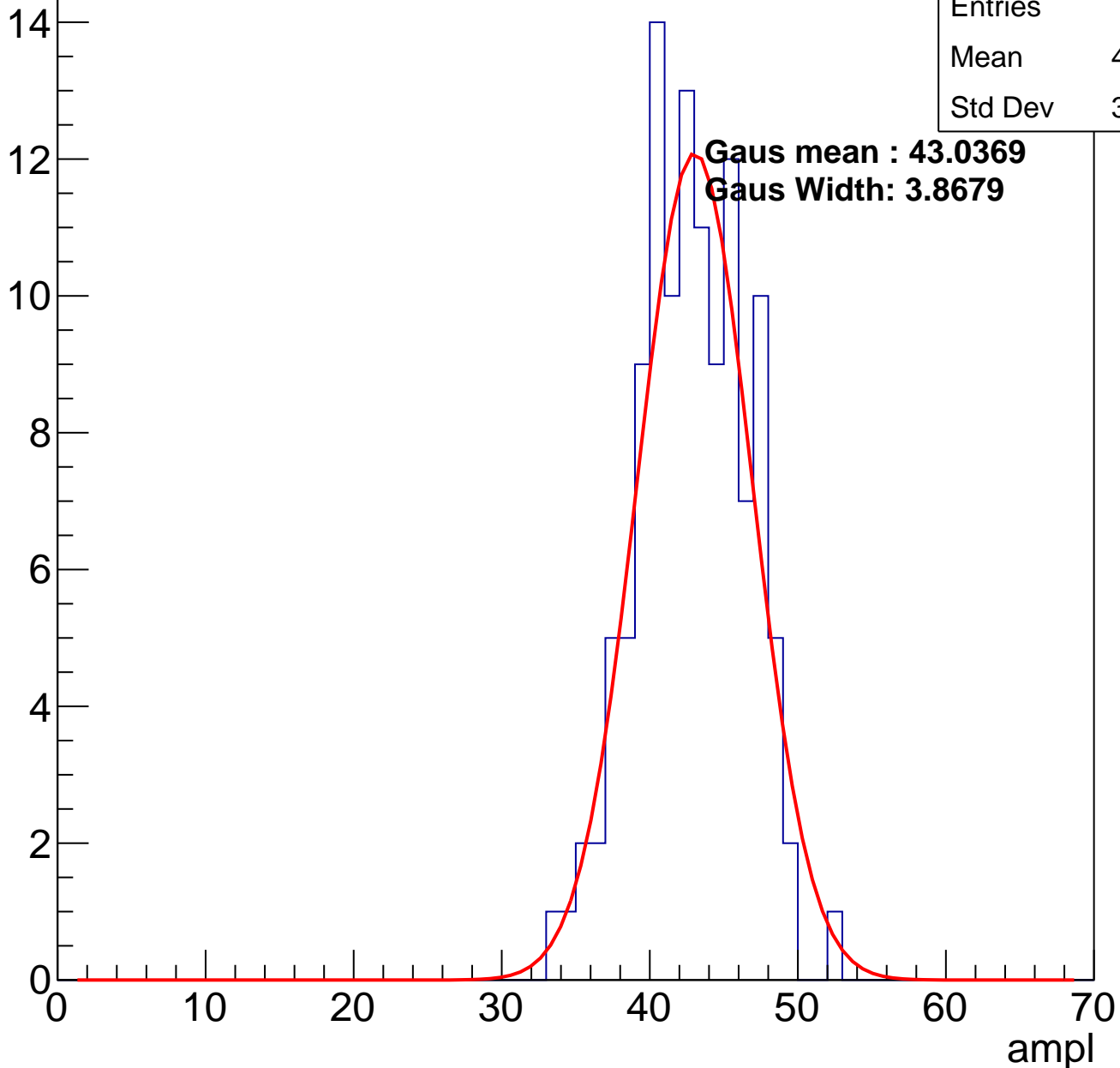
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	119
Mean	42.37
Std Dev	3.604

Gaus mean : 43.0369

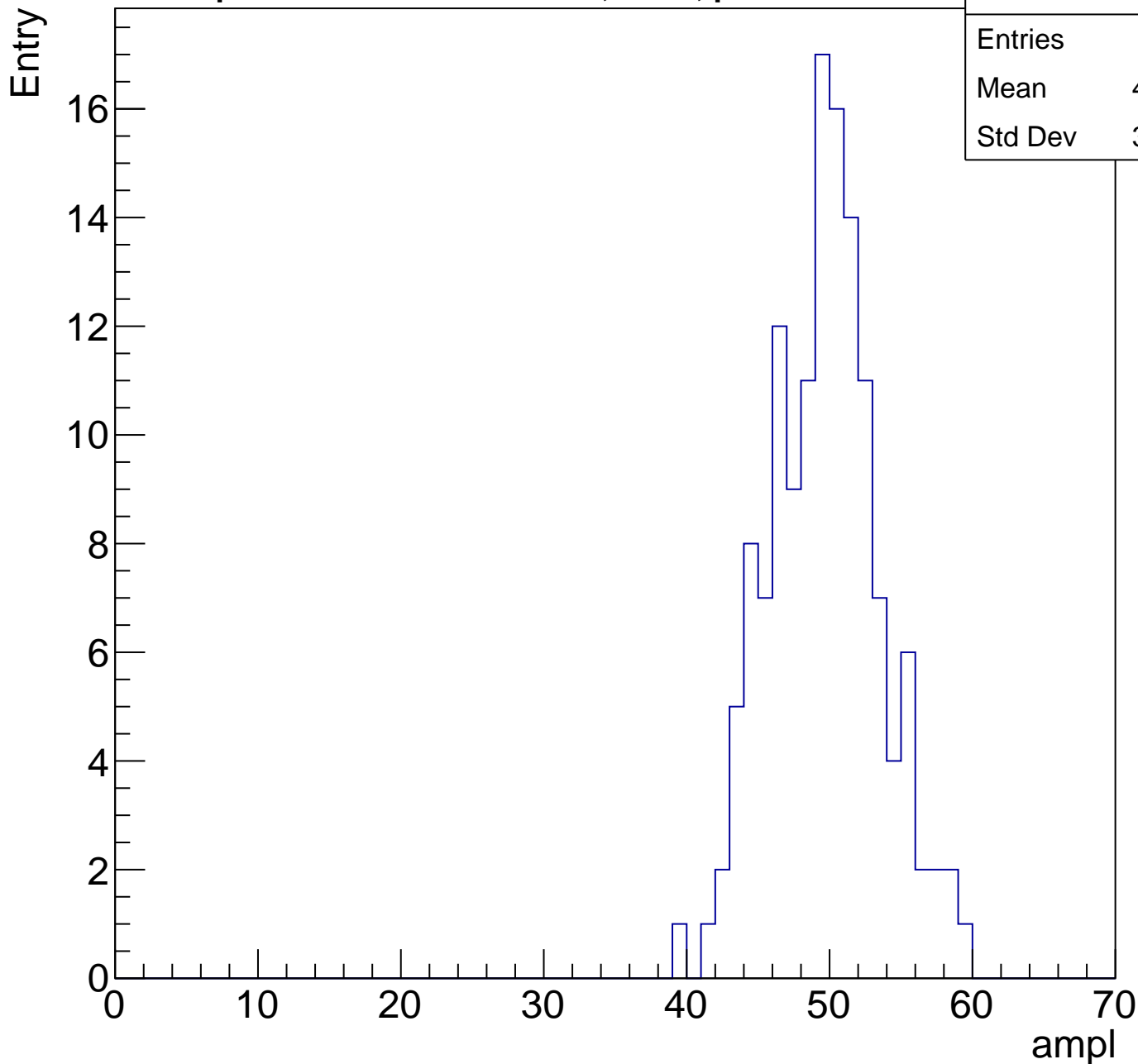
Gaus Width: 3.8679



B0L101S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	138
Mean	49.17
Std Dev	3.799



B0L101S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	103
Mean	55.19
Std Dev	3.459

Entry

12

10

8

6

4

2

0

0

10

20

30

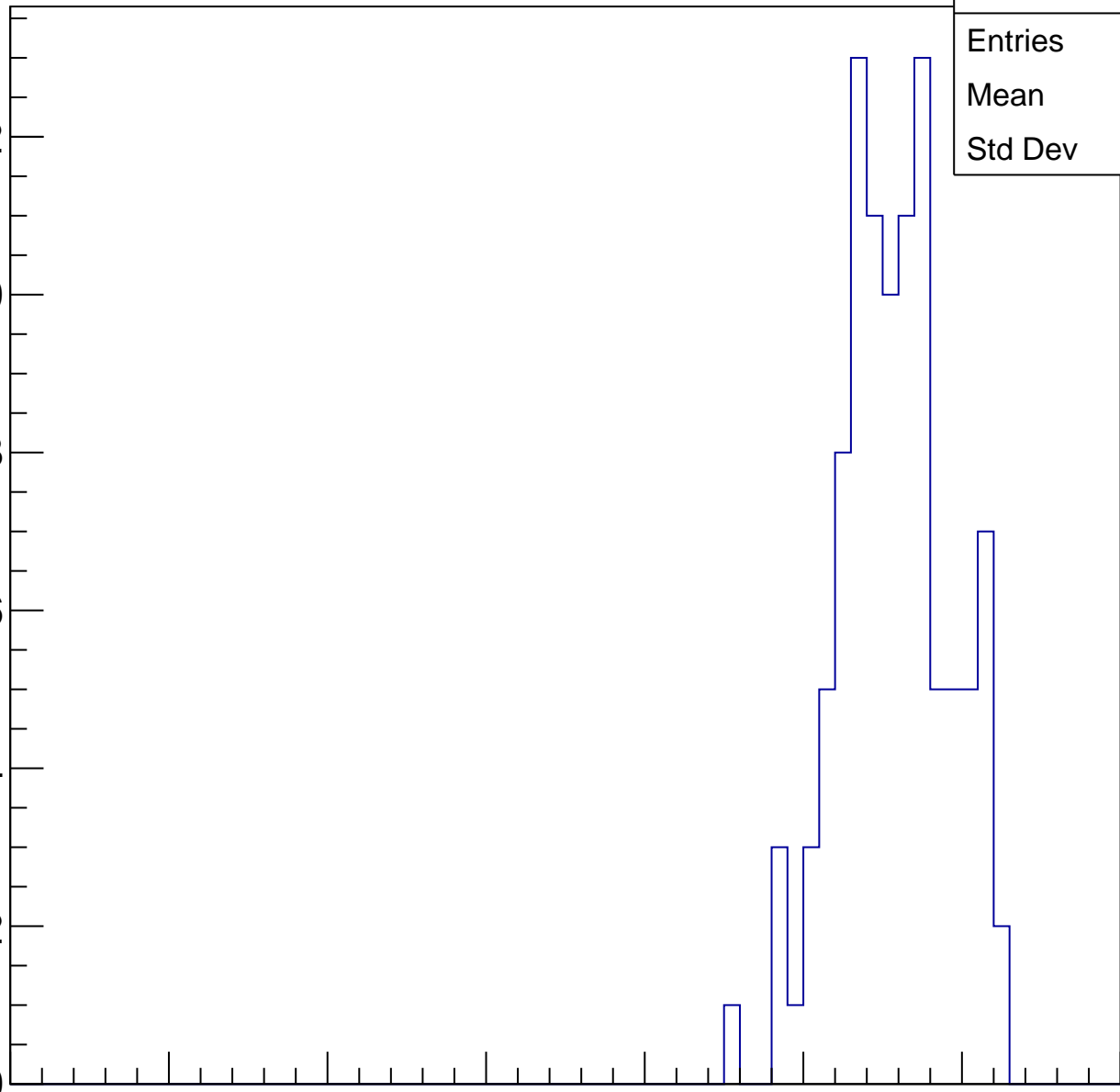
40

50

60

70

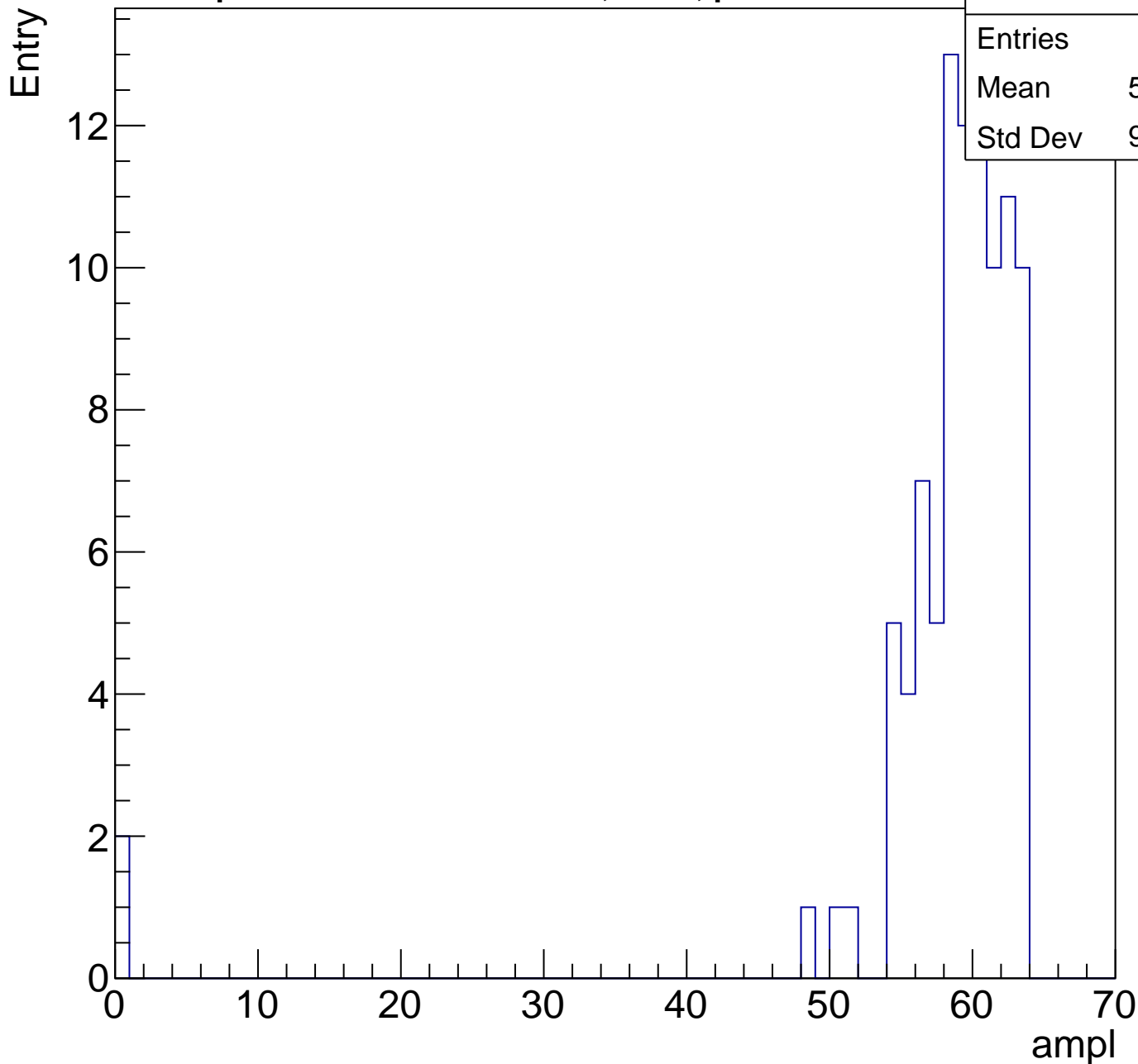
ampl



B0L101S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	94
Mean	57.66
Std Dev	9.024

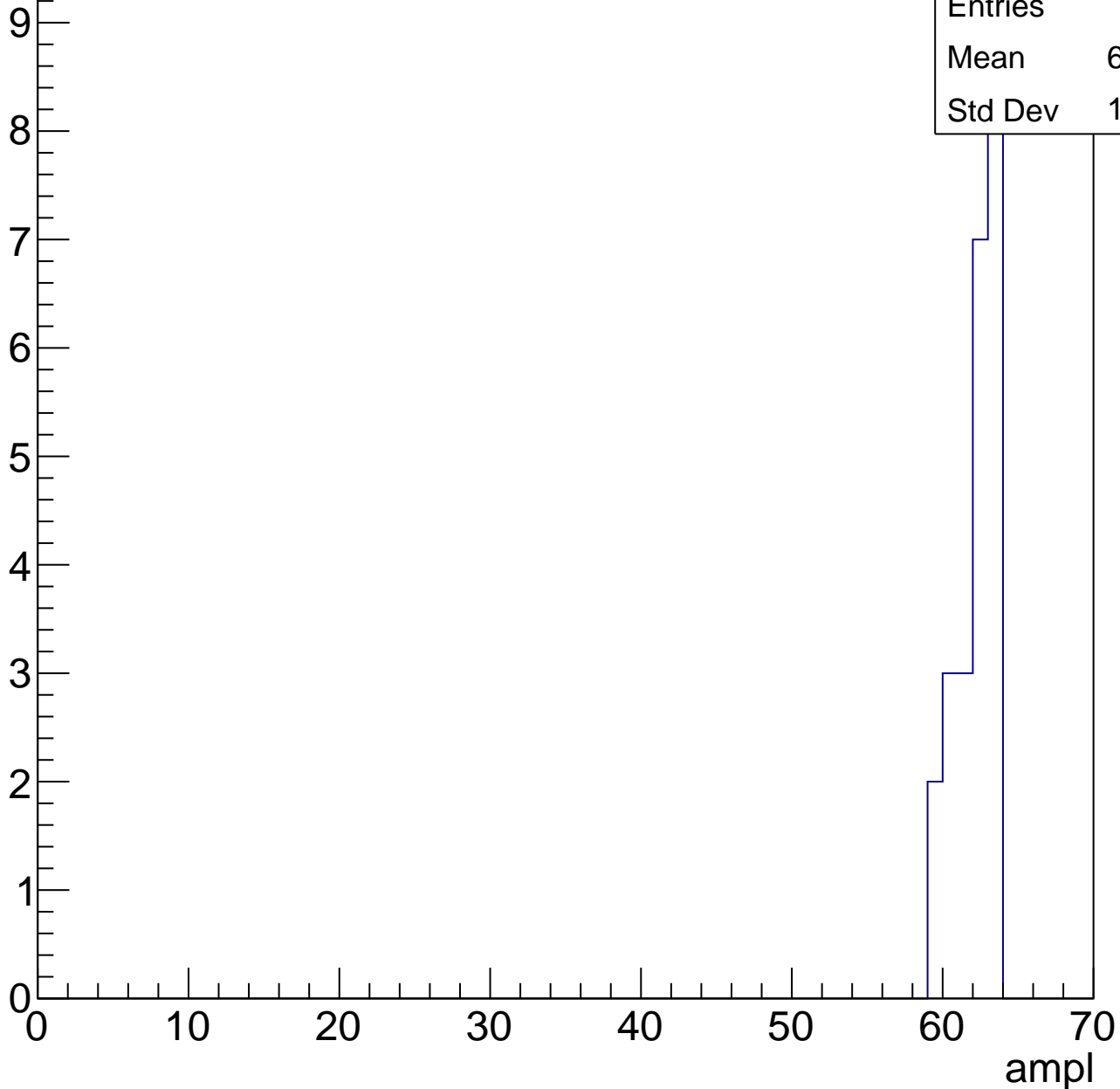


B0L101S, U15-ch111, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	24
Mean	61.75
Std Dev	1.299



B0L101S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch112, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	157
Mean	27.54
Std Dev	4.372

Gaus mean : 28.5411

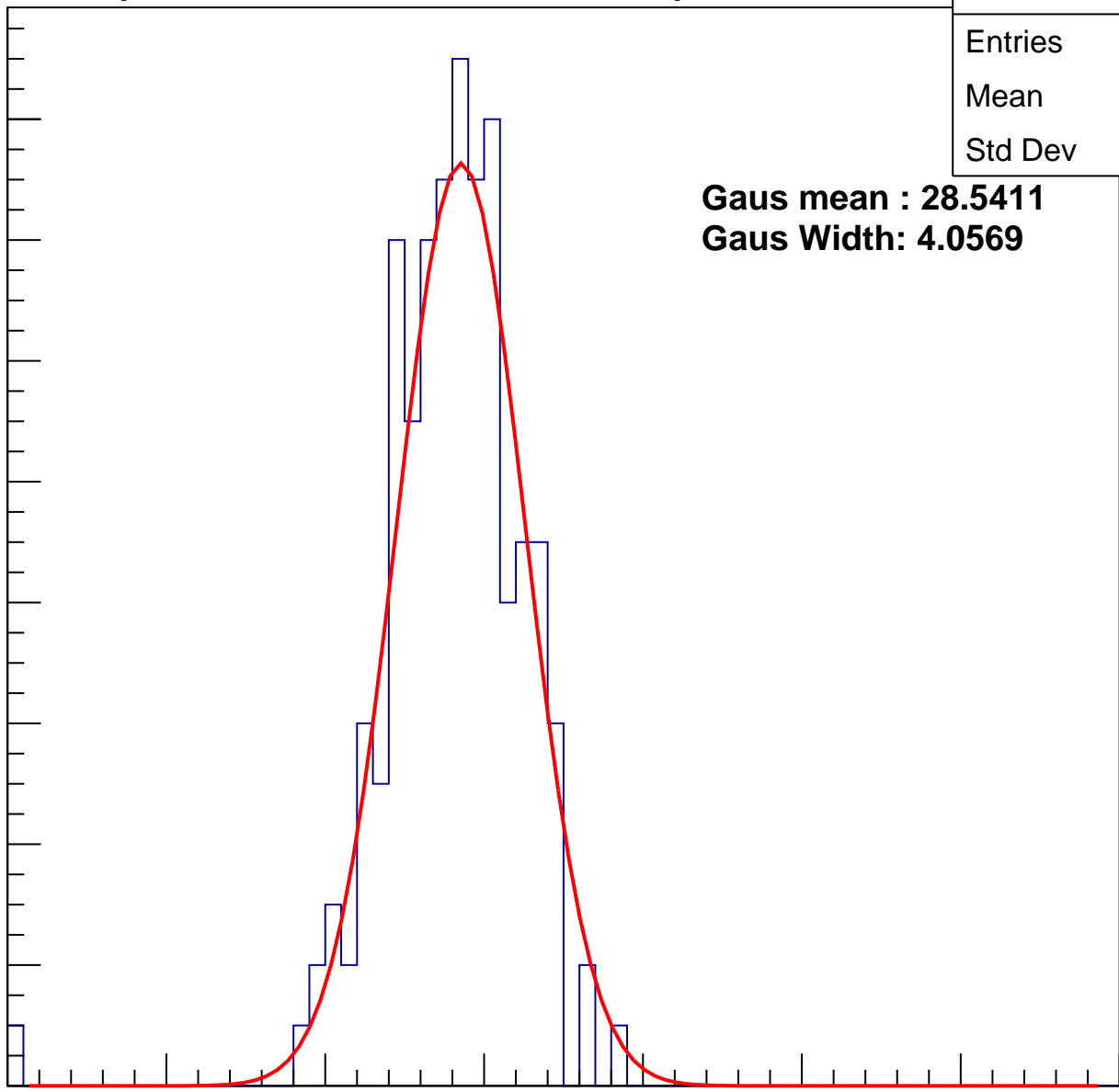
Gaus Width: 4.0569

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch112, adc1

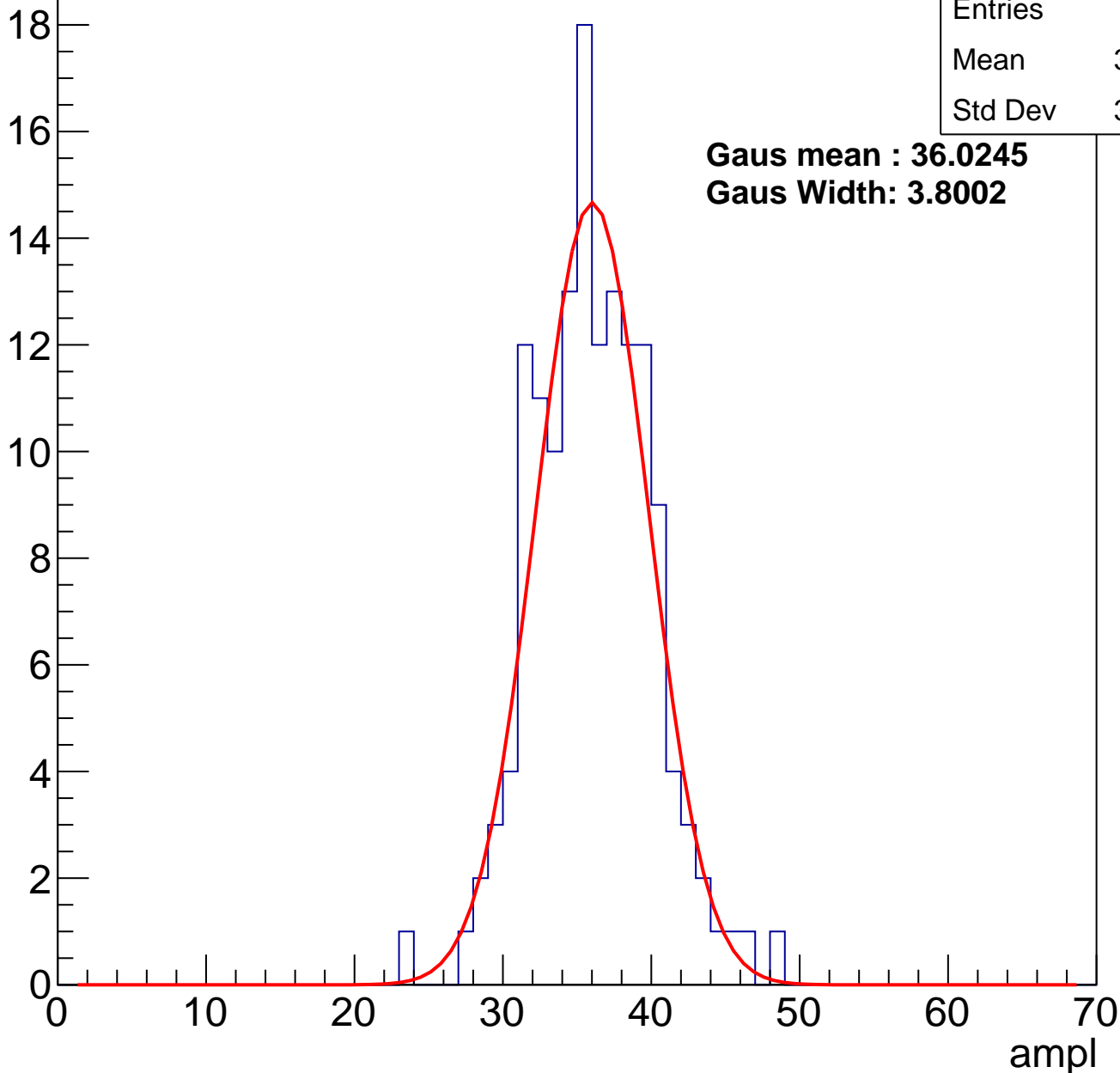
calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	35.58
Std Dev	3.958

Gaus mean : 36.0245

Gaus Width: 3.8002

Entry



B0L101S, U15-ch112, adc2

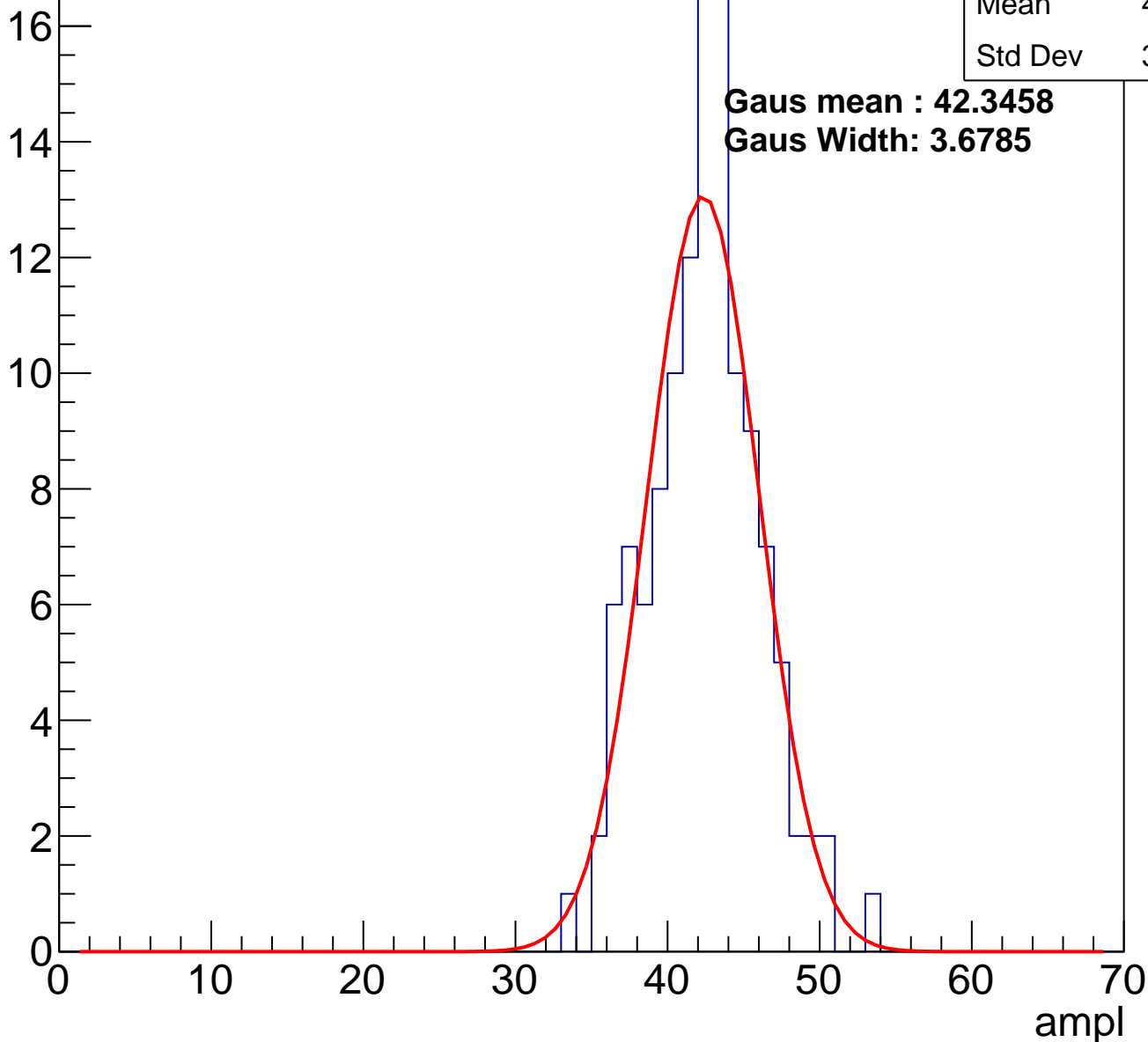
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	124
Mean	41.97
Std Dev	3.574

Gaus mean : 42.3458

Gaus Width: 3.6785



B0L101S, U15-ch112, adc3

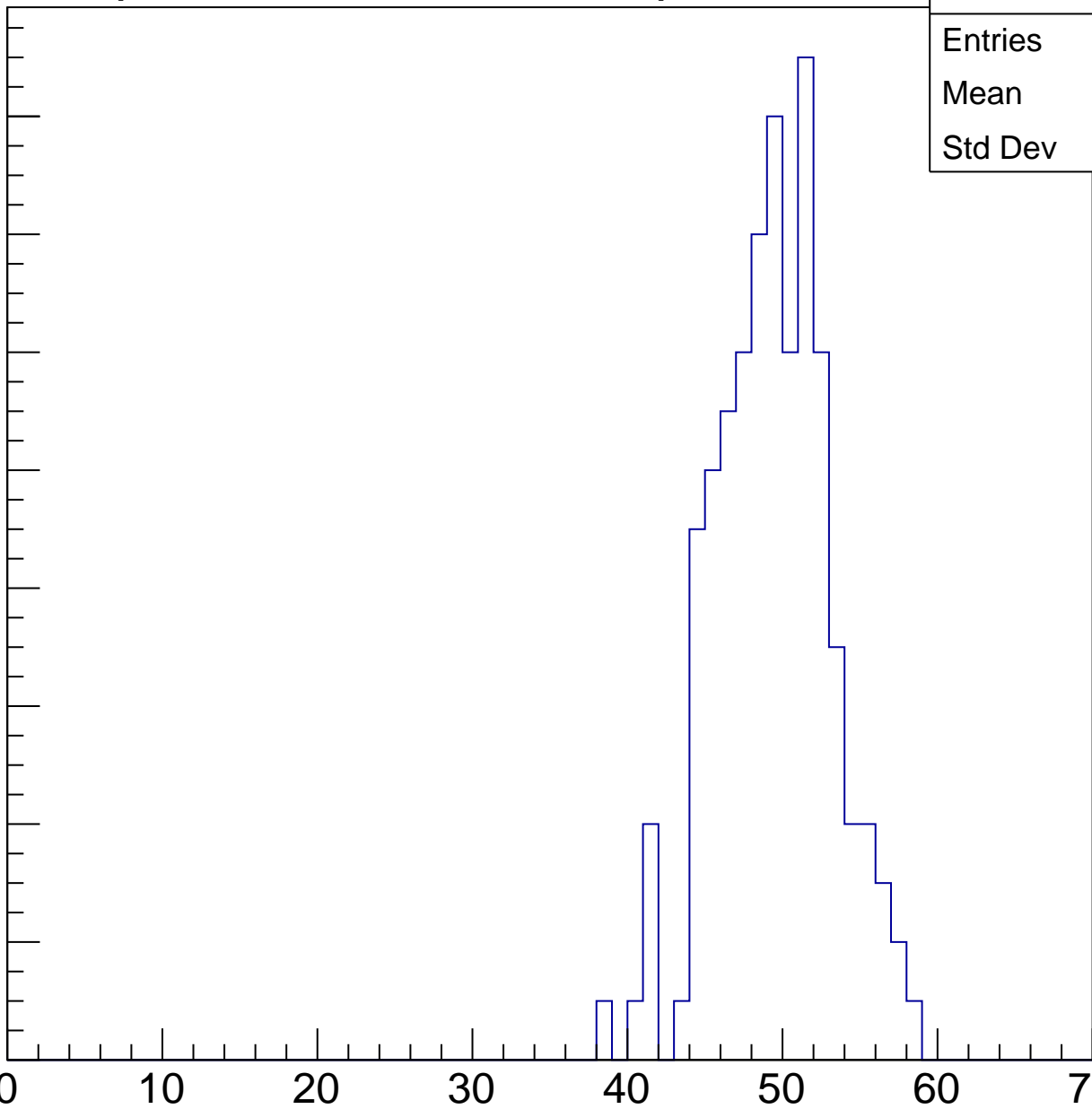
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	141
Mean	48.9
Std Dev	3.708

ampl



B0L101S, U15-ch112, adc4

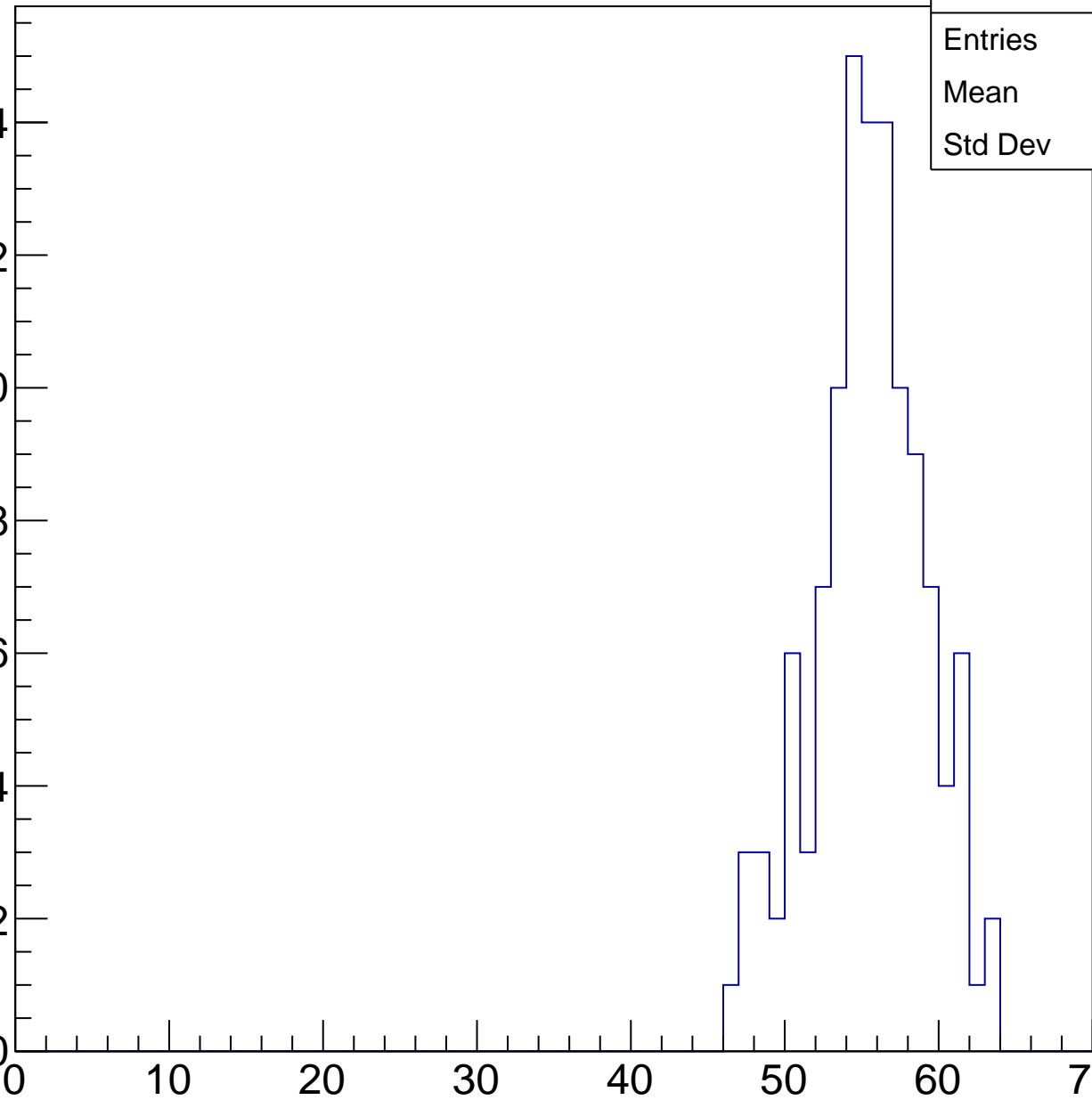
calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	55.03
Std Dev	3.637

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch112, adc5

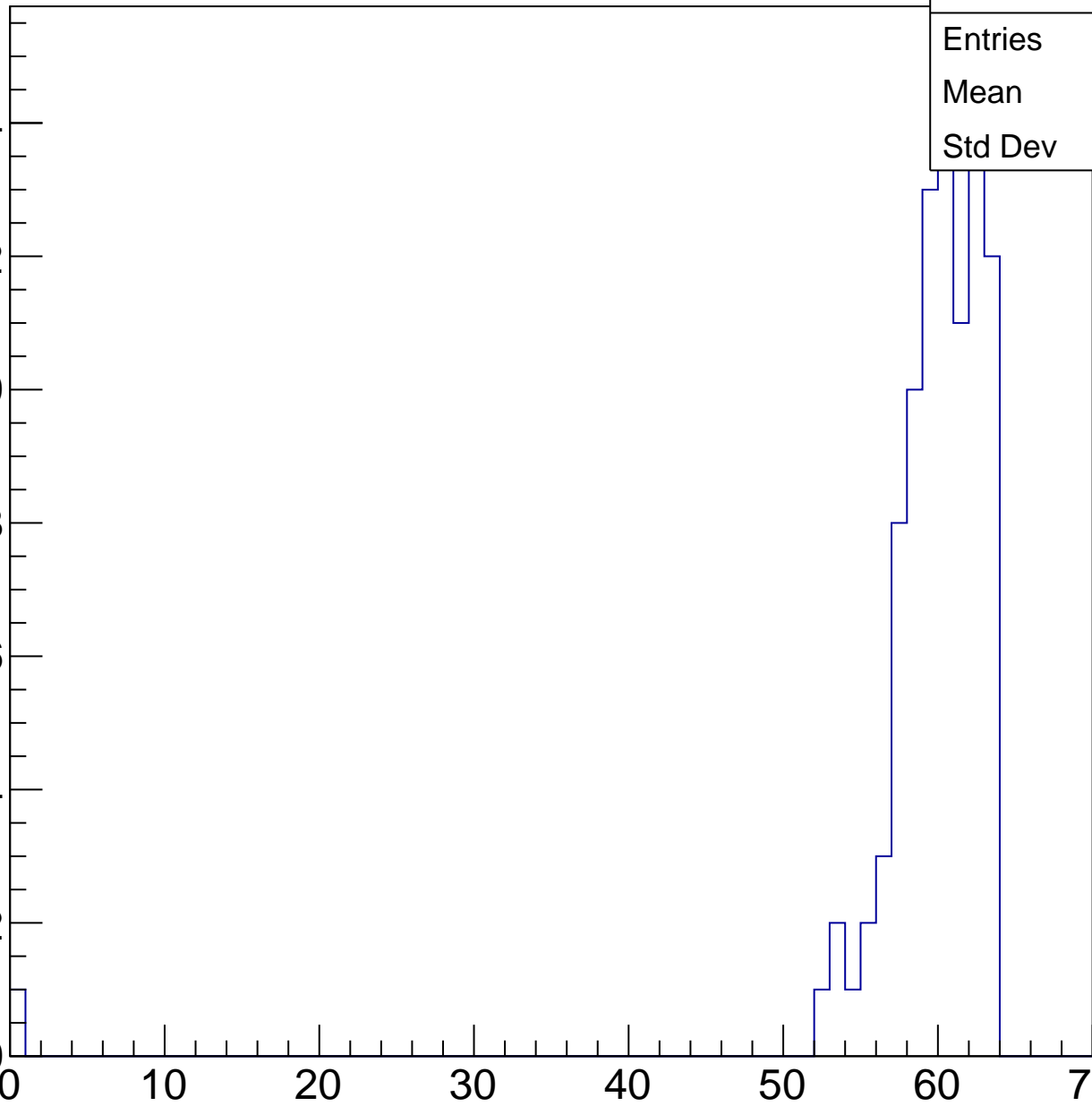
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	59.04
Std Dev	6.615

ampl

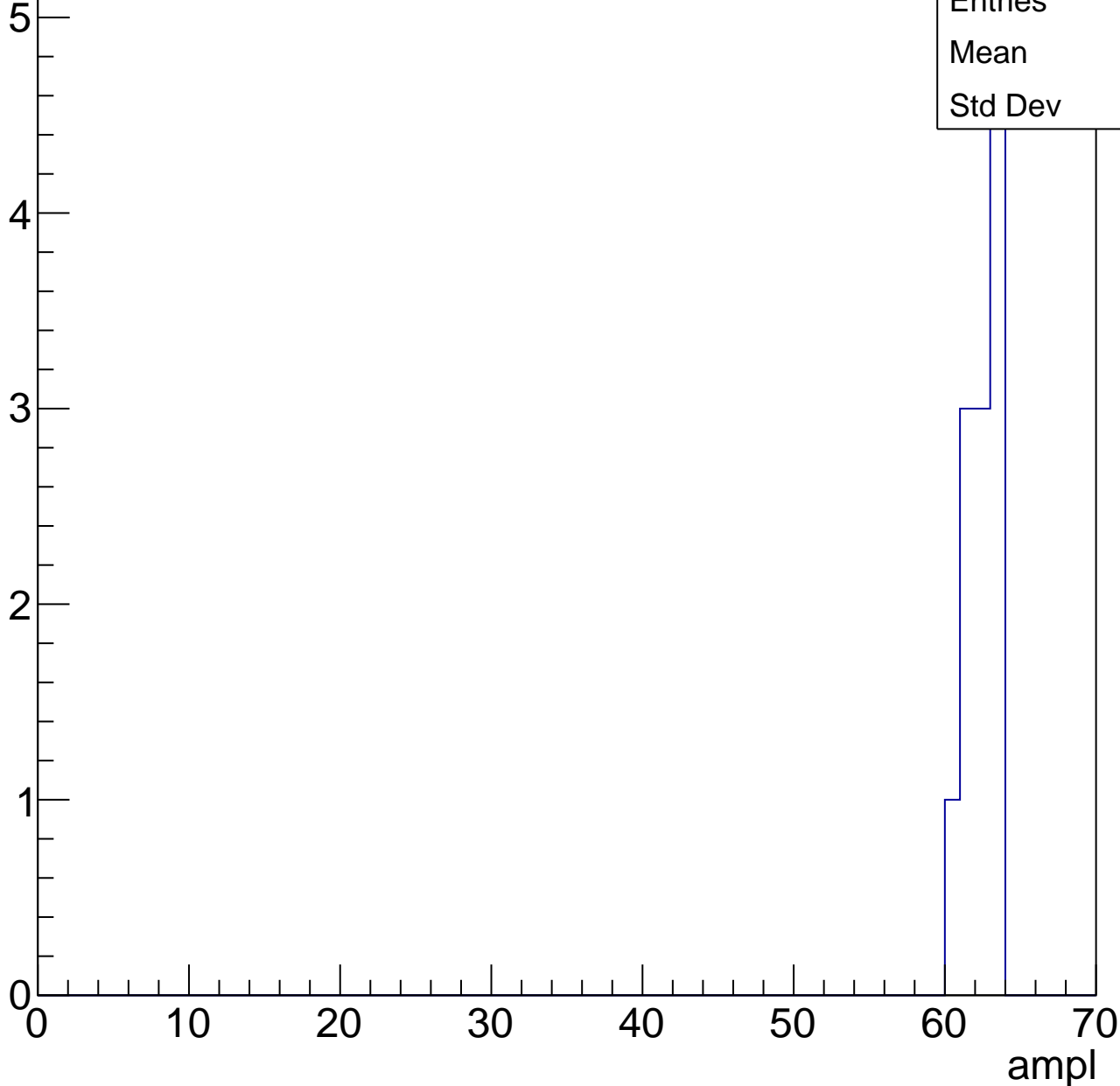


B0L101S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

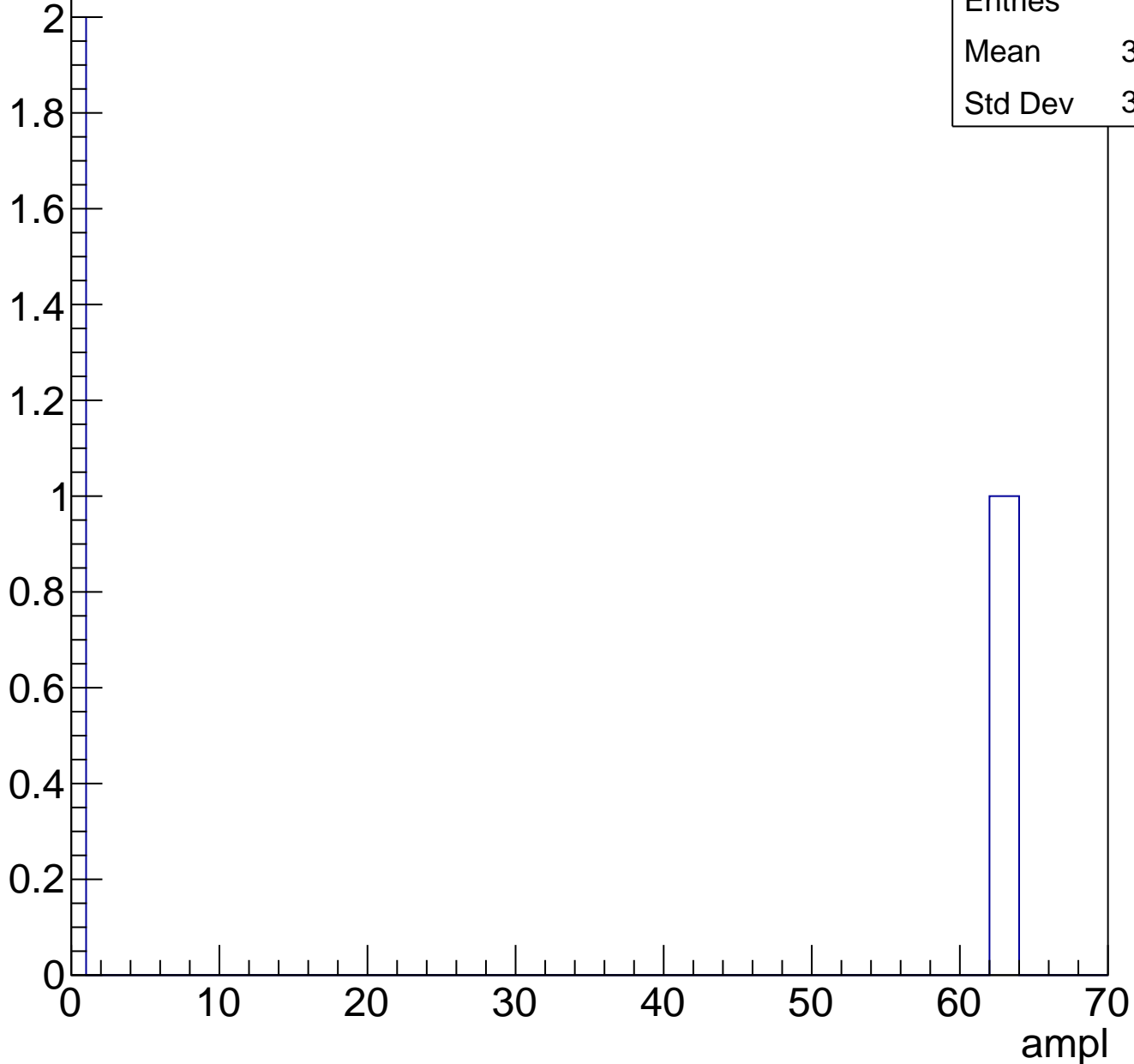
Entries	12
Mean	62
Std Dev	1



B0L101S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch113, adc0

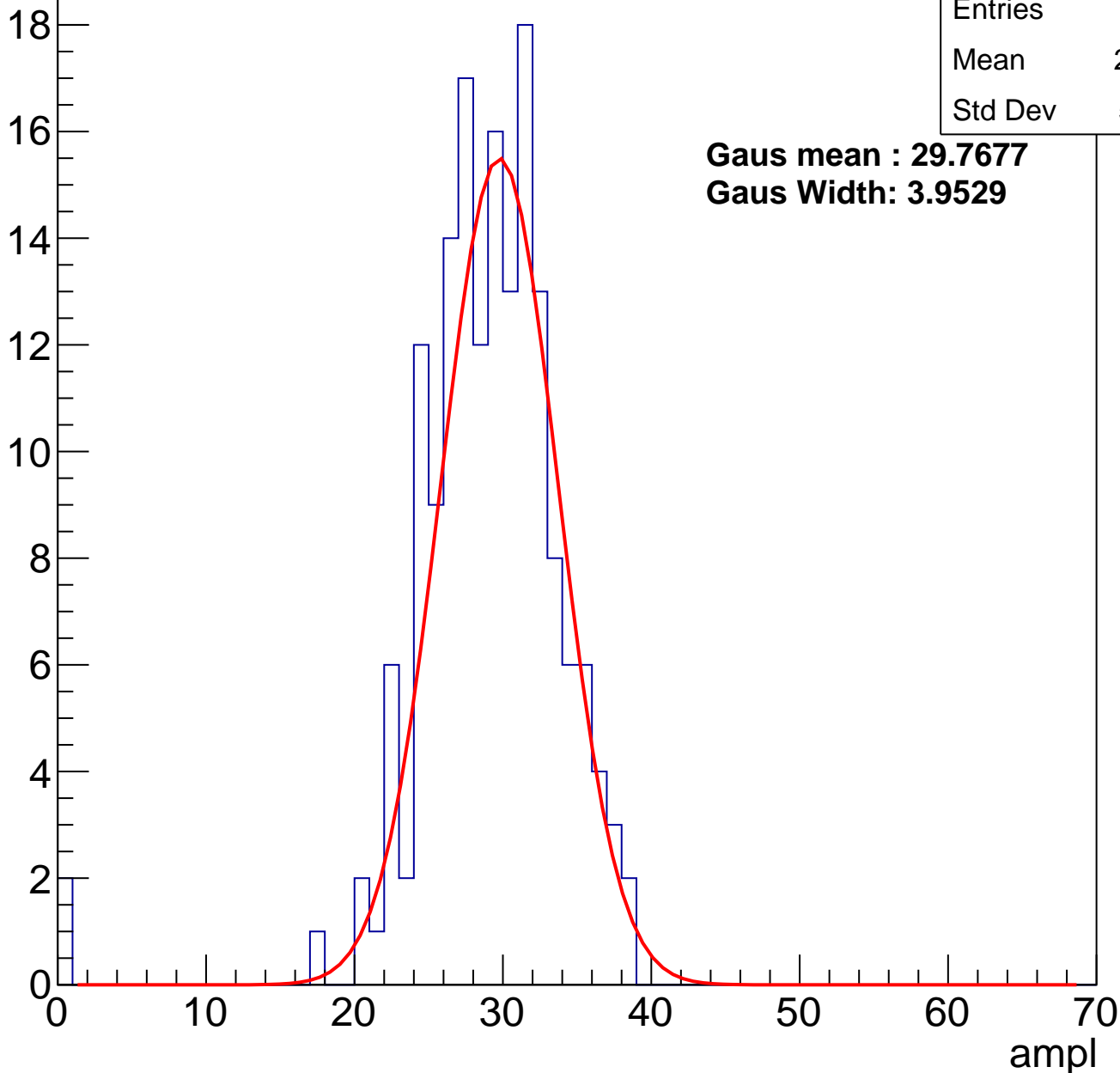
calib_packv5_042523_0143.root, FC#1, port C1

Entries	167
Mean	28.53
Std Dev	5.061

Gaus mean : 29.7677

Gaus Width: 3.9529

Entry



B0L101S, U15-ch113, adc1

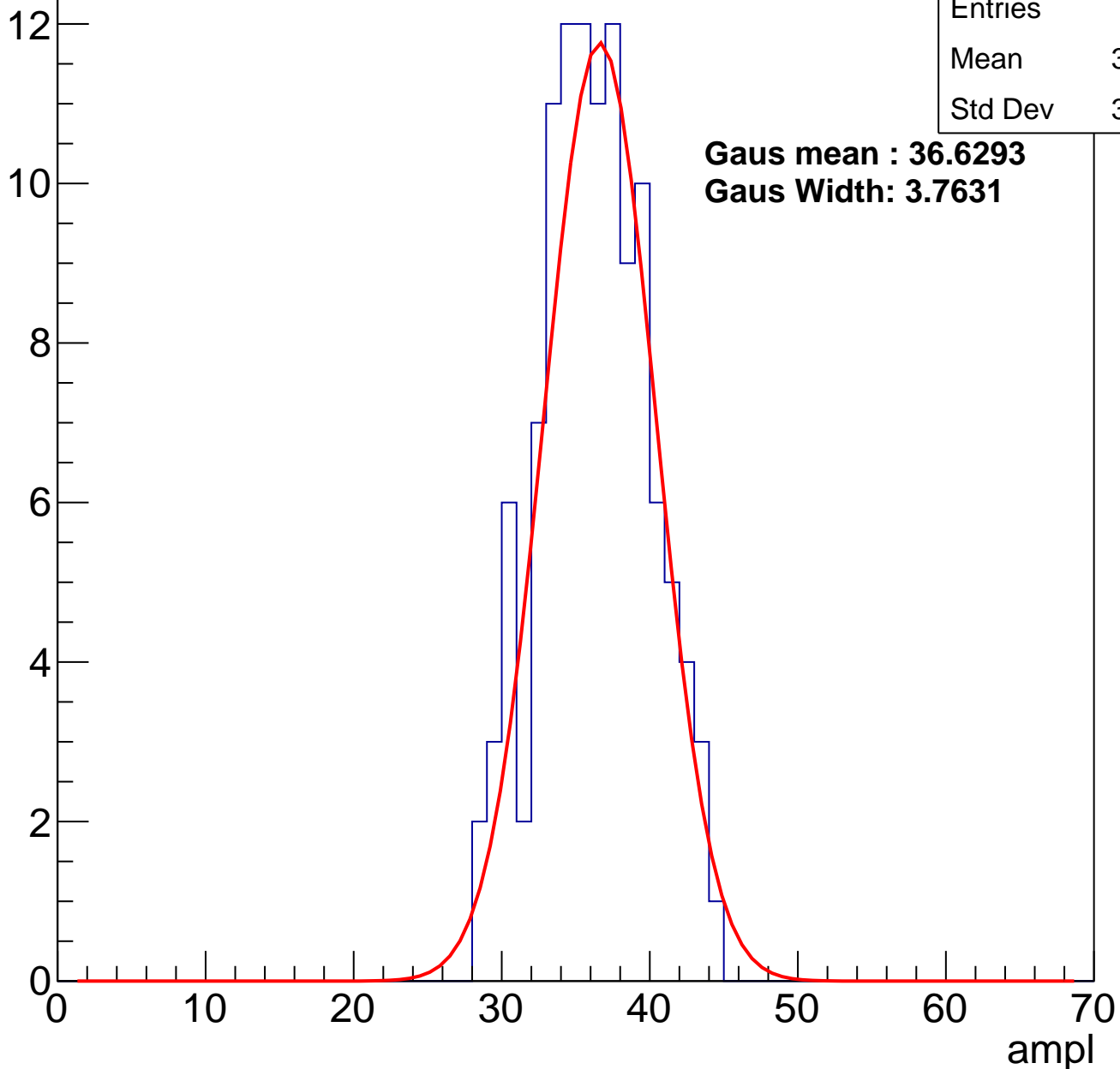
calib_packv5_042523_0143.root, FC#1, port C1

Entries	116
Mean	35.84
Std Dev	3.624

Gaus mean : 36.6293

Gaus Width: 3.7631

Entry



B0L101S, U15-ch113, adc2

calib_packv5_042523_0143.root, FC#1, port C1

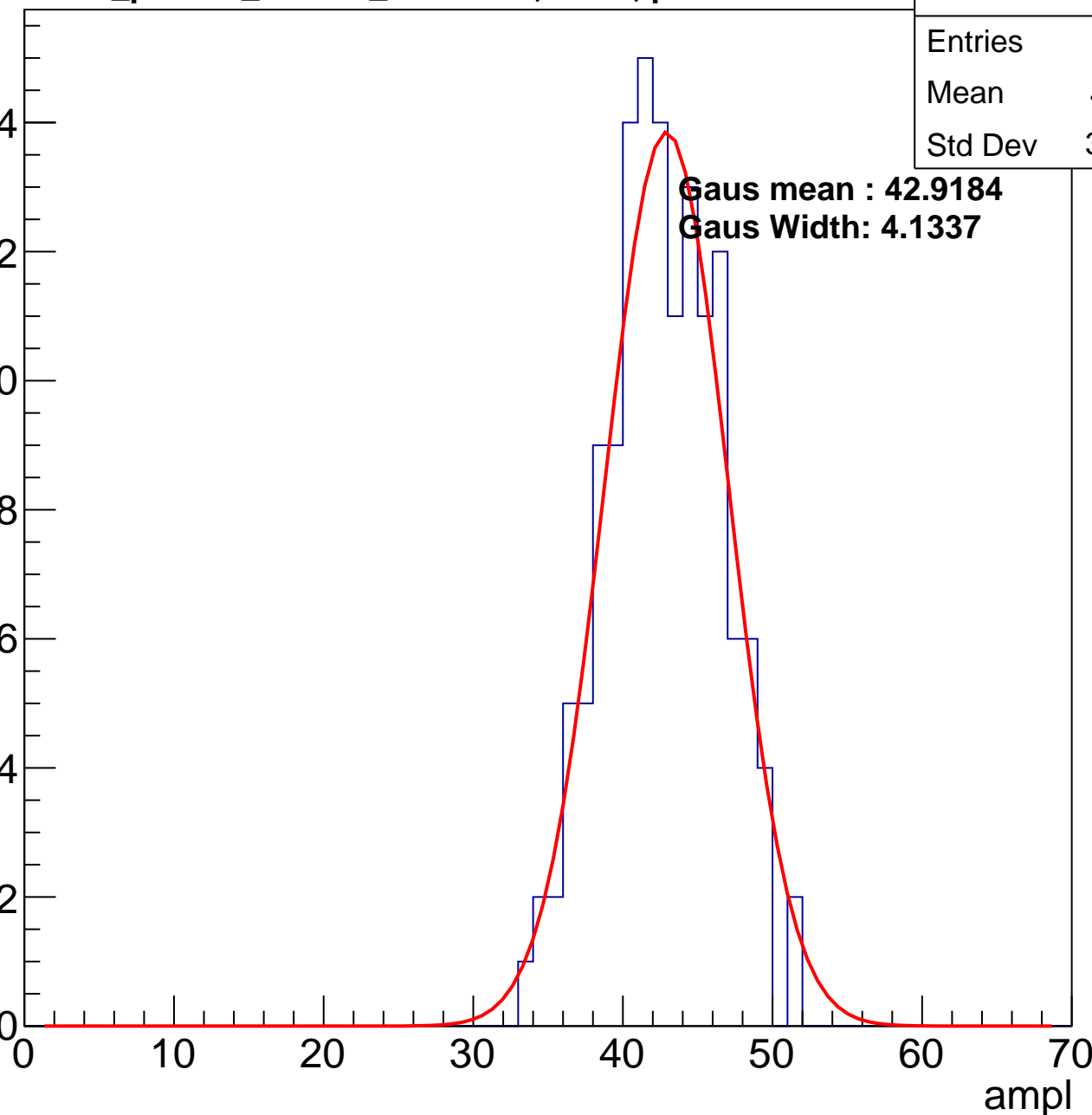
Entry

14
12
10
8
6
4
2
0

Entries	141
Mean	42.21
Std Dev	3.747

Gaus mean : 42.9184

Gaus Width: 4.1337

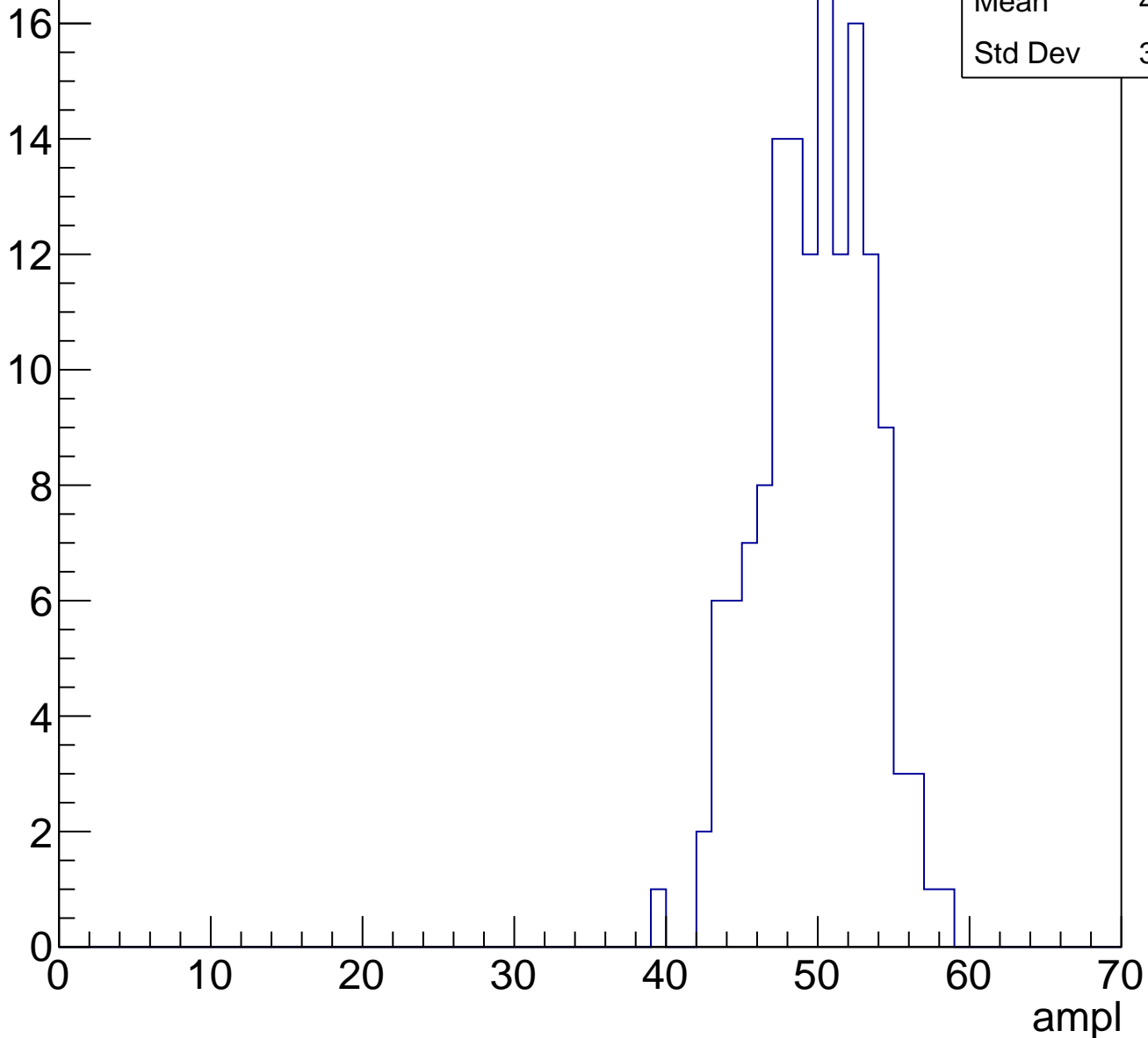


B0L101S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	144
Mean	49.38
Std Dev	3.559



B0L101S, U15-ch113, adc4

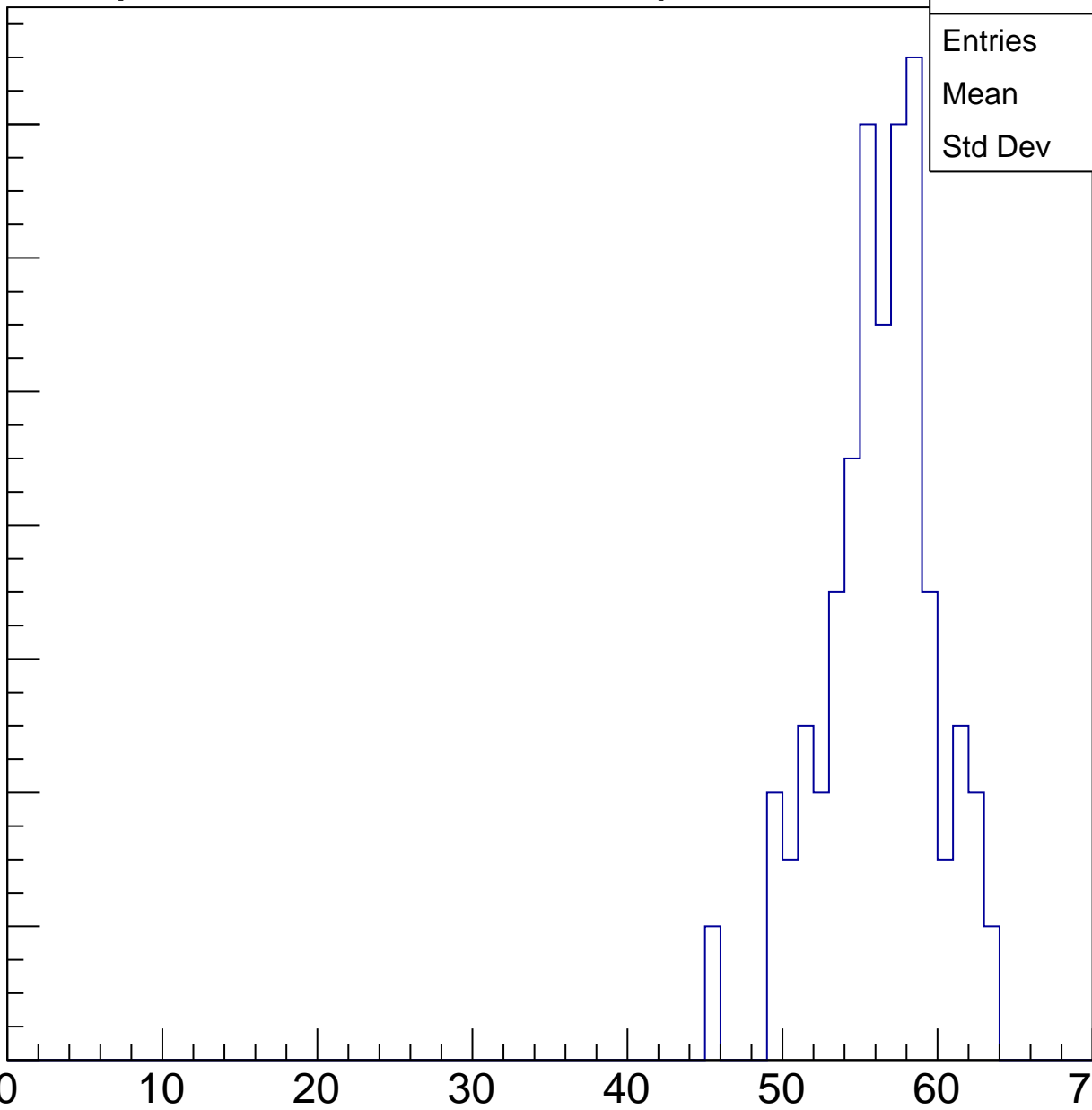
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	55.8
Std Dev	3.578

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

Entries	82
Mean	58.13
Std Dev	9.575

ampl

0

10

20

30

40

50

60

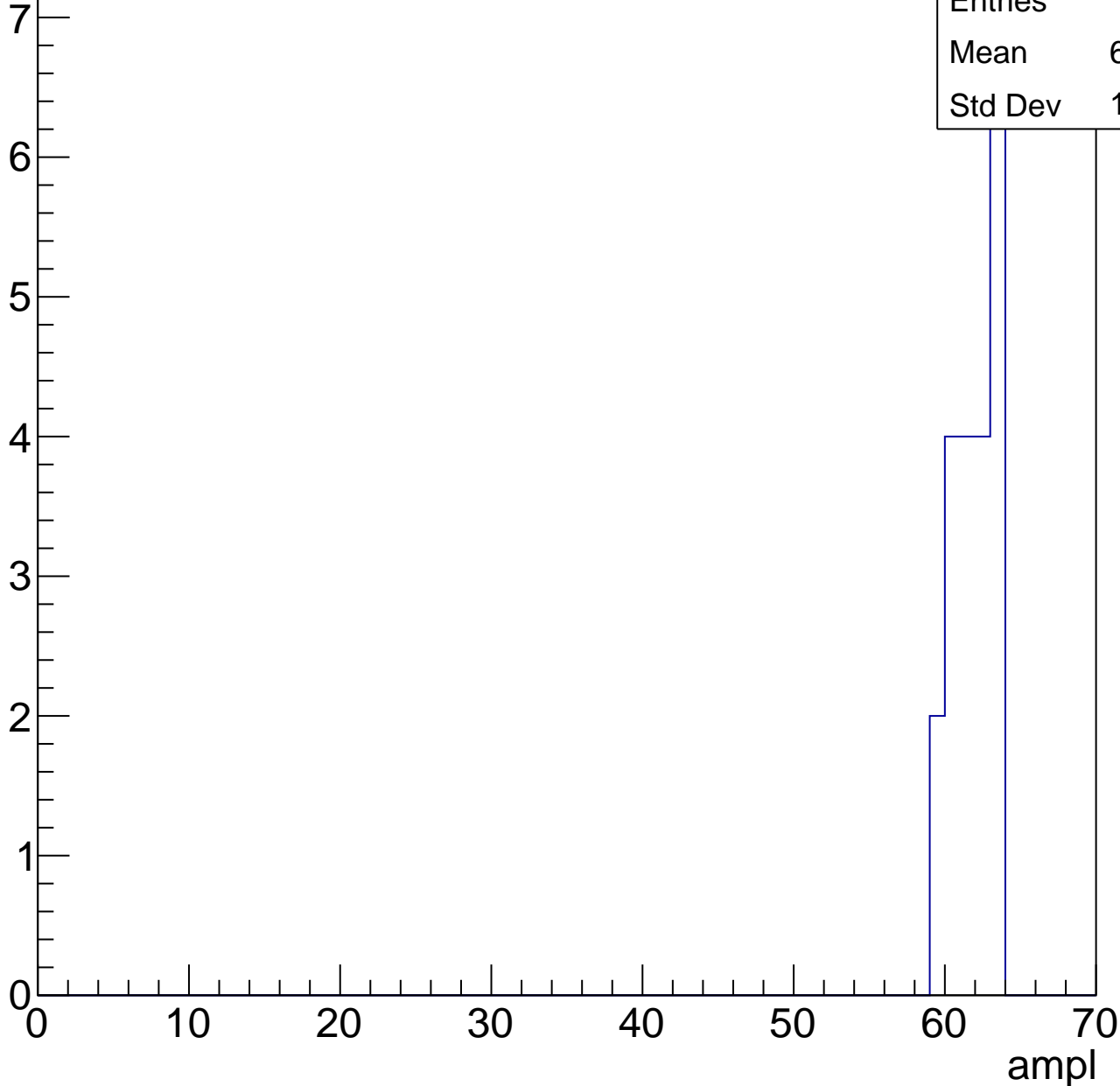
70

B0L101S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	61.48
Std Dev	1.367



B0L101S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch114, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	122
Mean	28.59
Std Dev	6.346

Gaus mean : 29.7170

Gaus Width: 3.0934

Entry

12

10

8

6

4

2

0

0

10

20

30

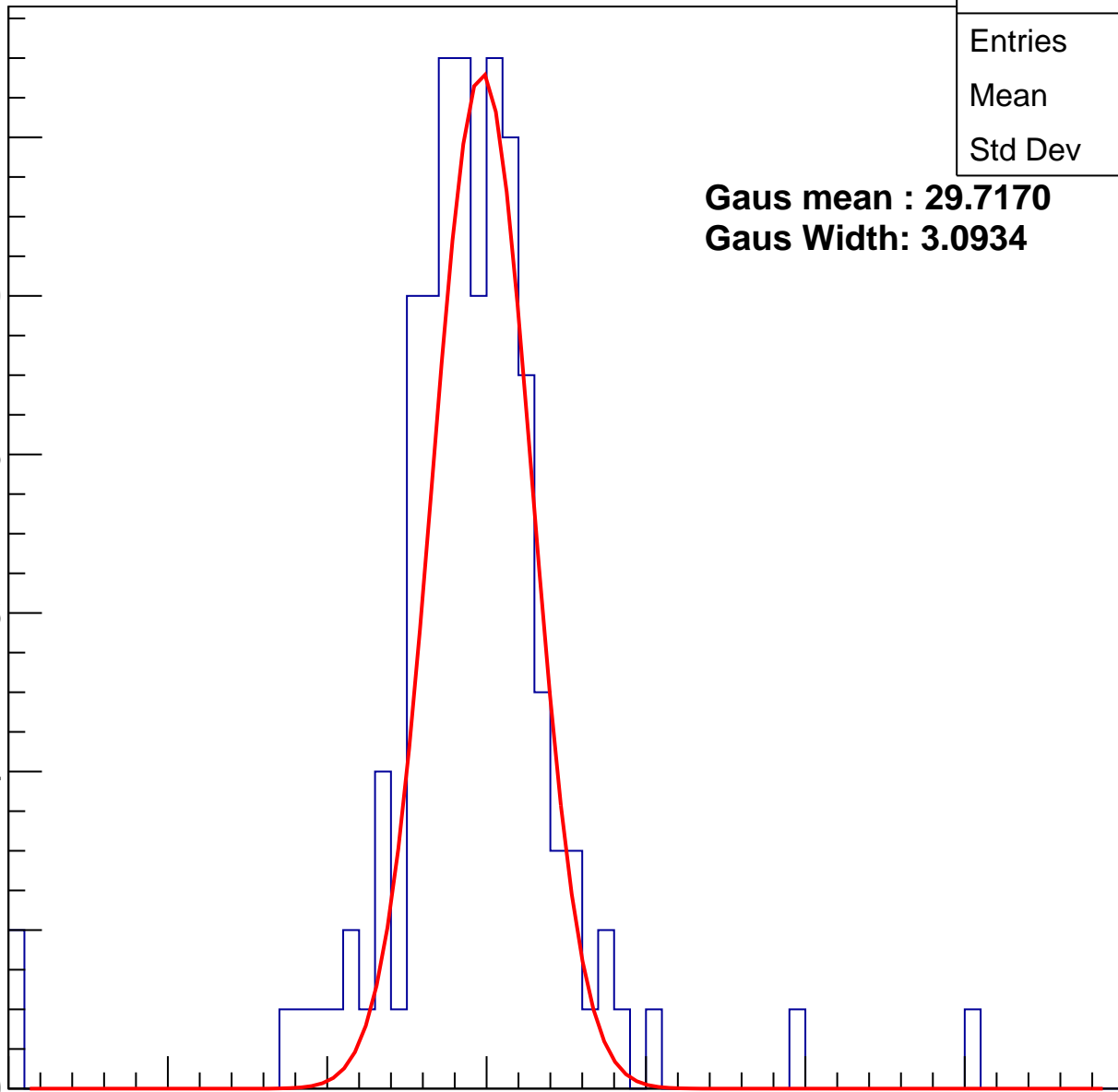
40

50

60

70

ampl



B0L101S, U15-ch114, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	34.59
Std Dev	4.085

Gaus mean : 35.2407

Gaus Width: 3.9232

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

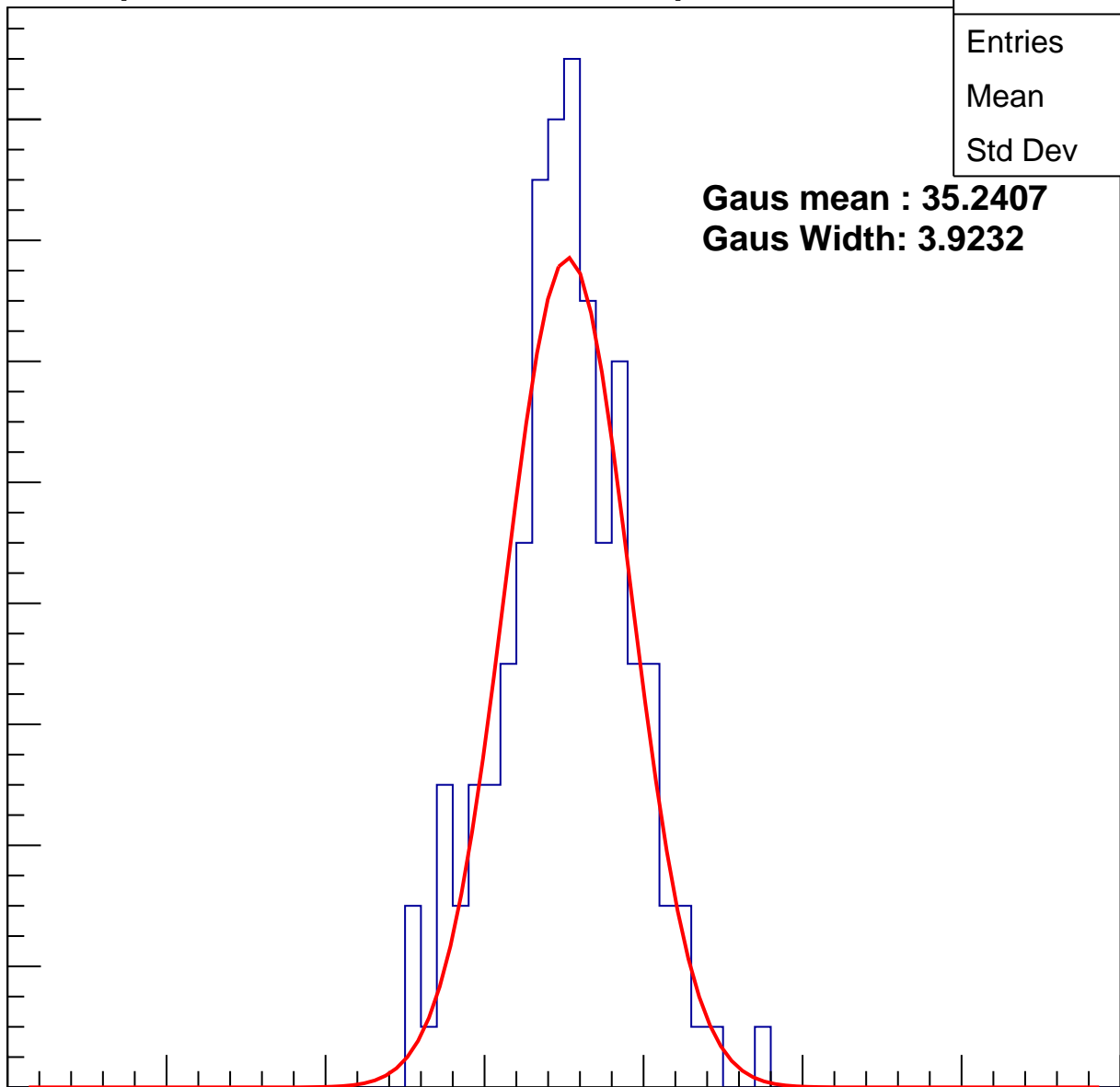
40

50

60

70

ampl



B0L101S, U15-ch114, adc2

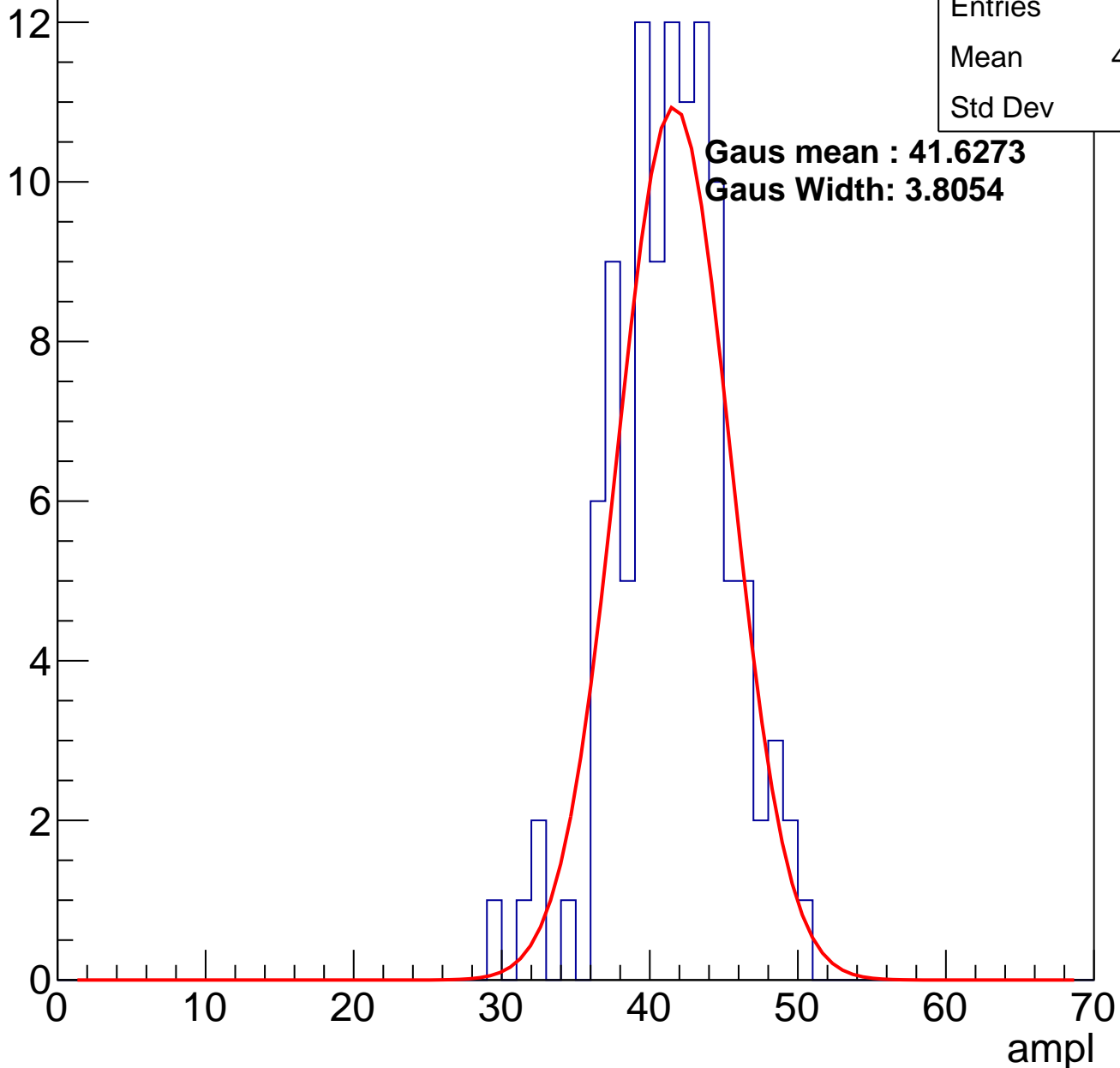
calib_packv5_042523_0143.root, FC#1, port C1

Entries	109
Mean	41.06
Std Dev	3.87

Gaus mean : 41.6273

Gaus Width: 3.8054

Entry



B0L101S, U15-ch114, adc3

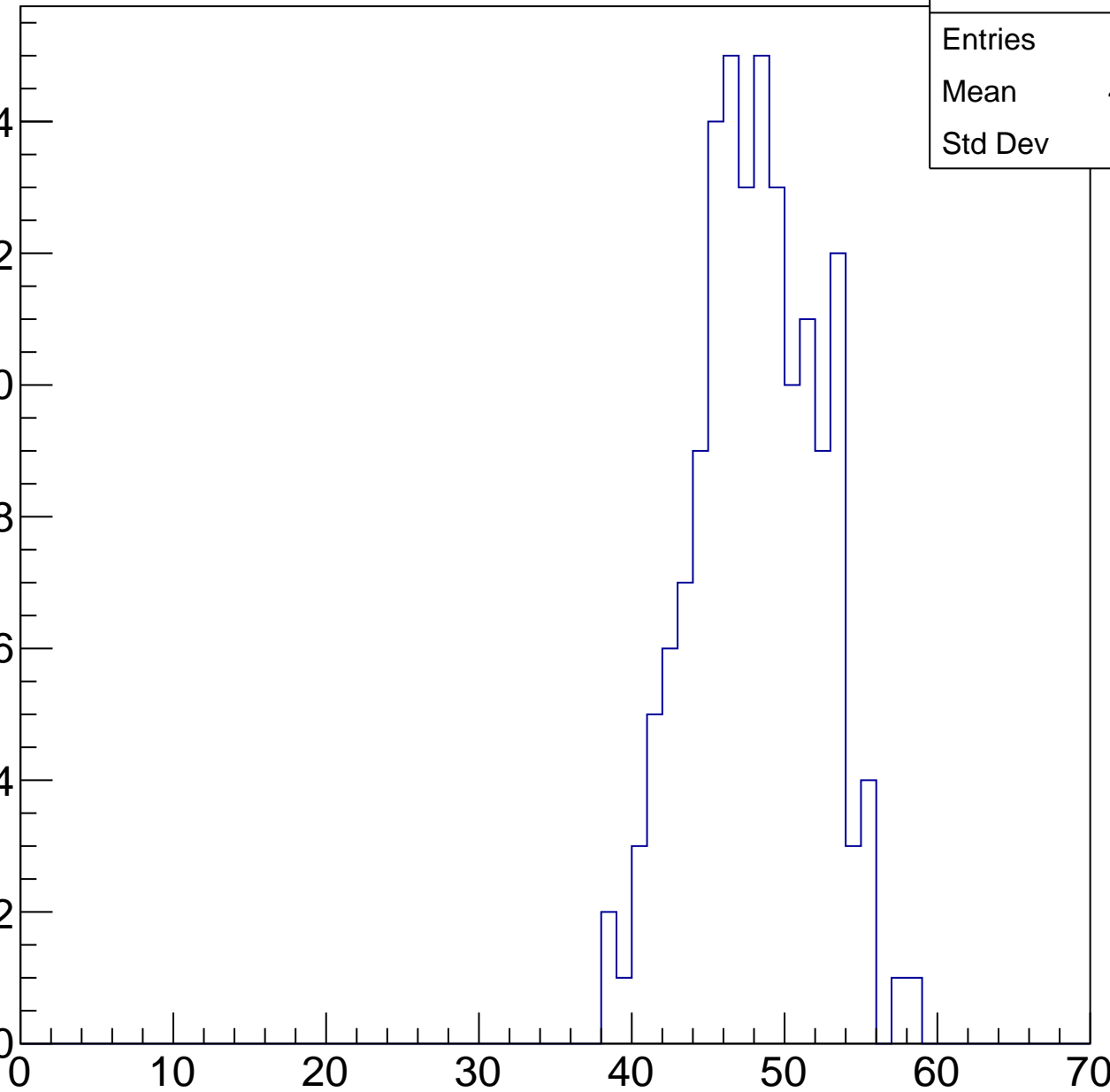
calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	47.66
Std Dev	4.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch114, adc4

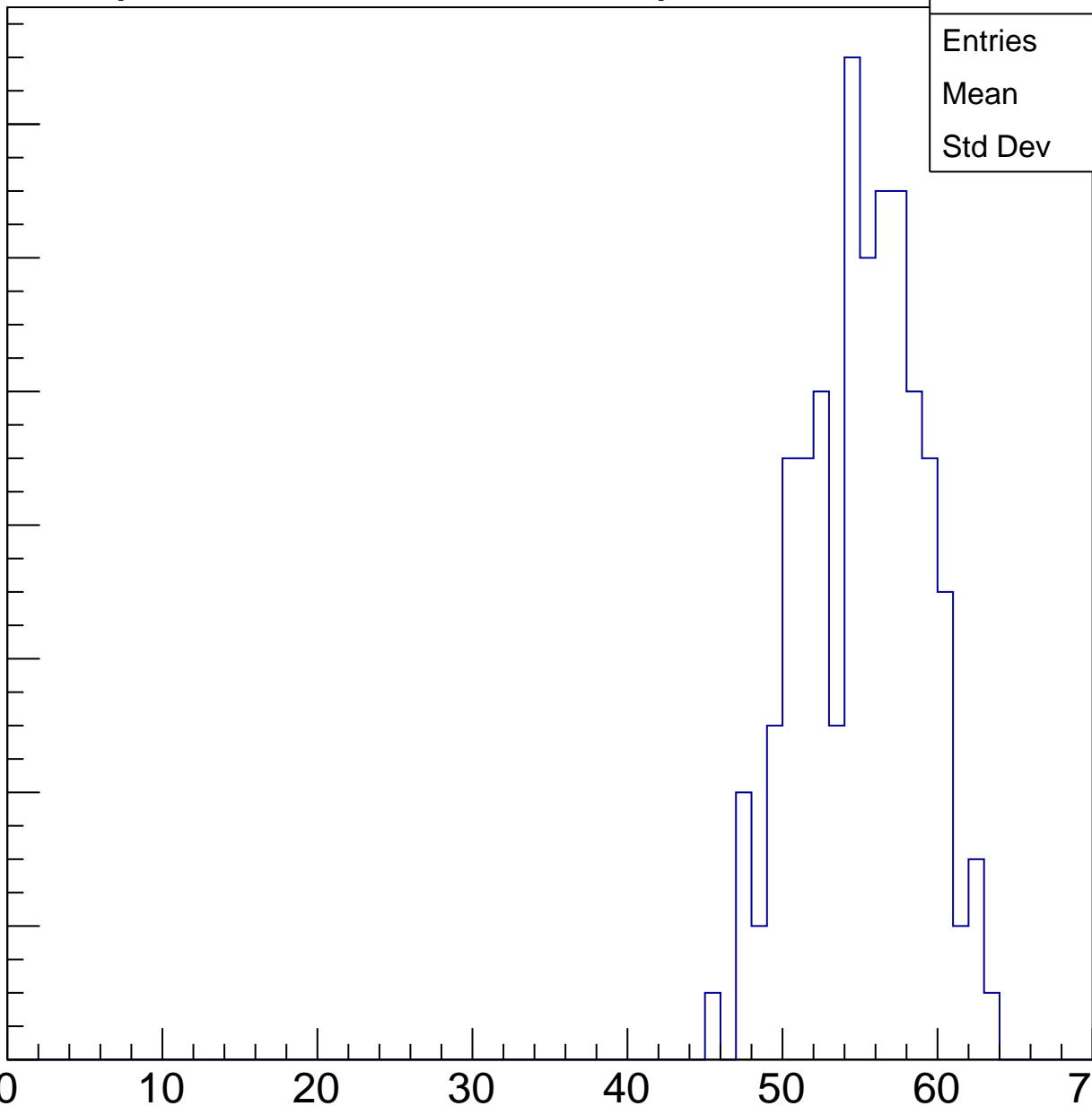
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	130
Mean	54.68
Std Dev	3.795

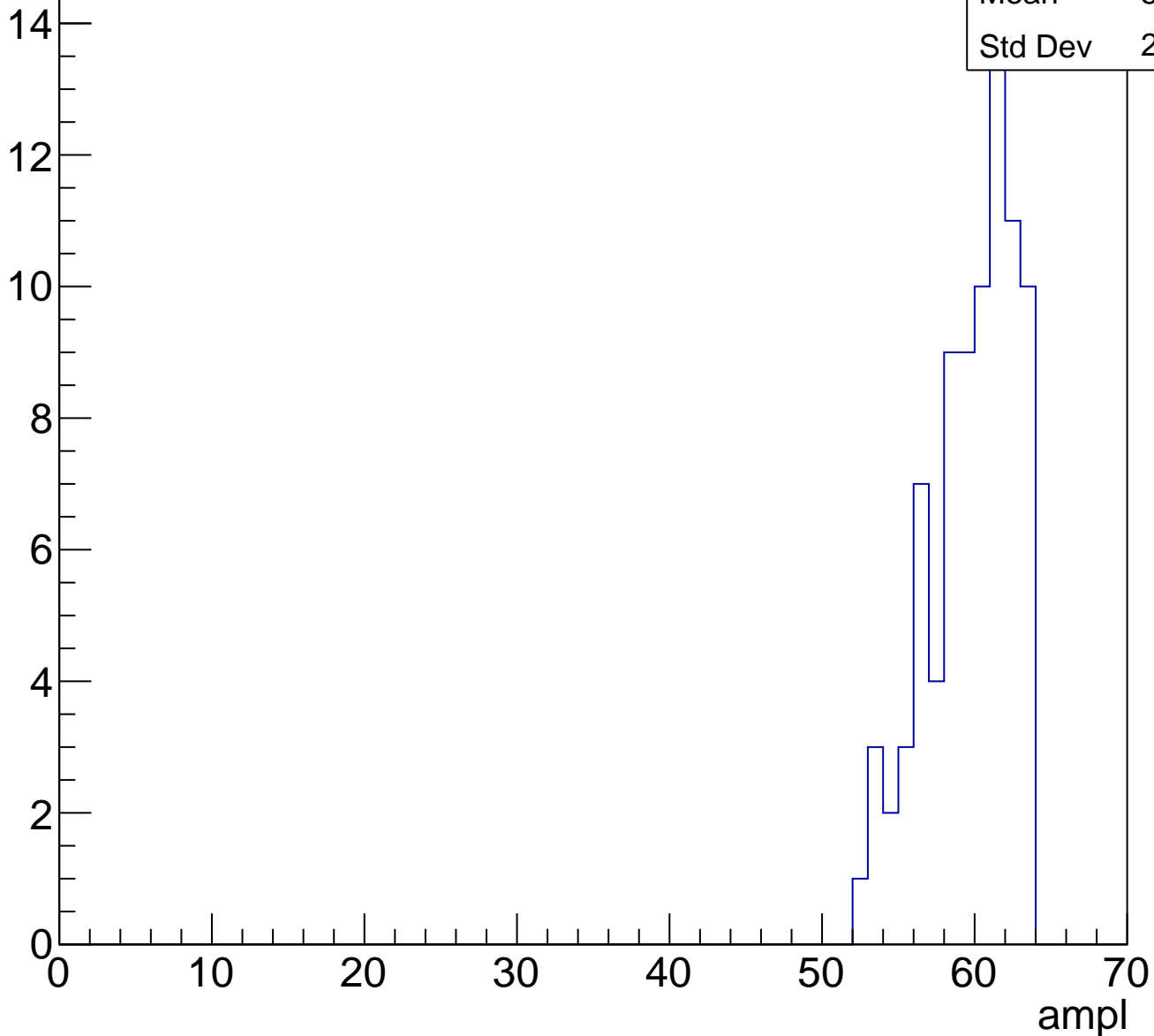
ampl



B0L101S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entry

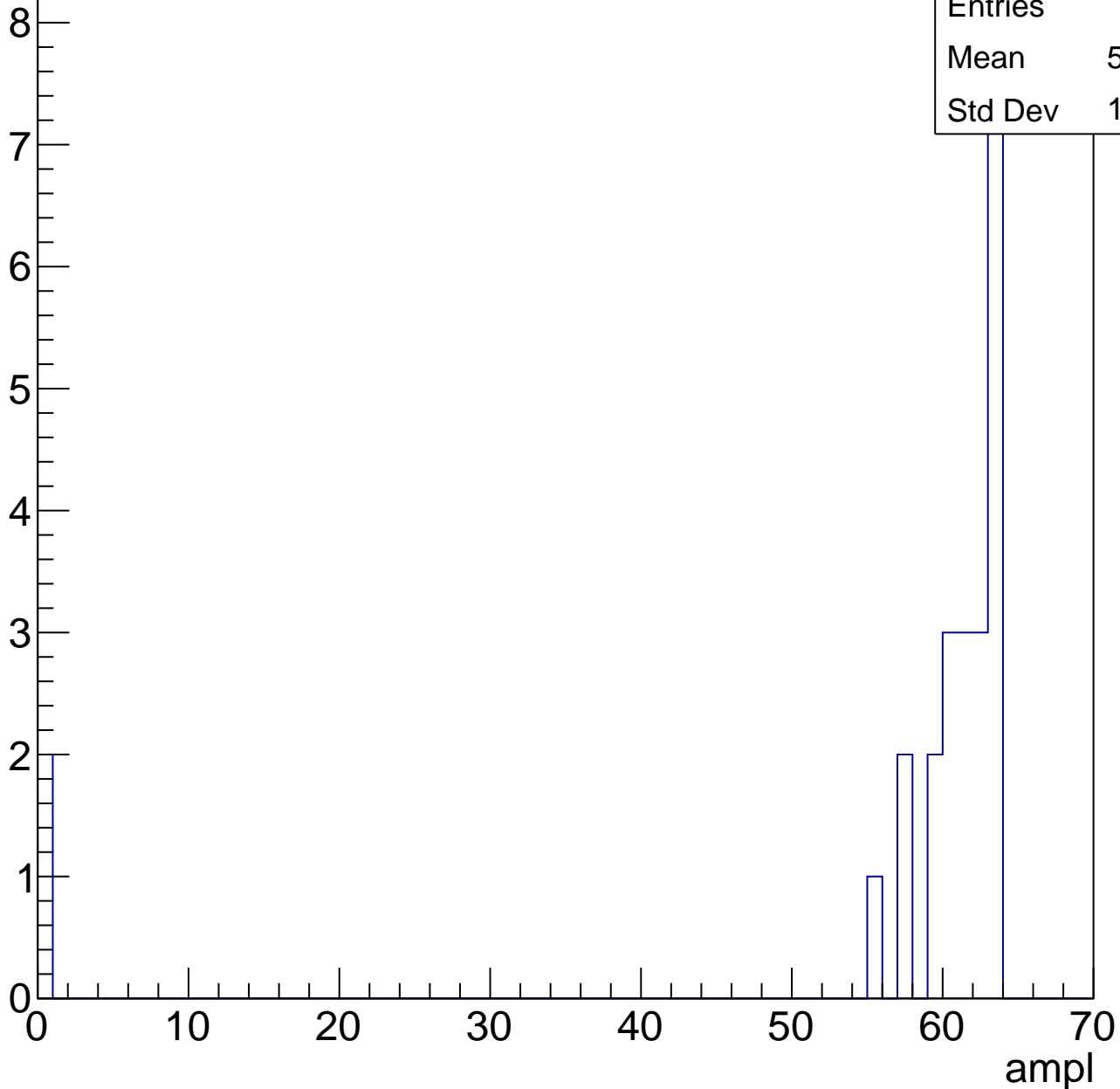


B0L101S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

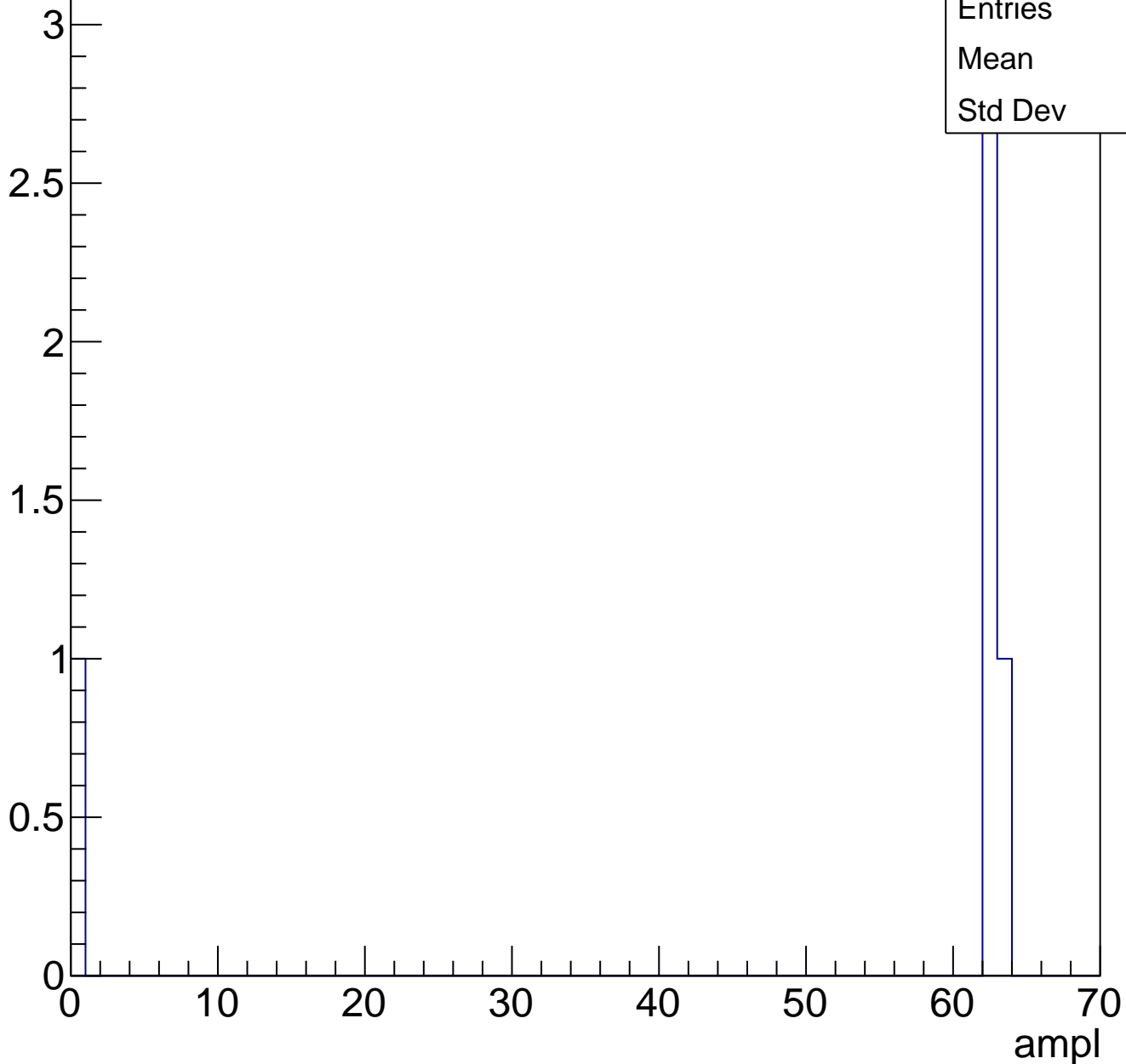
Entries	24
Mean	55.83
Std Dev	16.97



B0L101S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch115, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	145
Mean	28.76
Std Dev	5.136

Gaus mean : 30.0155

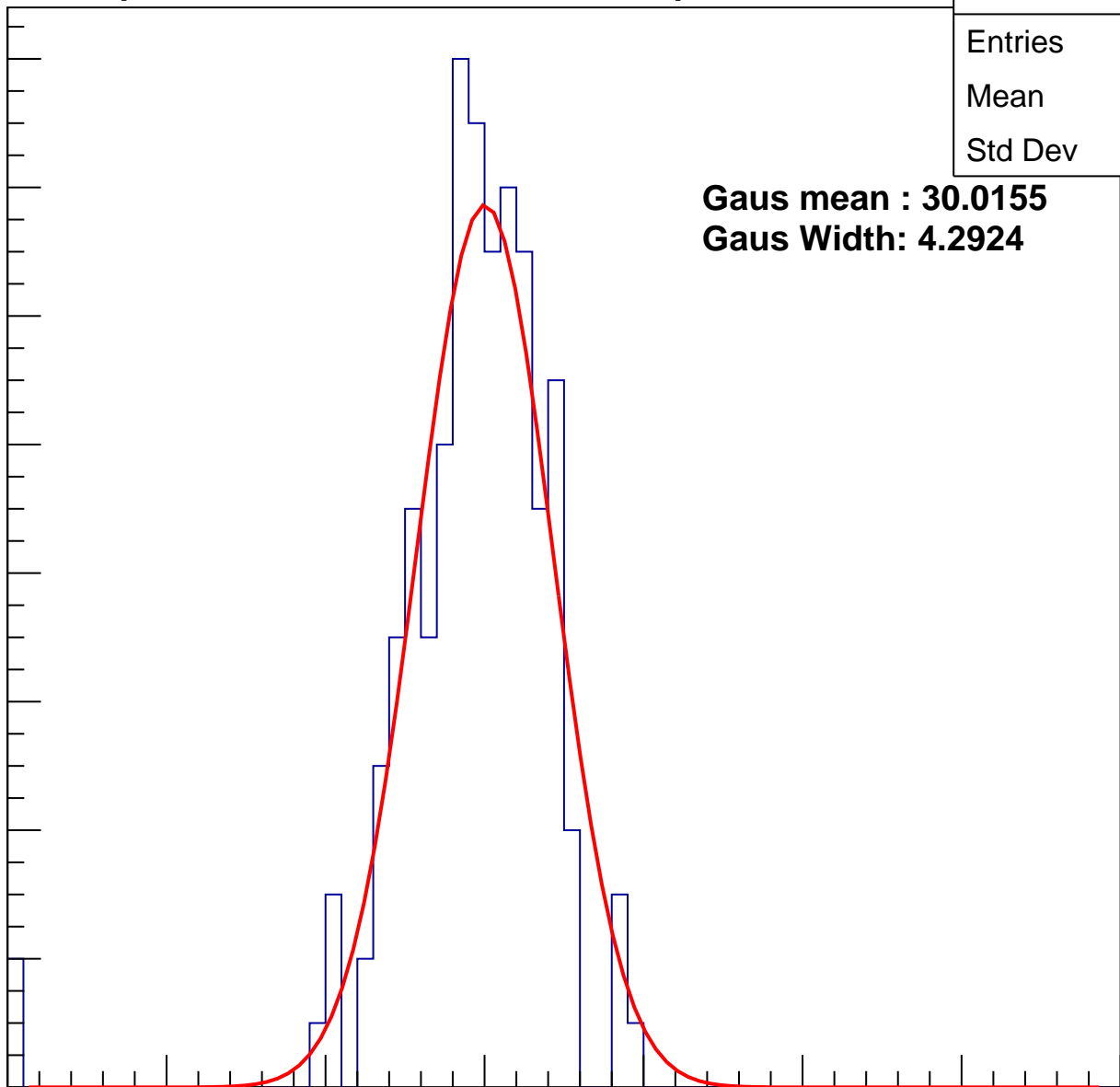
Gaus Width: 4.2924

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch115, adc1

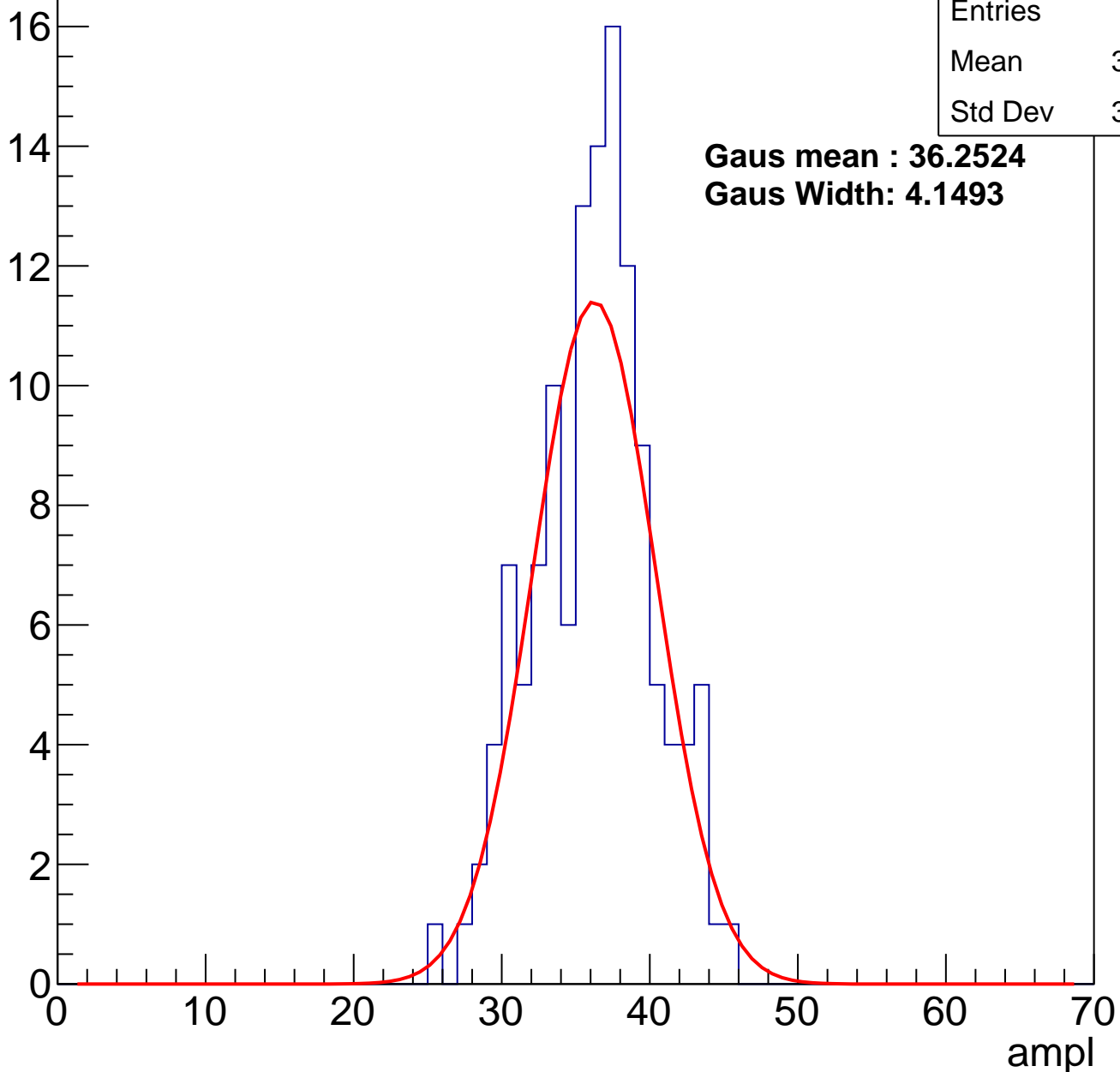
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	35.76
Std Dev	3.962

Gaus mean : 36.2524

Gaus Width: 4.1493

Entry



B0L101S, U15-ch115, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

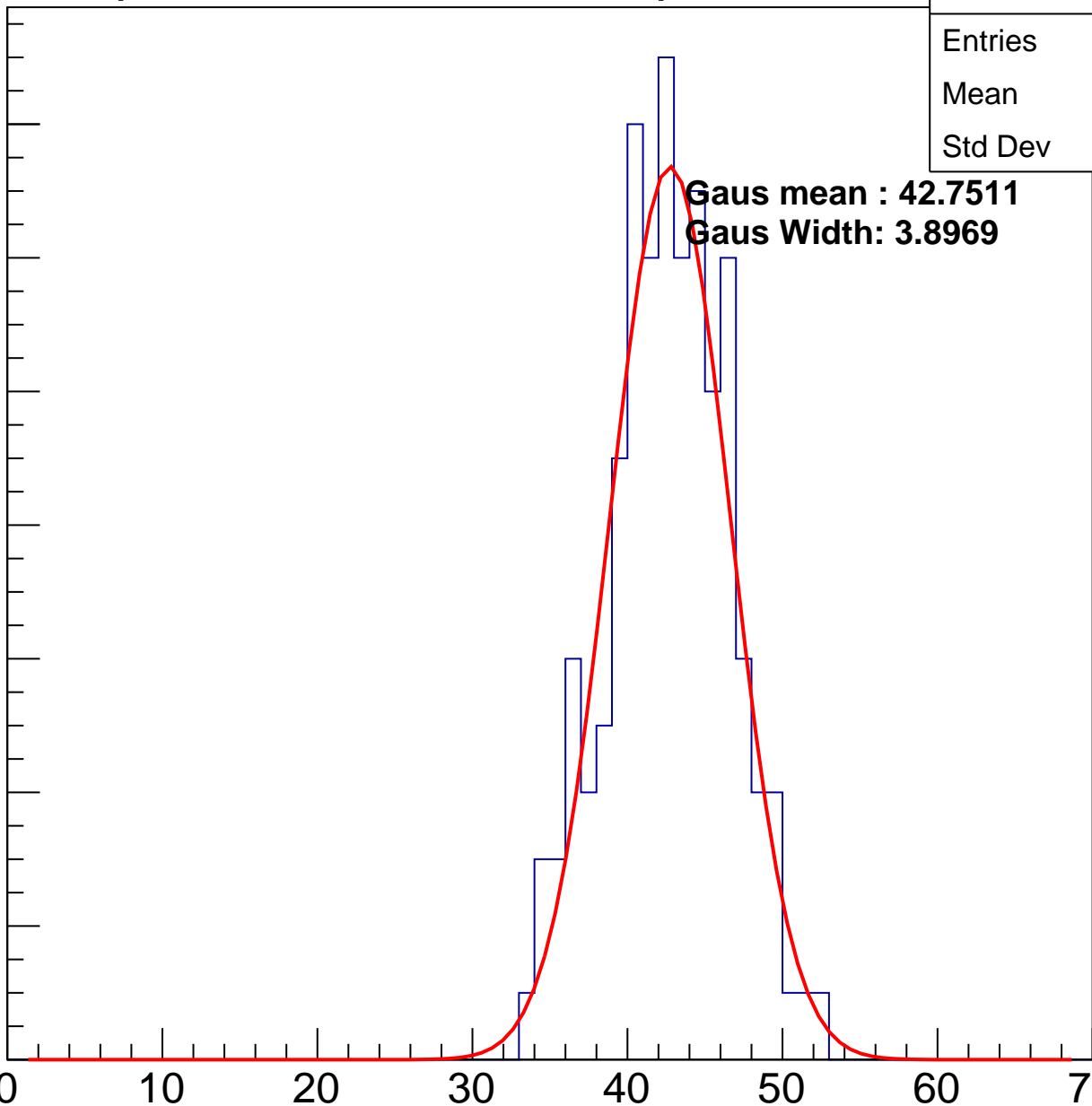
14
12
10
8
6
4
2
0

Entries	136
Mean	42.21
Std Dev	3.873

Gaus mean : 42.7511

Gaus Width: 3.8969

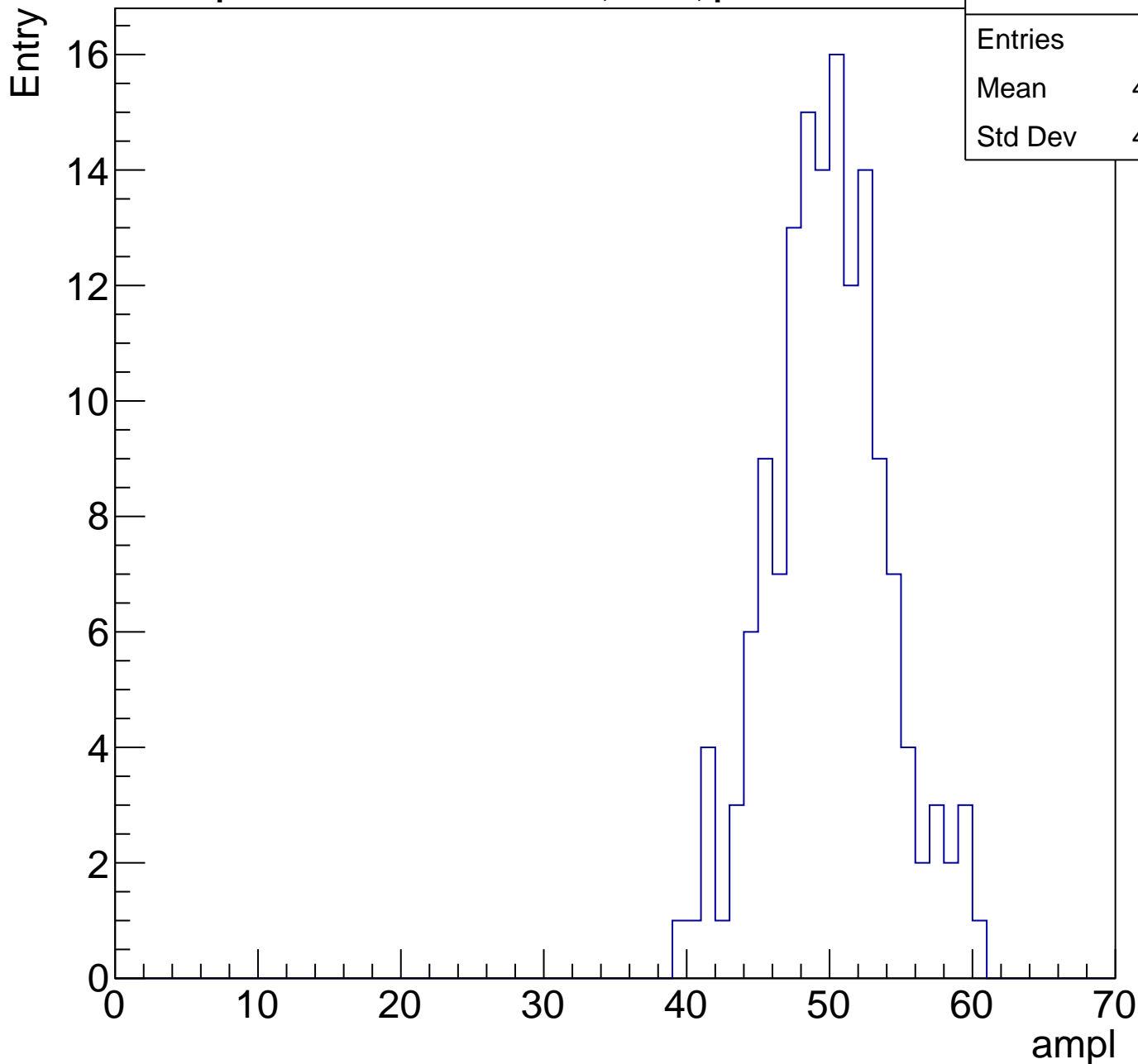
ampl



B0L101S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	147
Mean	49.48
Std Dev	4.133



B0L101S, U15-ch115, adc4

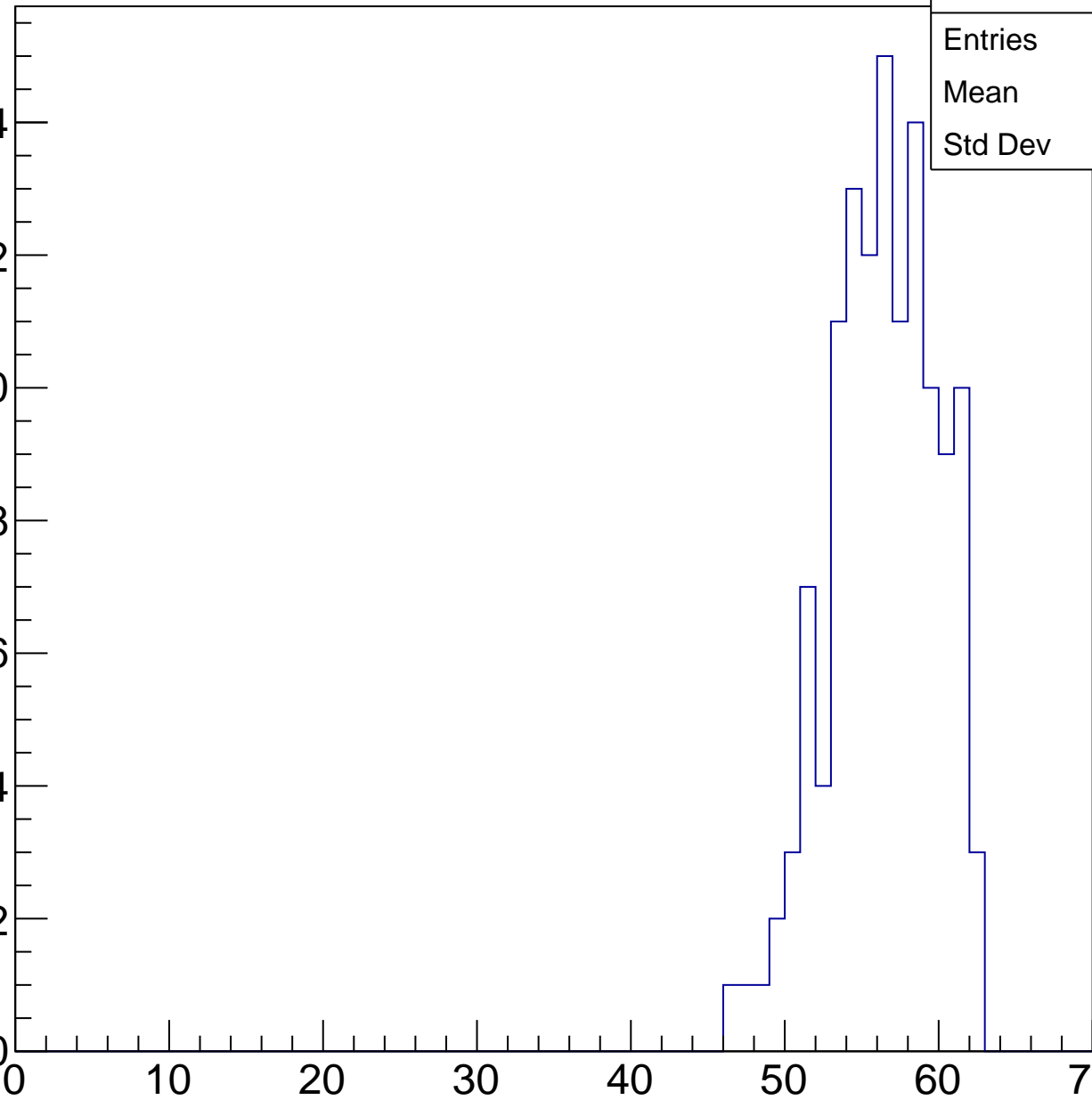
calib_packv5_042523_0143.root, FC#1, port C1

Entries	127
Mean	55.94
Std Dev	3.45

Entry

14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch115, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	63
Mean	59.11
Std Dev	7.968

Entry

14
12
10
8
6
4
2
0

0

10

20

30

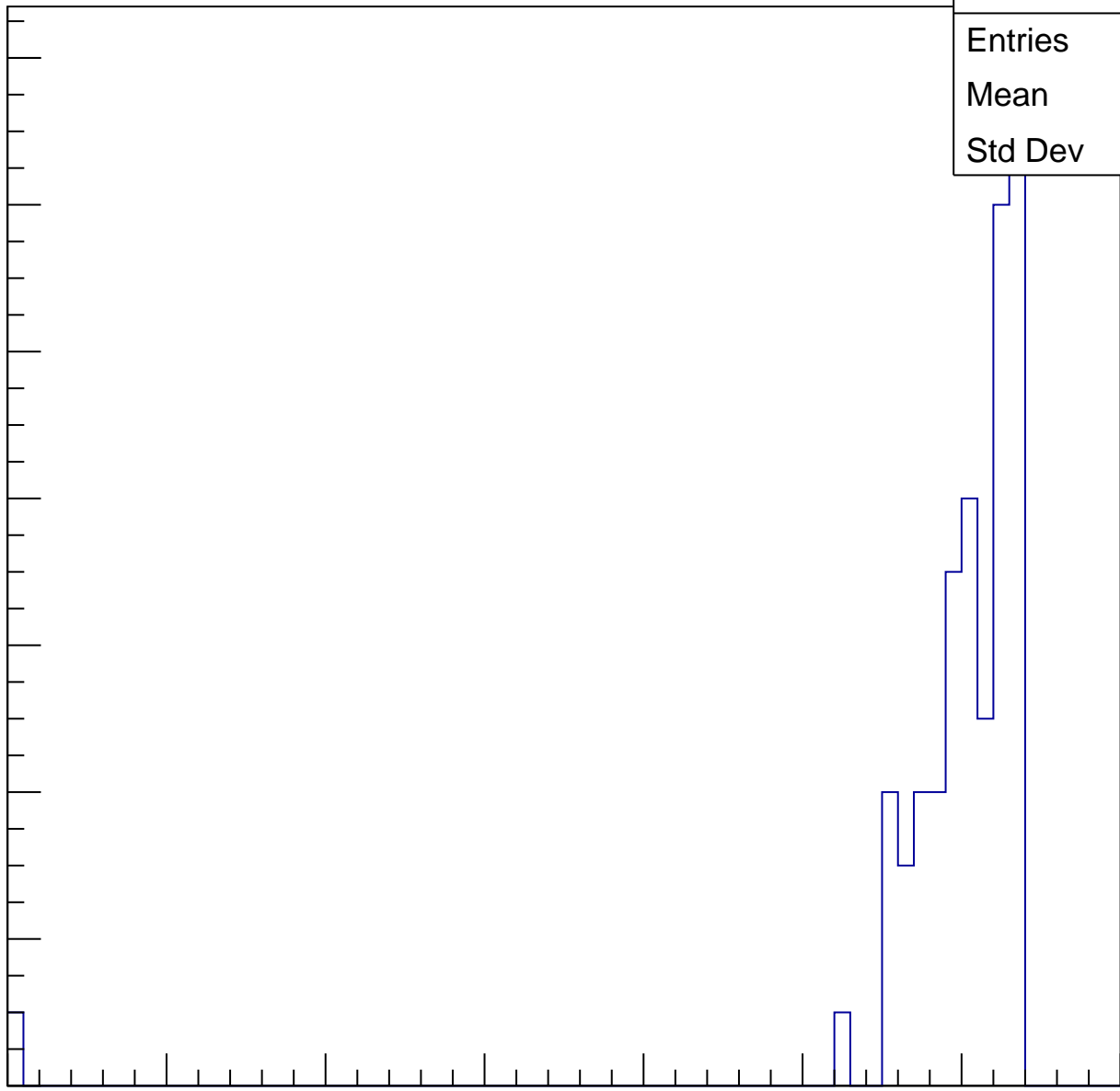
40

50

60

70

ampl

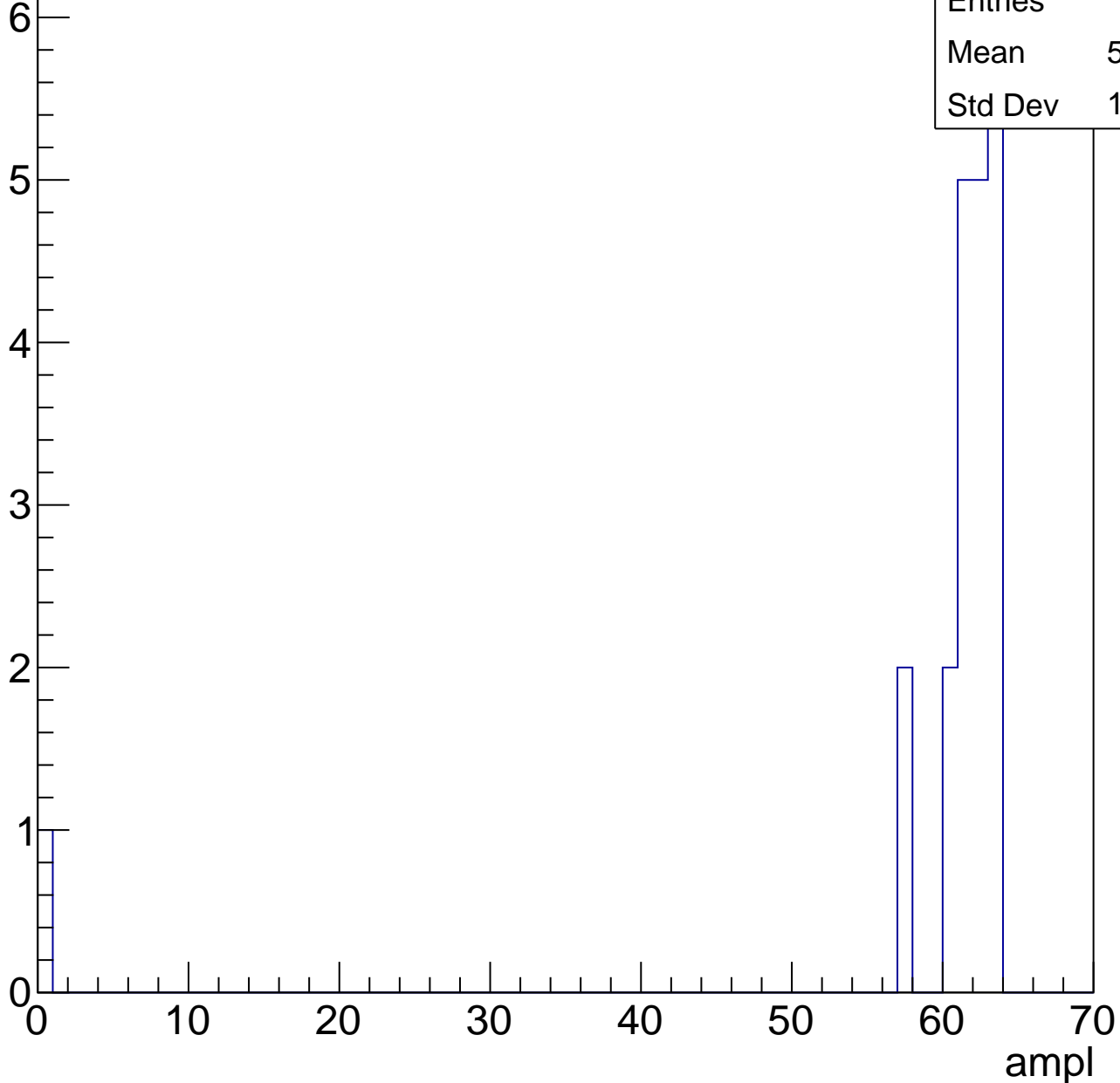


B0L101S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	58.43
Std Dev	13.17



B0L101S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L101S, U15-ch116, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	29.87
Std Dev	5.751

Gaus mean : 29.6579

Gaus Width: 3.8237

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch116, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	142
Mean	34.17
Std Dev	5.273

Gaus mean : 35.5952

Gaus Width: 4.1978

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

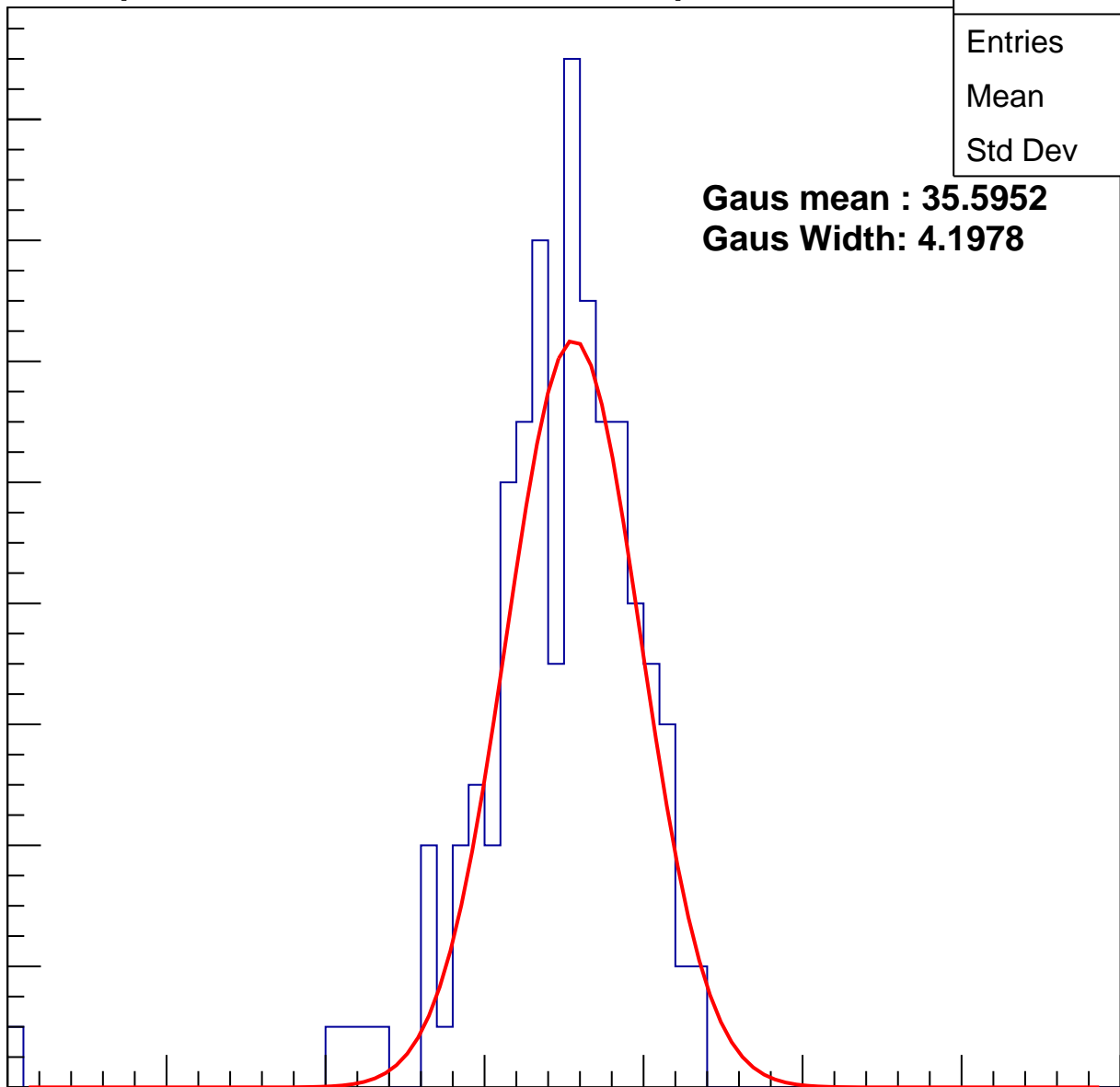
40

50

60

70

ampl



B0L101S, U15-ch116, adc2

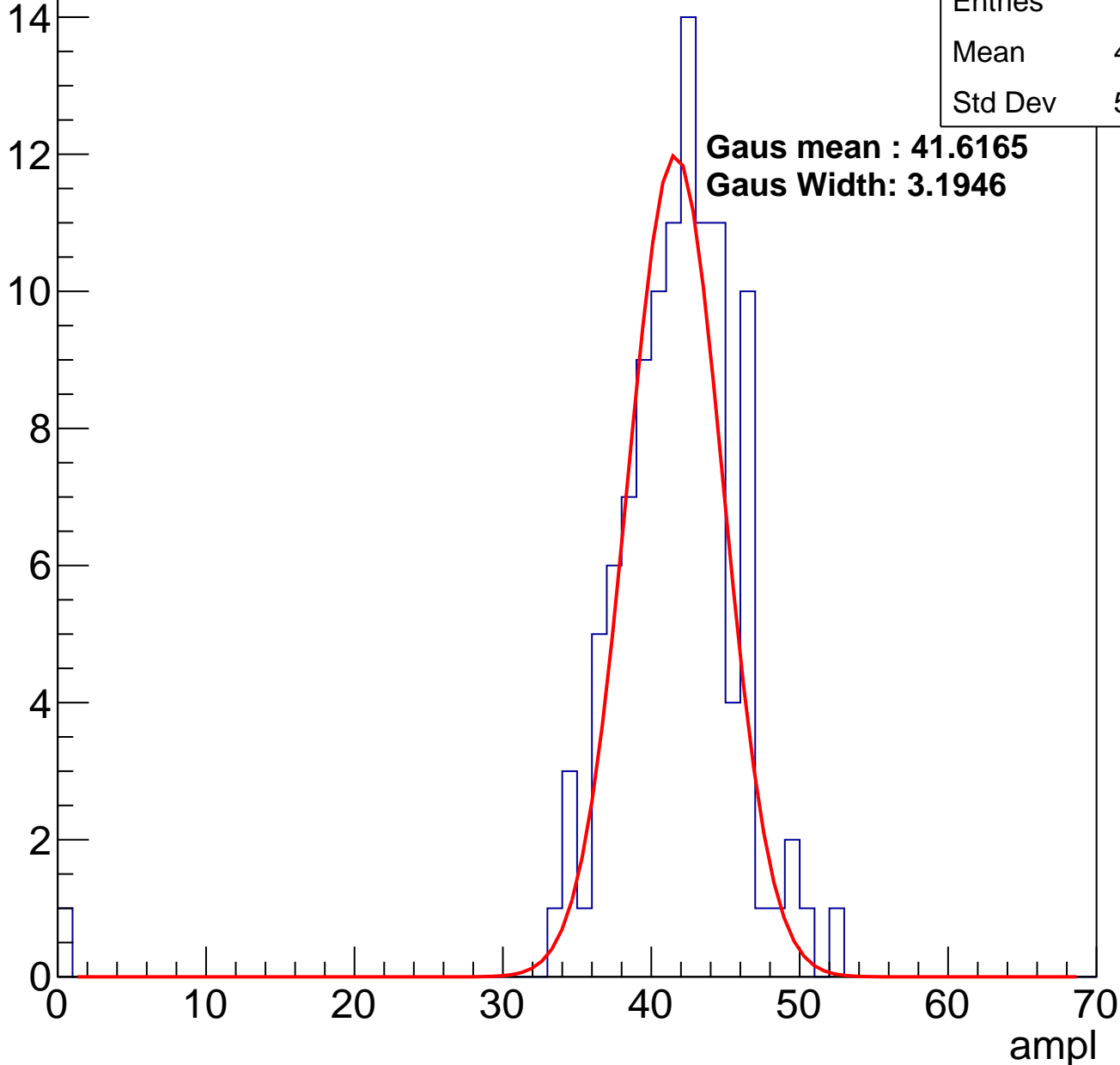
calib_packv5_042523_0143.root, FC#1, port C1

Entries	110
Mean	41.09
Std Dev	5.345

Gaus mean : 41.6165

Gaus Width: 3.1946

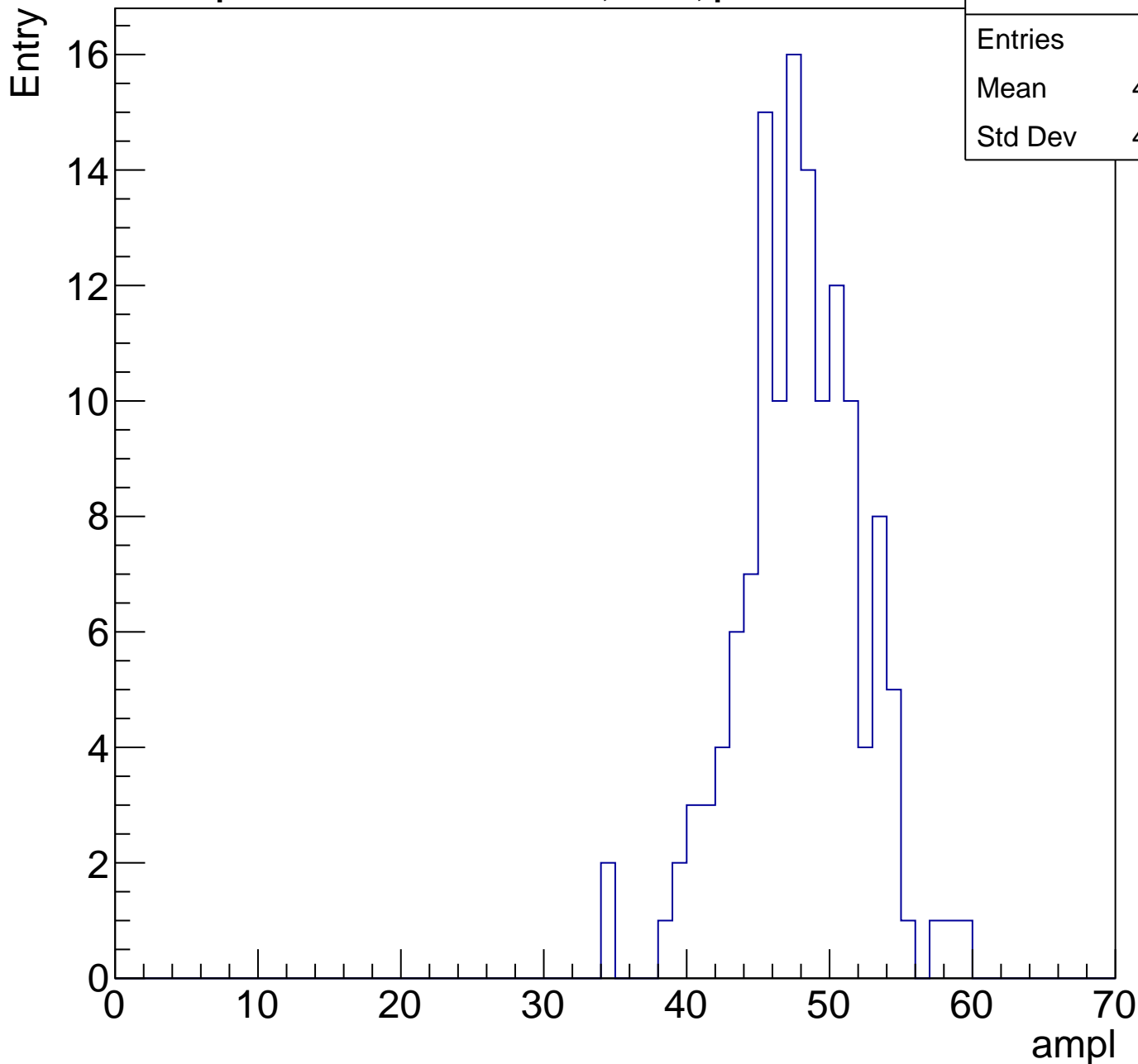
Entry



B0L101S, U15-ch116, adc3

calib_packv5_042523_0143.root, FC#1, port C1

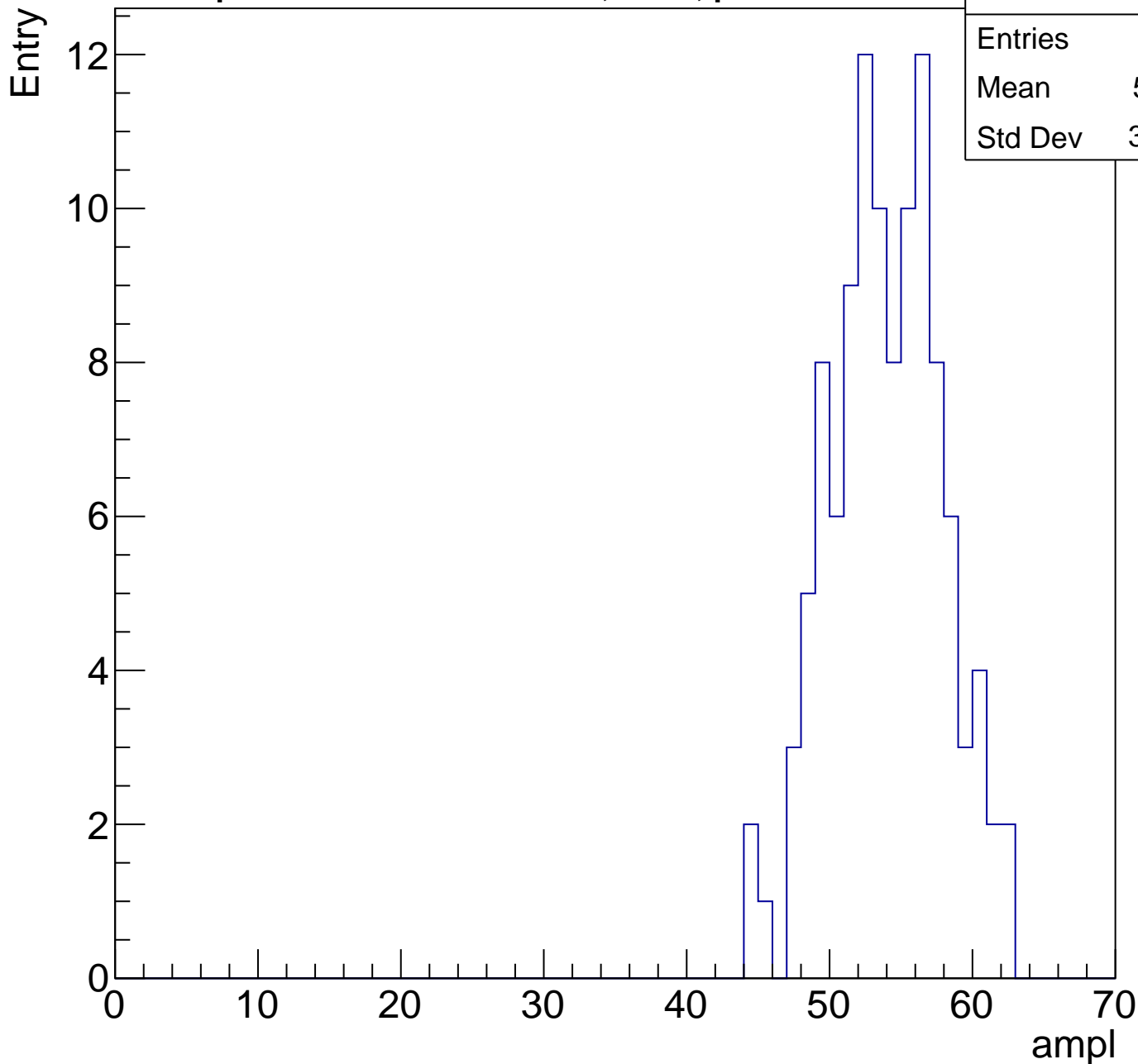
Entries	136
Mean	47.43
Std Dev	4.278



B0L101S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	111
Mean	53.51
Std Dev	3.899



B0L101S, U15-ch116, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	57.46
Std Dev	8.323

Entry

12

10

8

6

4

2

0

0

10

20

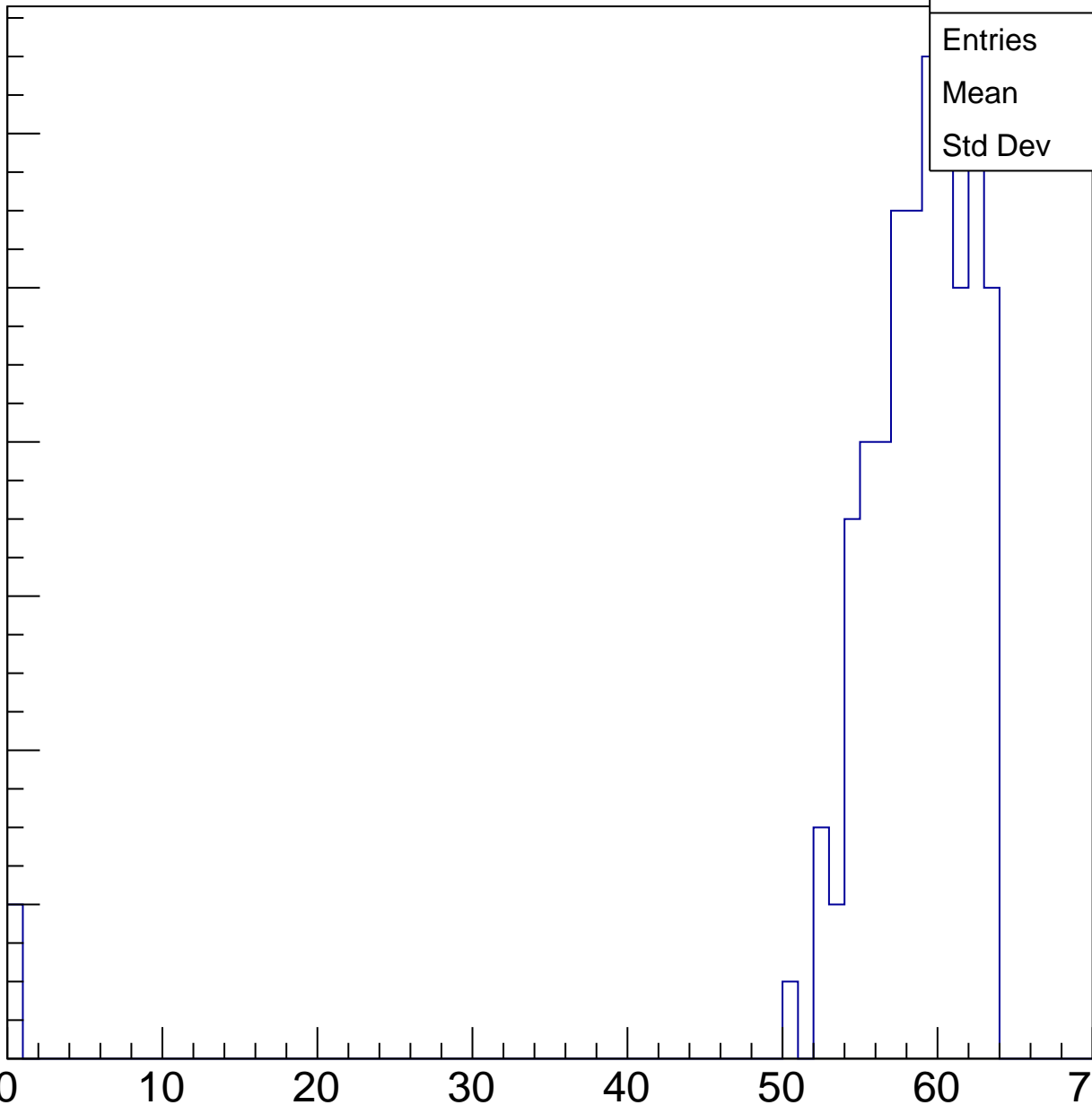
30

40

50

60

ampl

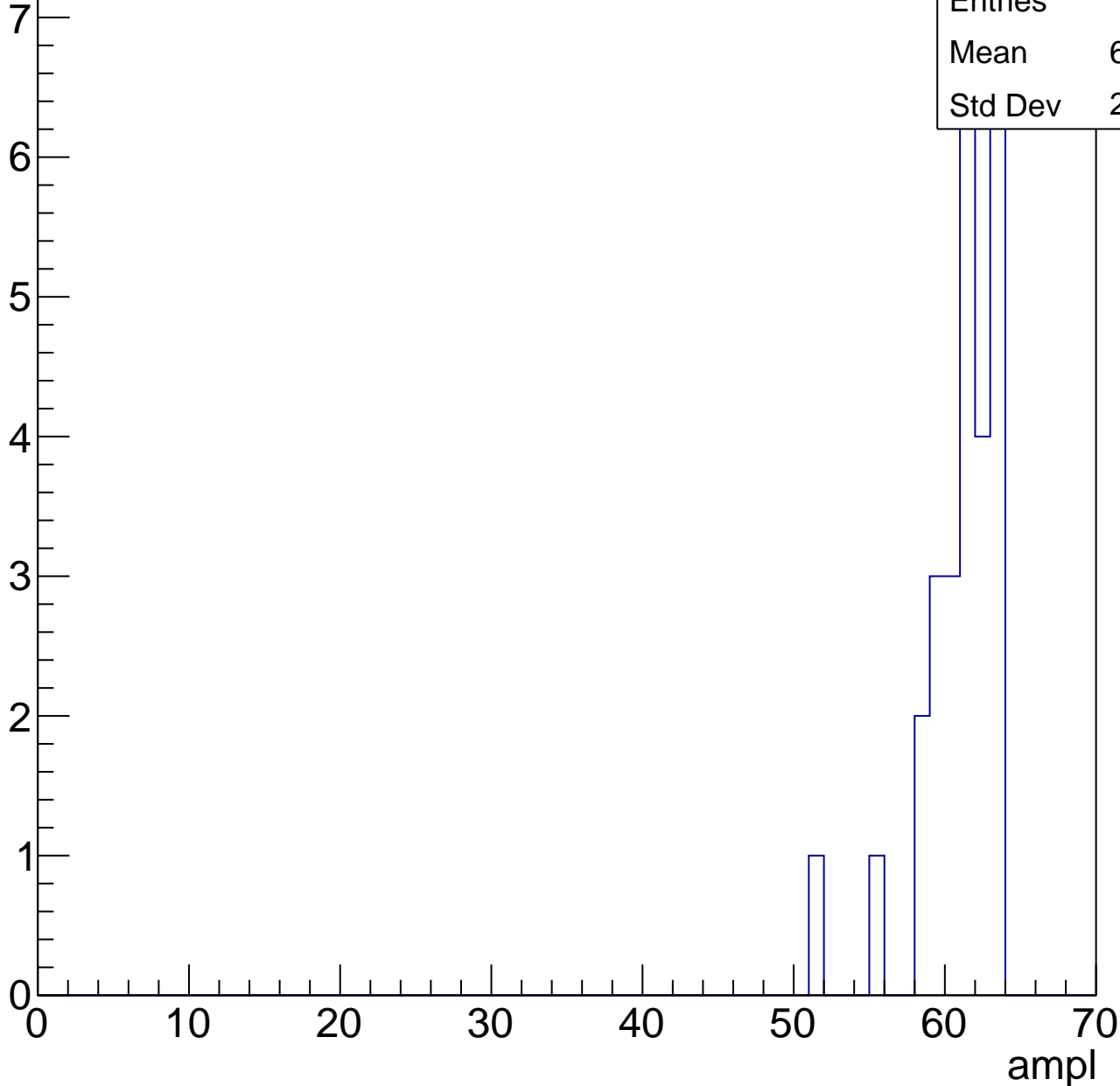


B0L101S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

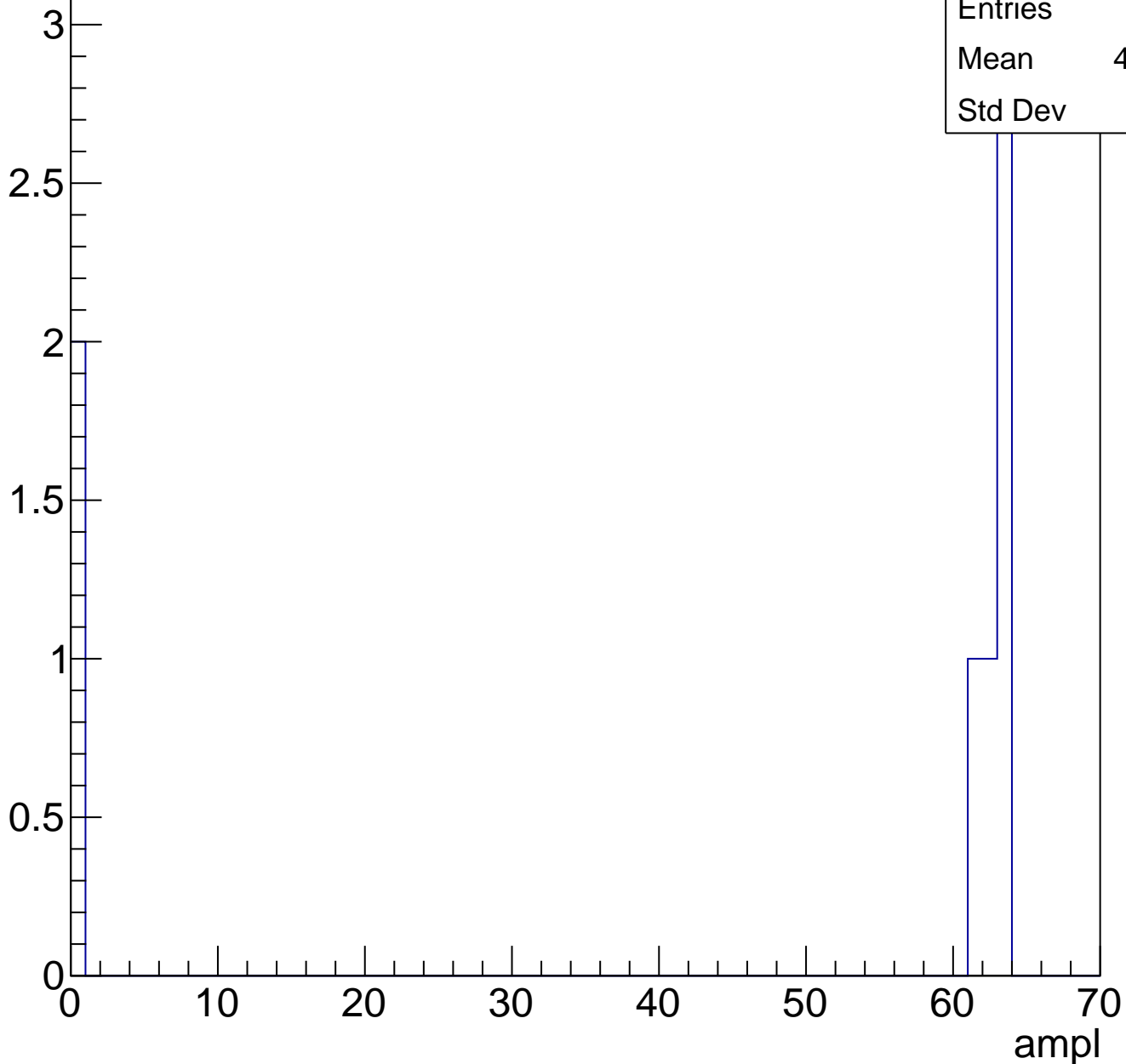
Entries	28
Mean	60.54
Std Dev	2.639



B0L101S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch117, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	135
Mean	27.95
Std Dev	4.499

Gaus mean : 28.5023

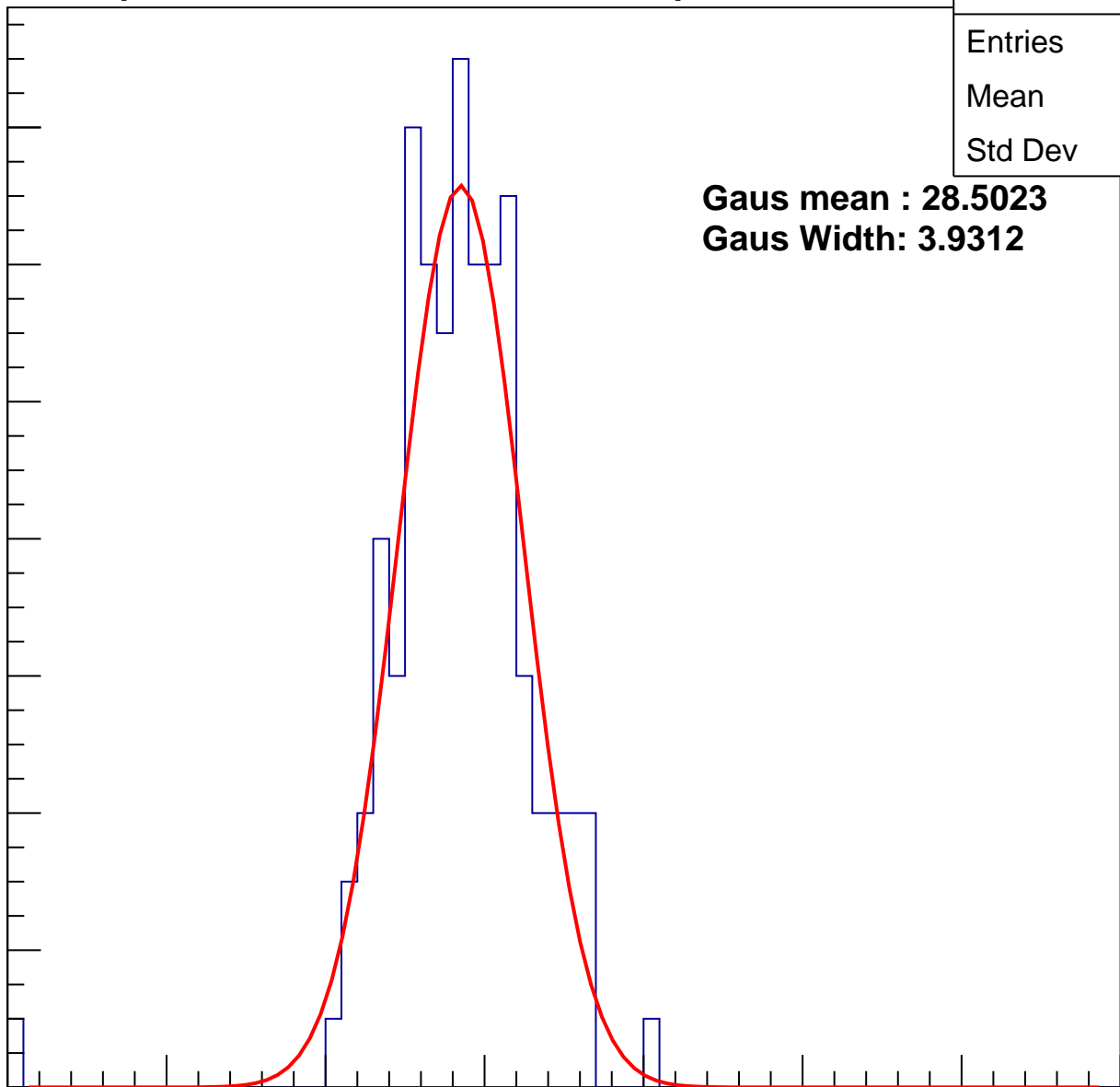
Gaus Width: 3.9312

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch117, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	140
Mean	34.69
Std Dev	3.734

Gaus mean : 35.1167

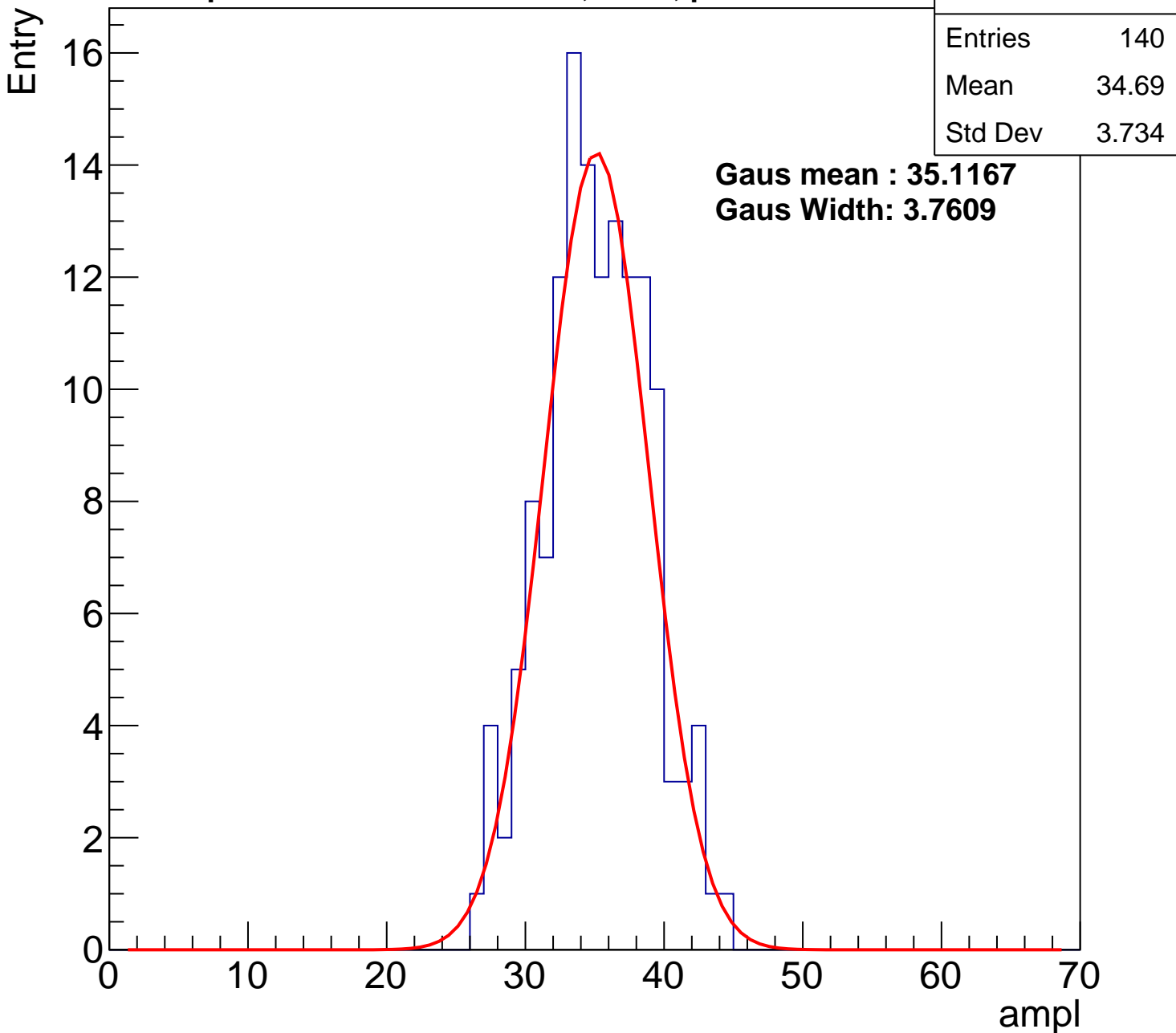
Gaus Width: 3.7609

Entry

16
14
12
10
8
6
4
2
0

ampl

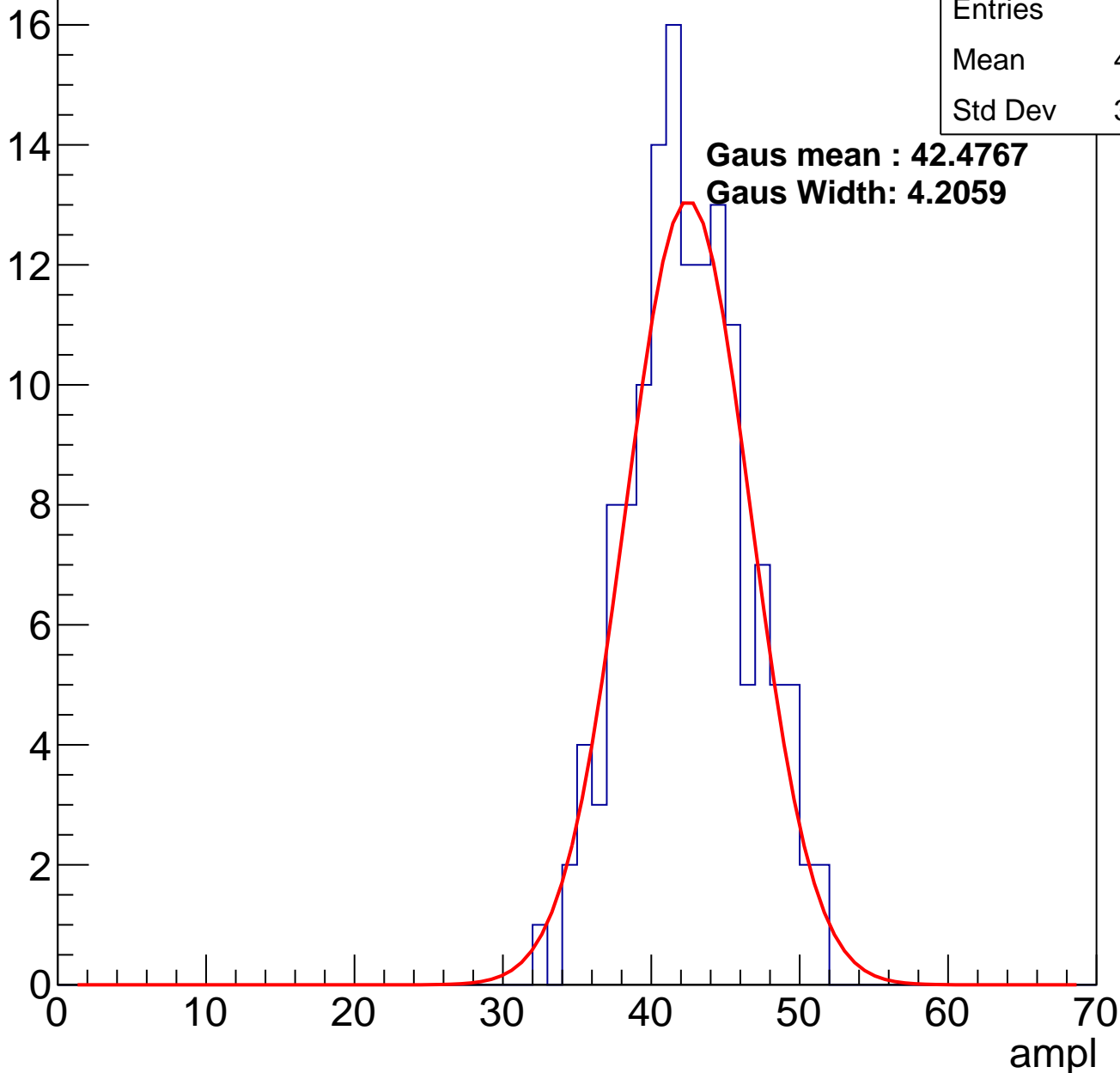
0 10 20 30 40 50 60 70



B0L101S, U15-ch117, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch117, adc3

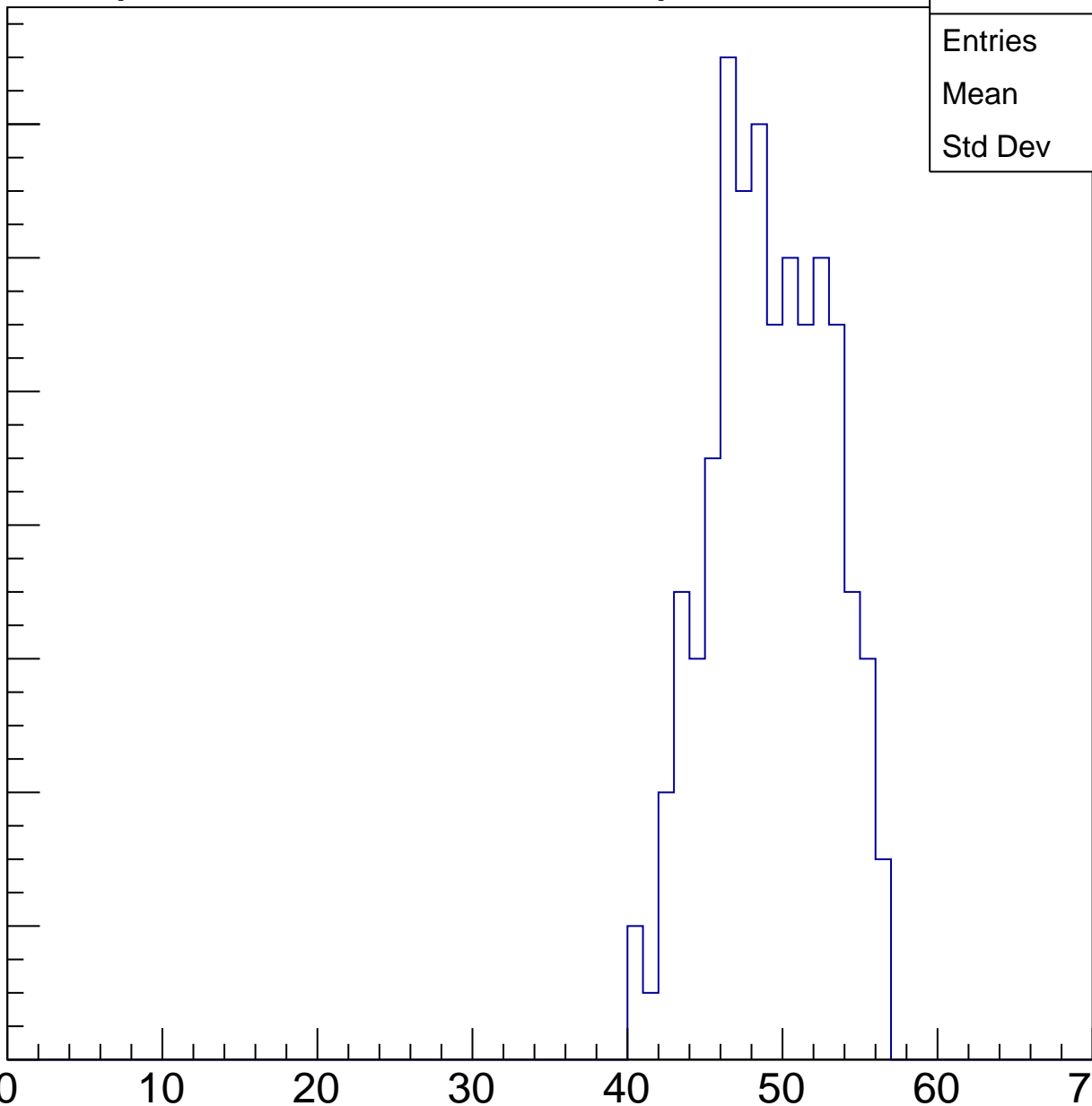
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	144
Mean	48.72
Std Dev	3.76

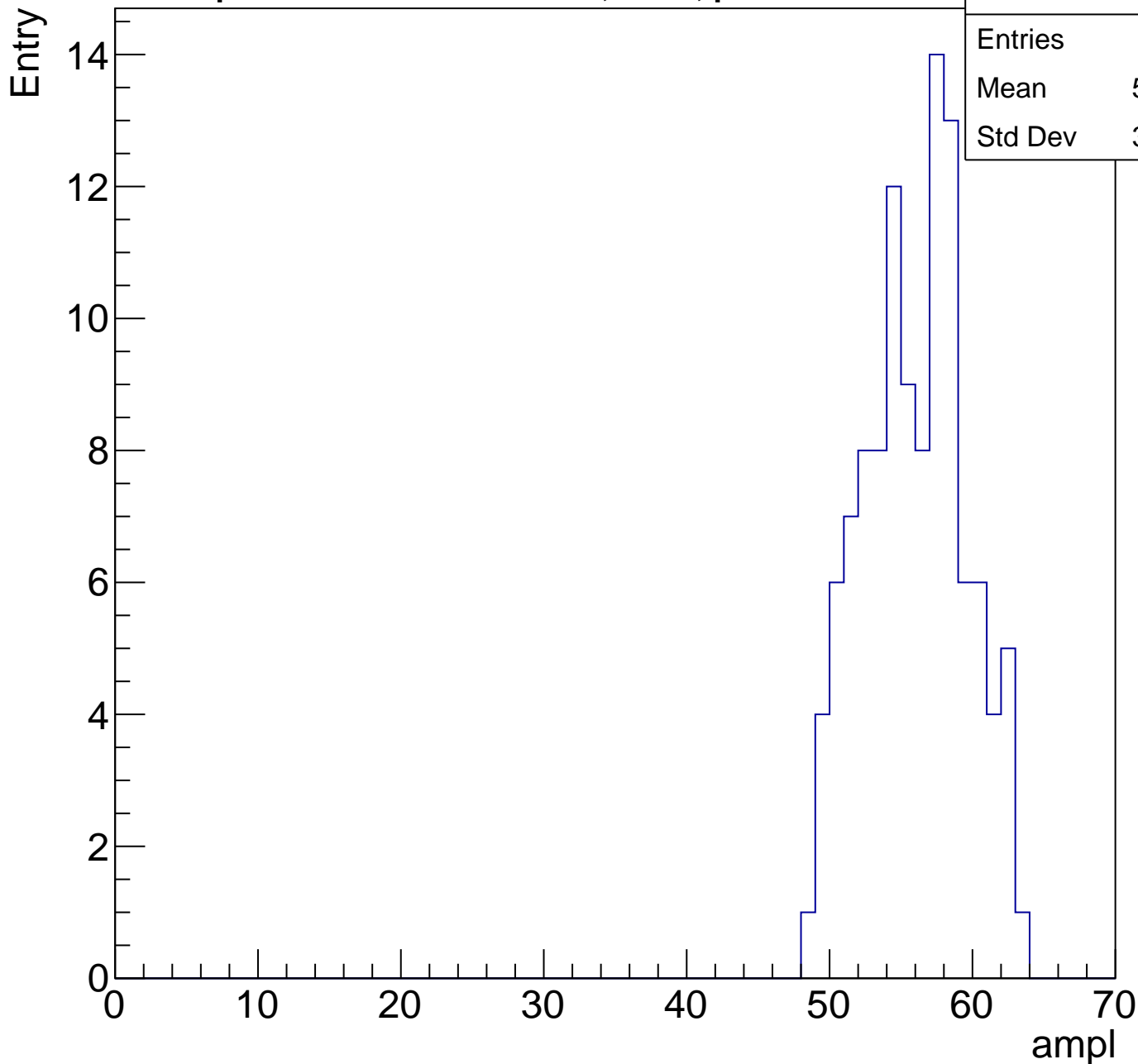
ampl



B0L101S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#1, port C1

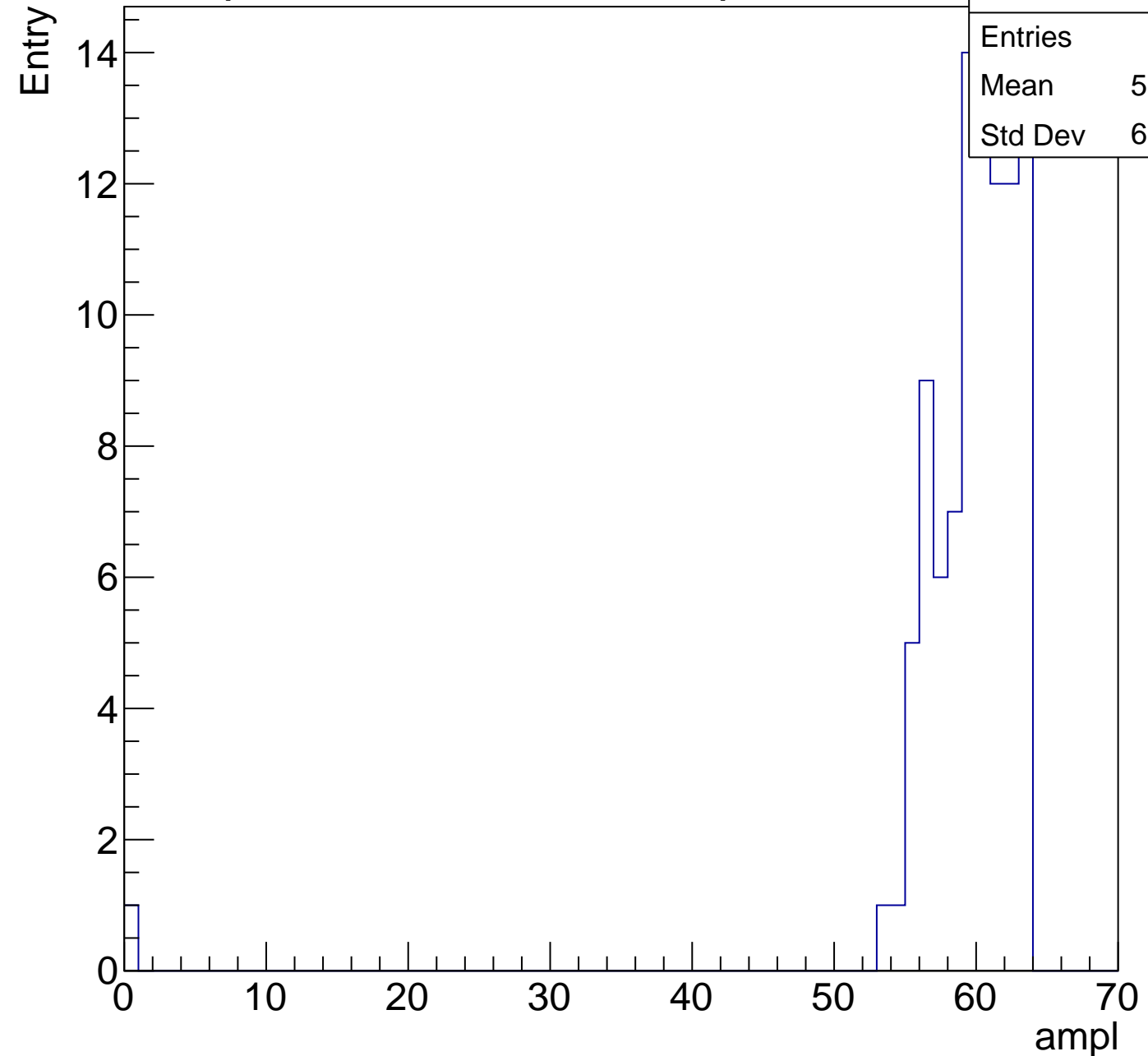
Entries	112
Mean	55.49
Std Dev	3.566



B0L101S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	96
Mean	58.94
Std Dev	6.557

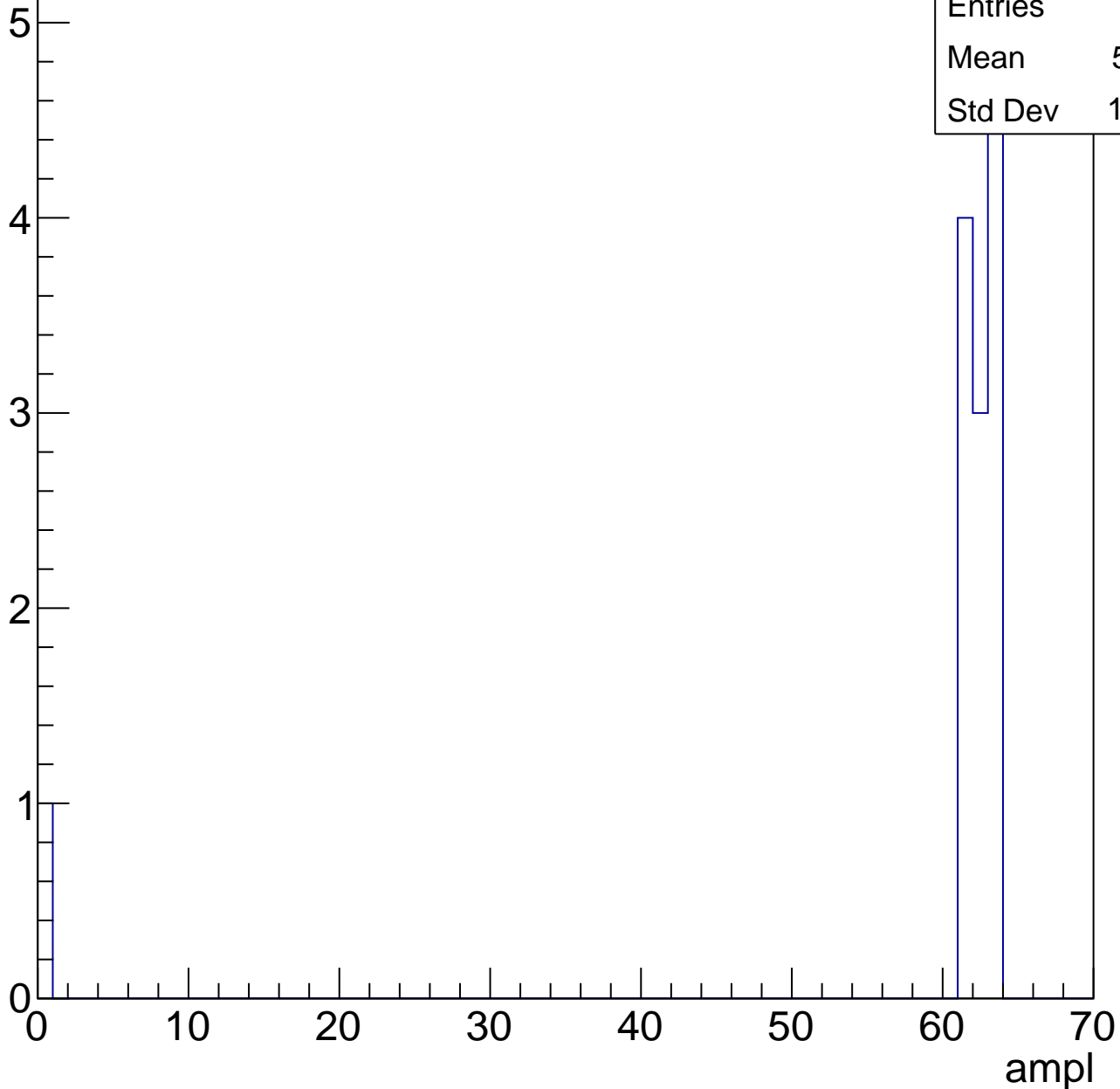


B0L101S, U15-ch117, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	13
Mean	57.31
Std Dev	16.56



B0L101S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

B0L101S, U15-ch118, adc0

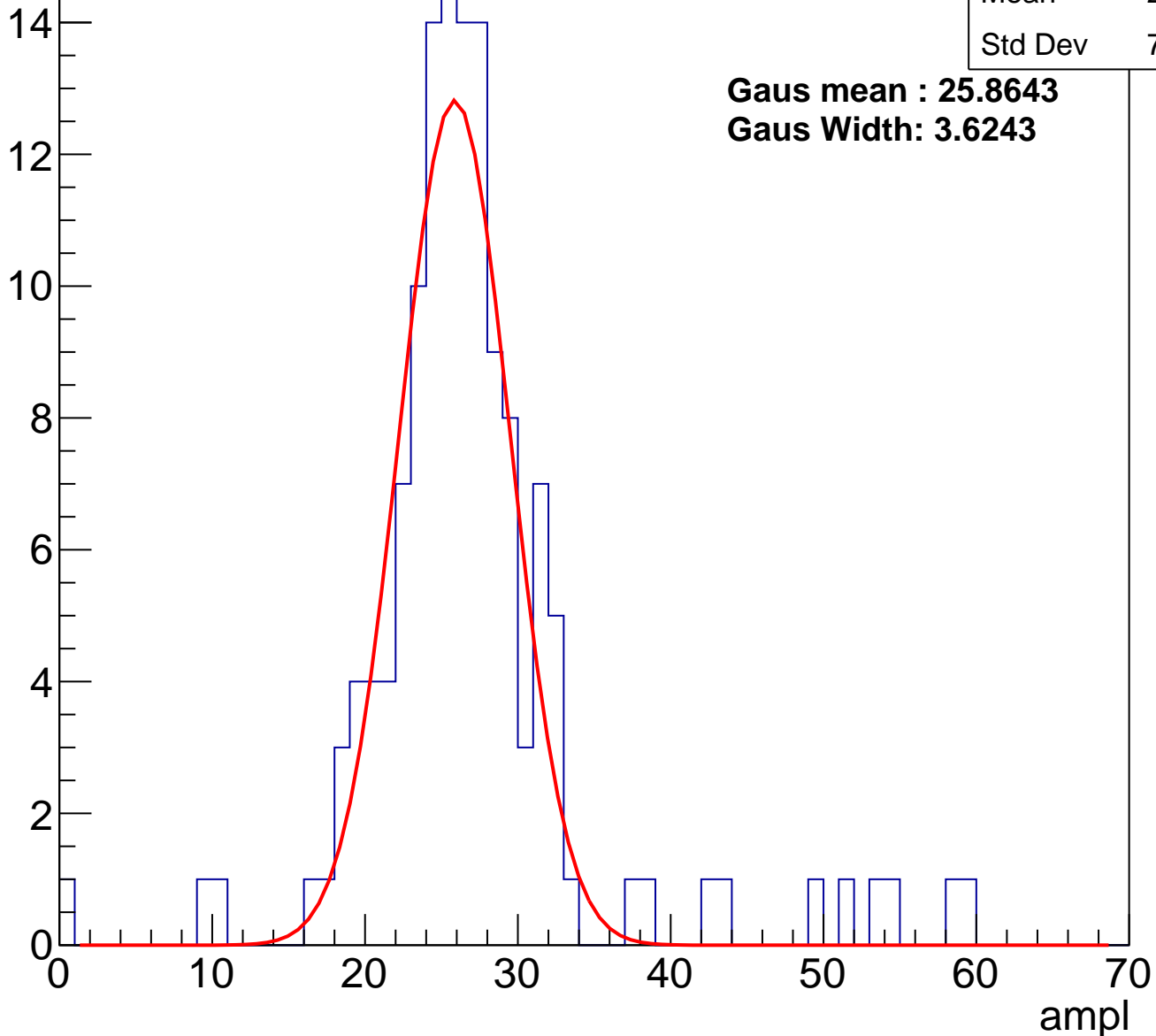
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	26.65
Std Dev	7.836

Gaus mean : 25.8643

Gaus Width: 3.6243

Entry



B0L101S, U15-ch118, adc1

calib_packv5_042523_0143.root, FC#1, port C1

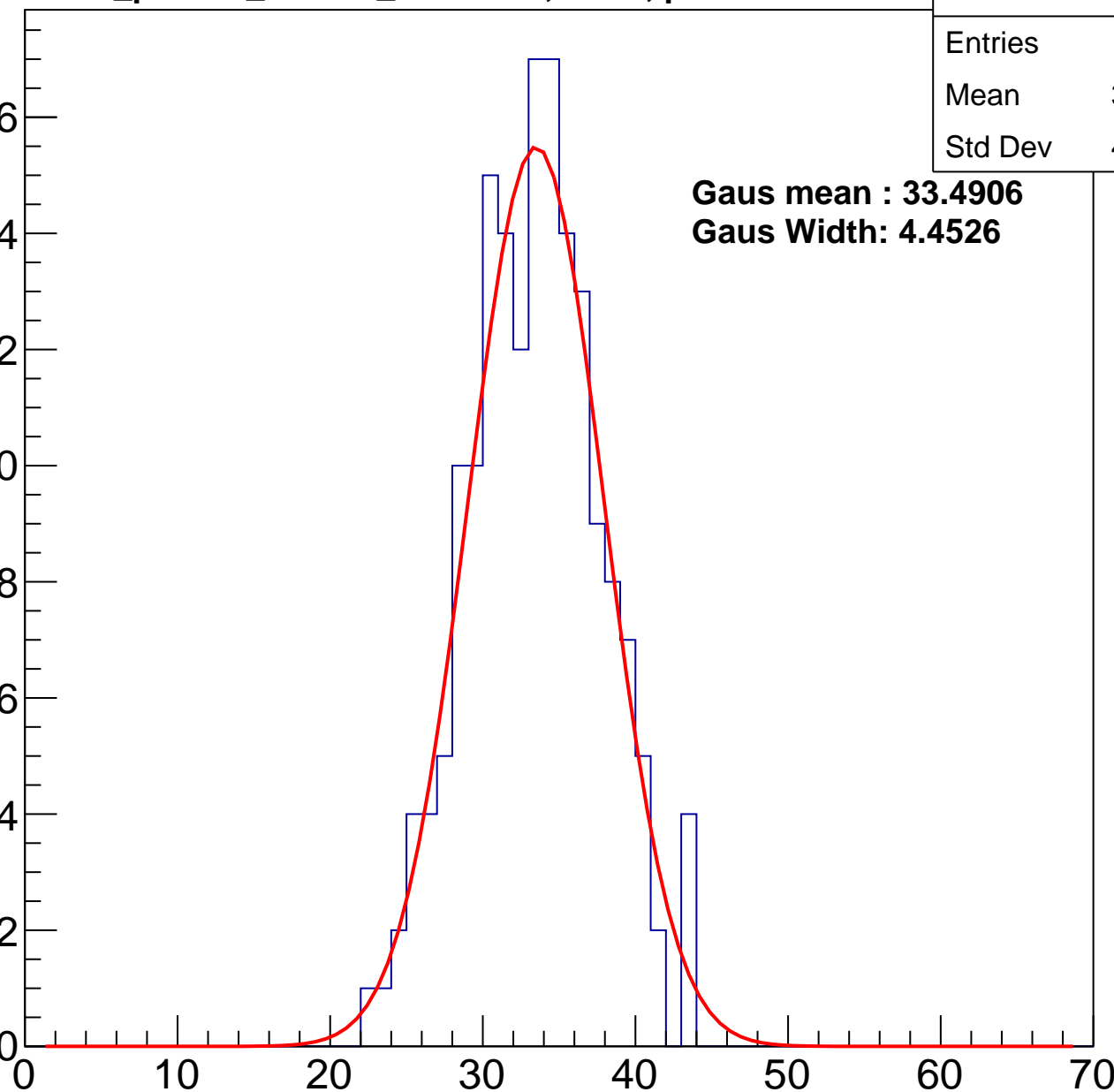
Entry

16
14
12
10
8
6
4
2
0

Entries	174
Mean	32.94
Std Dev	4.279

Gaus mean : 33.4906

Gaus Width: 4.4526



ampl

B0L101S, U15-ch118, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

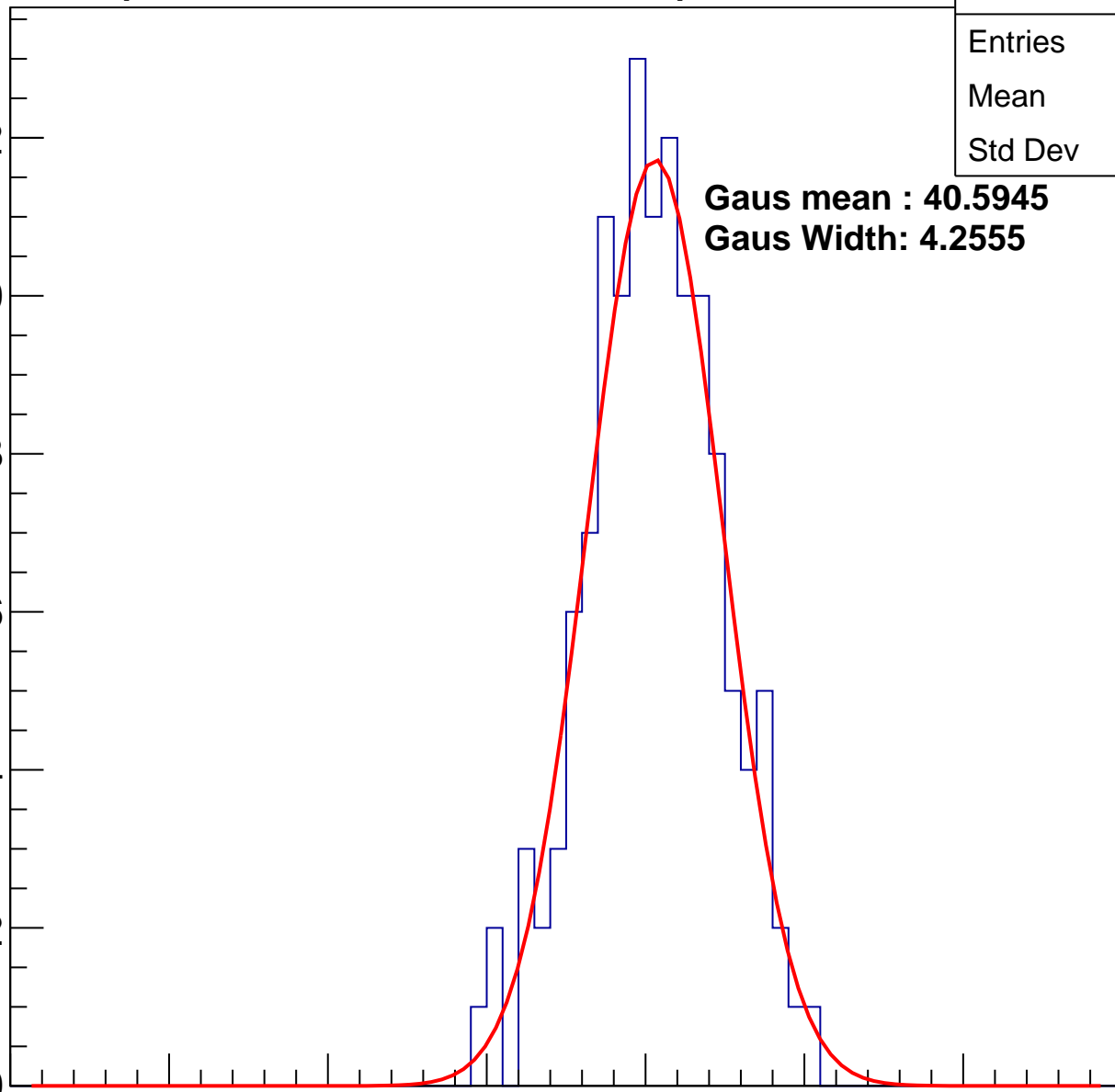
70

ampl

Entries	127
Mean	40.02
Std Dev	4.187

Gaus mean : 40.5945

Gaus Width: 4.2555

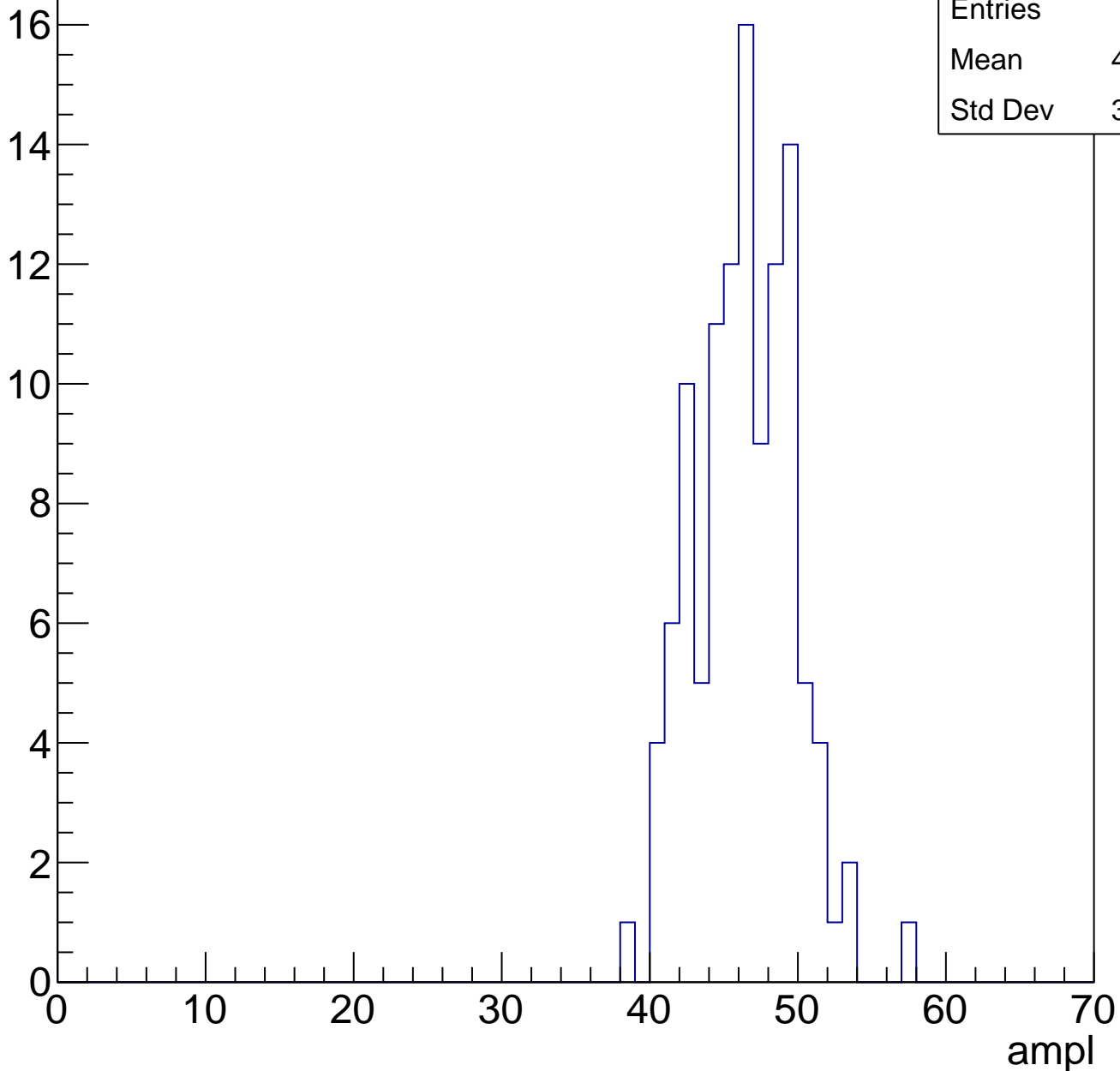


B0L101S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entry

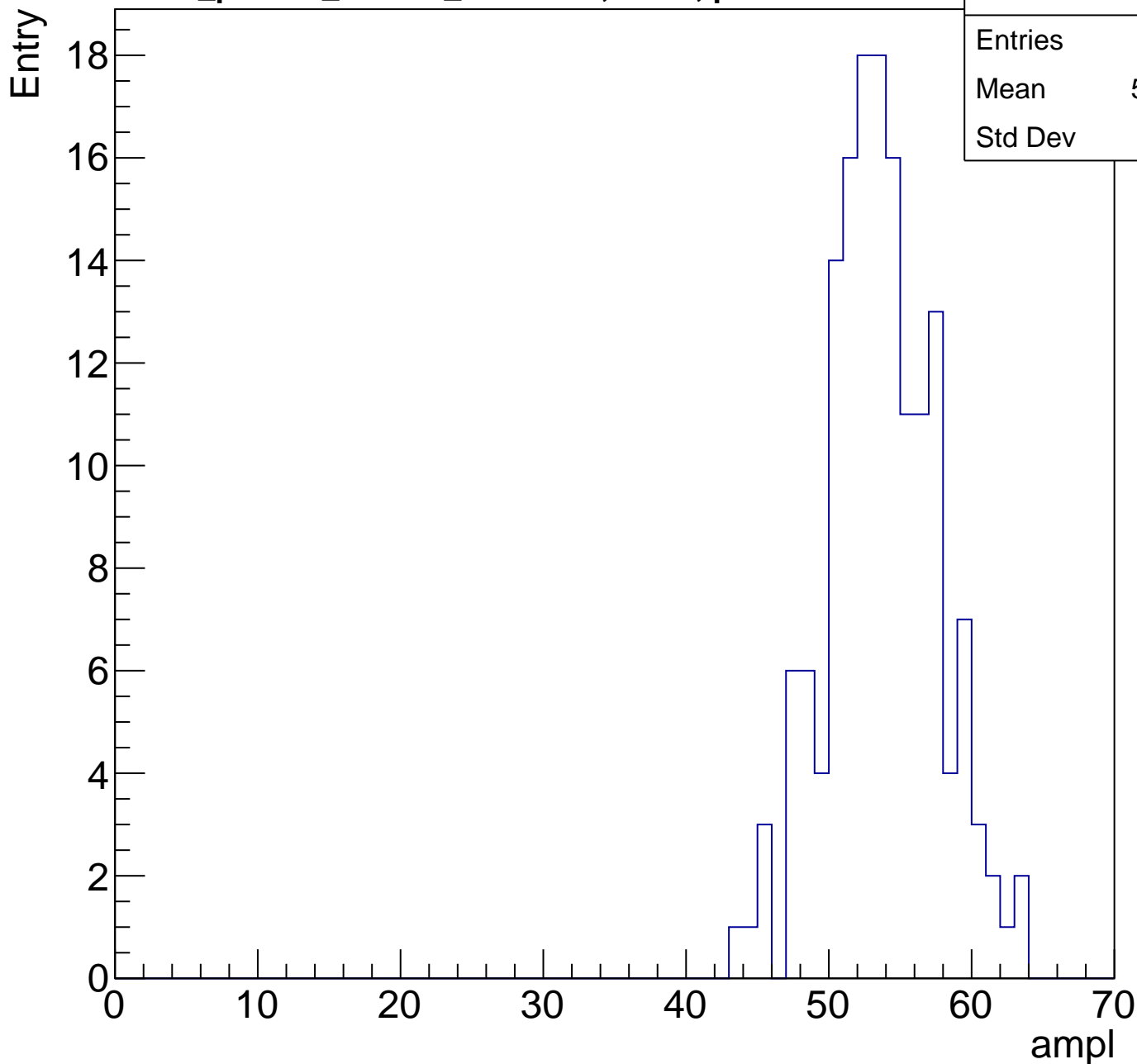
Entries	113
Mean	45.96
Std Dev	3.308



B0L101S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#1, port C1

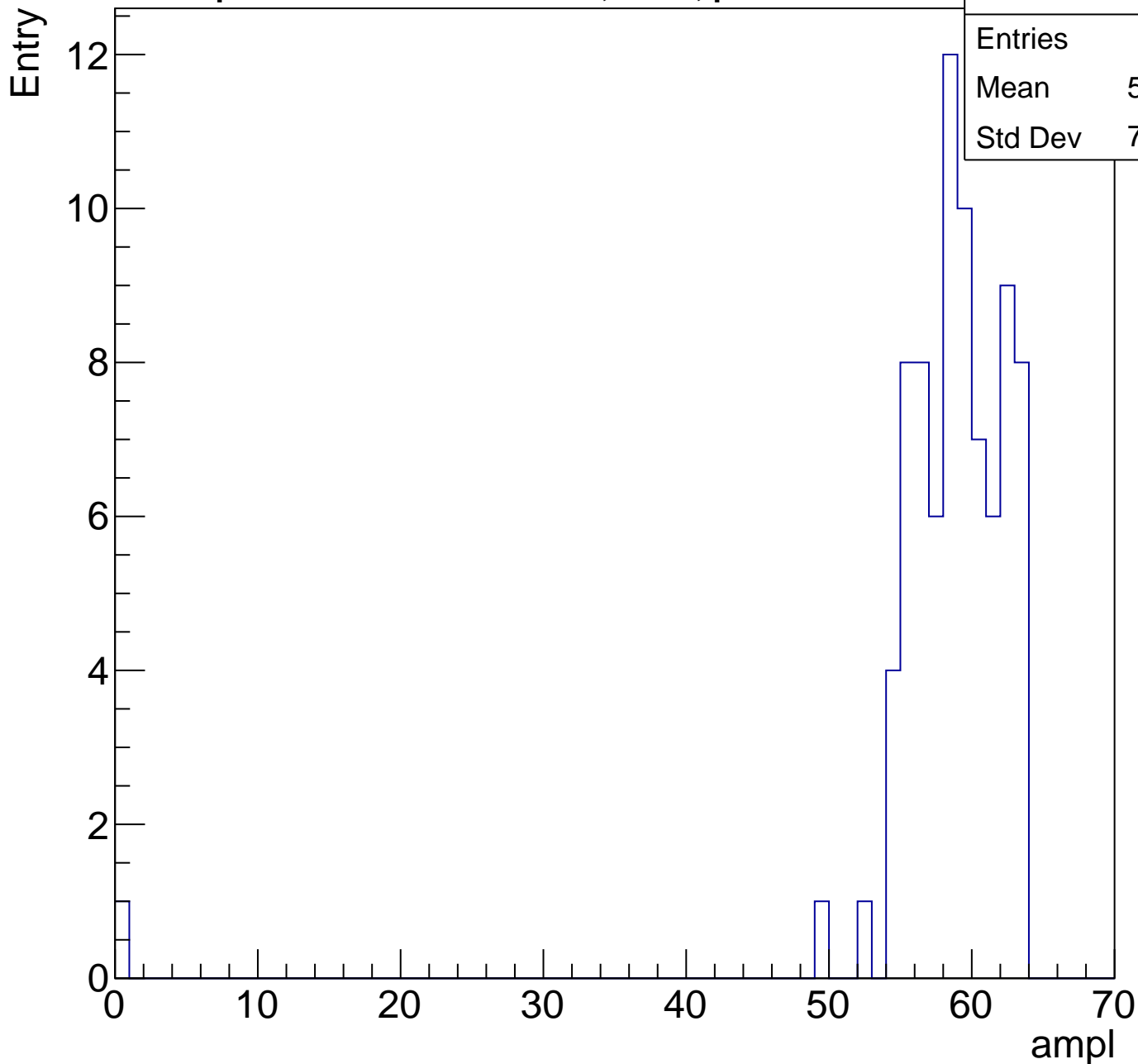
Entries	157
Mean	53.22
Std Dev	3.81



B0L101S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	81
Mean	57.79
Std Dev	7.104



B0L101S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entries	50
Mean	59.56
Std Dev	8.697

Entry

10

8

6

4

2

0

0

10

20

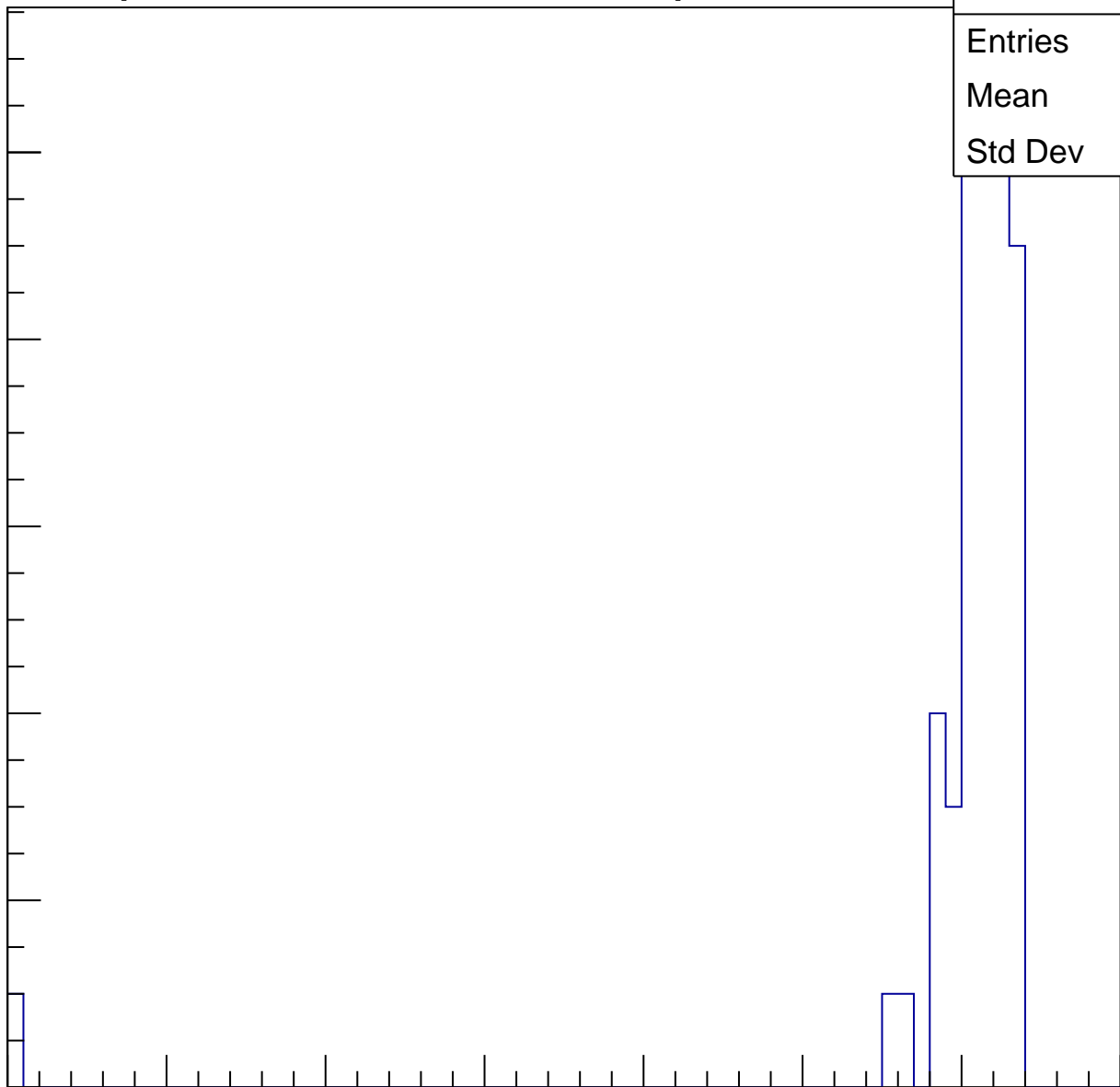
30

40

50

60

ampl



B0L101S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch119, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	152
Mean	28.74
Std Dev	4.222

Gaus mean : 29.6478

Gaus Width: 3.9231

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

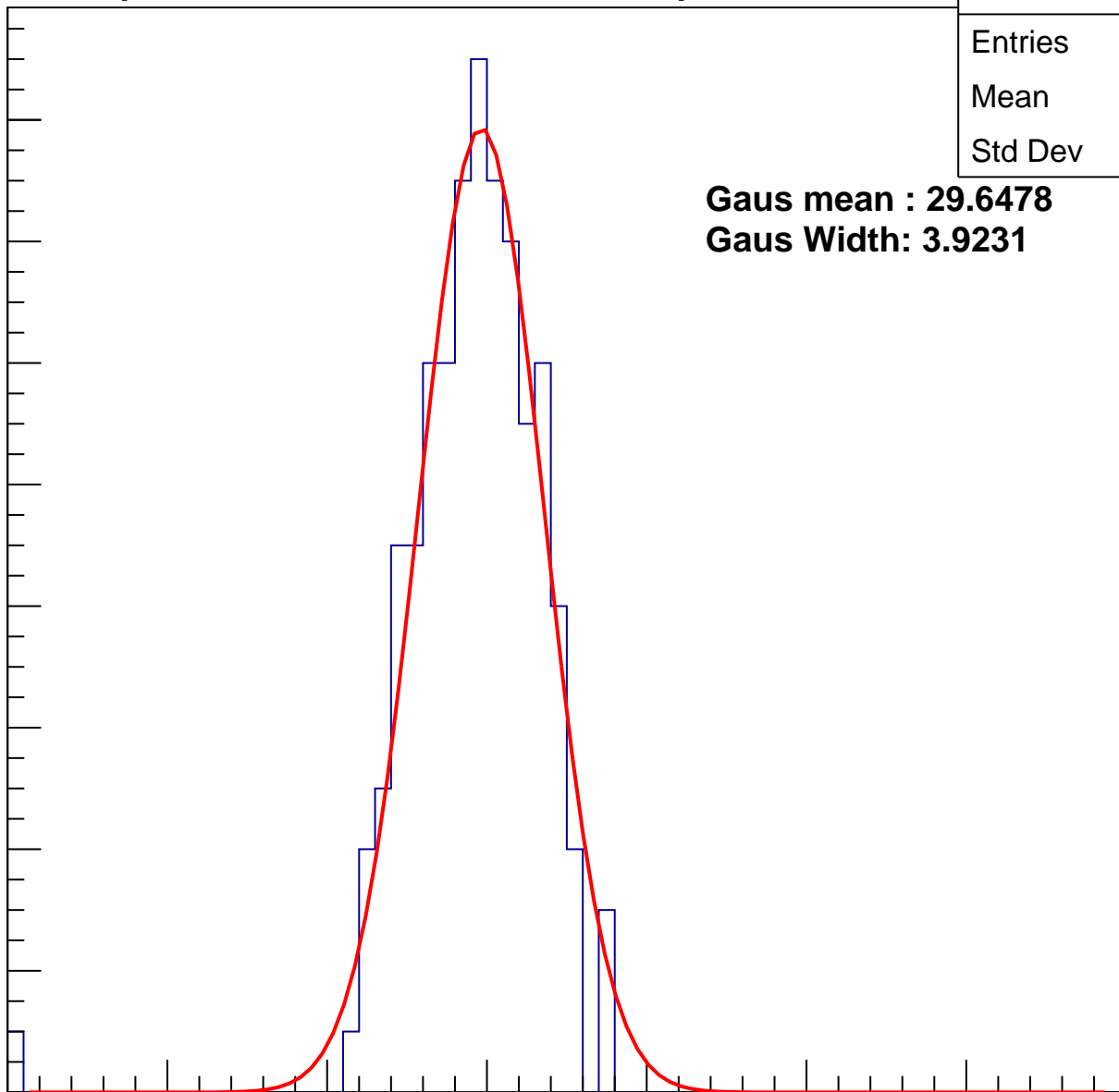
40

50

60

70

ampl



B0L101S, U15-ch119, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	141
Mean	36.32
Std Dev	3.821

Gaus mean : 36.8708

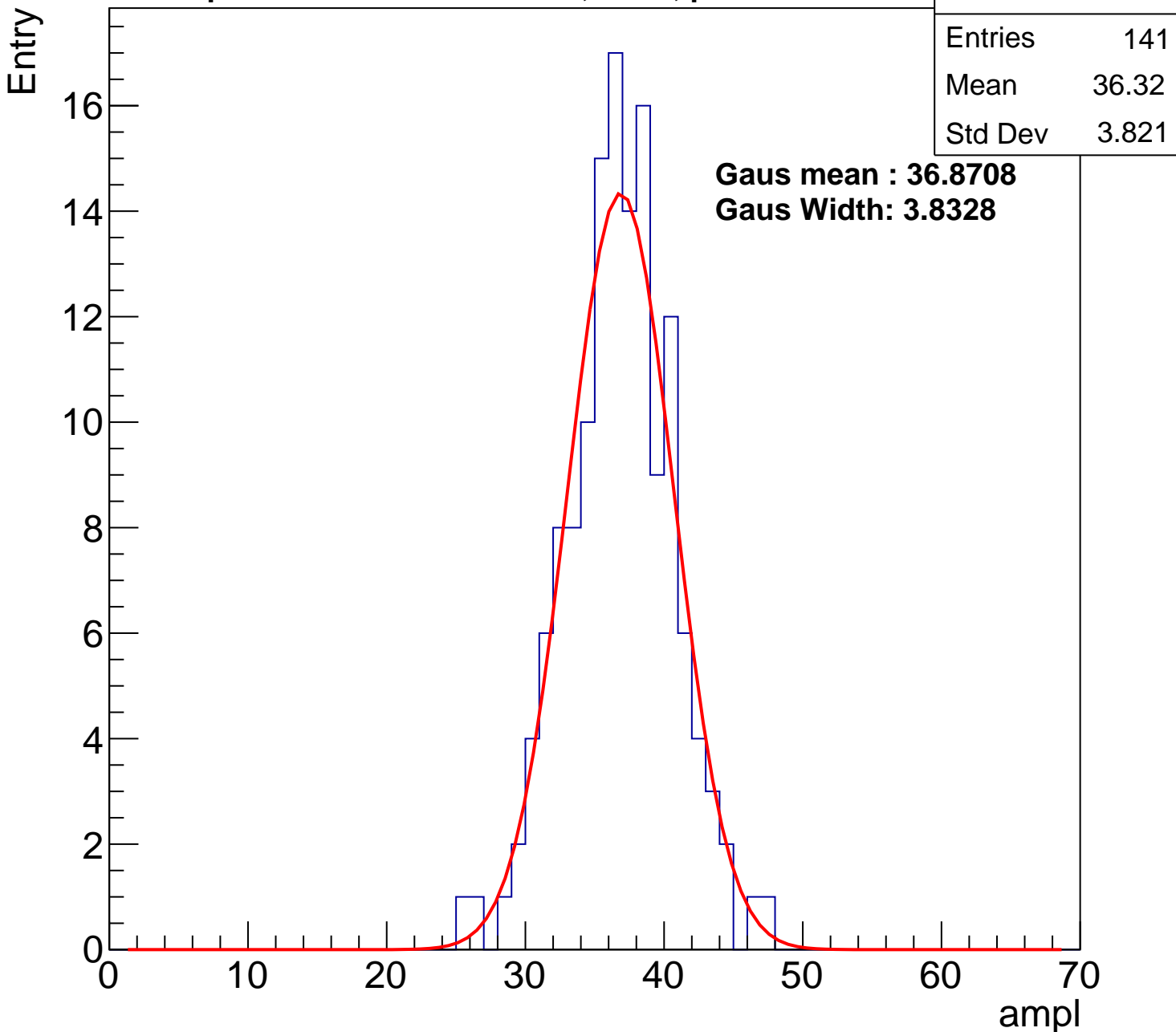
Gaus Width: 3.8328

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch119, adc2

calib_packv5_042523_0143.root, FC#1, port C1

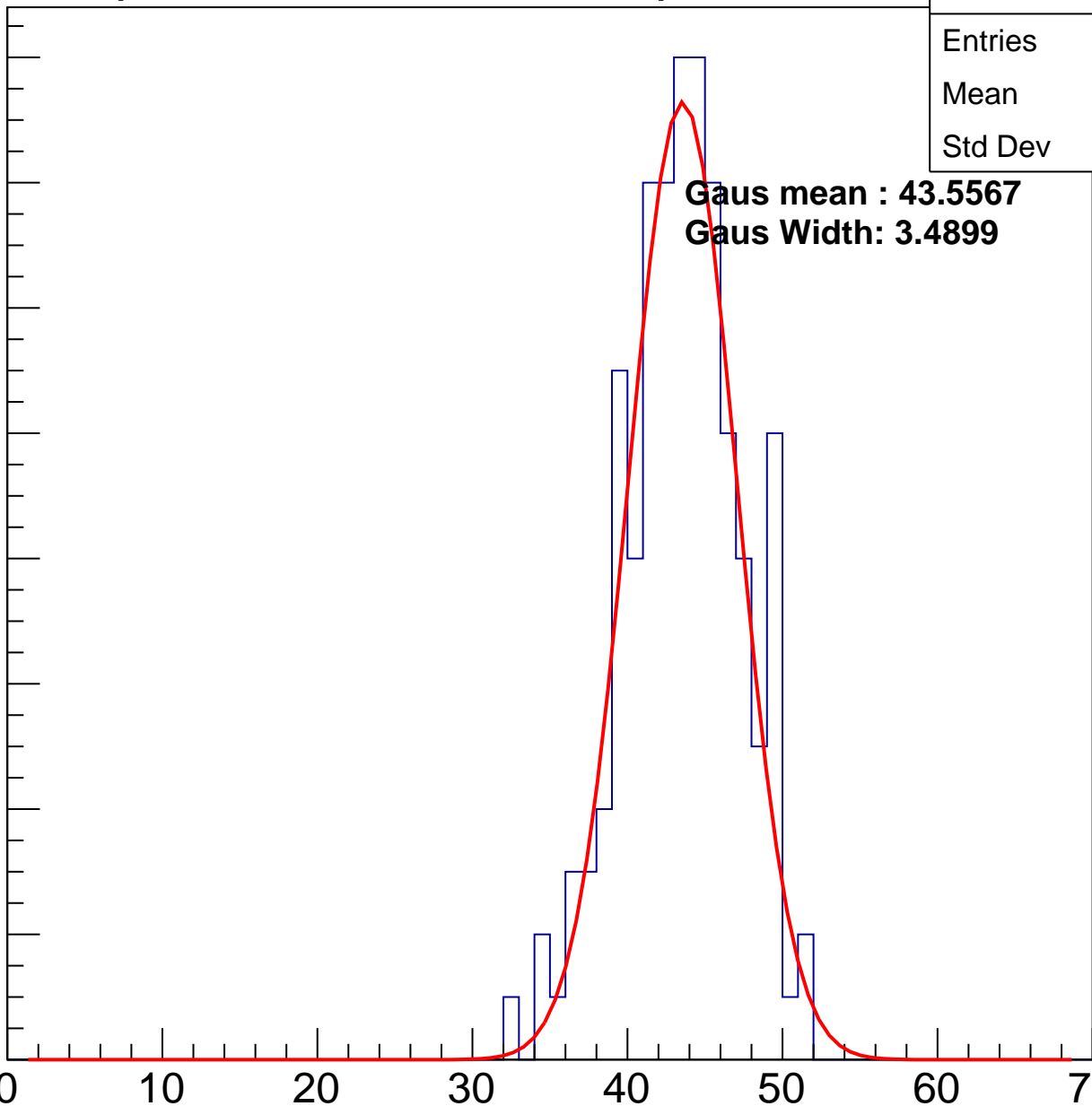
Entry

16
14
12
10
8
6
4
2
0

Entries	143
Mean	43.06
Std Dev	3.713

Gaus mean : 43.5567
Gaus Width: 3.4899

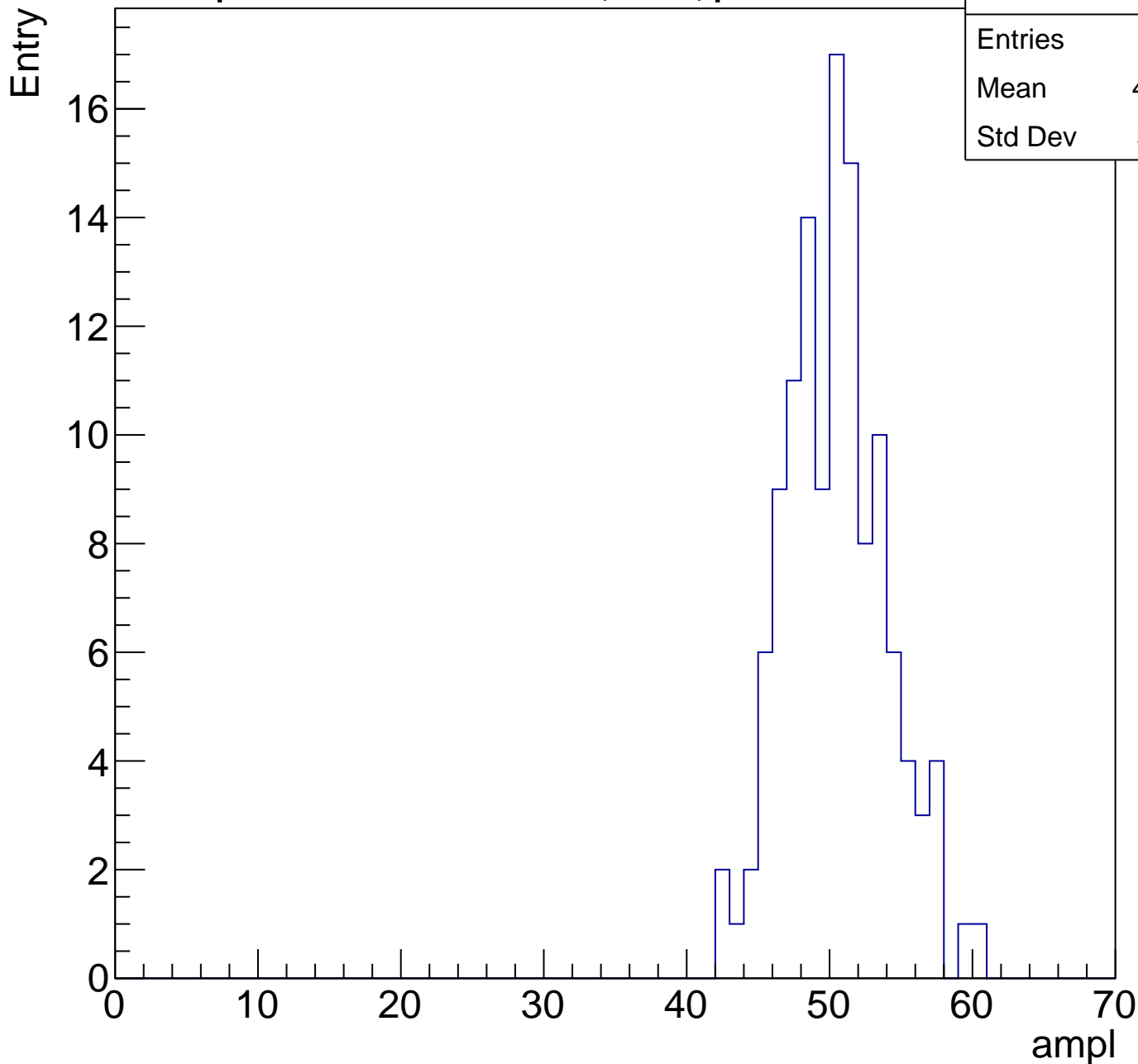
0 10 20 30 40 50 60 70
ampl



B0L101S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#1, port C1

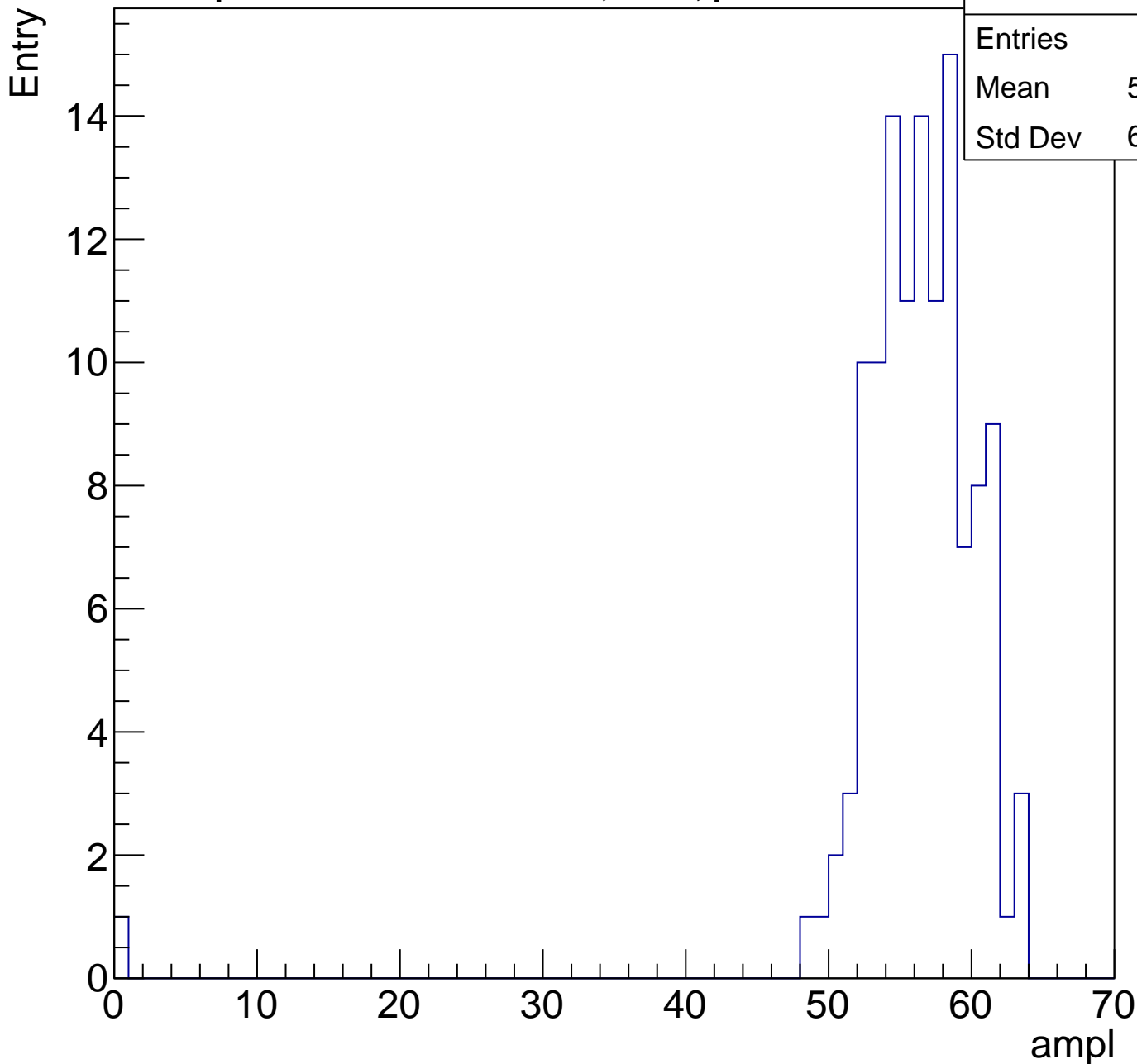
Entries	123
Mean	49.99
Std Dev	3.521



B0L101S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	55.66
Std Dev	6.009



B0L101S, U15-ch119, adc5

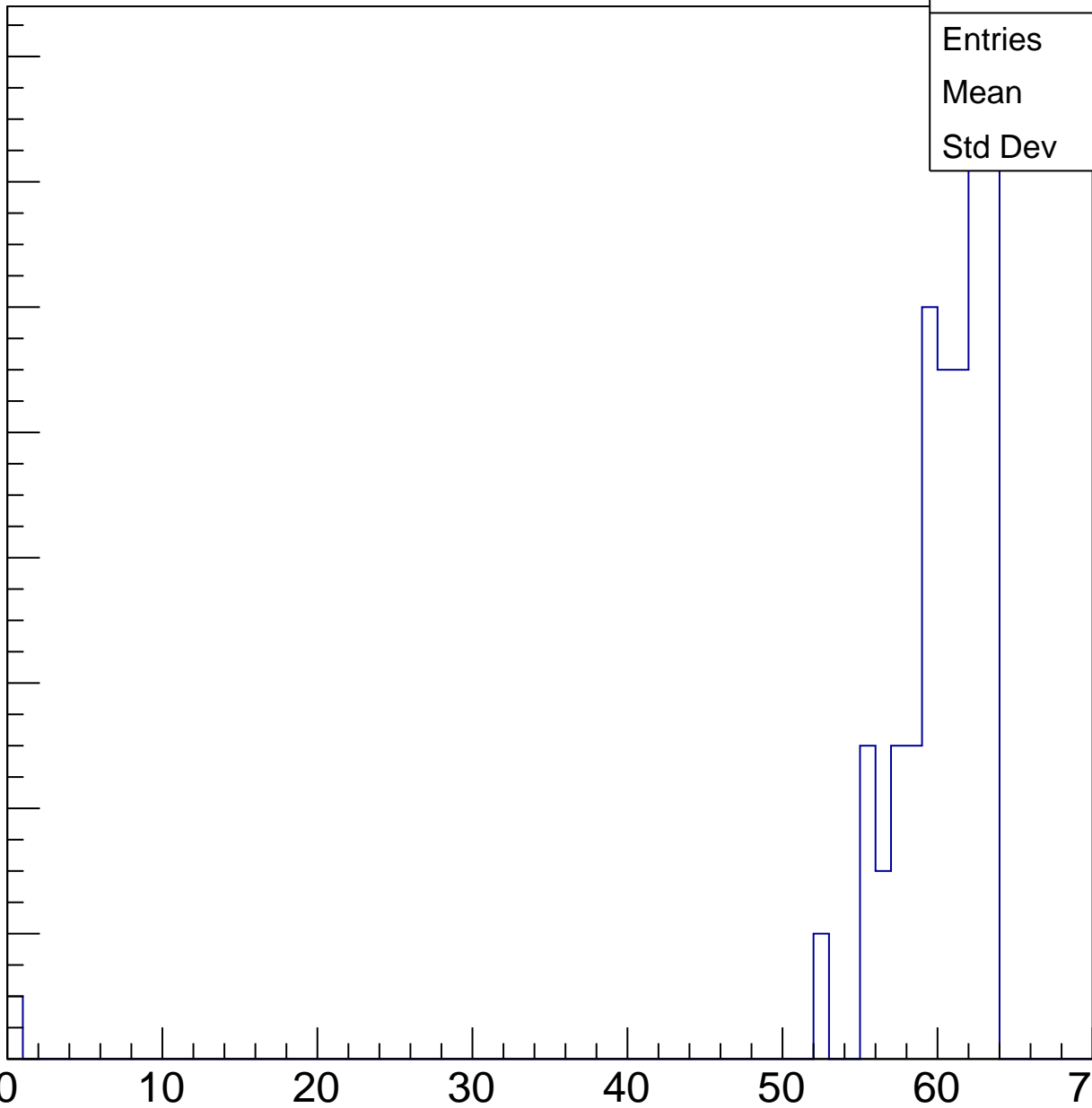
calib_packv5_042523_0143.root, FC#1, port C1

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.32
Std Dev	6.912

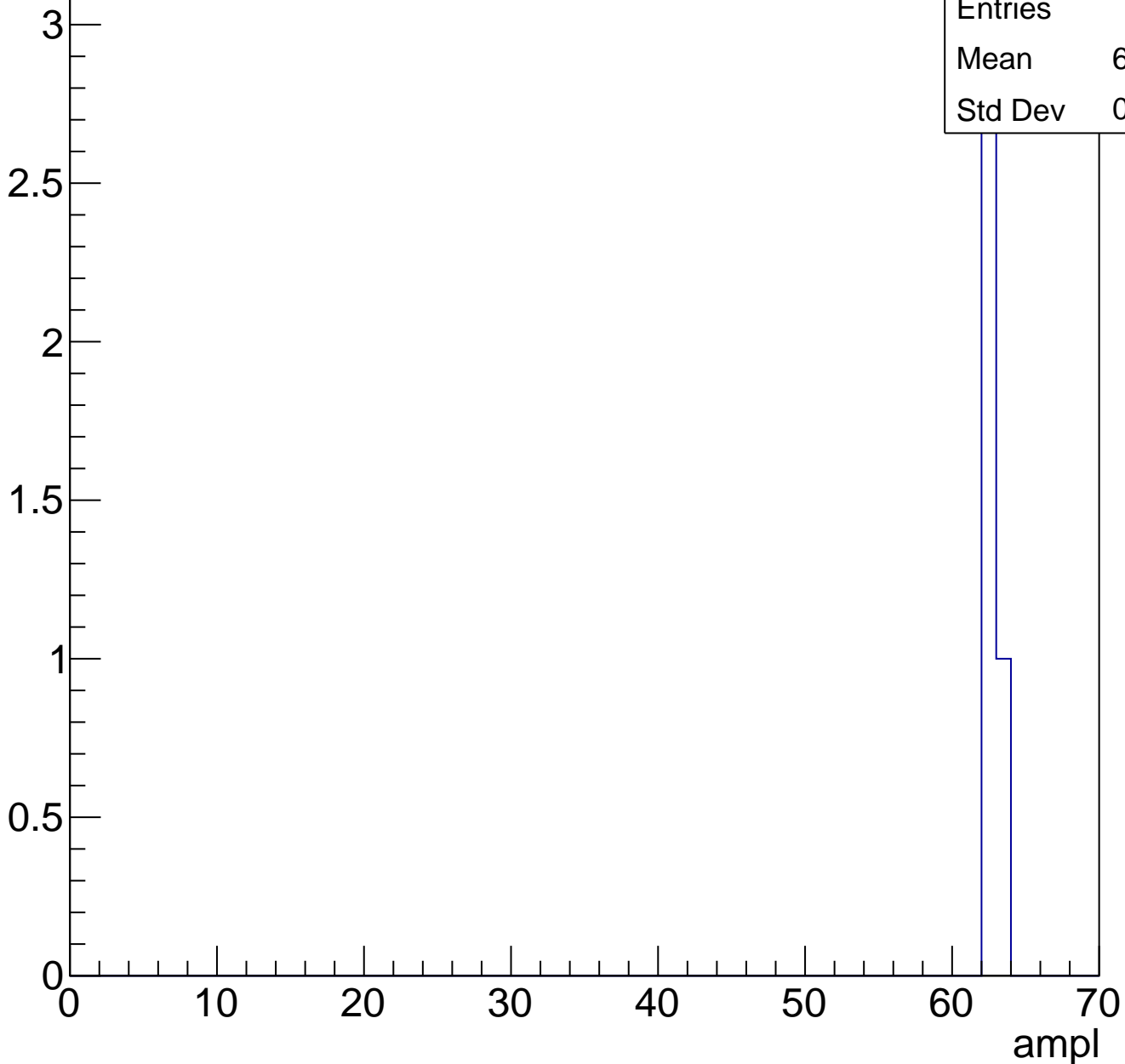
ampl



B0L101S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L101S, U15-ch120, adc0

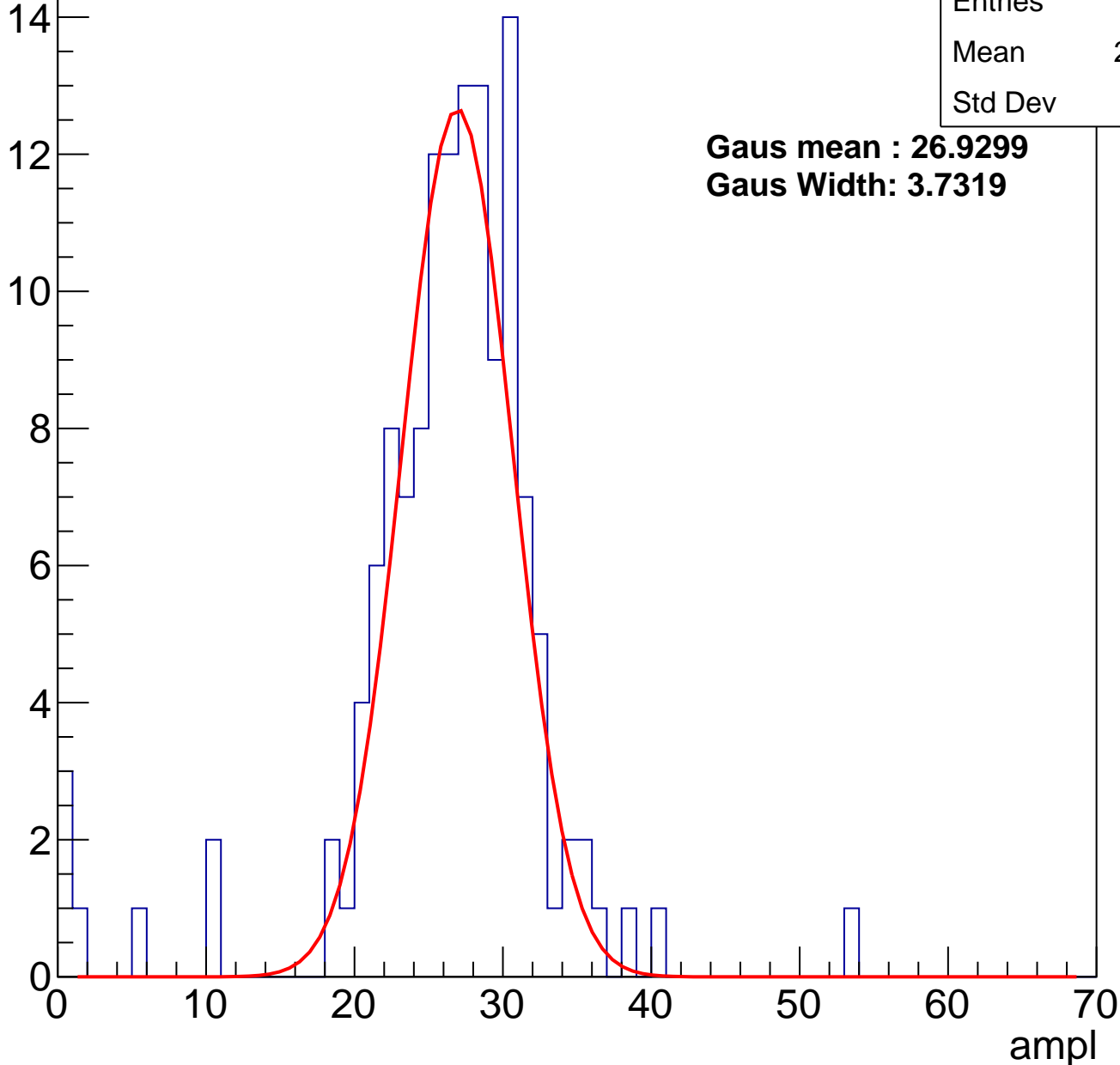
calib_packv5_042523_0143.root, FC#1, port C1

Entries	137
Mean	25.83
Std Dev	6.91

Gaus mean : 26.9299

Gaus Width: 3.7319

Entry



B0L101S, U15-ch120, adc1

calib_packv5_042523_0143.root, FC#1, port C1

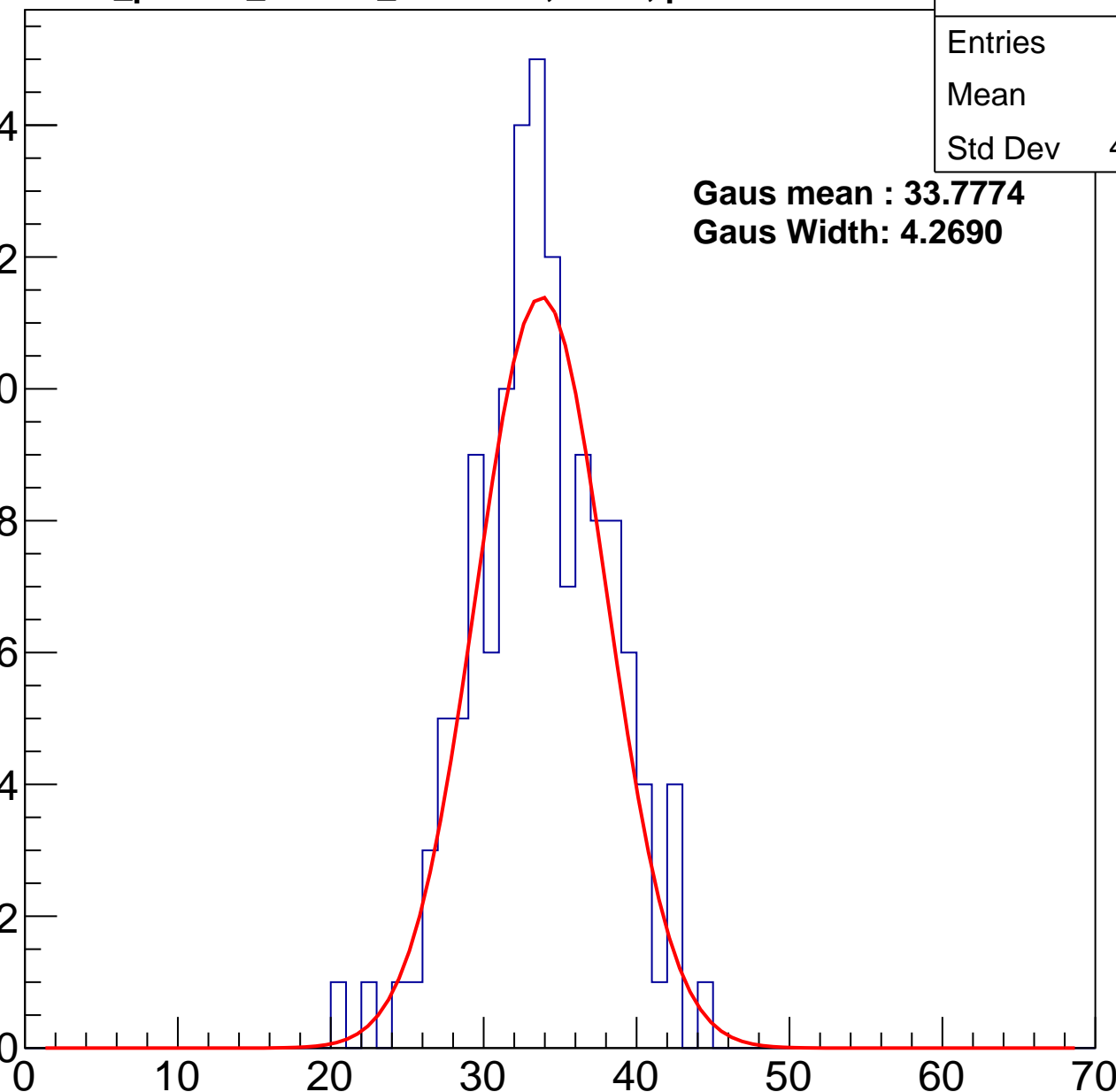
Entry

14
12
10
8
6
4
2
0

Entries	131
Mean	33.3
Std Dev	4.366

Gaus mean : 33.7774

Gaus Width: 4.2690



ampl

B0L101S, U15-ch120, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

Entries

143

Mean

39.8

Std Dev

4.207

Gaus mean : 40.2529

Gaus Width: 3.9771

0

10

20

30

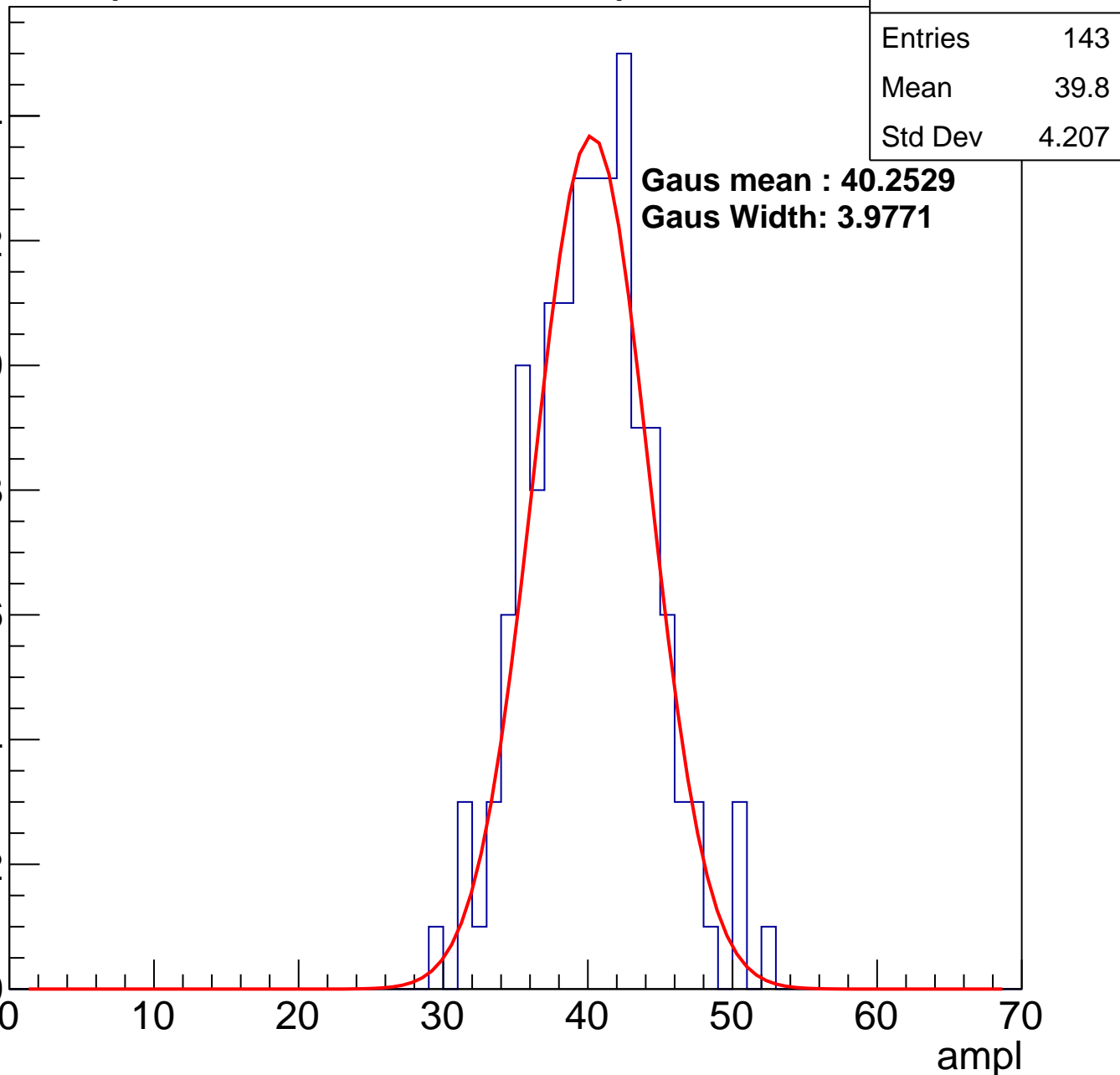
40

50

60

70

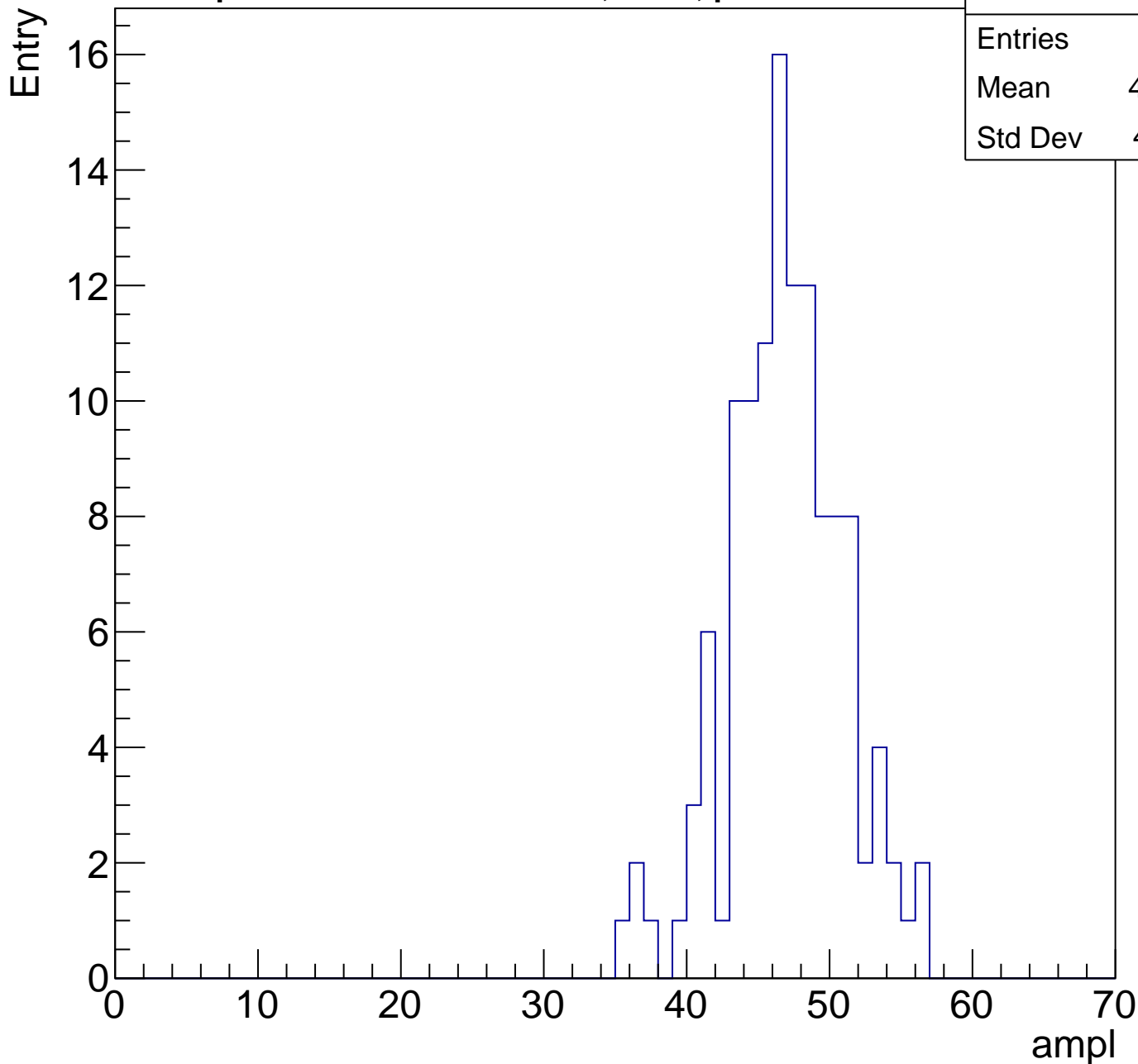
ampl



B0L101S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	46.47
Std Dev	4.041



B0L101S, U15-ch120, adc4

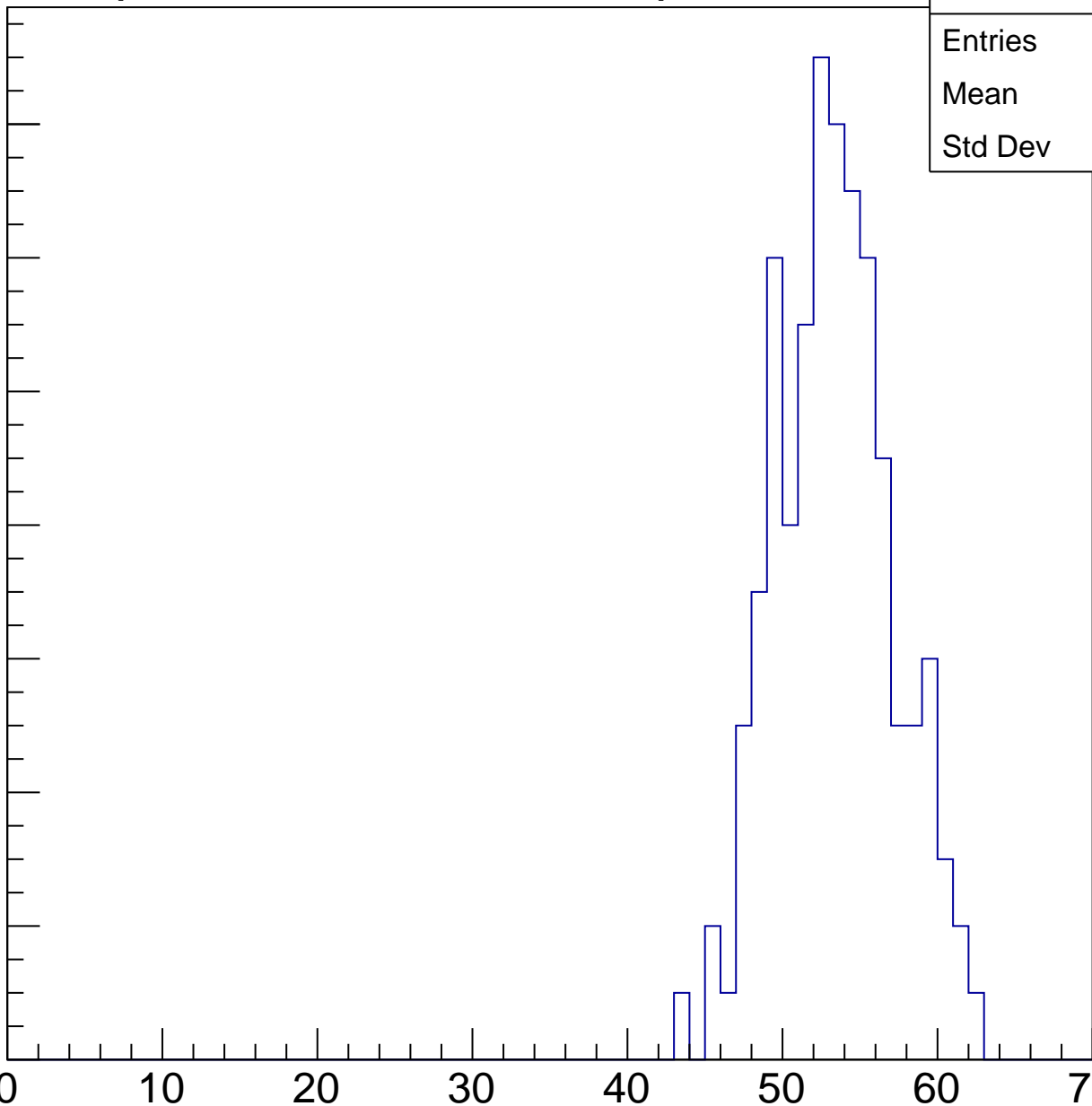
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	132
Mean	52.88
Std Dev	3.766

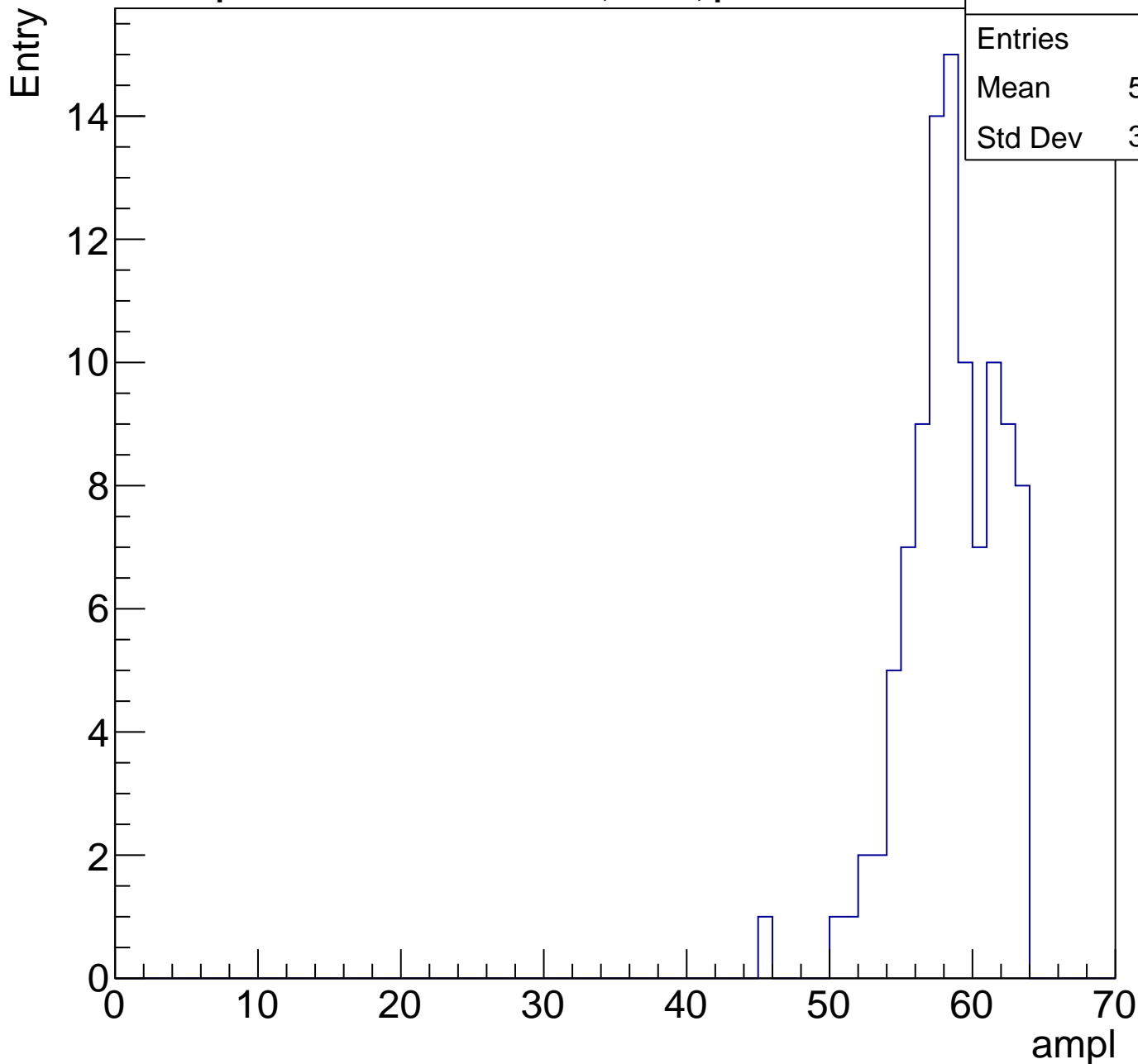
ampl



B0L101S, U15-ch120, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	101
Mean	58.07
Std Dev	3.253



B0L101S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

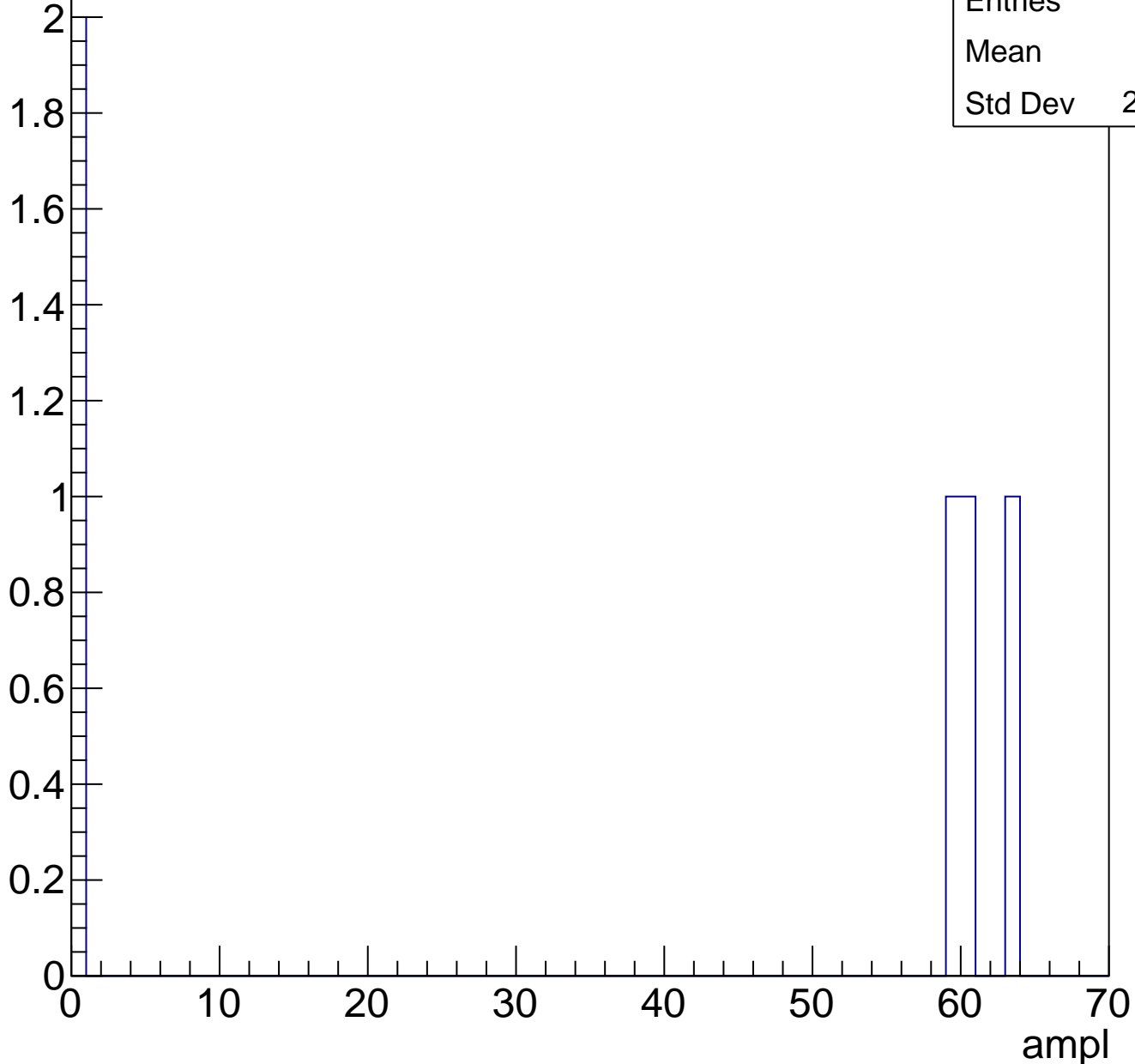
ampl

Entries	43
Mean	59.88
Std Dev	9.349

B0L101S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	5
Mean	36.4
Std Dev	29.75

B0L101S, U15-ch121, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	132
Mean	28.91
Std Dev	5.132

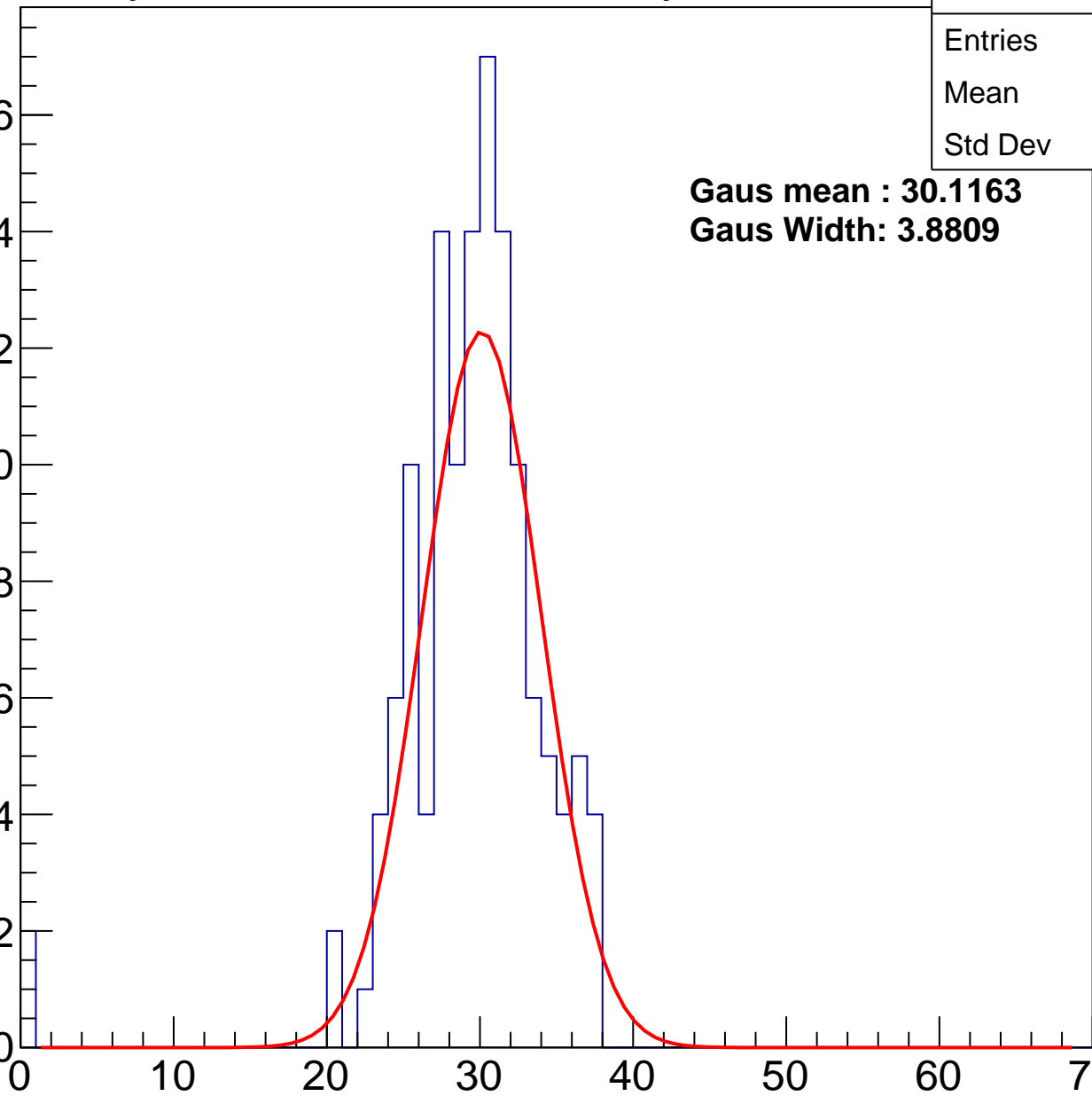
Gaus mean : 30.1163

Gaus Width: 3.8809

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch121, adc1

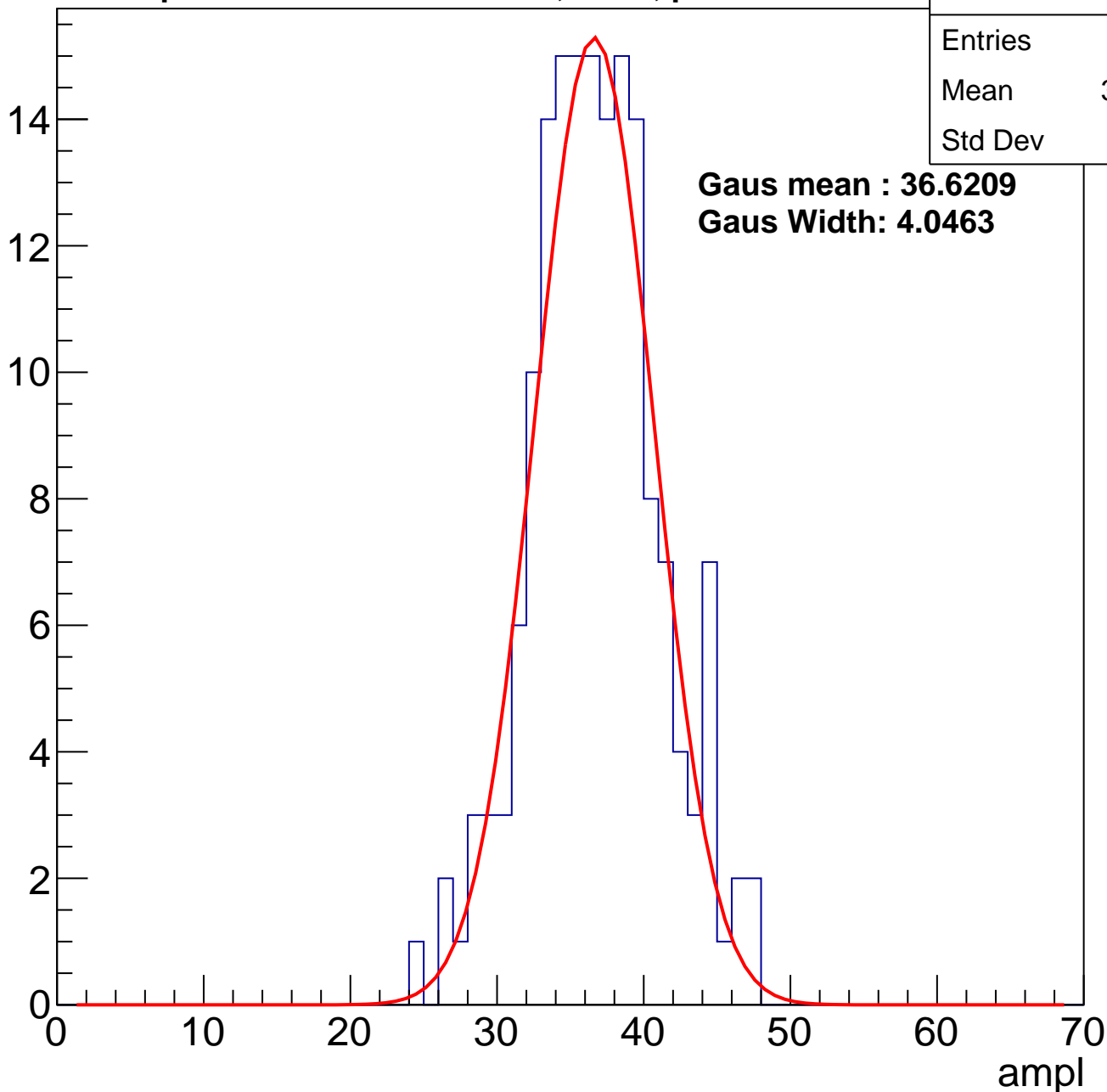
calib_packv5_042523_0143.root, FC#1, port C1

Entries	165
Mean	36.27
Std Dev	4.36

Gaus mean : 36.6209

Gaus Width: 4.0463

Entry



B0L101S, U15-ch121, adc2

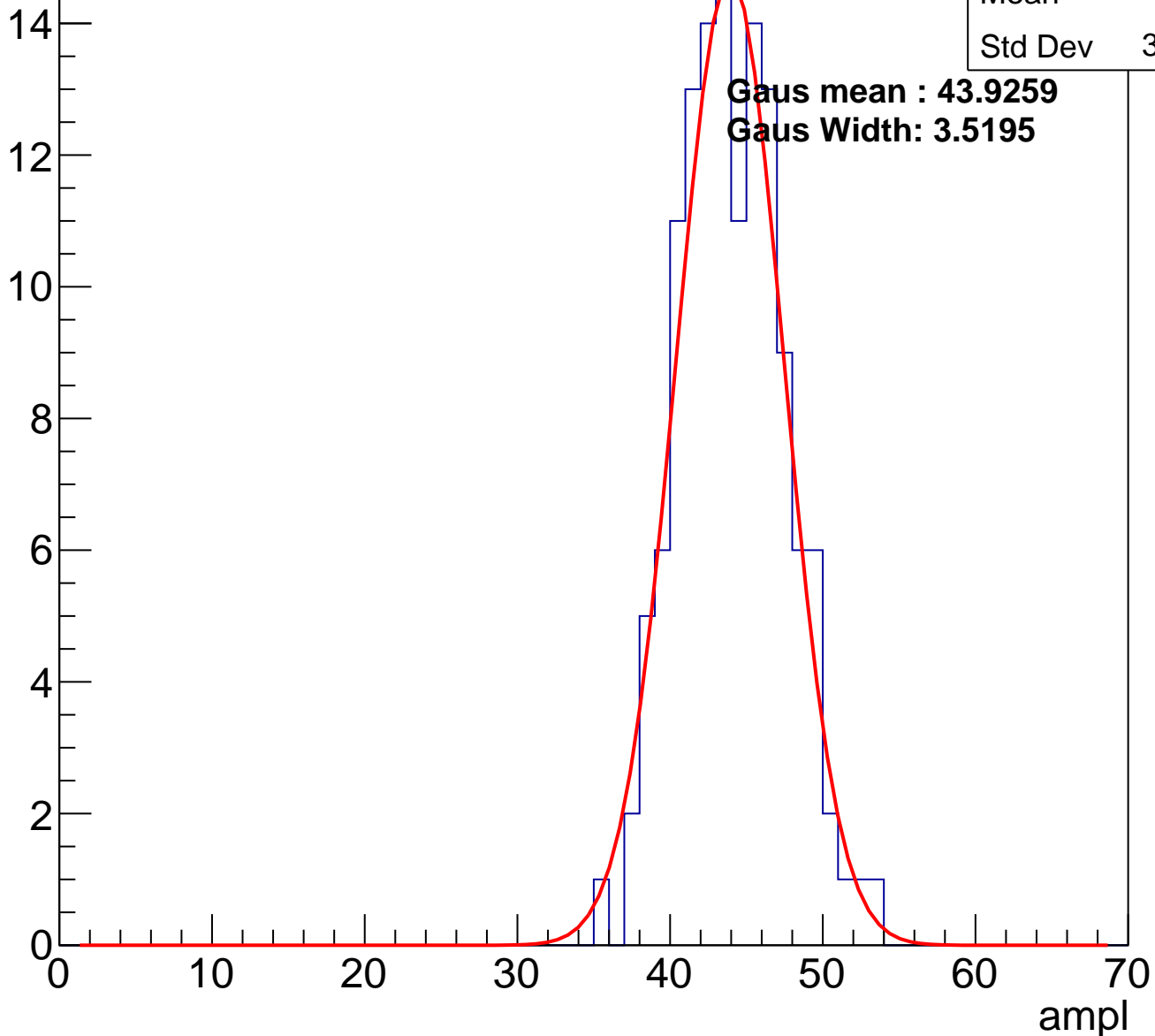
calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	43.6
Std Dev	3.397

Gaus mean : 43.9259

Gaus Width: 3.5195

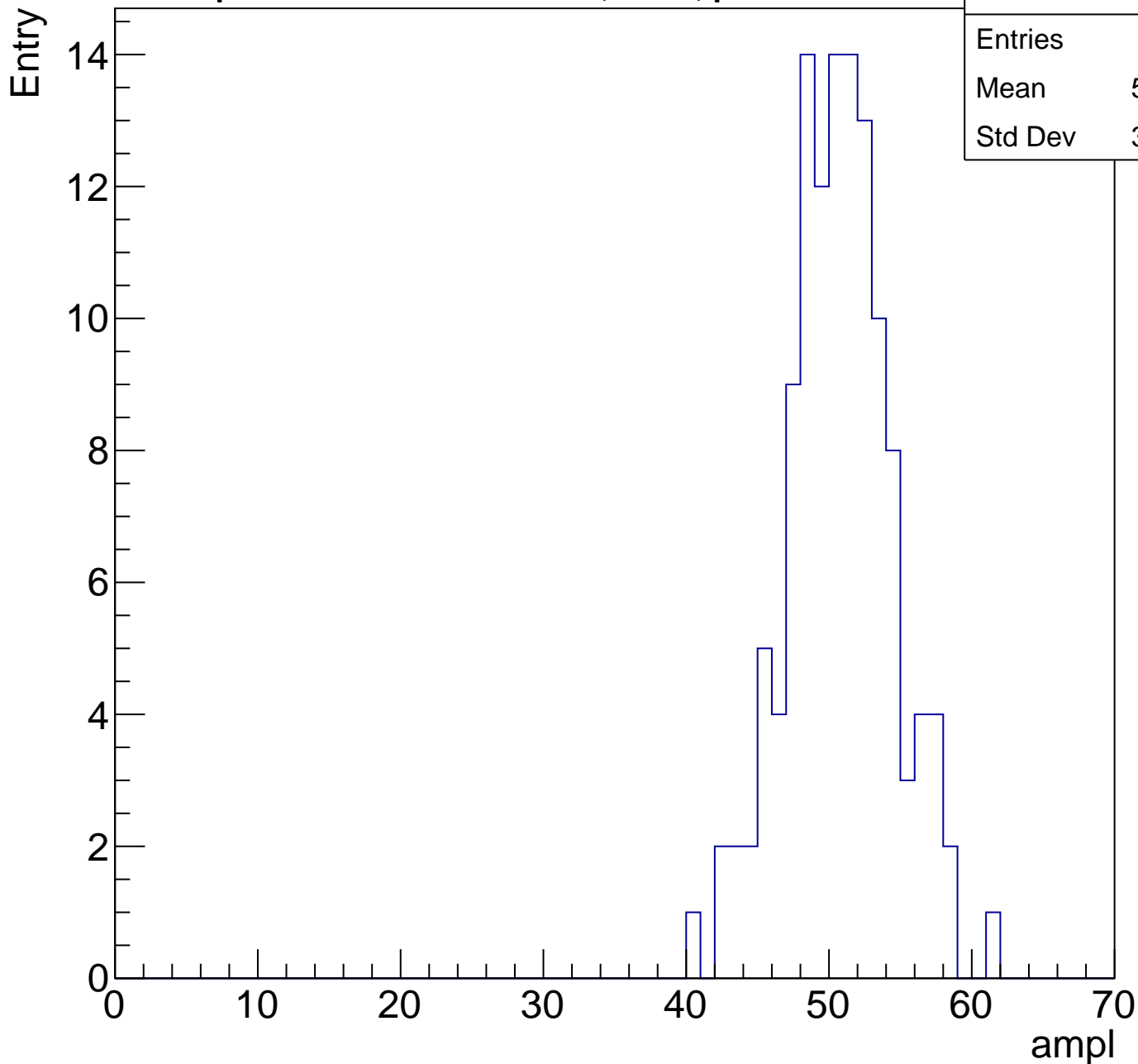
Entry



B0L101S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#1, port C1

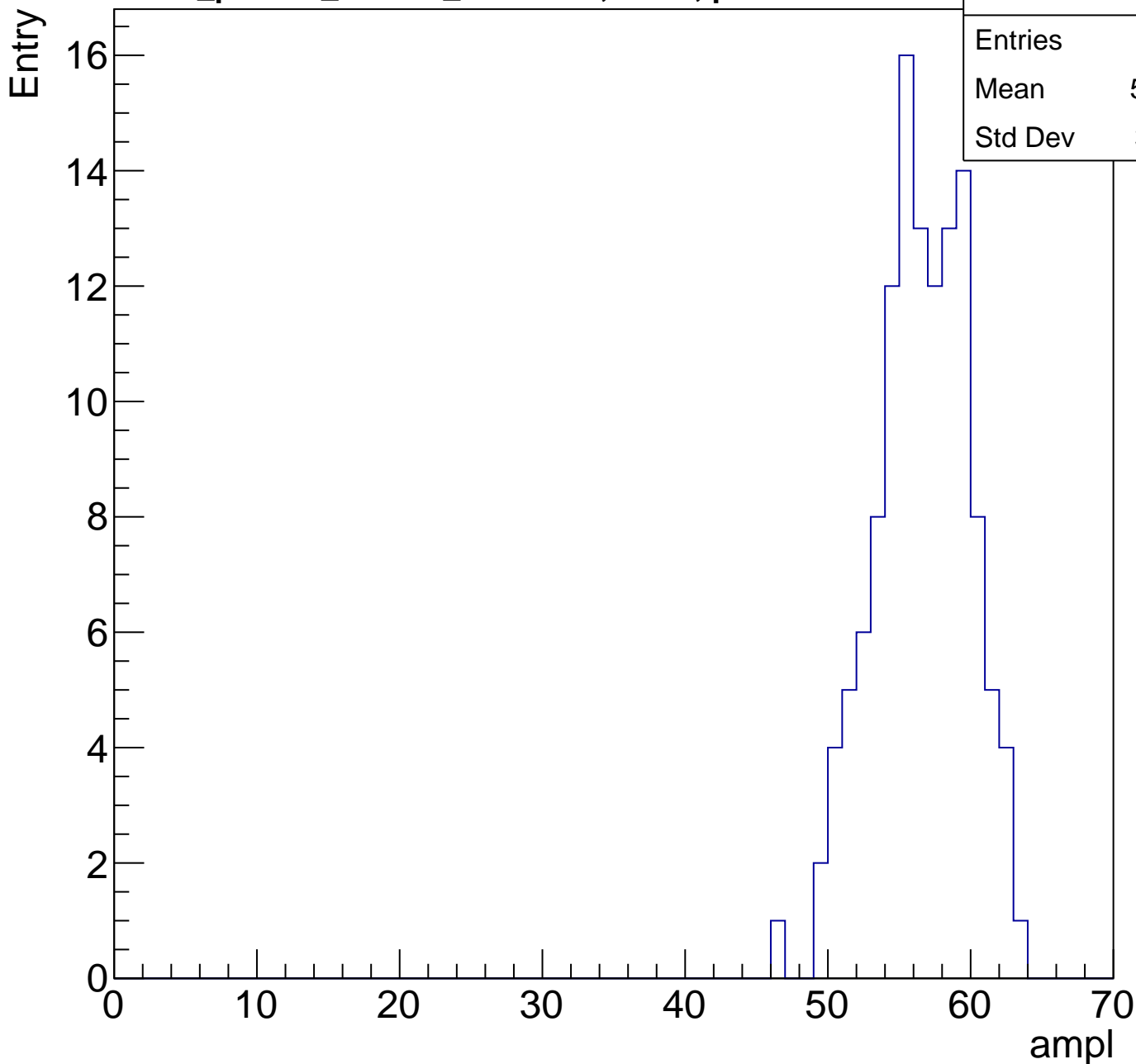
Entries	124
Mean	50.29
Std Dev	3.674



B0L101S, U15-ch121, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	56.06
Std Dev	3.281



B0L101S, U15-ch121, adc5

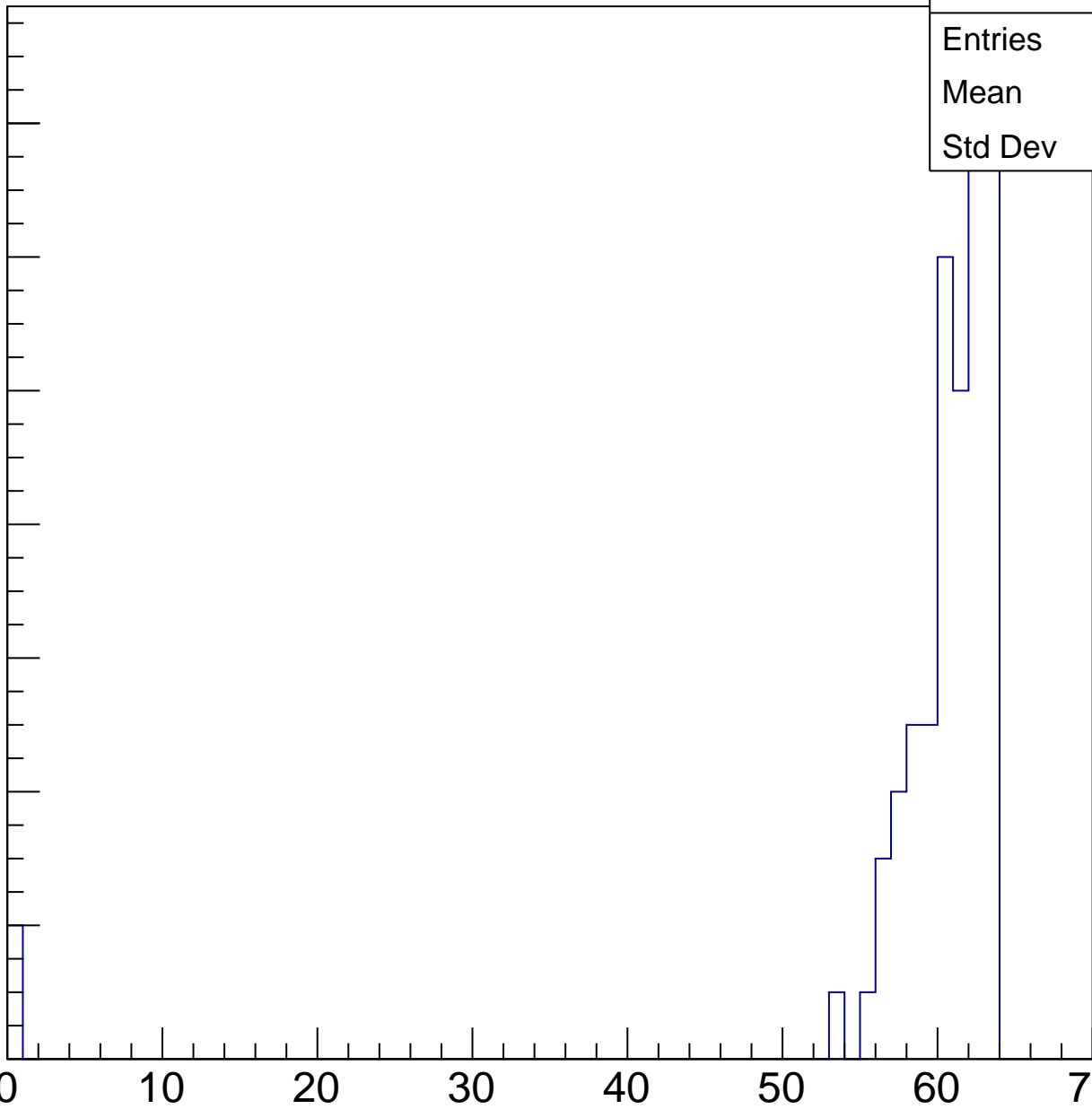
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	72
Mean	58.78
Std Dev	10.19

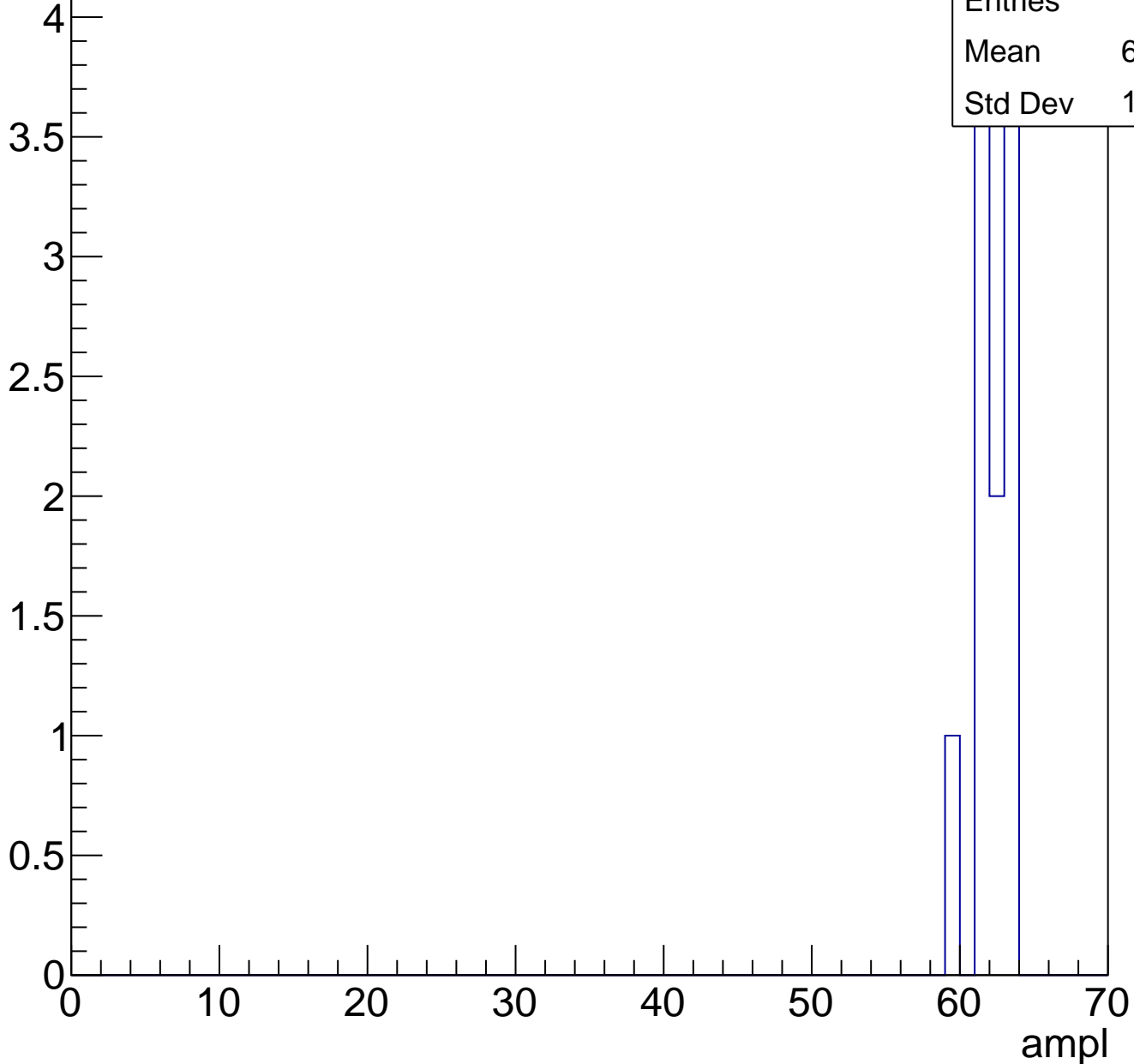
ampl



B0L101S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B0L101S, U15-ch122, adc0

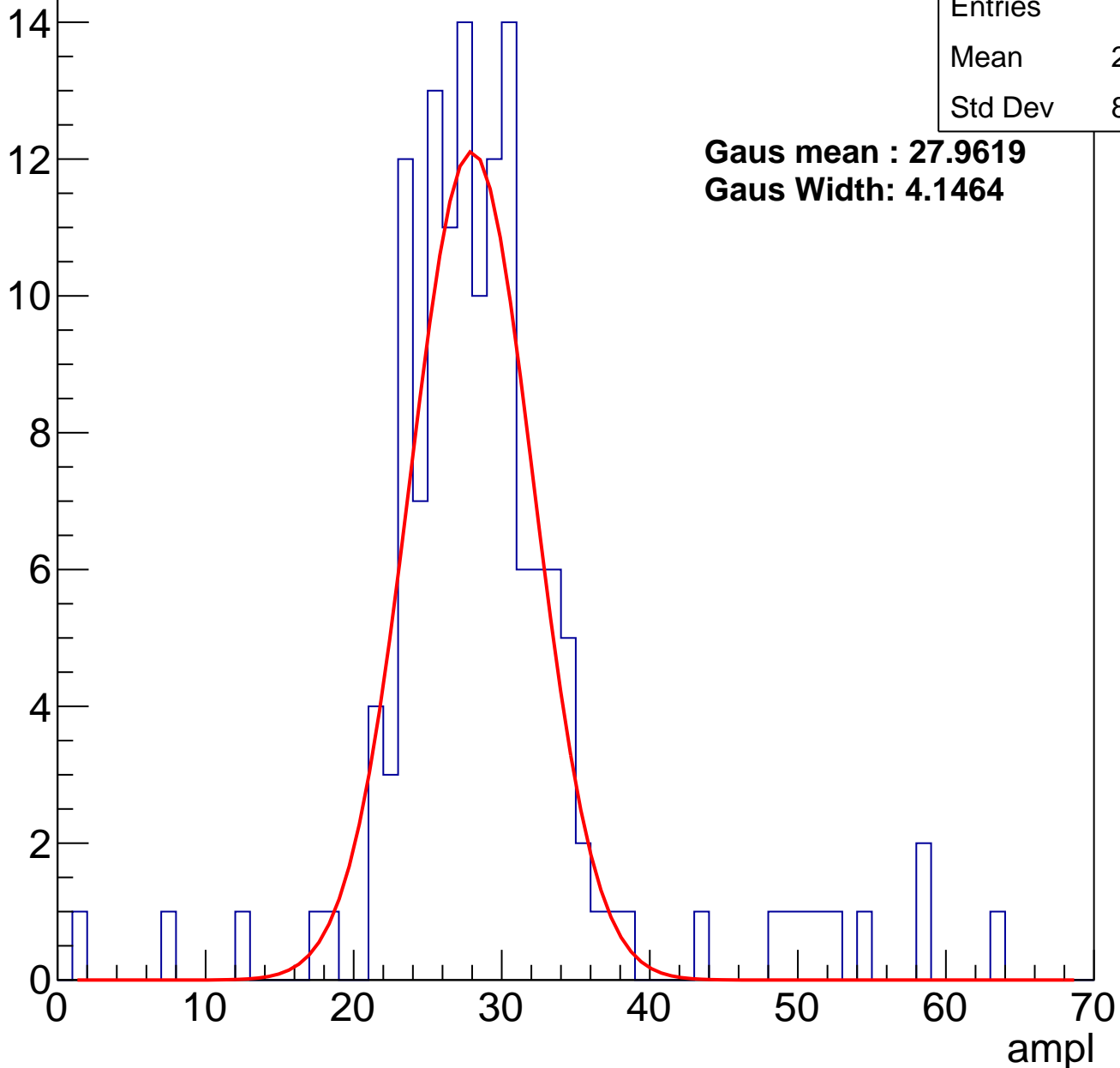
calib_packv5_042523_0143.root, FC#1, port C1

Entries	143
Mean	28.94
Std Dev	8.218

Gaus mean : 27.9619

Gaus Width: 4.1464

Entry



B0L101S, U15-ch122, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	139
Mean	33.93
Std Dev	4.934

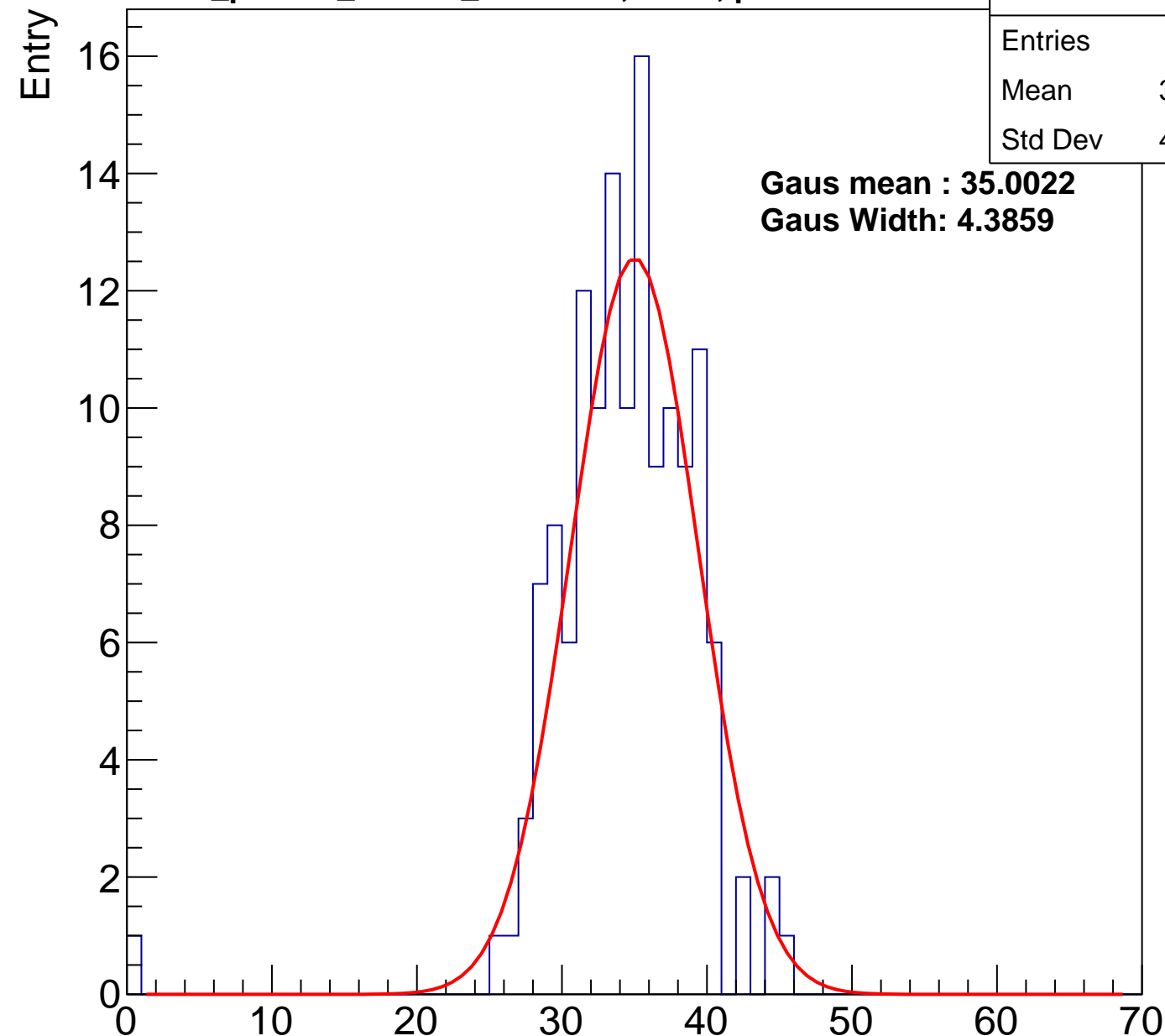
Gaus mean : 35.0022

Gaus Width: 4.3859

Entry

16
14
12
10
8
6
4
2
0

ampl



B0L101S, U15-ch122, adc2

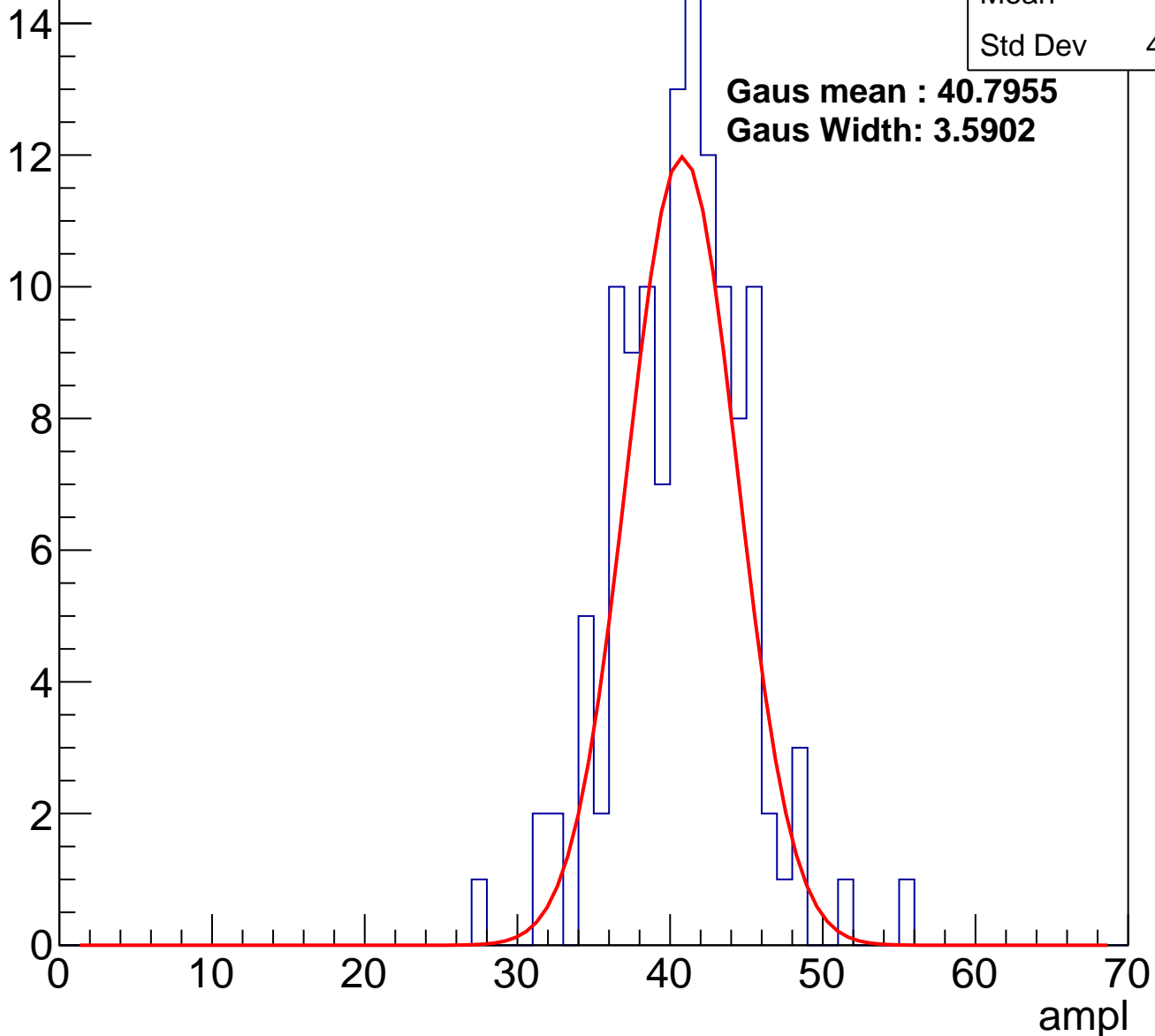
calib_packv5_042523_0143.root, FC#1, port C1

Entries	124
Mean	40.31
Std Dev	4.169

Gaus mean : 40.7955

Gaus Width: 3.5902

Entry



B0L101S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#1, port C1

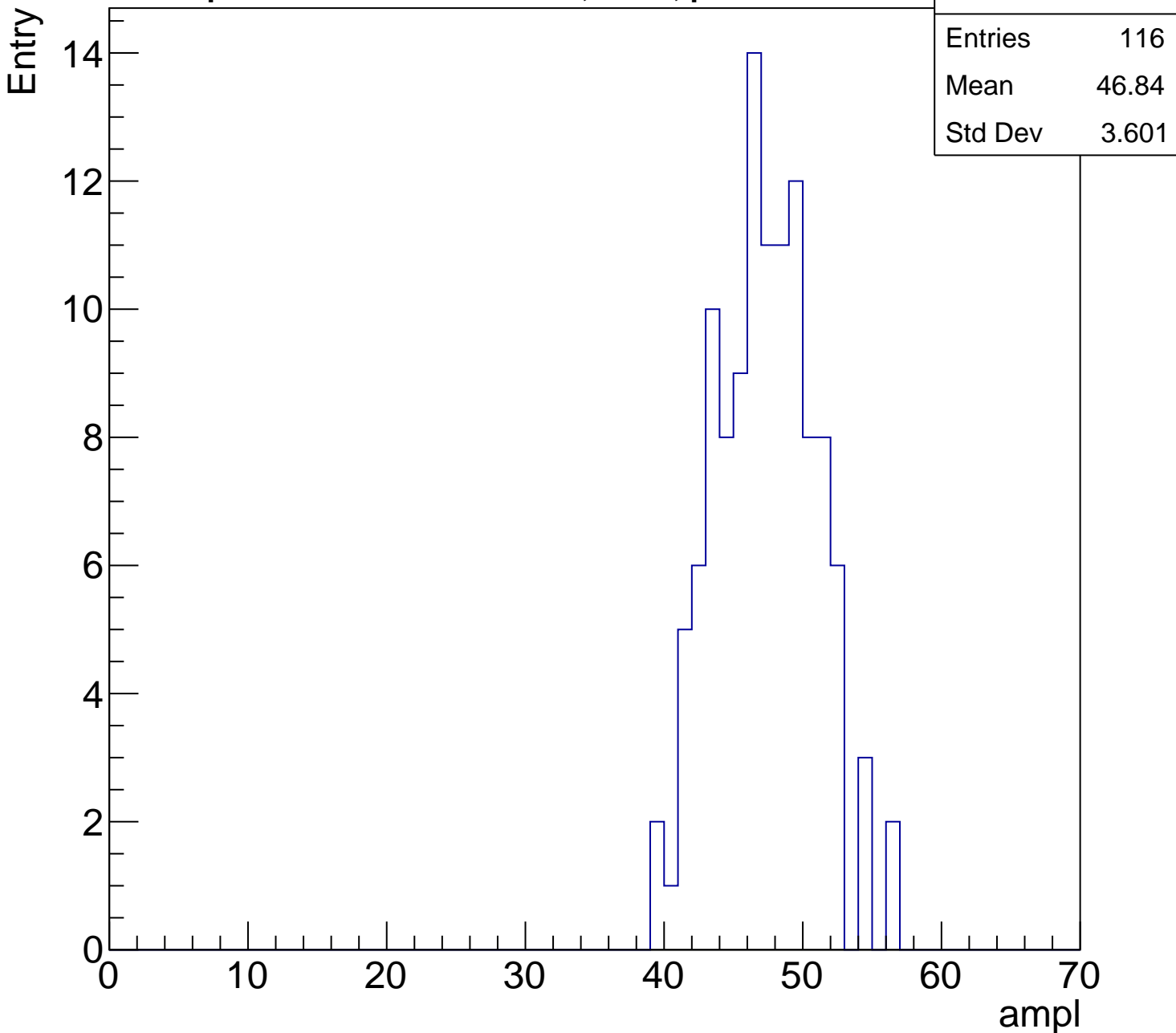
Entries	116
Mean	46.84
Std Dev	3.601

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

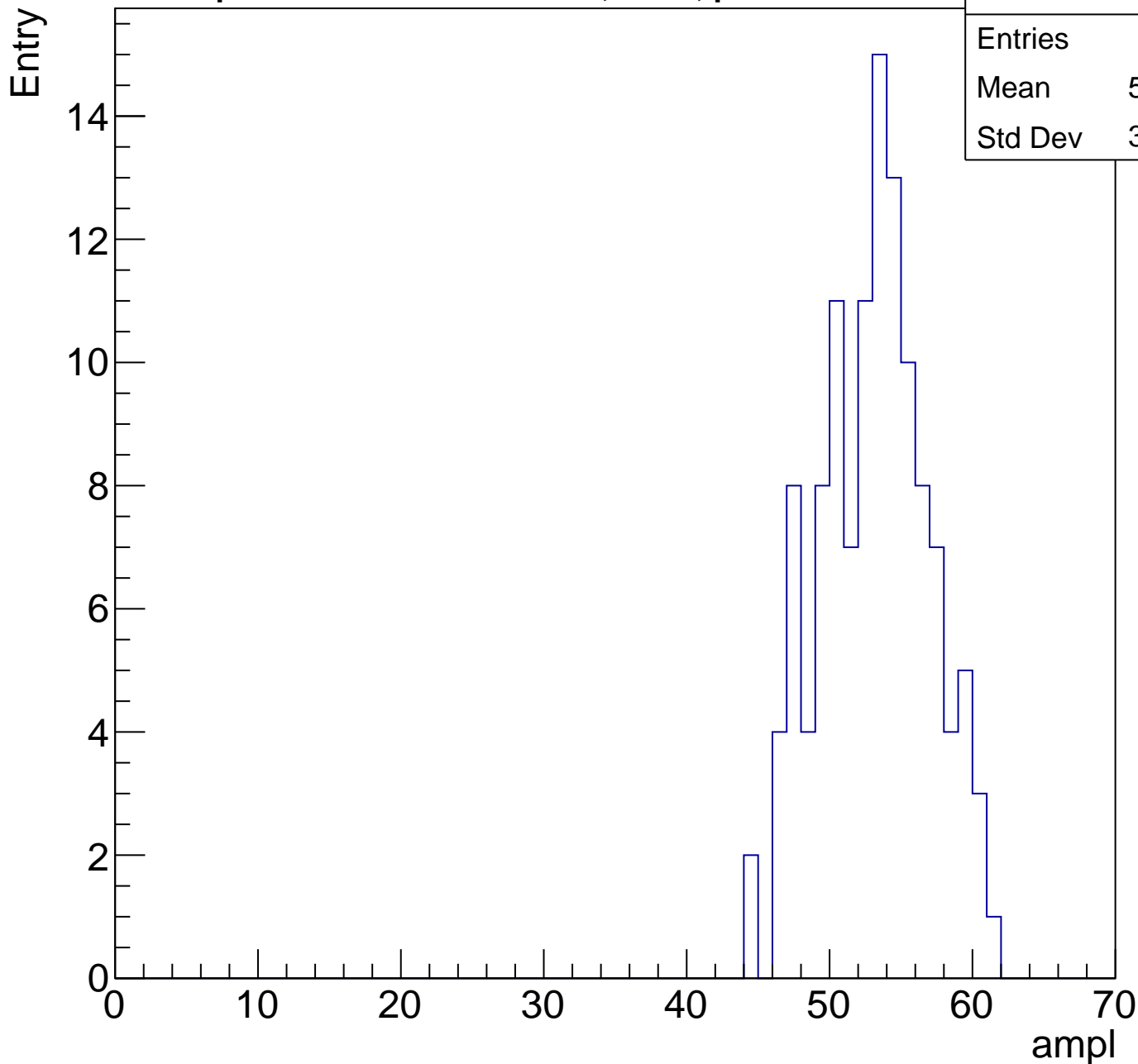
ampl



B0L101S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#1, port C1

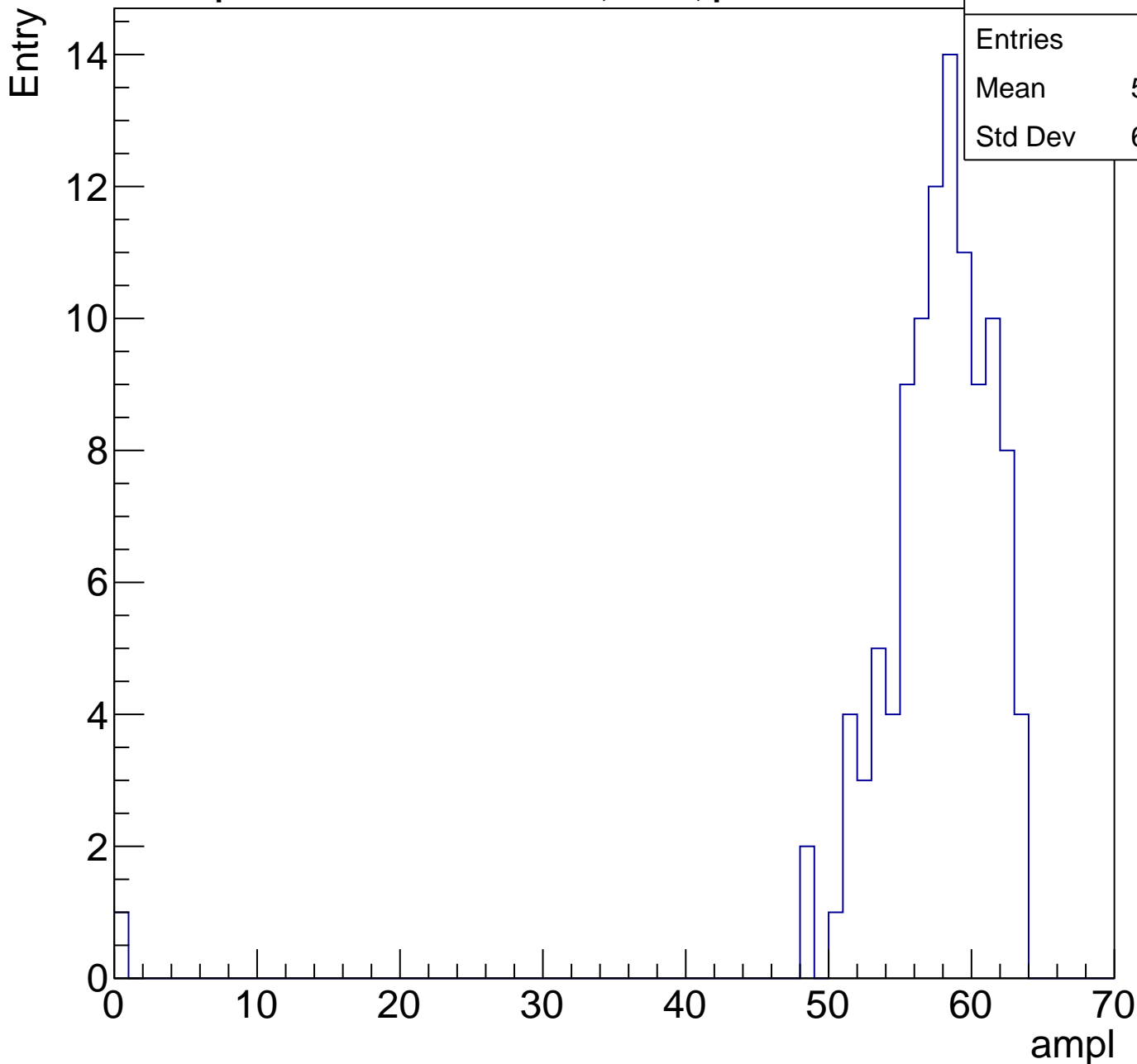
Entries	121
Mean	52.67
Std Dev	3.778



B0L101S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#1, port C1

Entries	107
Mean	56.87
Std Dev	6.472



B0L101S, U15-ch122, adc6

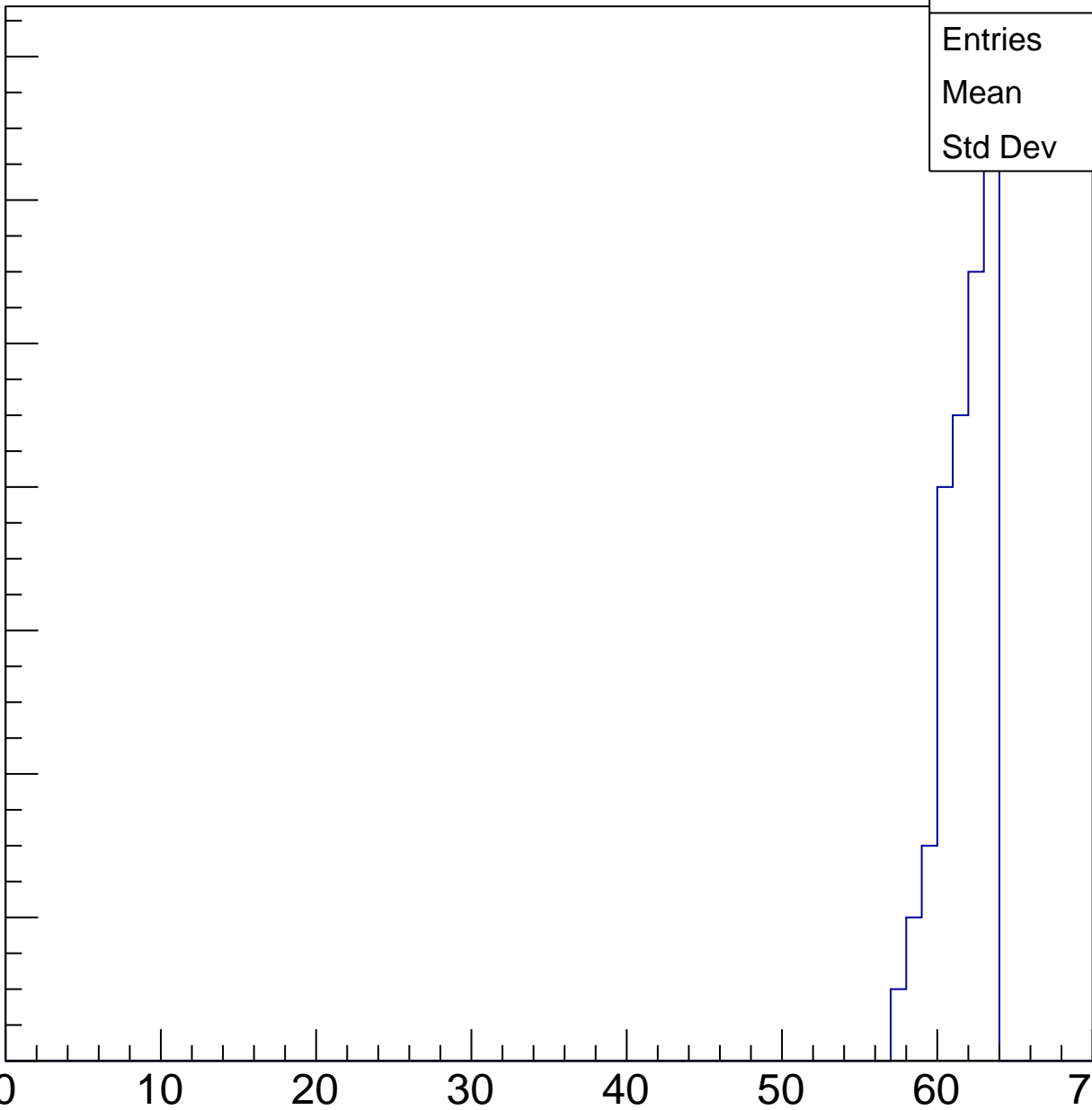
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	48
Mean	61.31
Std Dev	1.557

ampl

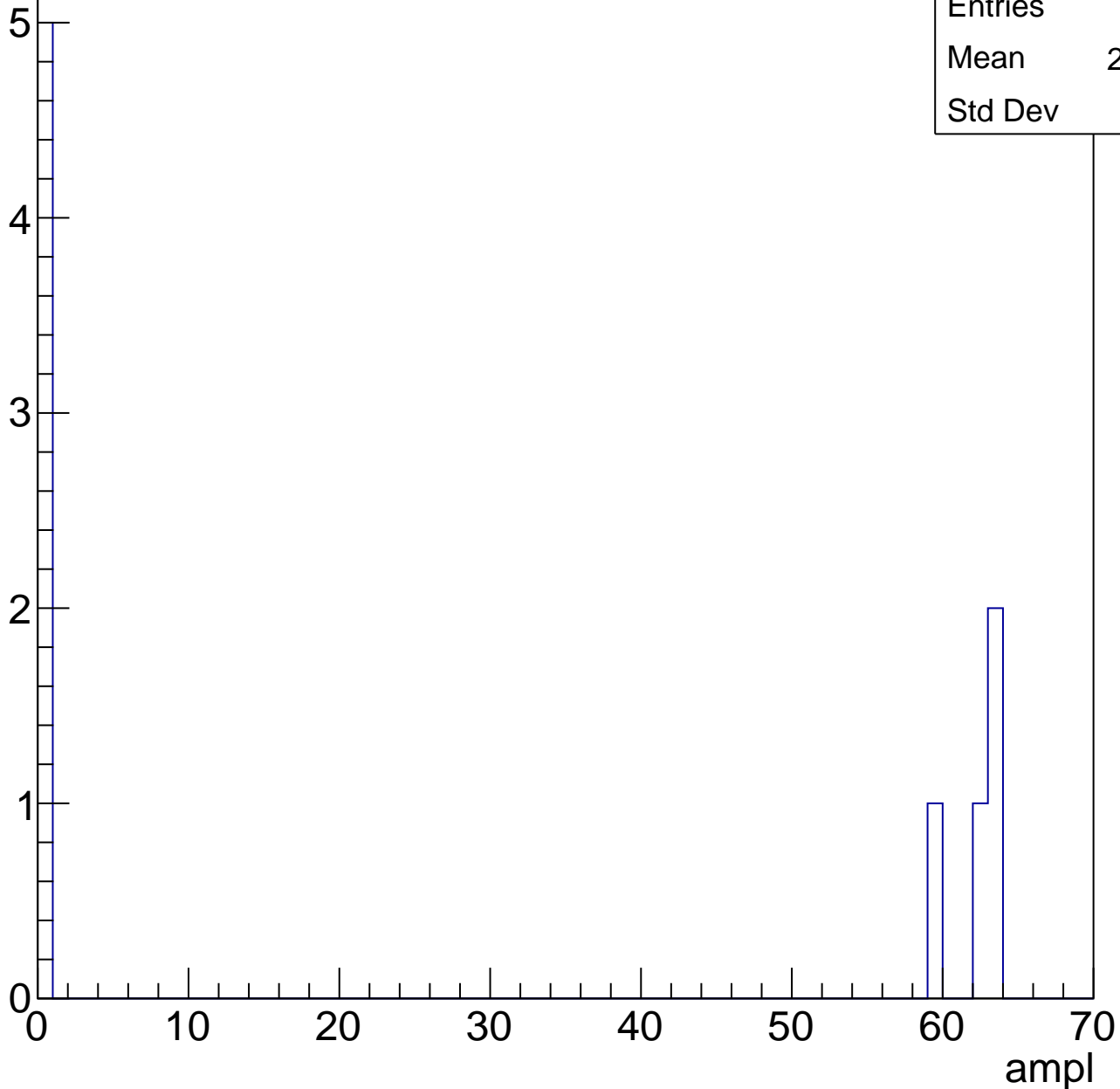


B0L101S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	9
Mean	27.44
Std Dev	30.7



B0L101S, U15-ch123, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	164
Mean	27.82
Std Dev	5.854

Gaus mean : 28.5080

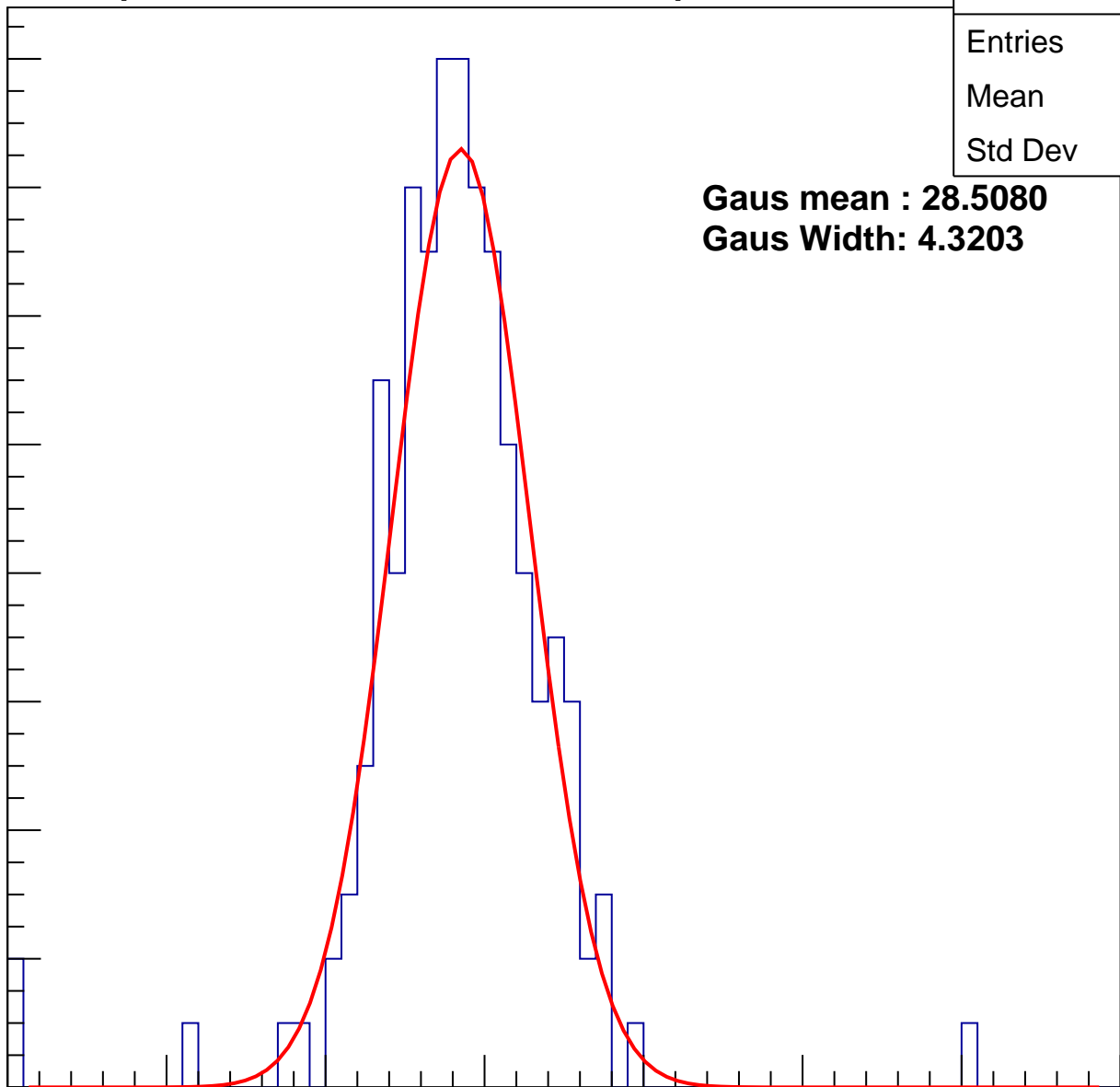
Gaus Width: 4.3203

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

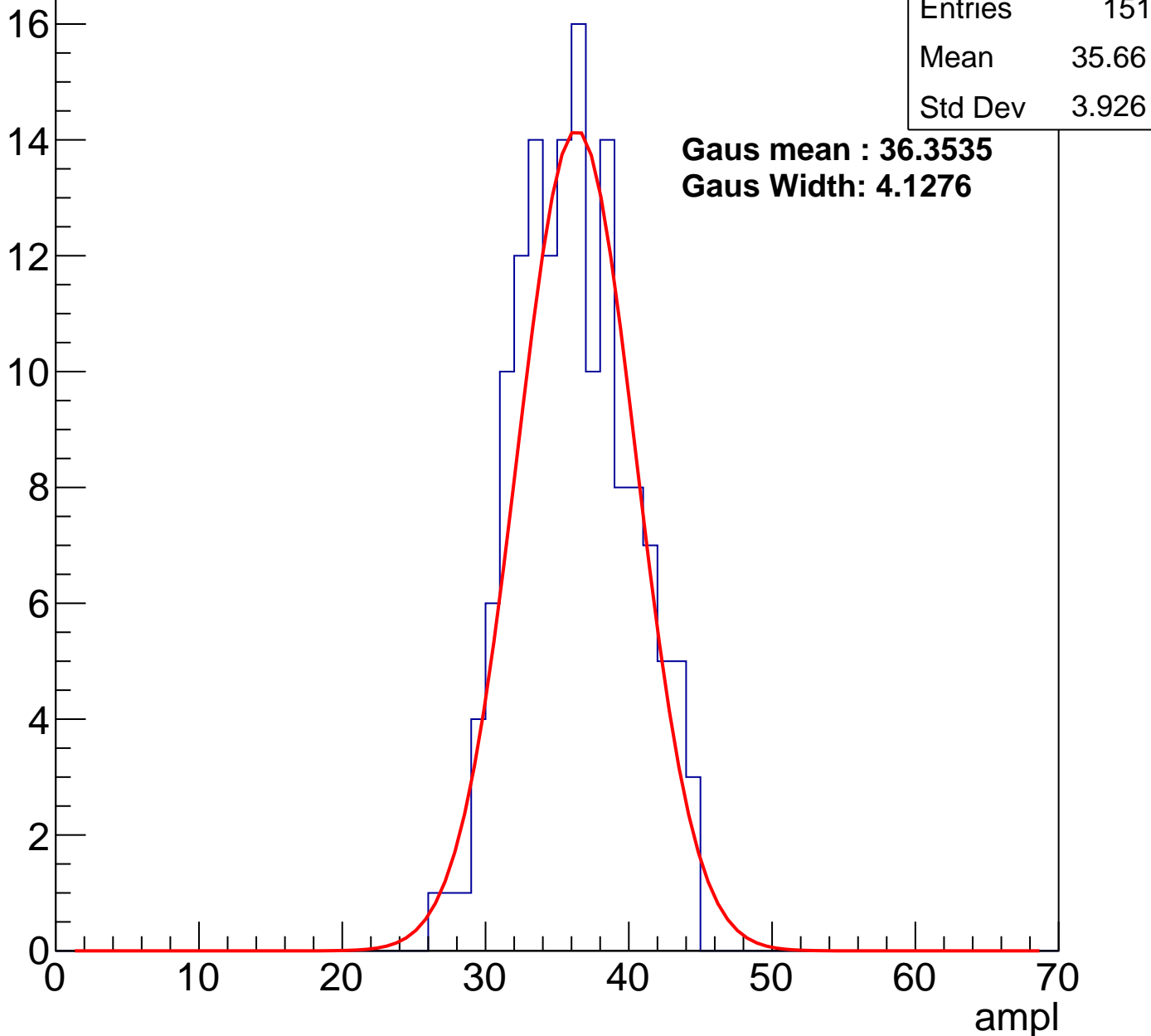
ampl



B0L101S, U15-ch123, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch123, adc2

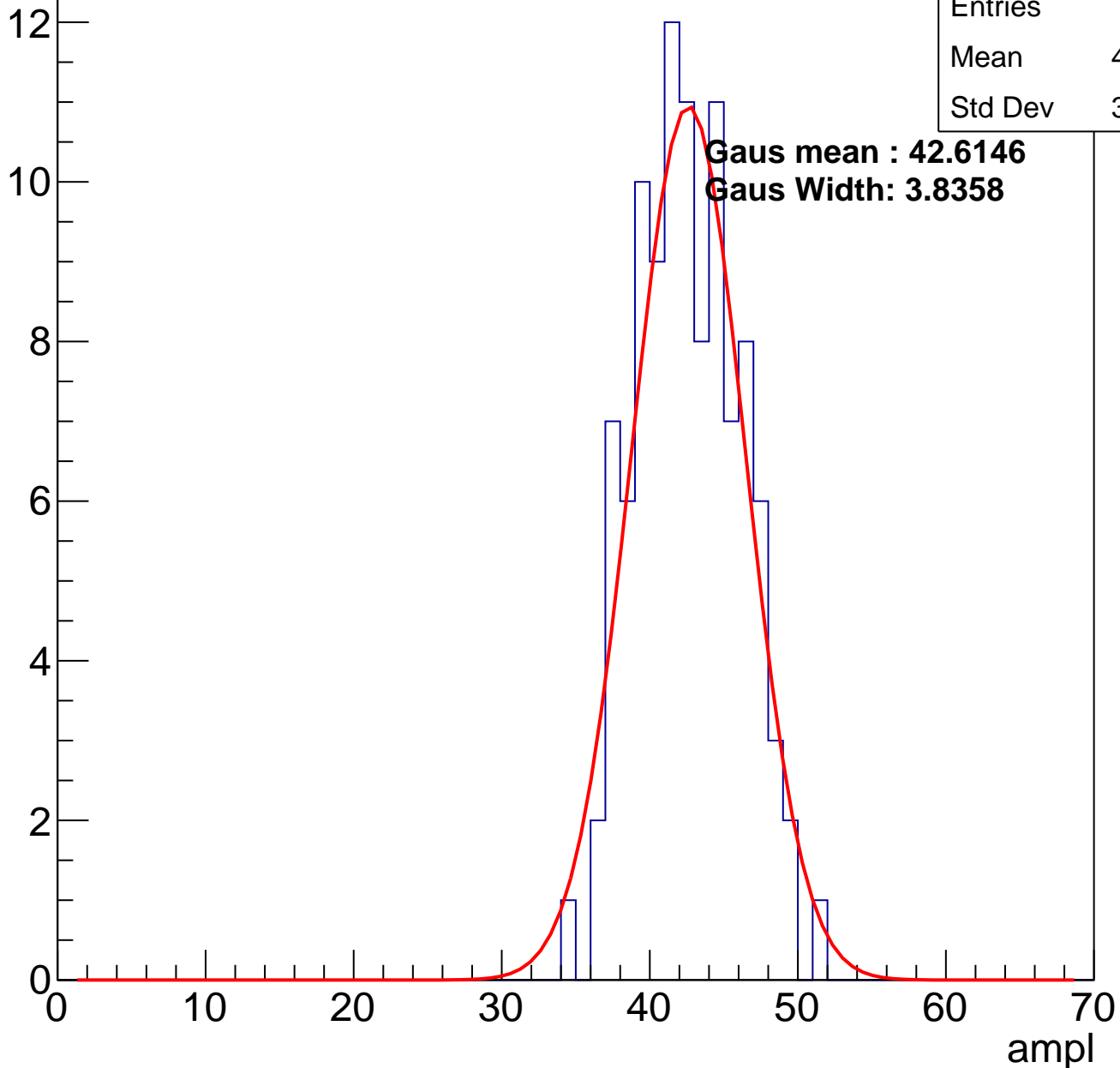
calib_packv5_042523_0143.root, FC#1, port C1

Entries	104
Mean	42.14
Std Dev	3.429

Gaus mean : 42.6146

Gaus Width: 3.8358

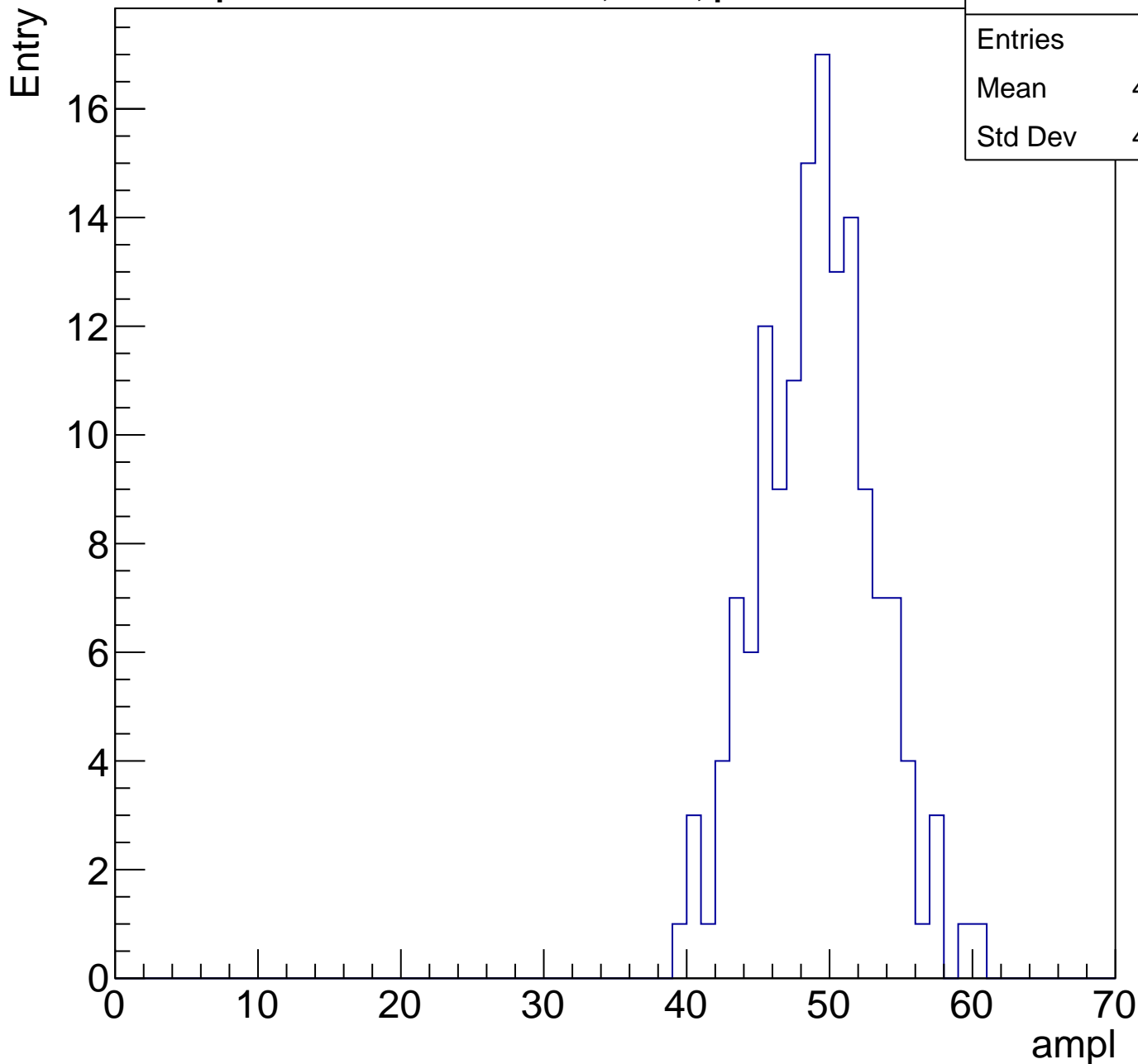
Entry



B0L101S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	146
Mean	48.66
Std Dev	4.023



B0L101S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#1, port C1

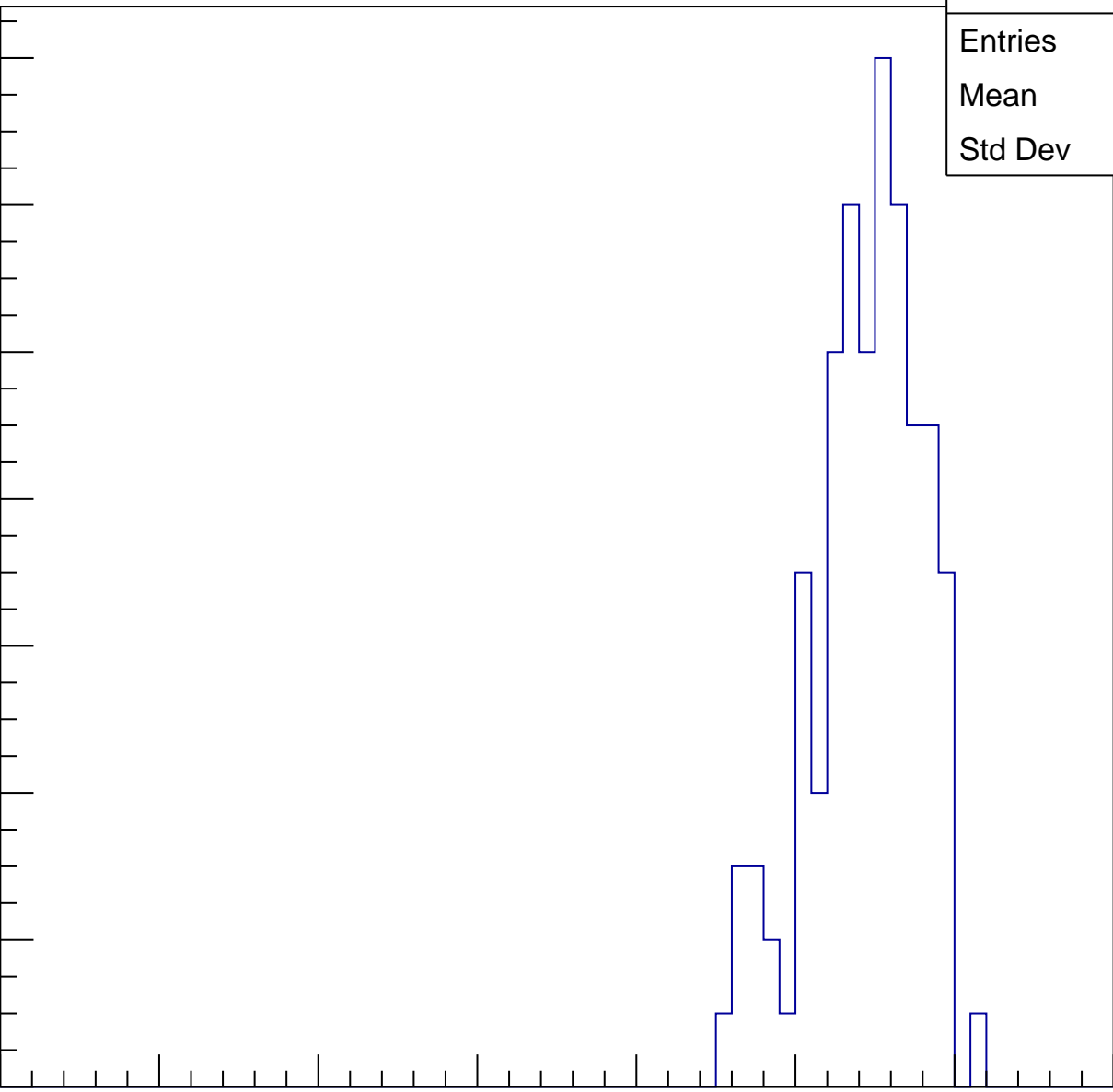
Entries	105
Mean	54
Std Dev	3.42

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch123, adc5

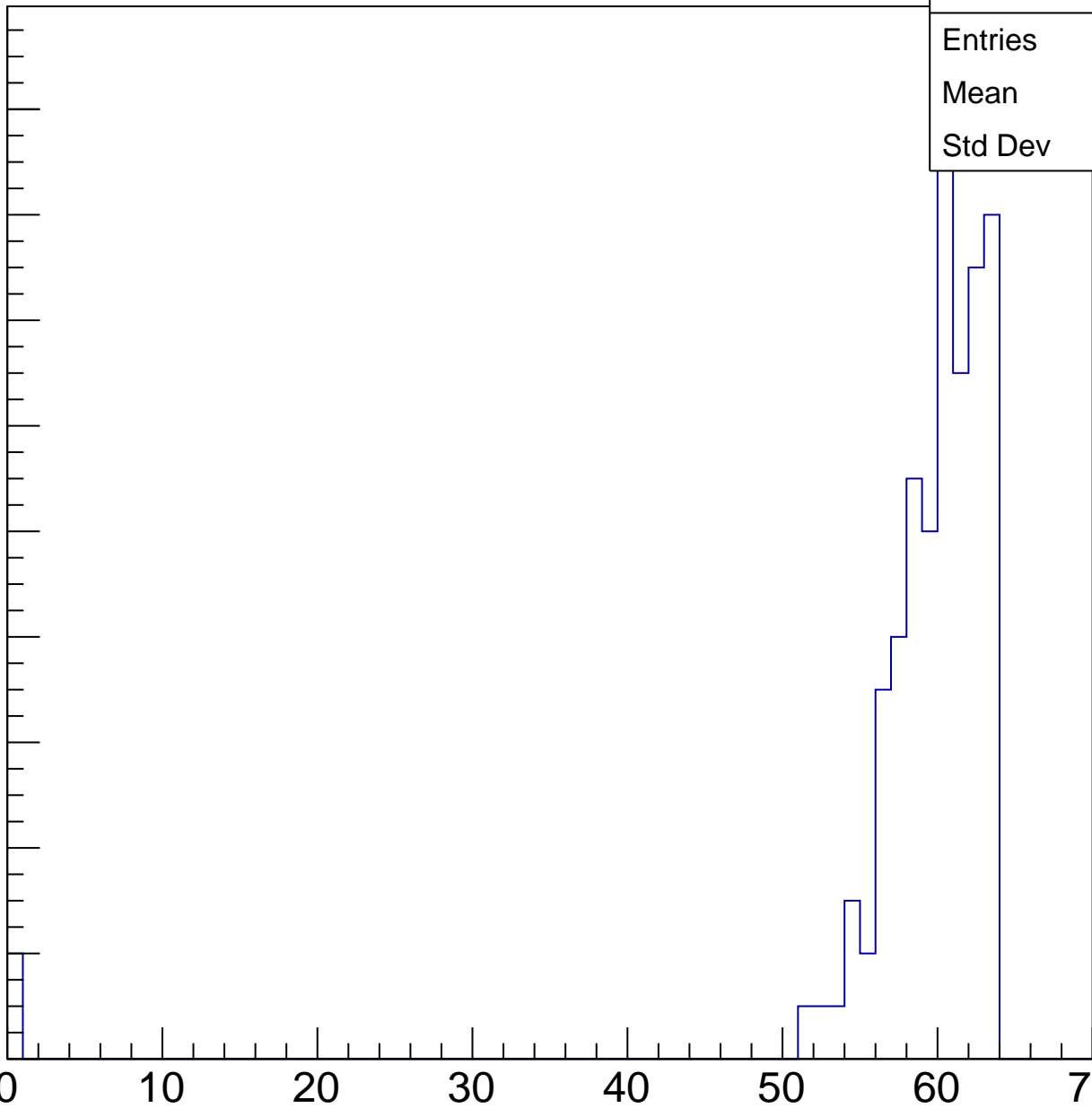
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	109
Mean	58.49
Std Dev	8.436

ampl

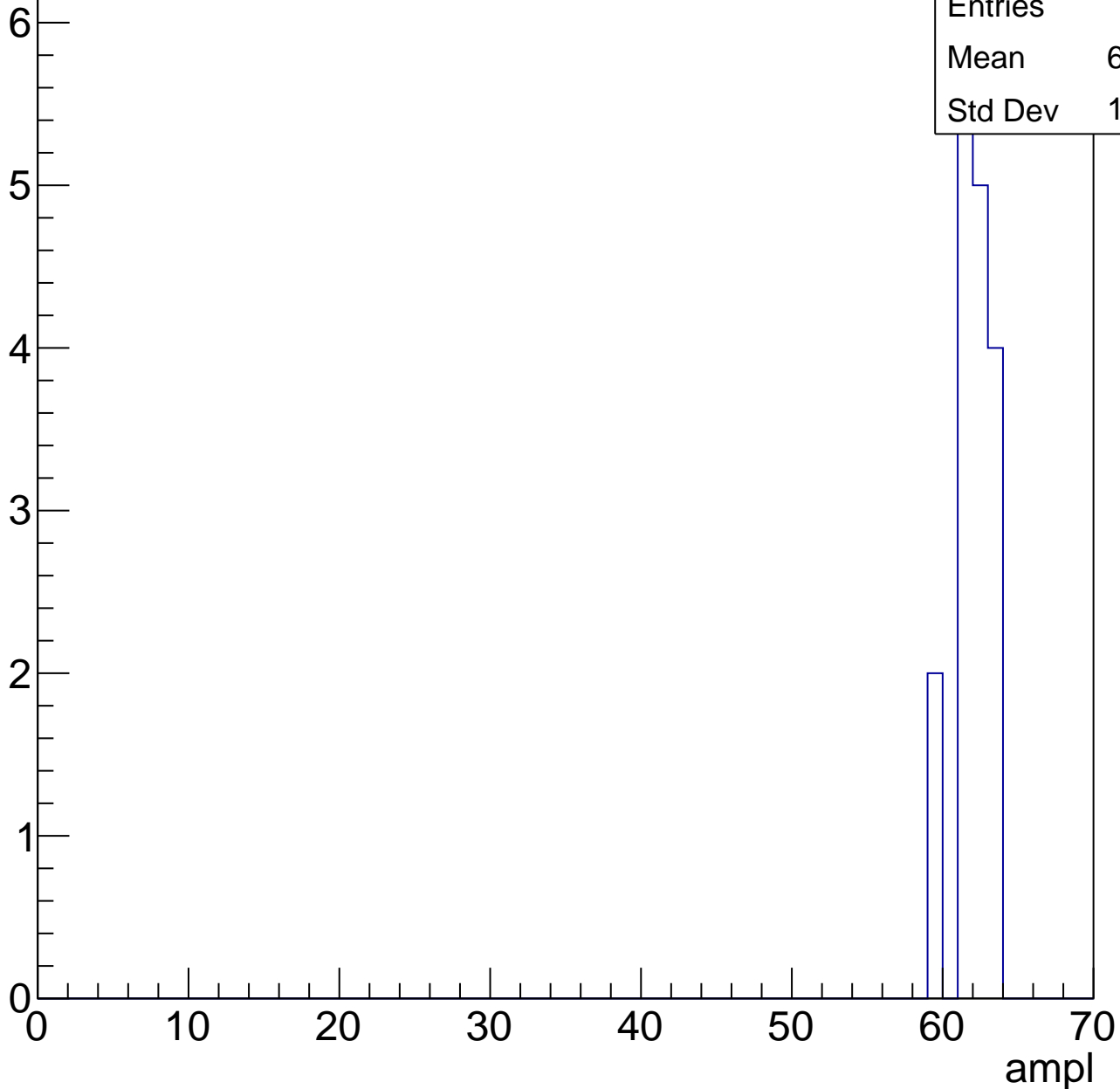


B0L101S, U15-ch123, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	17
Mean	61.53
Std Dev	1.194



B0L101S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch124, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	154
Mean	26.89
Std Dev	6.204

Gaus mean : 27.6686

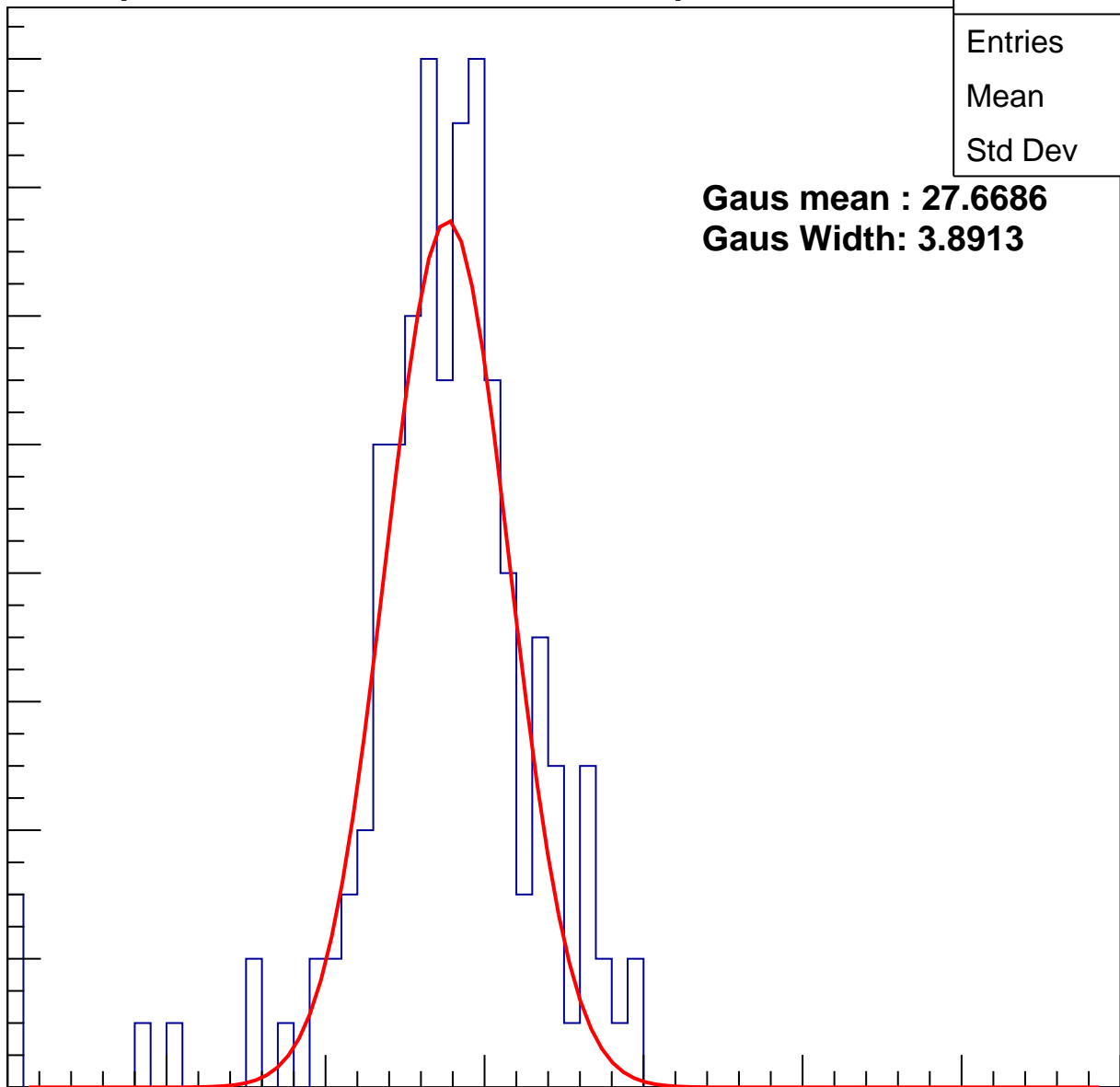
Gaus Width: 3.8913

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch124, adc1

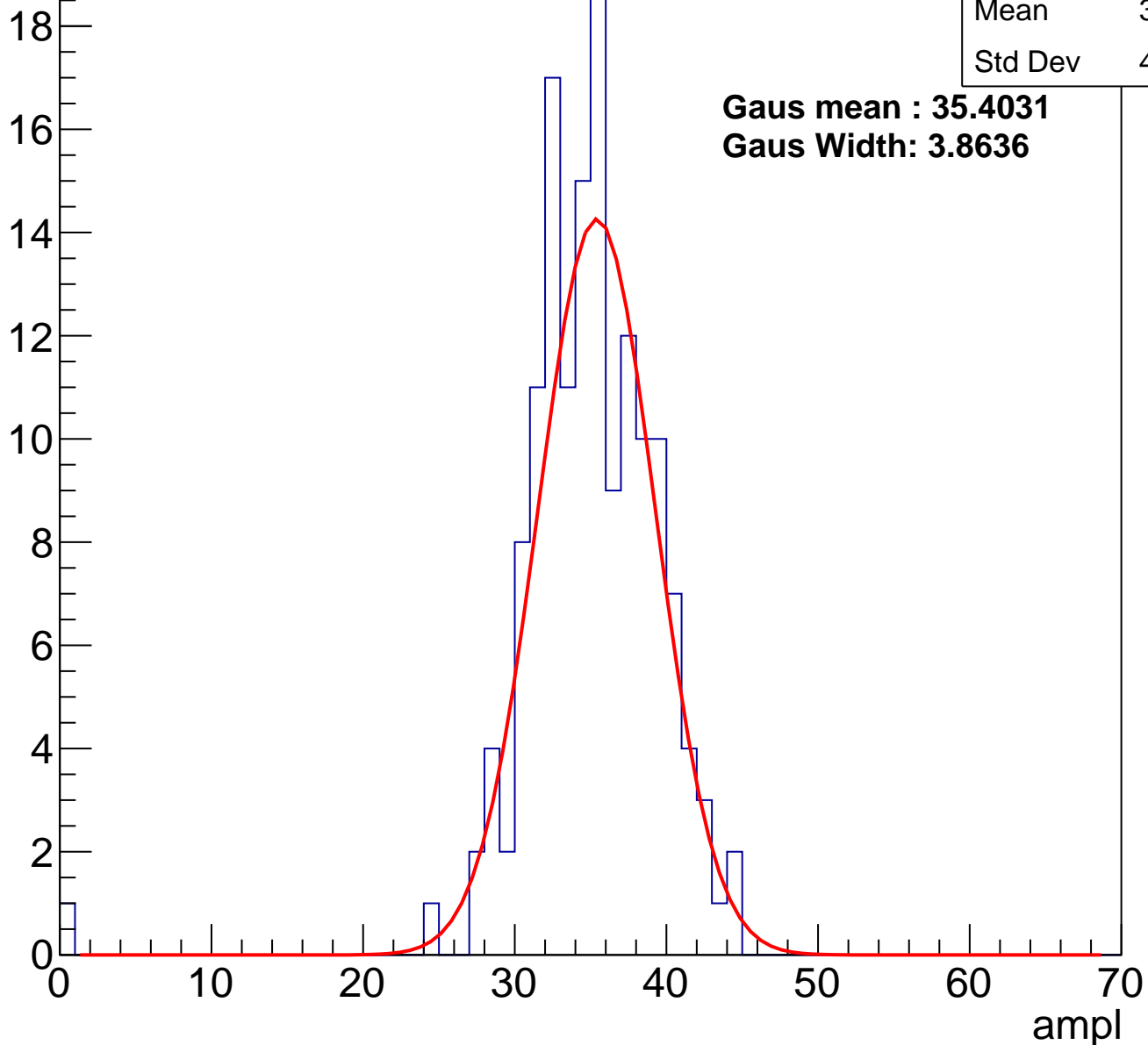
calib_packv5_042523_0143.root, FC#1, port C1

Entries	149
Mean	34.56
Std Dev	4.707

Gaus mean : 35.4031

Gaus Width: 3.8636

Entry



B0L101S, U15-ch124, adc2

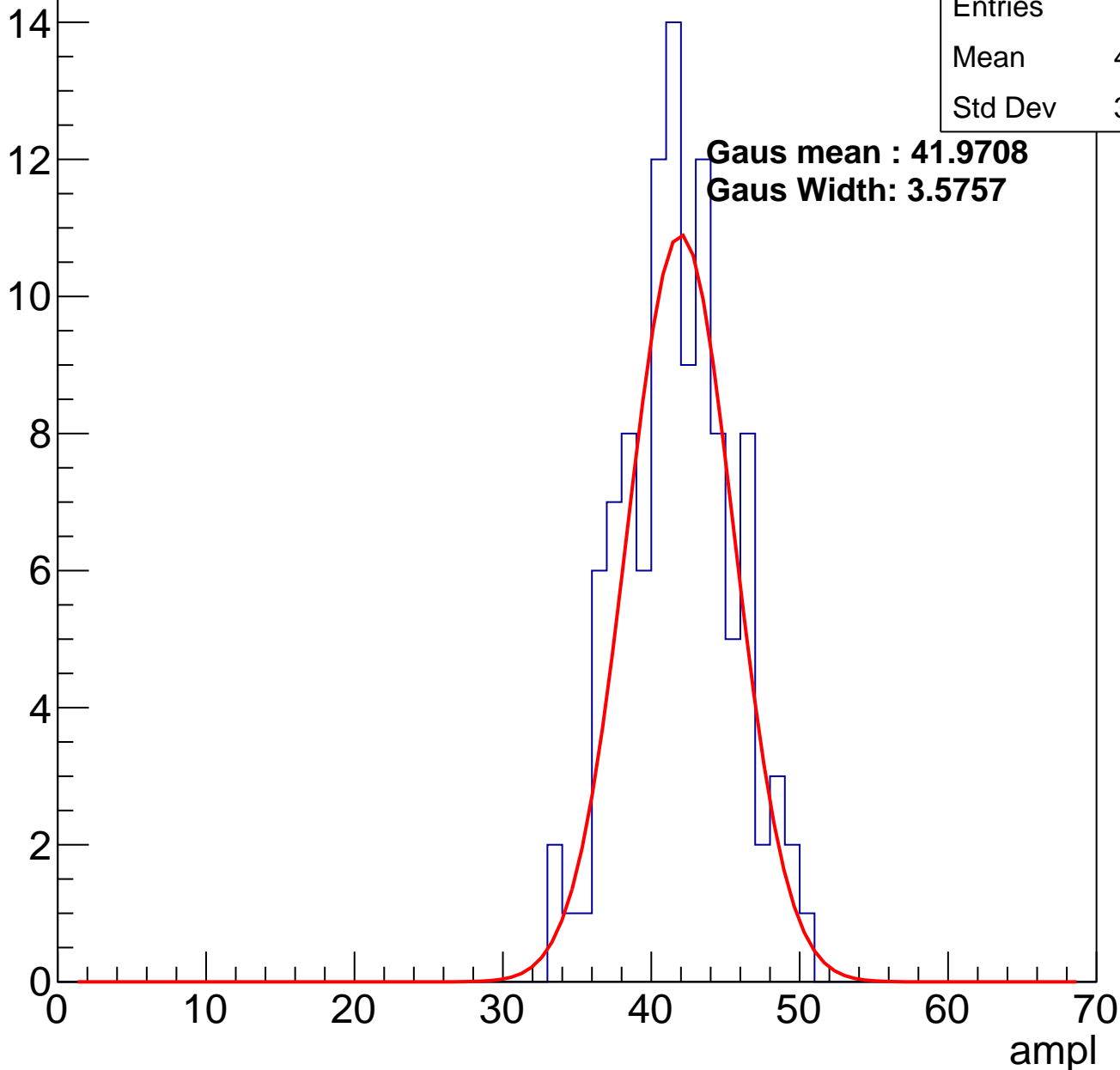
calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	107
Mean	41.37
Std Dev	3.624

Gaus mean : 41.9708

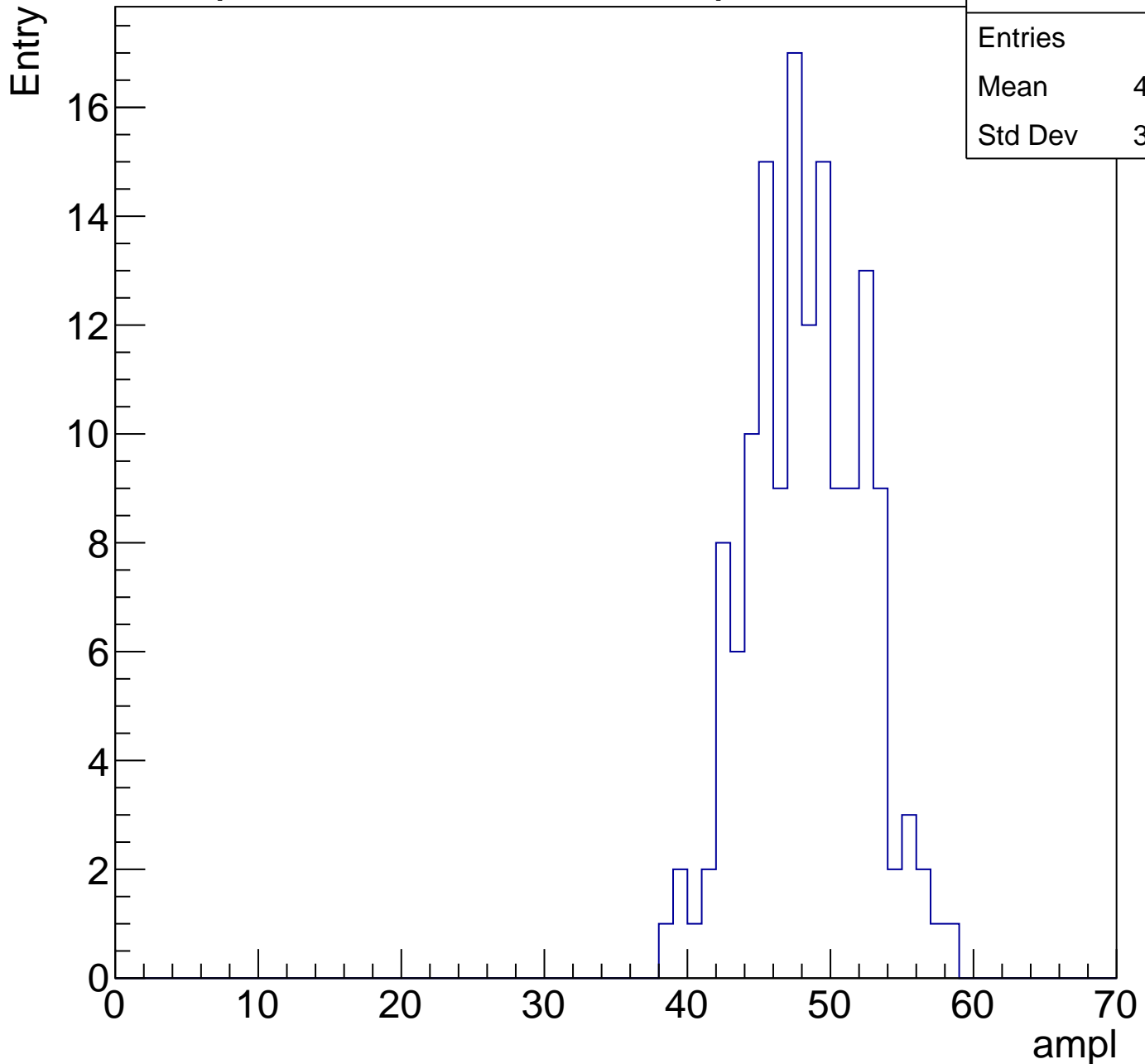
Gaus Width: 3.5757



B0L101S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#1, port C1

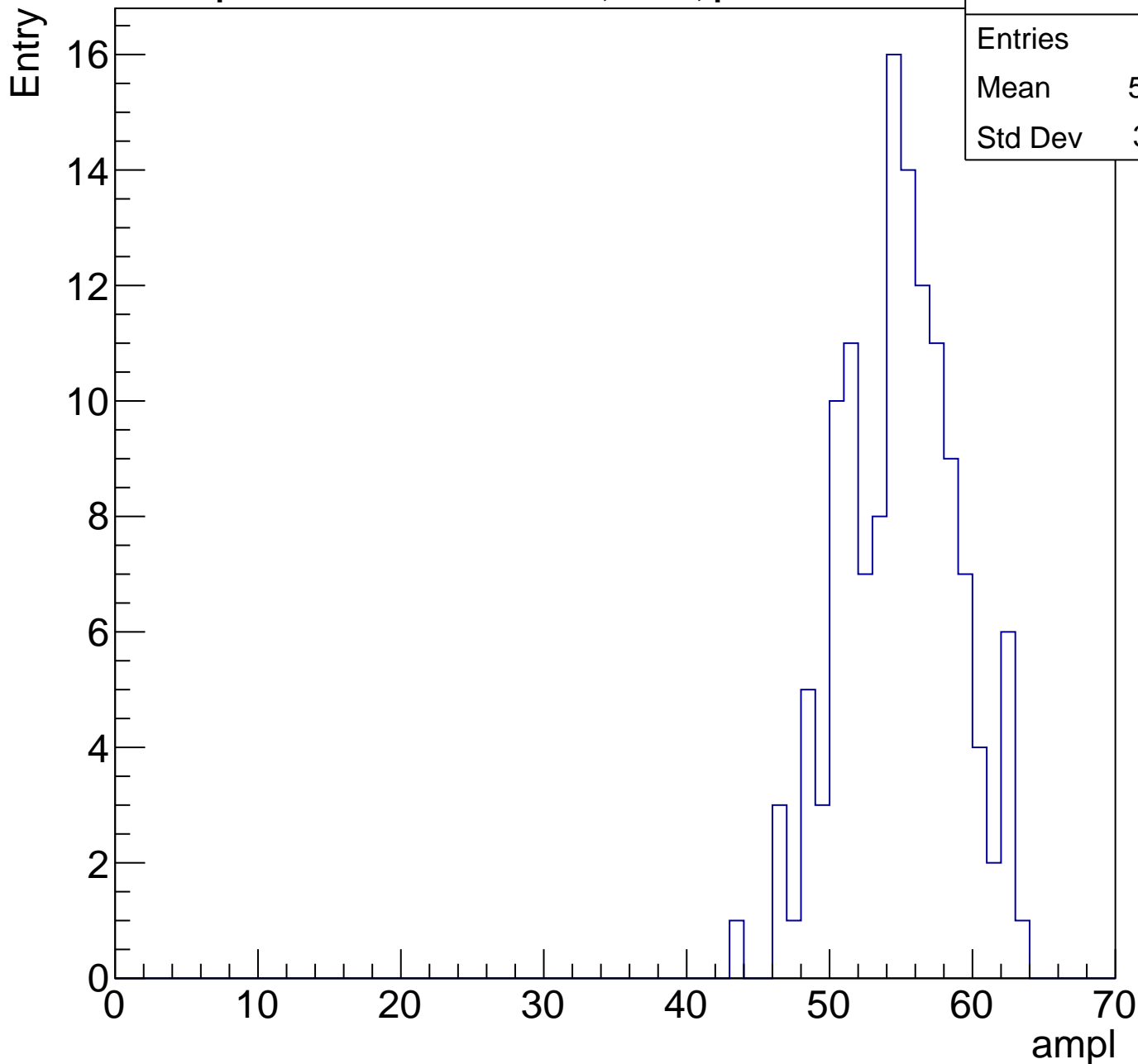
Entries	147
Mean	47.84
Std Dev	3.955



B0L101S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	131
Mean	54.42
Std Dev	3.981



B0L101S, U15-ch124, adc5

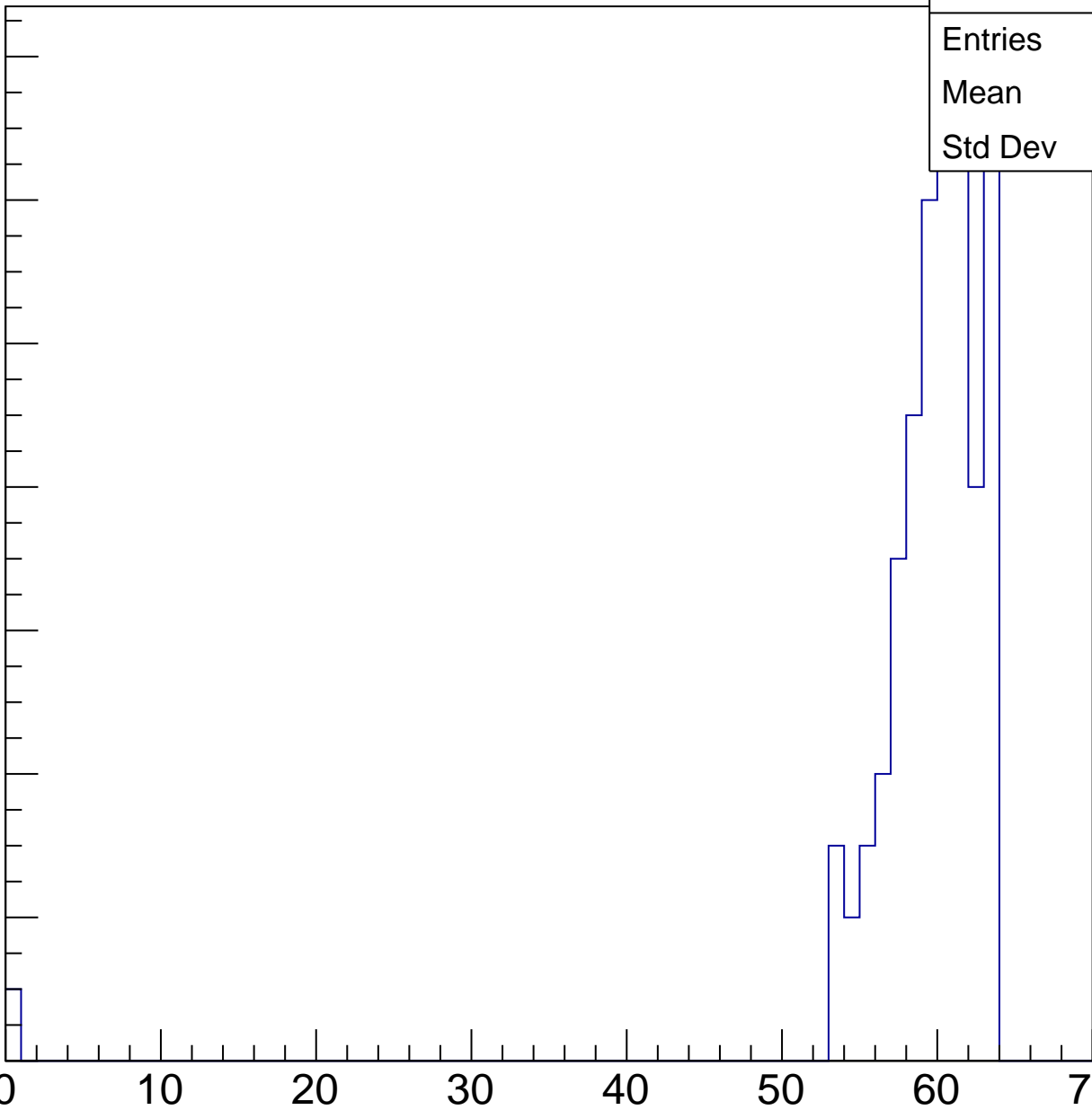
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	89
Mean	58.8
Std Dev	6.789

ampl

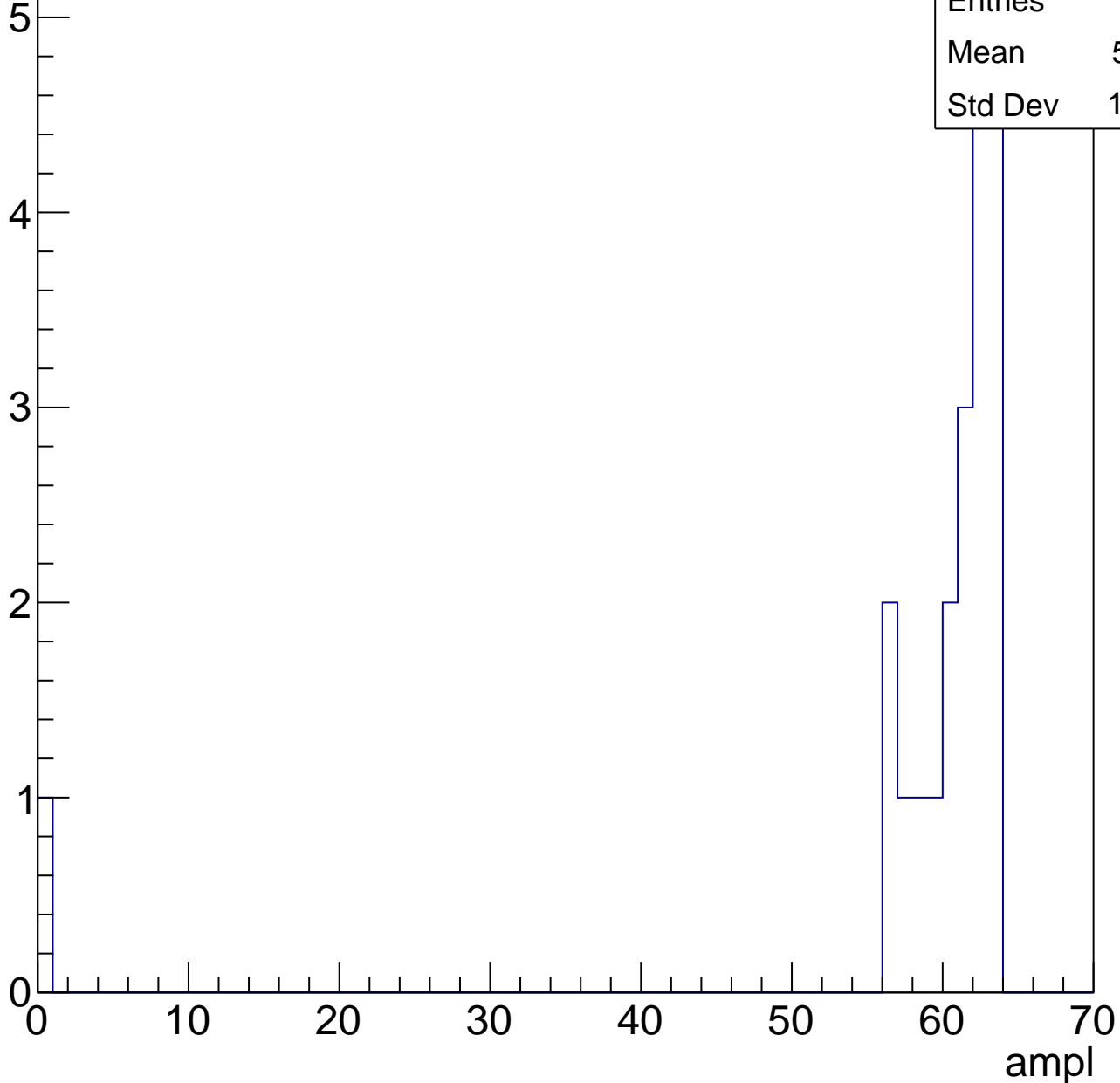


B0L101S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

Entries	21
Mean	57.81
Std Dev	13.12



B0L101S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	53.8
Std Dev	15.92

0 10 20 30 40 50 60 70

ampl

B0L101S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	53.8
Std Dev	15.92

0 10 20 30 40 50 60 70

ampl

B0L101S, U15-ch125, adc0

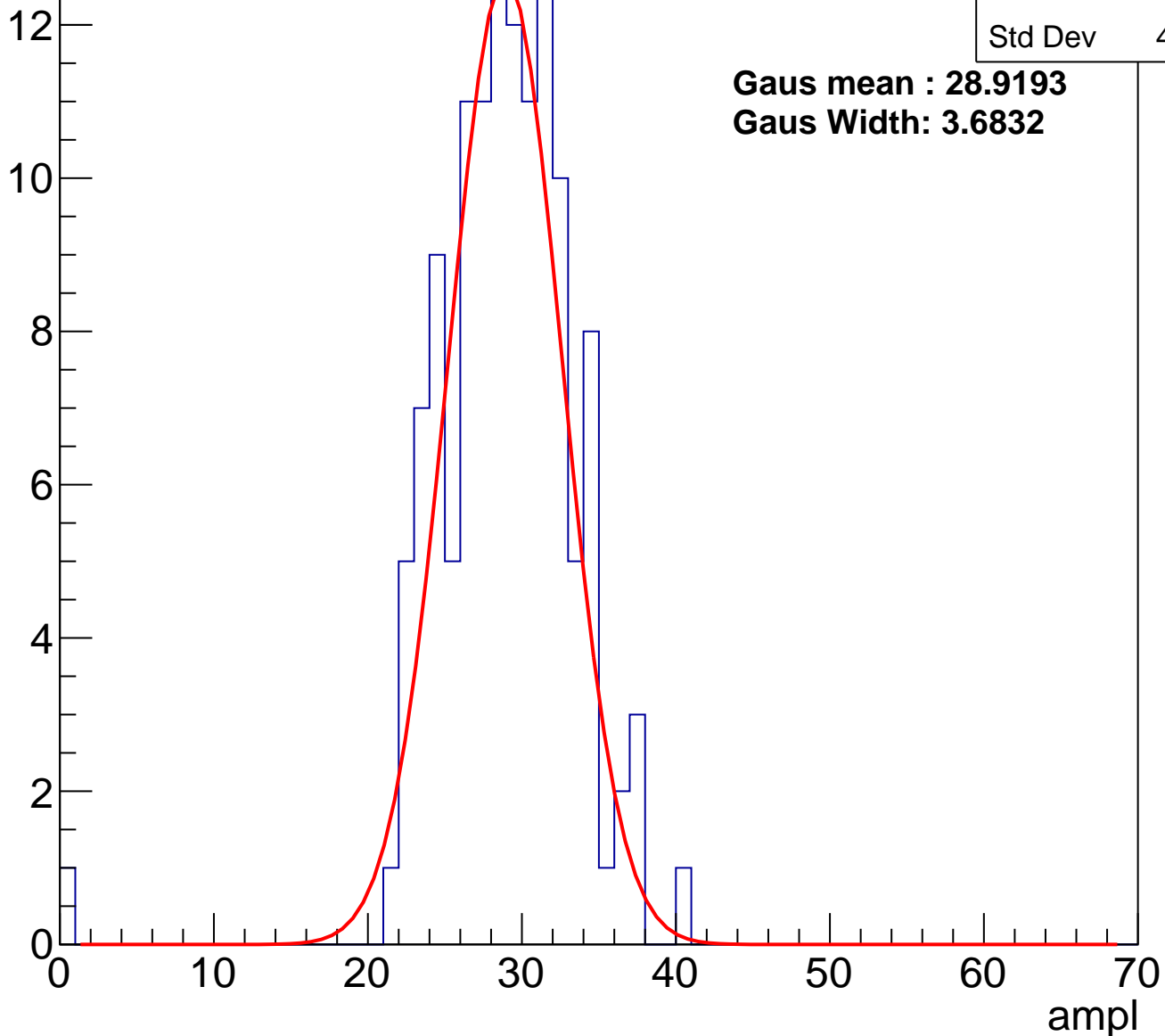
calib_packv5_042523_0143.root, FC#1, port C1

Entries	129
Mean	28.5
Std Dev	4.584

Gaus mean : 28.9193

Gaus Width: 3.6832

Entry

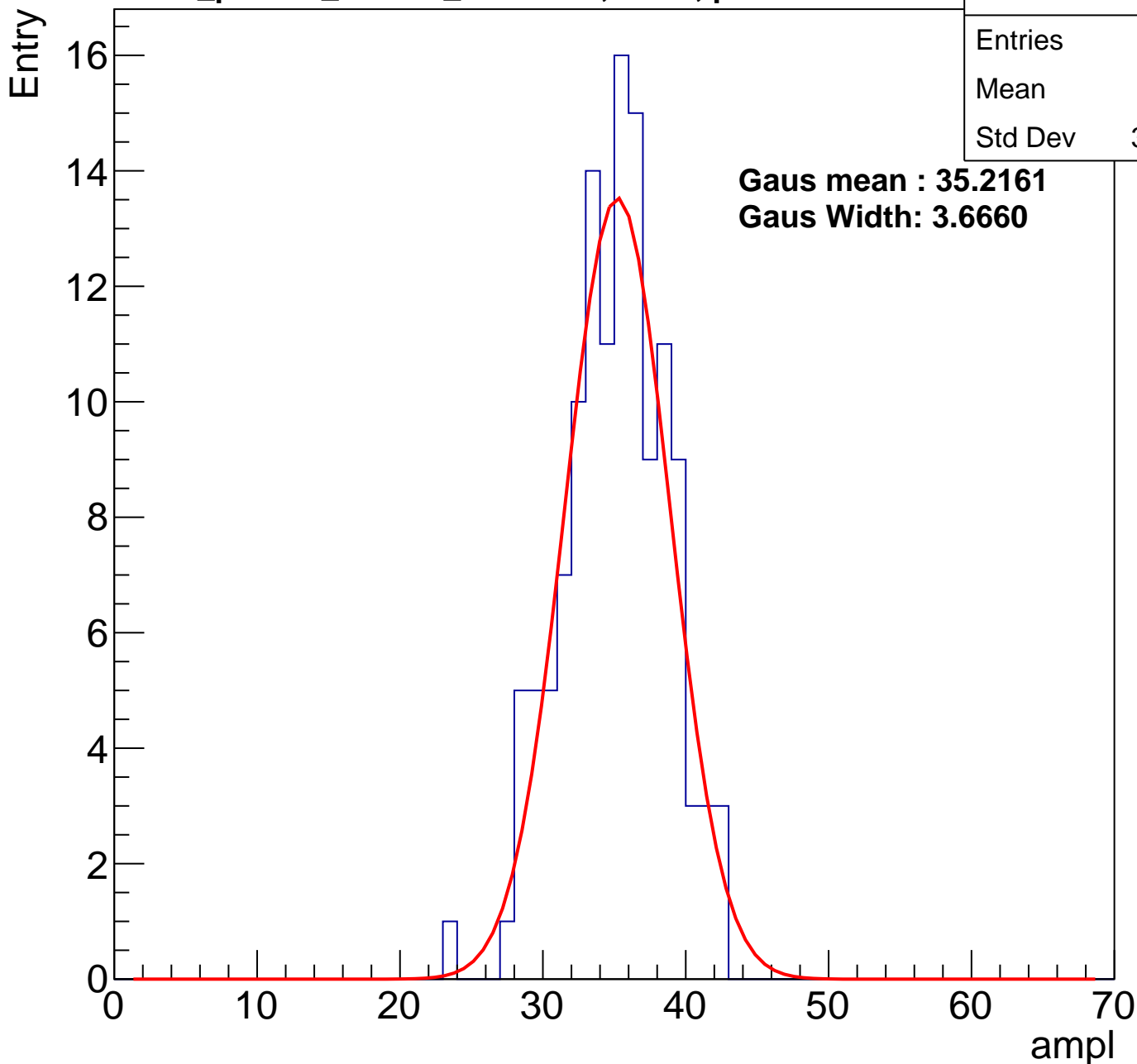


B0L101S, U15-ch125, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	128
Mean	34.6
Std Dev	3.569

Gaus mean : 35.2161
Gaus Width: 3.6660



B0L101S, U15-ch125, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

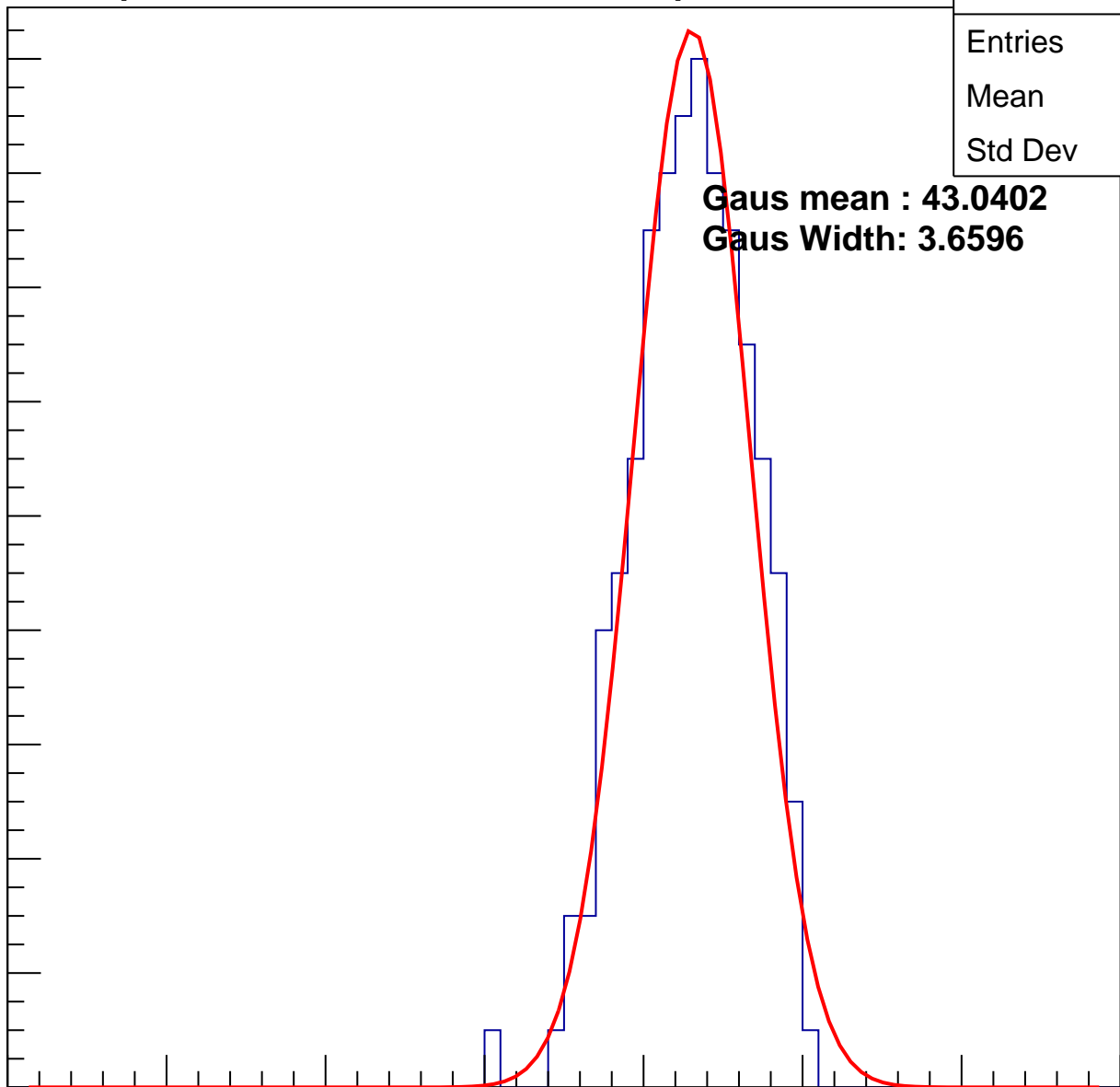
Entries	172
Mean	42.49
Std Dev	3.635

Gaus mean : 43.0402

Gaus Width: 3.6596

0 10 20 30 40 50 60 70

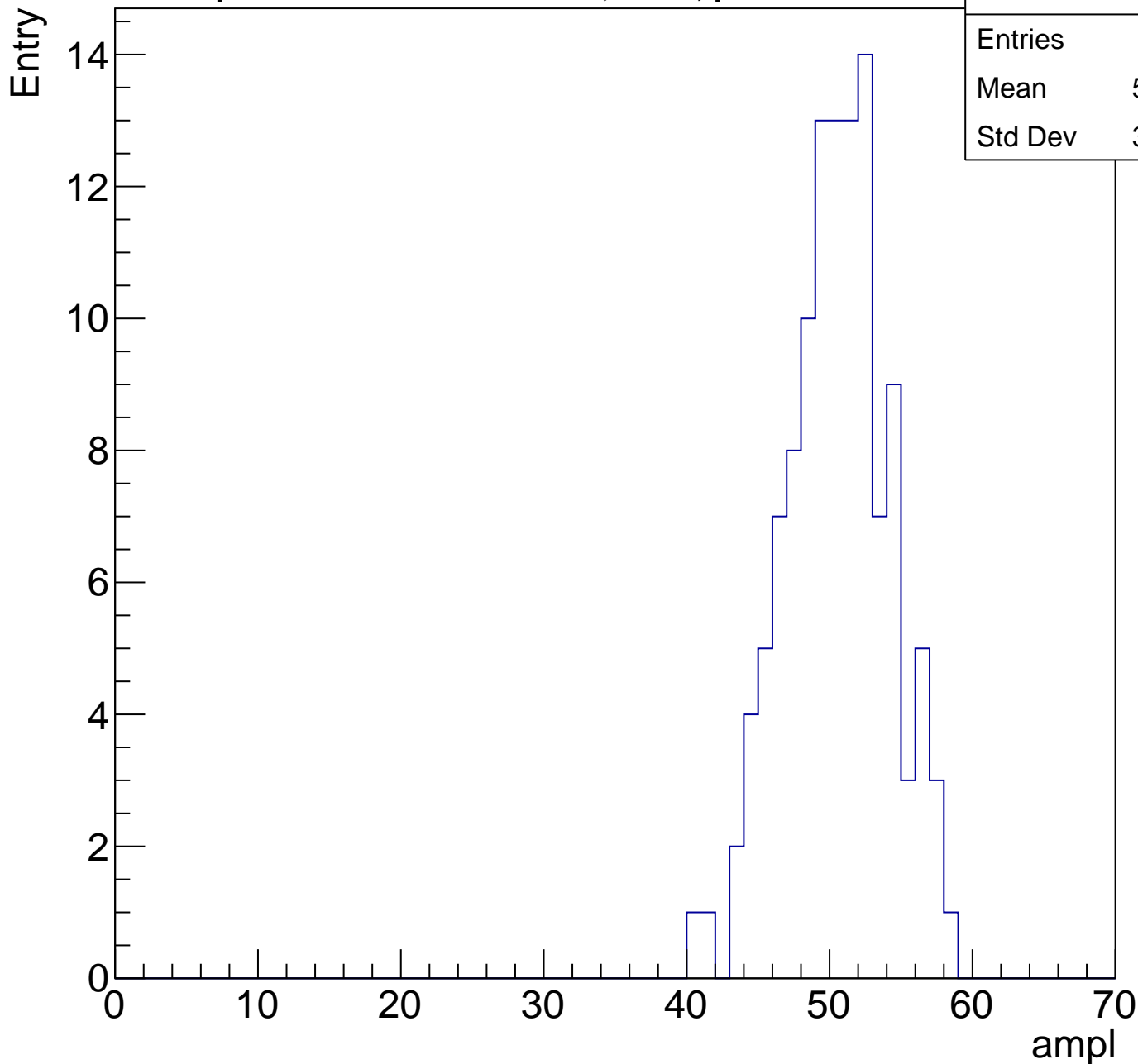
ampl



B0L101S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	119
Mean	50.04
Std Dev	3.582



B0L101S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

0

10

20

30

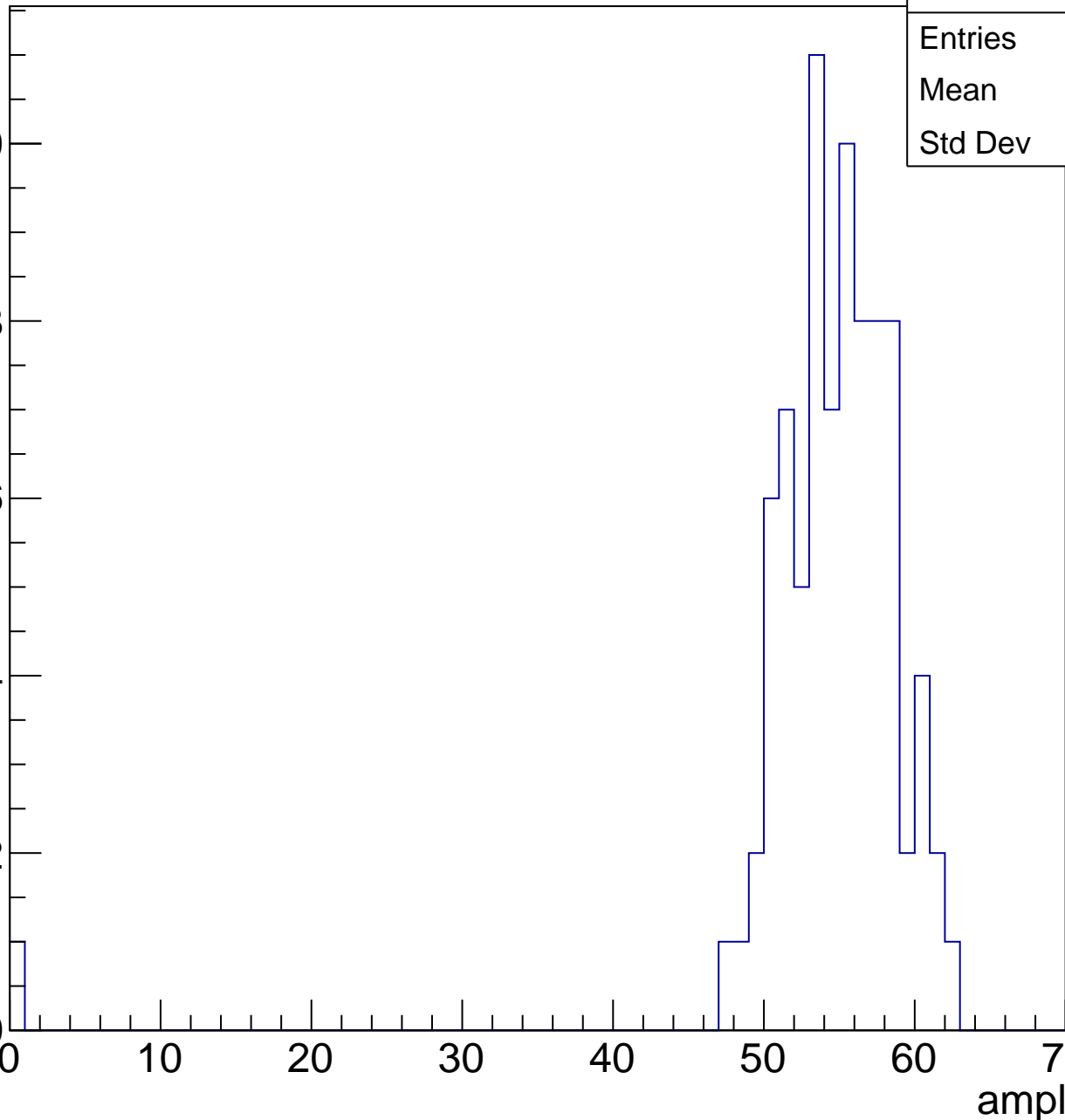
40

50

60

ampl

Entries	84
Mean	53.94
Std Dev	6.76



B0L101S, U15-ch125, adc5

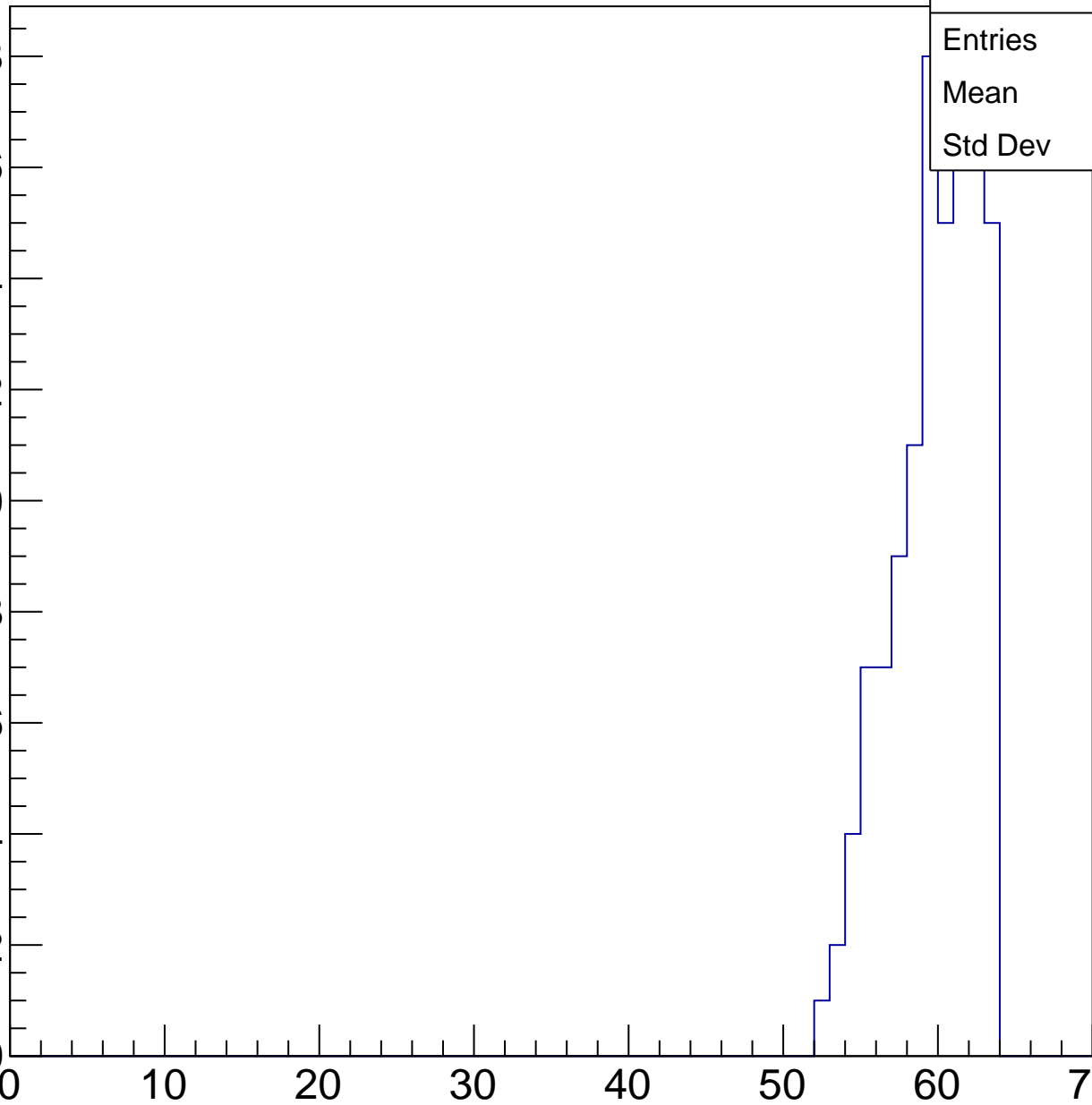
calib_packv5_042523_0143.root, FC#1, port C1

Entry

18
16
14
12
10
8
6
4
2
0

Entries	123
Mean	59.35
Std Dev	2.711

ampl

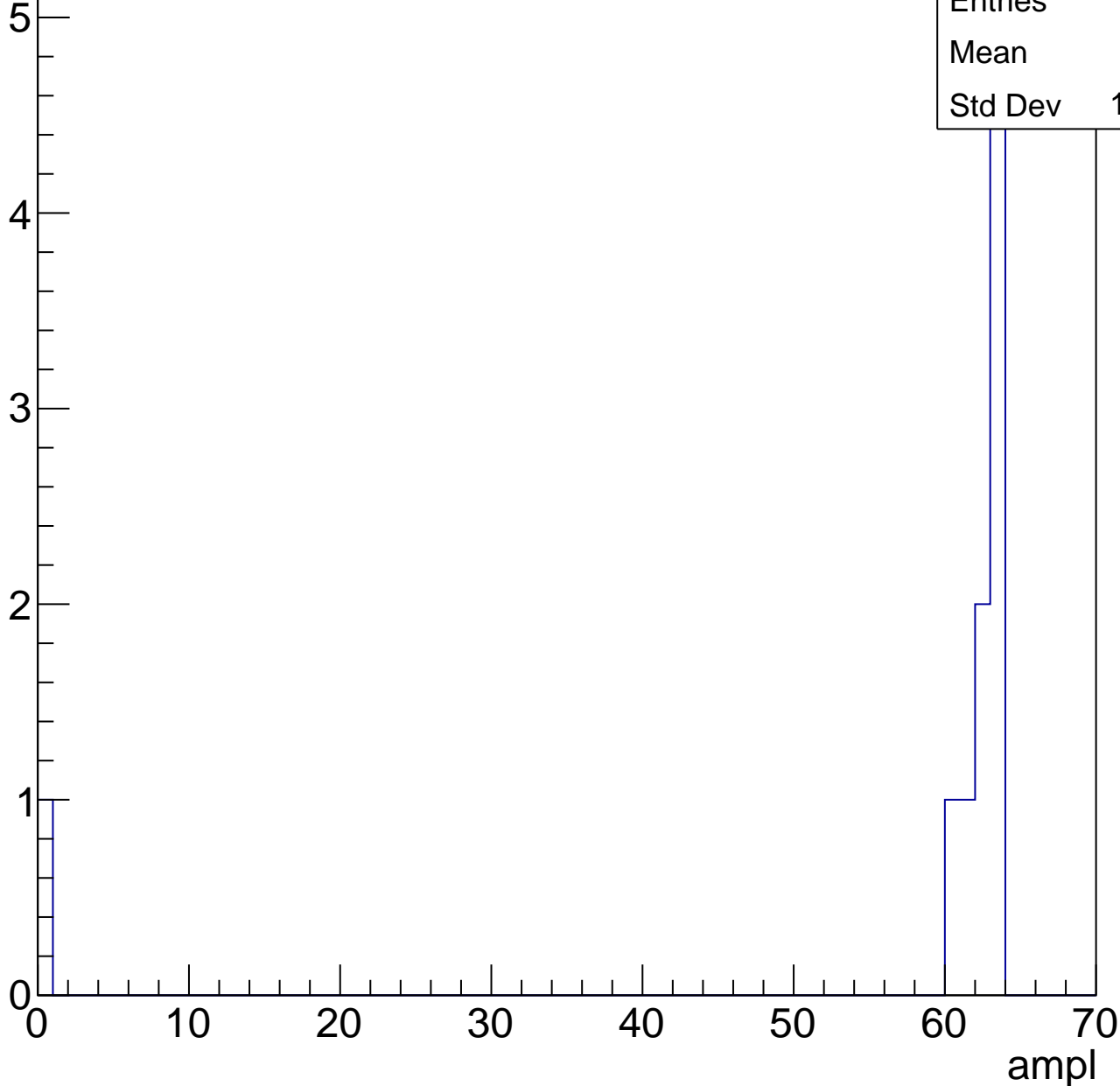


B0L101S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

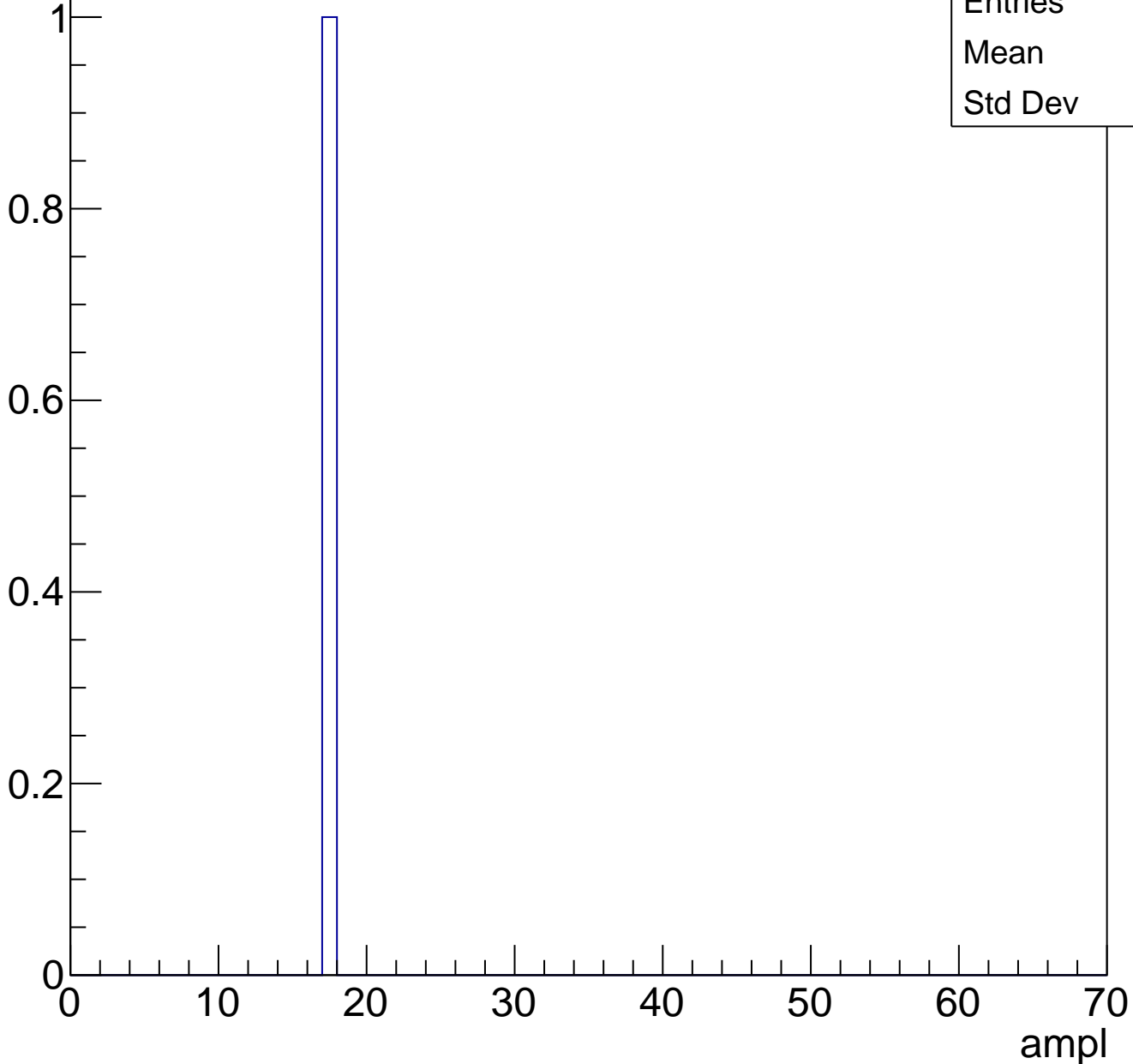
Entries	10
Mean	56
Std Dev	18.69



B0L101S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch126, adc0

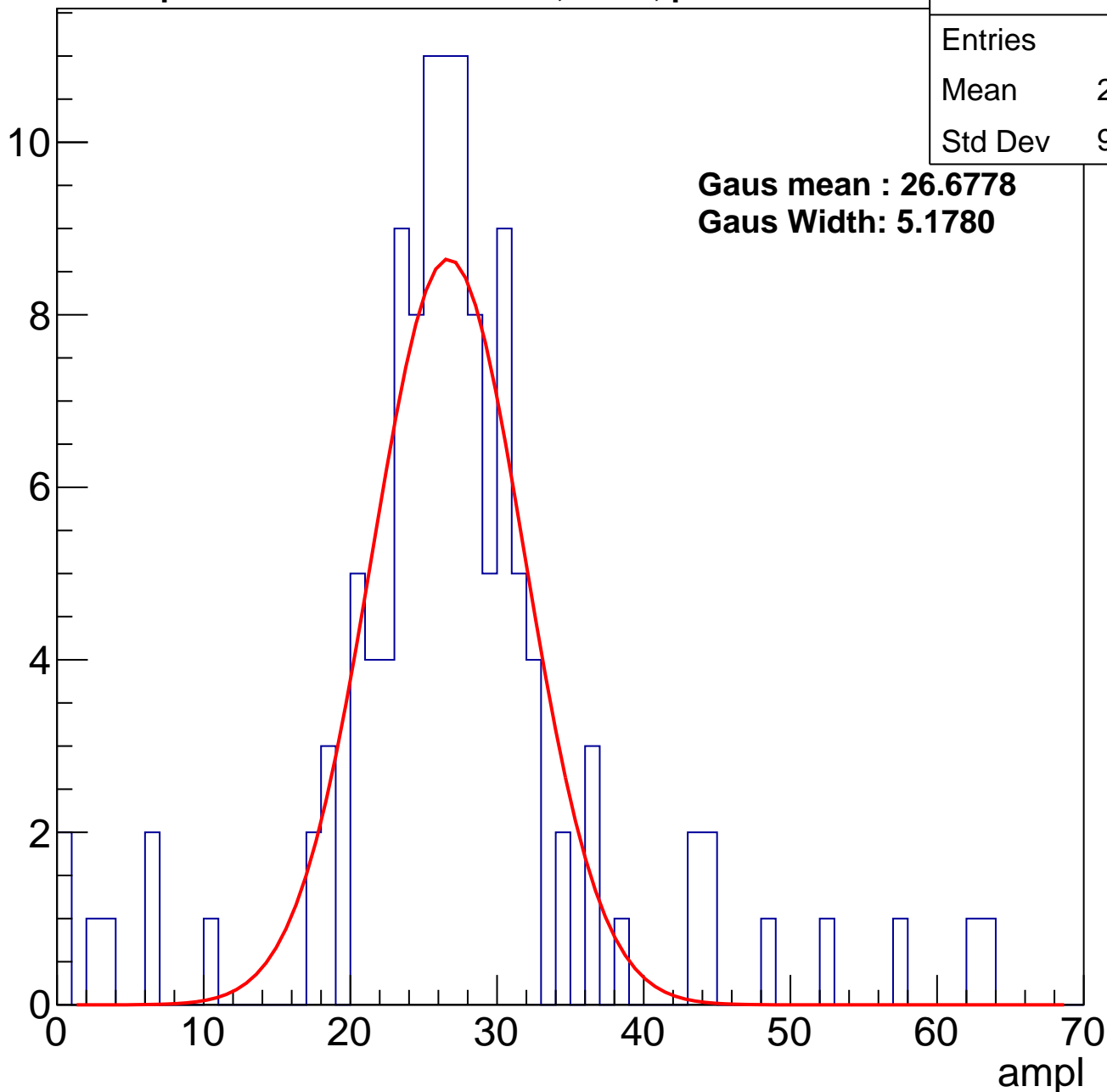
calib_packv5_042523_0143.root, FC#1, port C1

Entries	121
Mean	26.74
Std Dev	9.686

Gaus mean : 26.6778

Gaus Width: 5.1780

Entry



B0L101S, U15-ch126, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	174
Mean	33.37
Std Dev	5.985

Gaus mean : 33.3452

Gaus Width: 3.9358

Entry

16
14
12
10
8
6
4
2
0

0

10

20

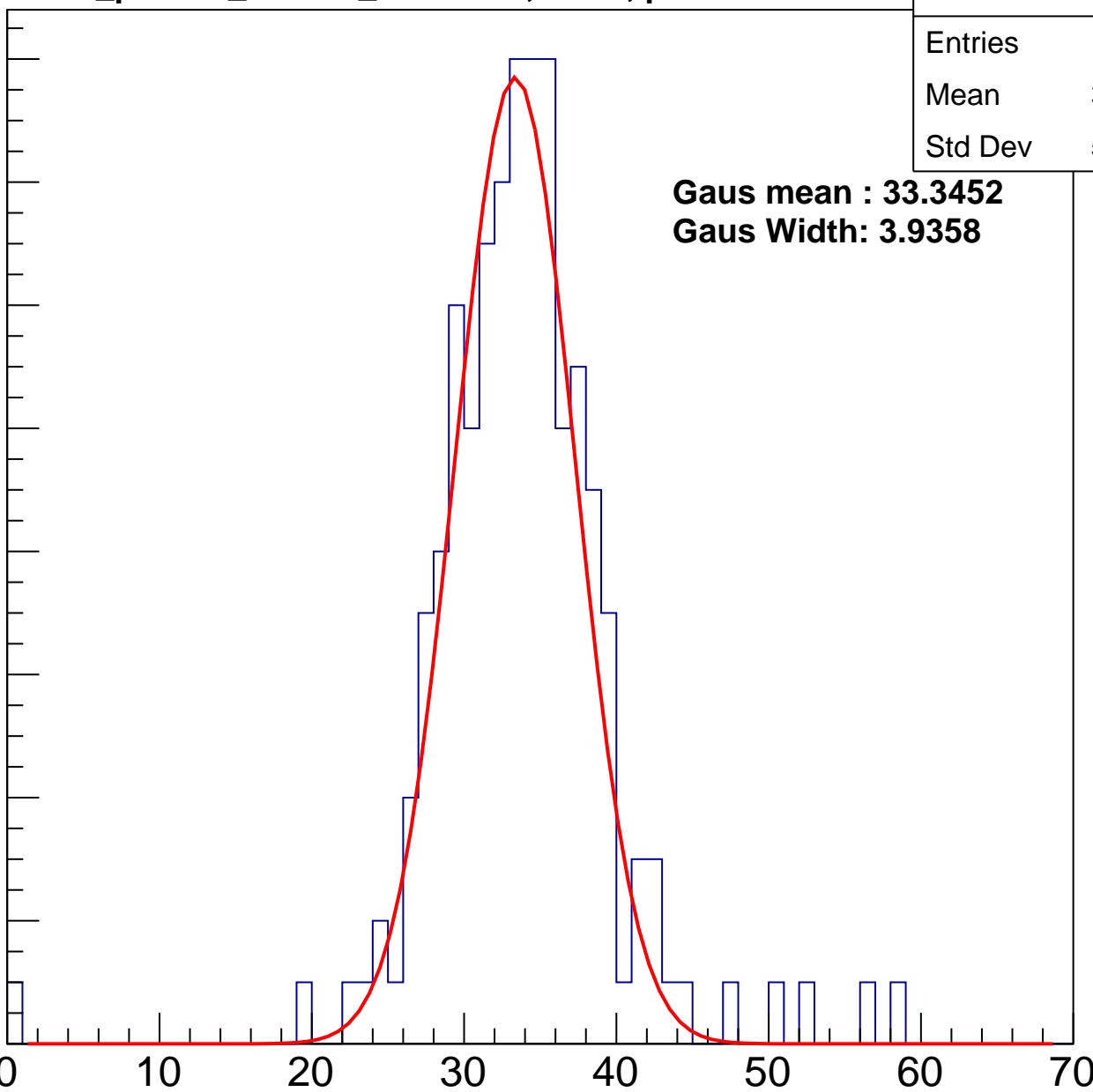
30

40

50

60

ampl



B0L101S, U15-ch126, adc2

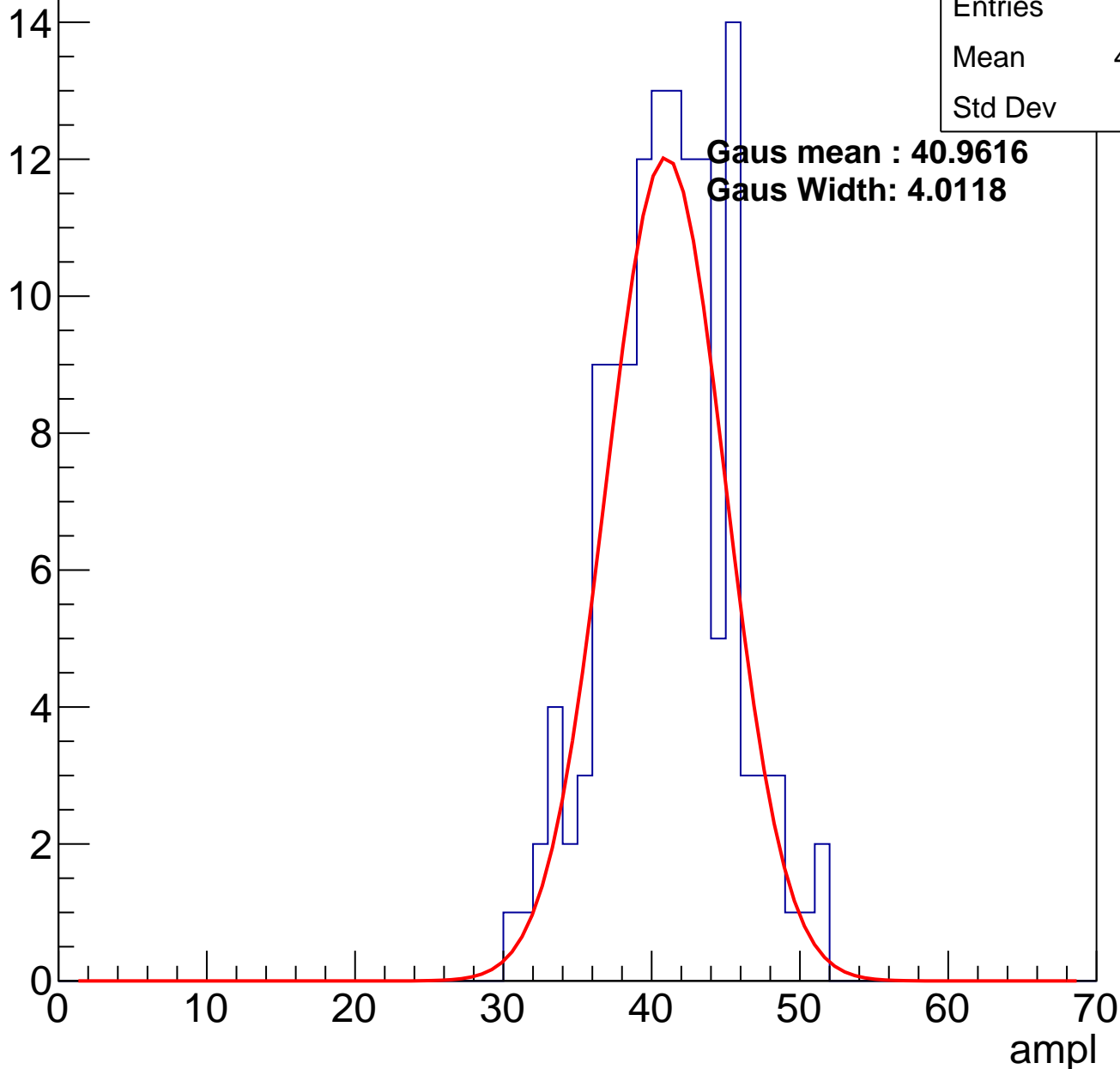
calib_packv5_042523_0143.root, FC#1, port C1

Entries	134
Mean	40.63
Std Dev	4.18

Gaus mean : 40.9616

Gaus Width: 4.0118

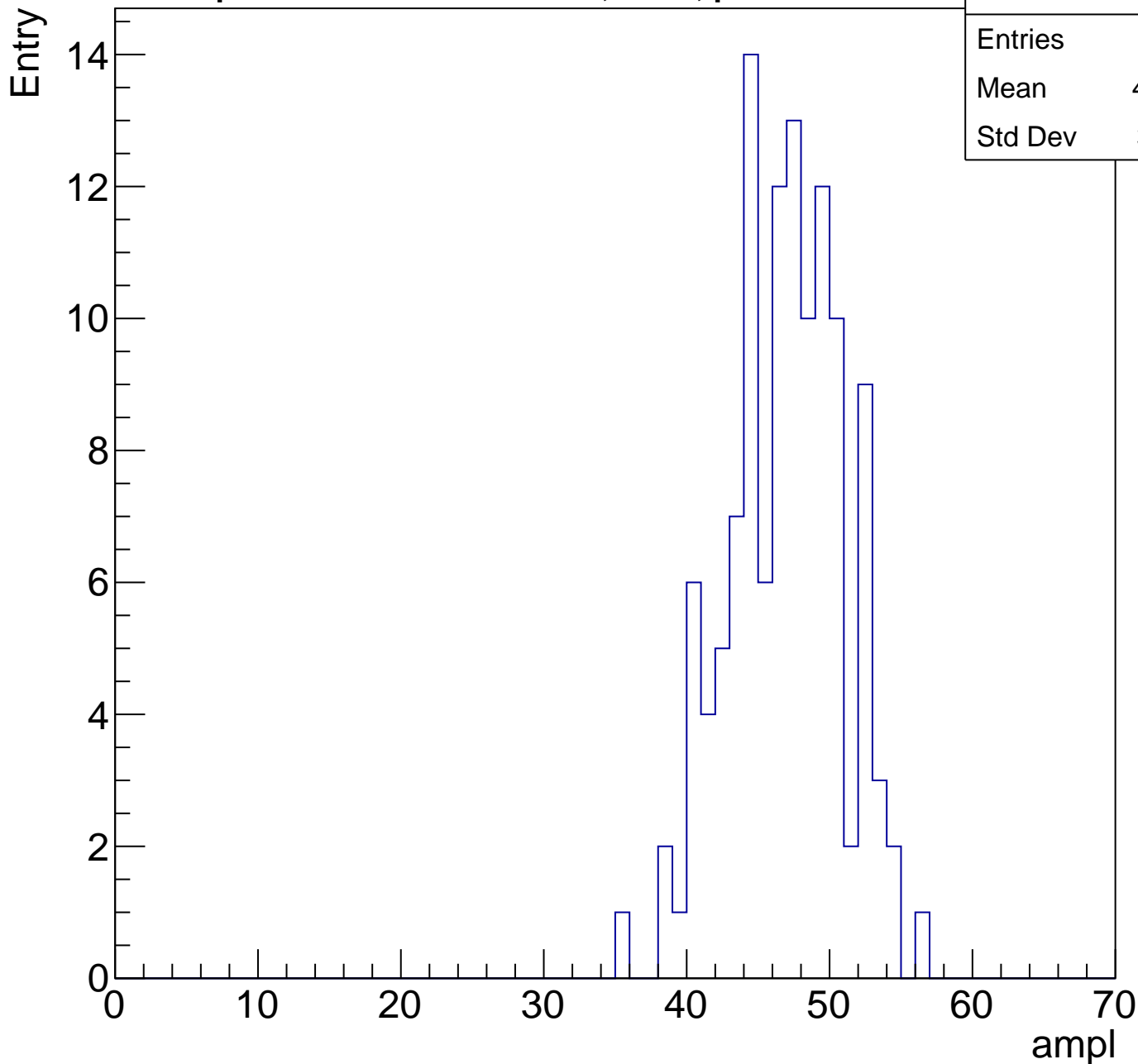
Entry



B0L101S, U15-ch126, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	120
Mean	46.46
Std Dev	3.941



B0L101S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#1, port C1

Entries	105
Mean	52.87
Std Dev	3.909

Entry

12

10

8

6

4

2

0

0

10

20

30

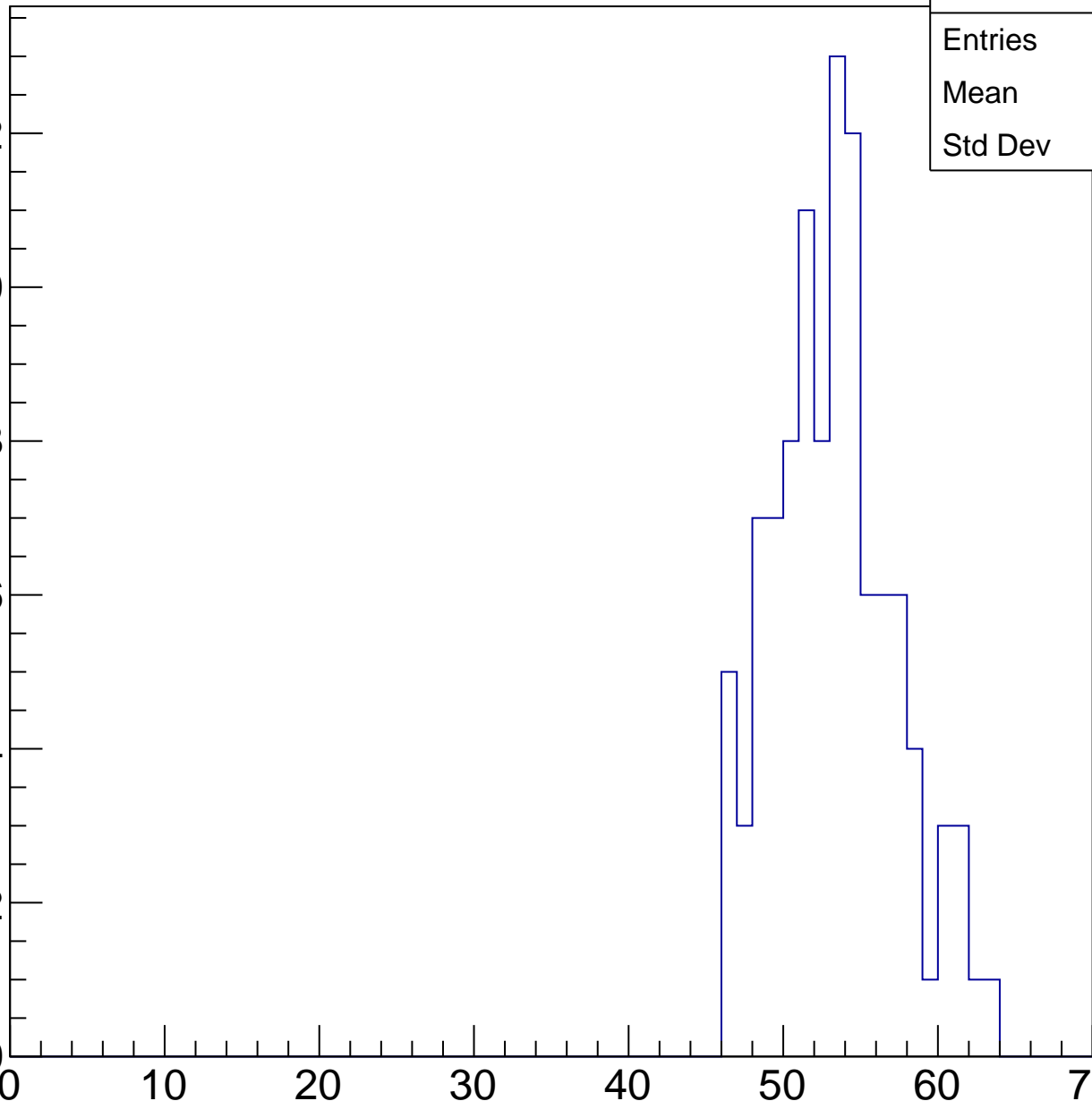
40

50

60

70

ampl



B0L101S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#1, port C1

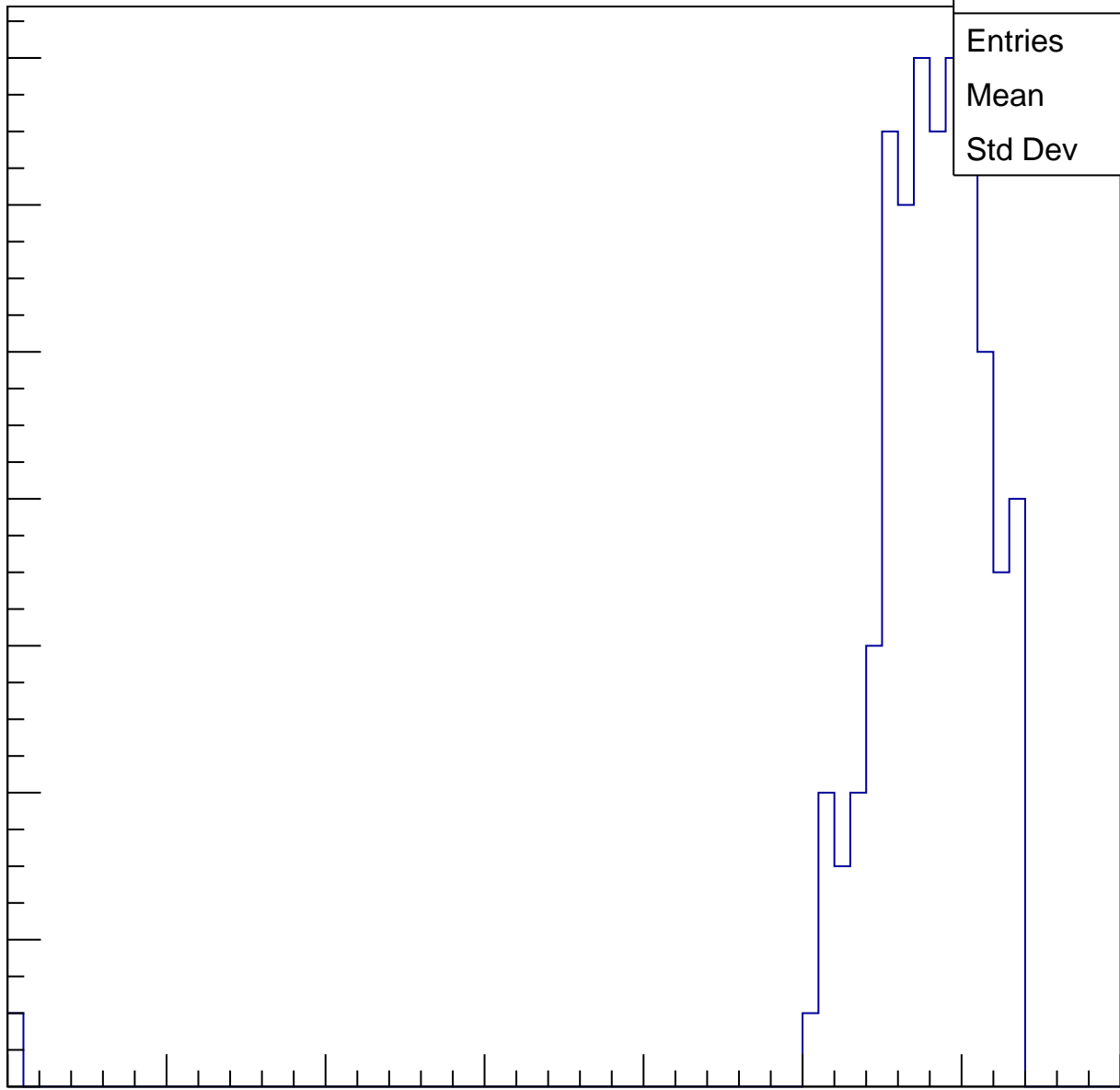
Entries	124
Mean	57.25
Std Dev	6.03

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L101S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#1, port C1

Entry

10

8

6

4

2

0

Entries	43
Mean	59.3
Std Dev	9.473

ampl

0

10

20

30

40

50

60

70

B0L101S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



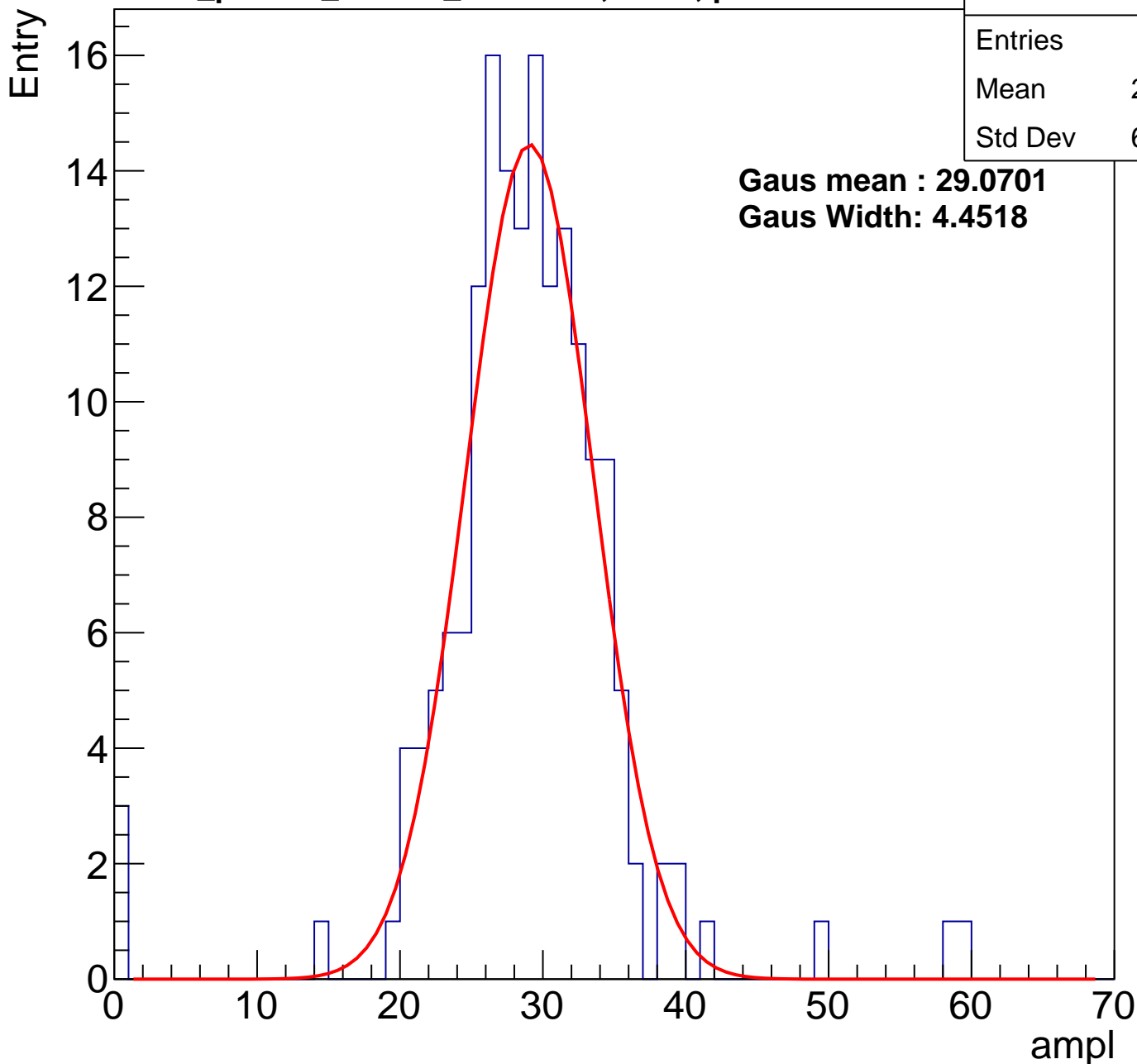
B0L101S, U15-ch127, adc0

calib_packv5_042523_0143.root, FC#1, port C1

Entries	170
Mean	28.45
Std Dev	6.795

Gaus mean : 29.0701

Gaus Width: 4.4518

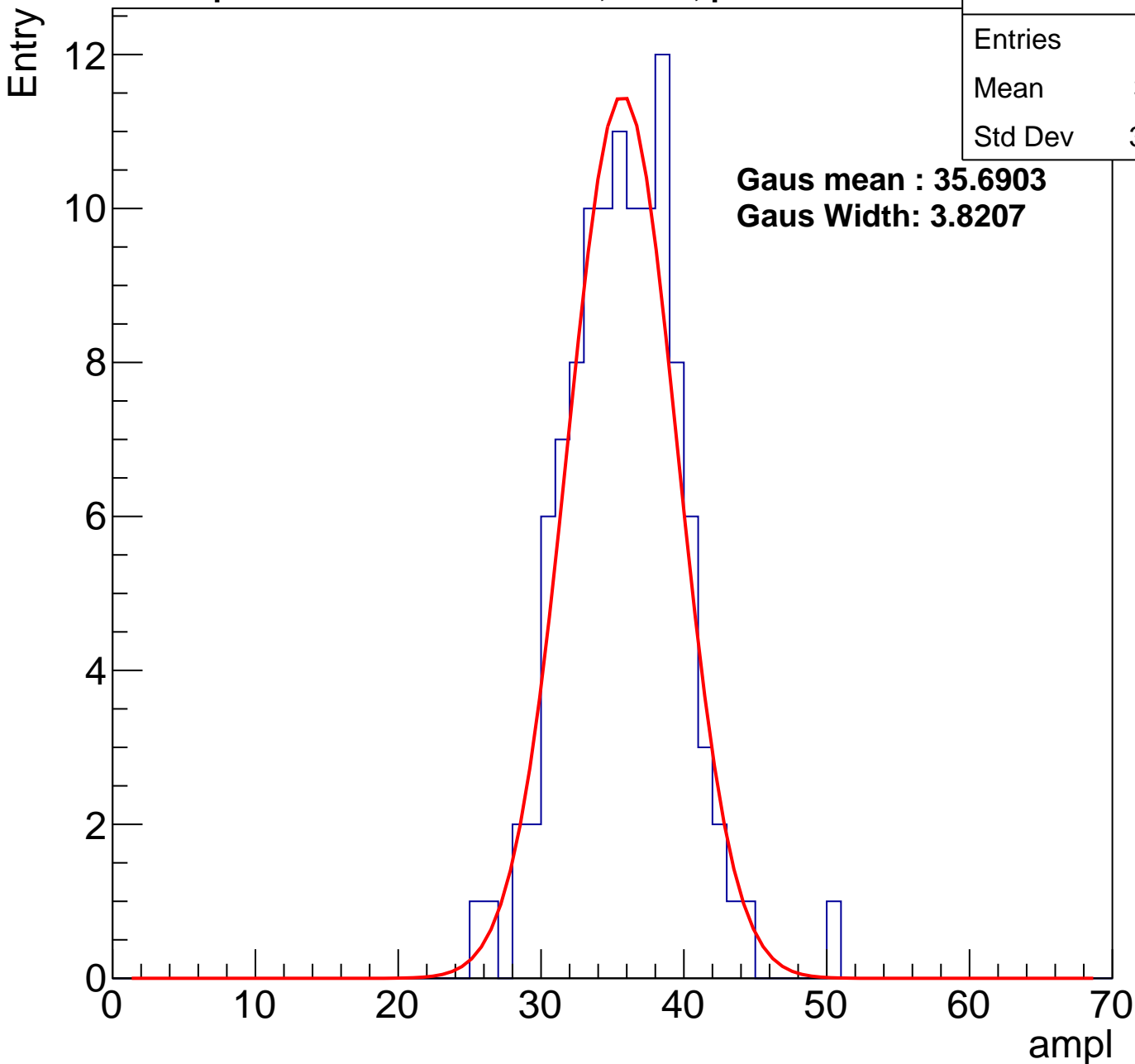


B0L101S, U15-ch127, adc1

calib_packv5_042523_0143.root, FC#1, port C1

Entries	112
Mean	35.31
Std Dev	3.937

Gaus mean : 35.6903
Gaus Width: 3.8207



B0L101S, U15-ch127, adc2

calib_packv5_042523_0143.root, FC#1, port C1

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

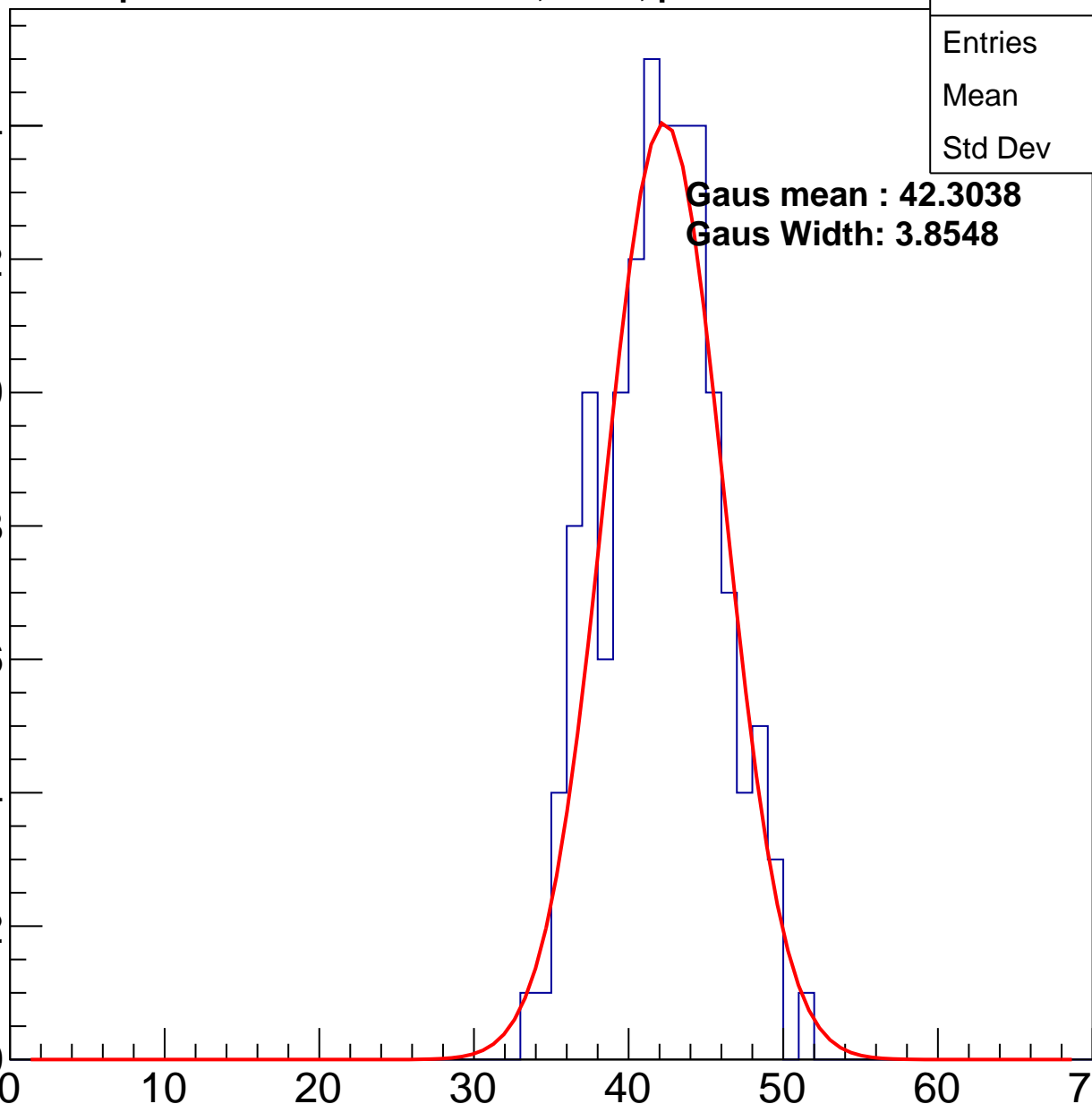
70

ampl

Entries	139
Mean	41.6
Std Dev	3.683

Gaus mean : 42.3038

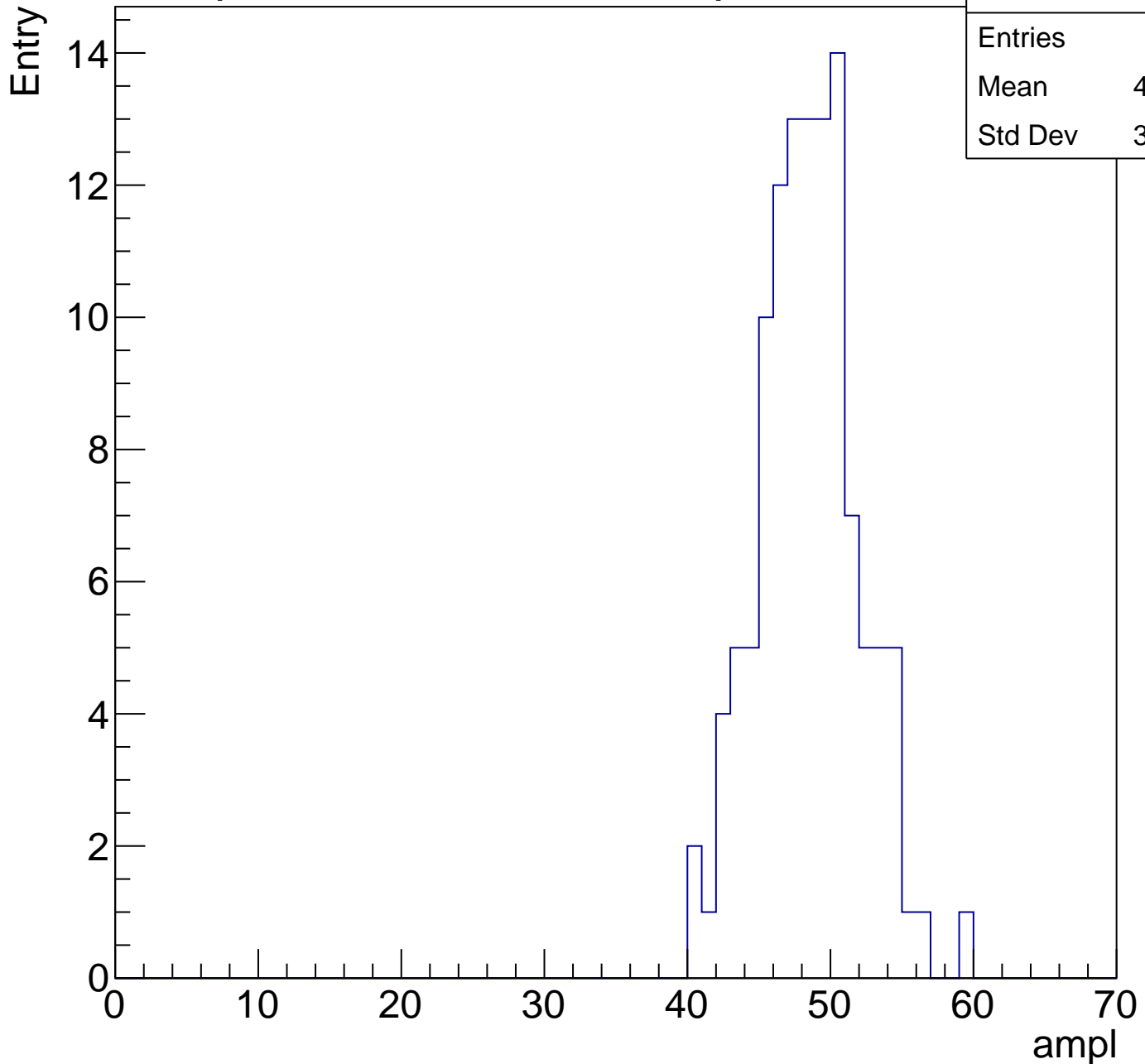
Gaus Width: 3.8548



B0L101S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#1, port C1

Entries	117
Mean	48.03
Std Dev	3.493



B0L101S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#1, port C1

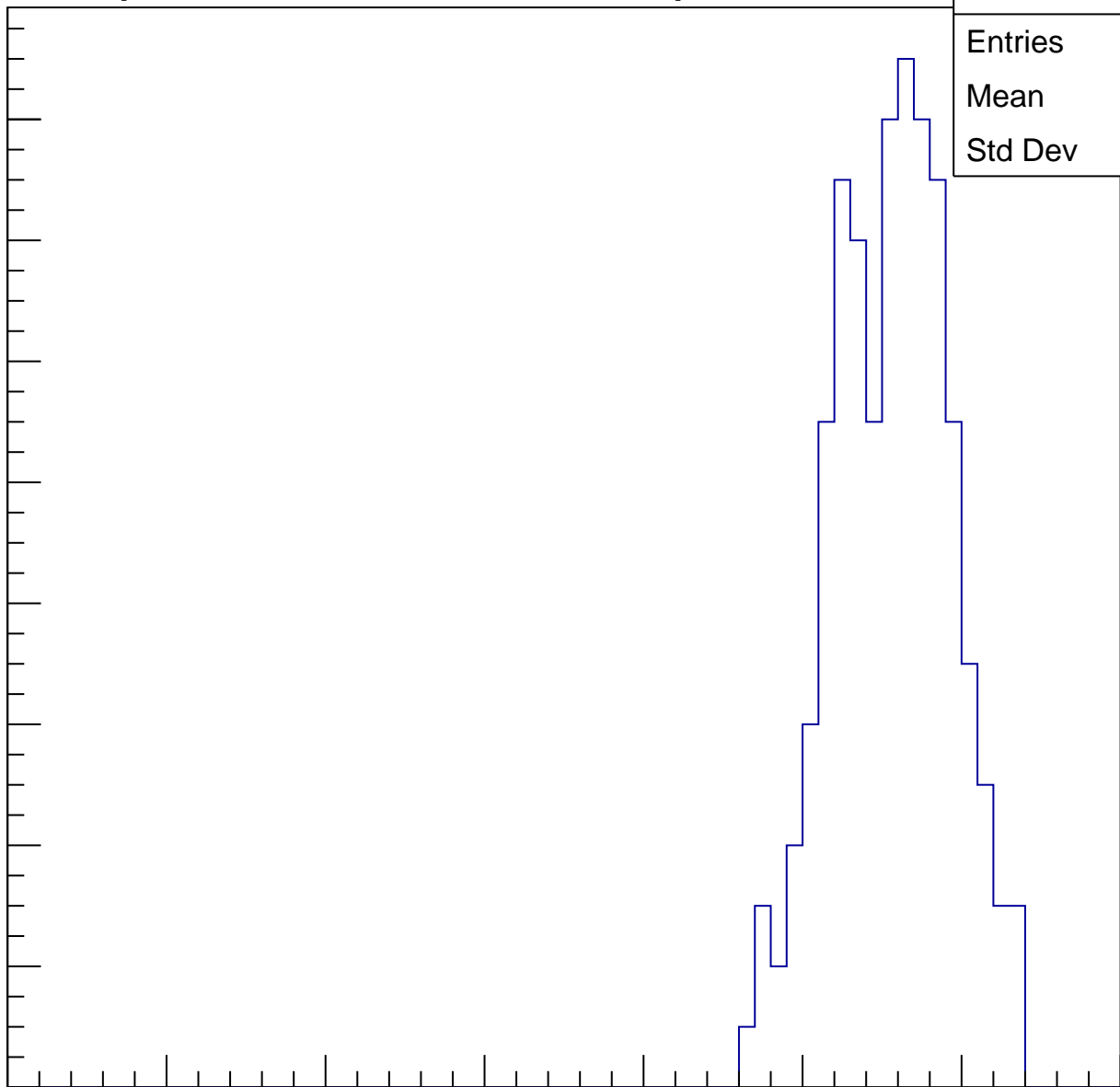
Entries	160
Mean	55.12
Std Dev	3.646

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L101S, U15-ch127, adc5

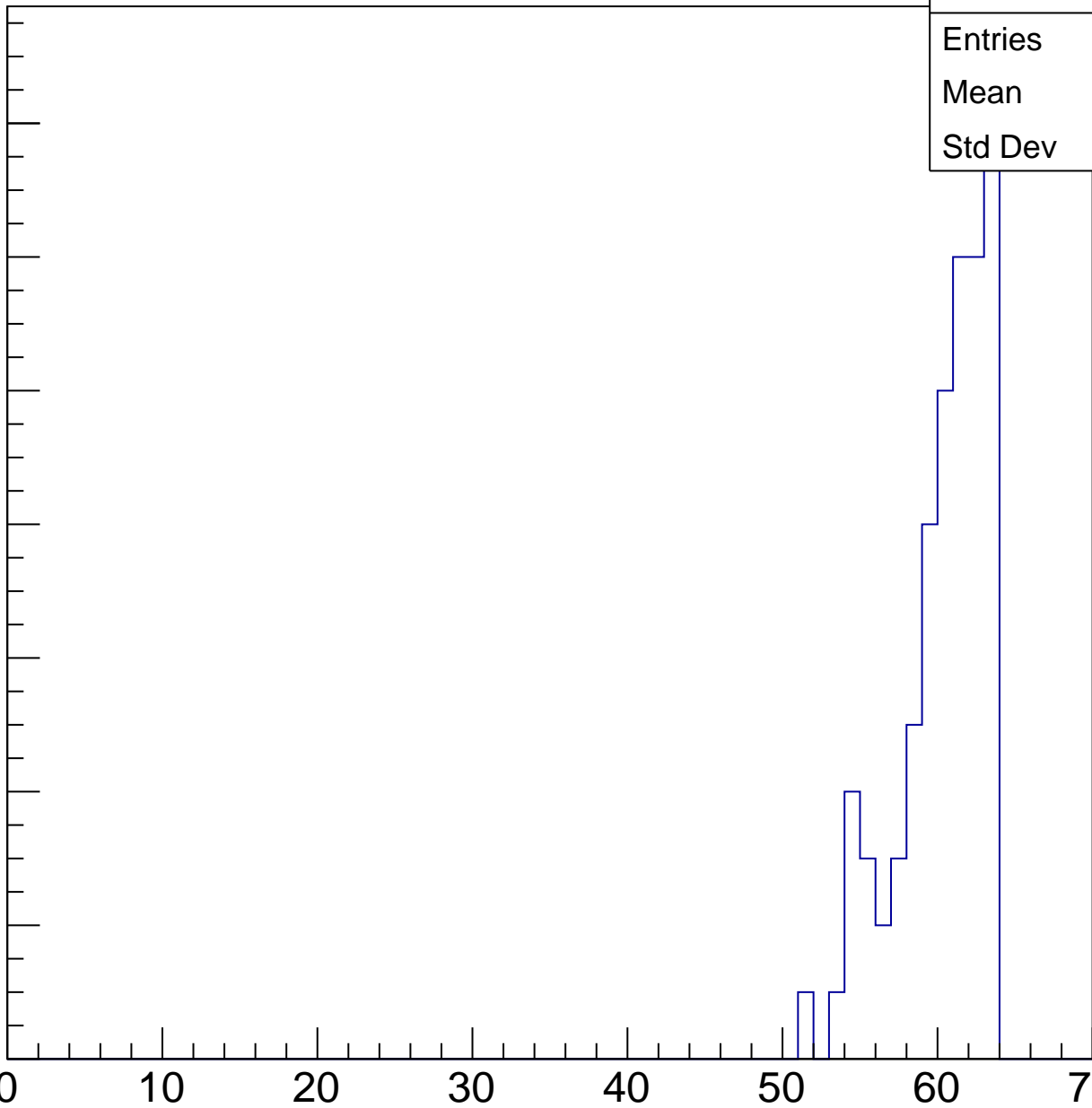
calib_packv5_042523_0143.root, FC#1, port C1

Entry

14
12
10
8
6
4
2
0

Entries	76
Mean	59.88
Std Dev	2.879

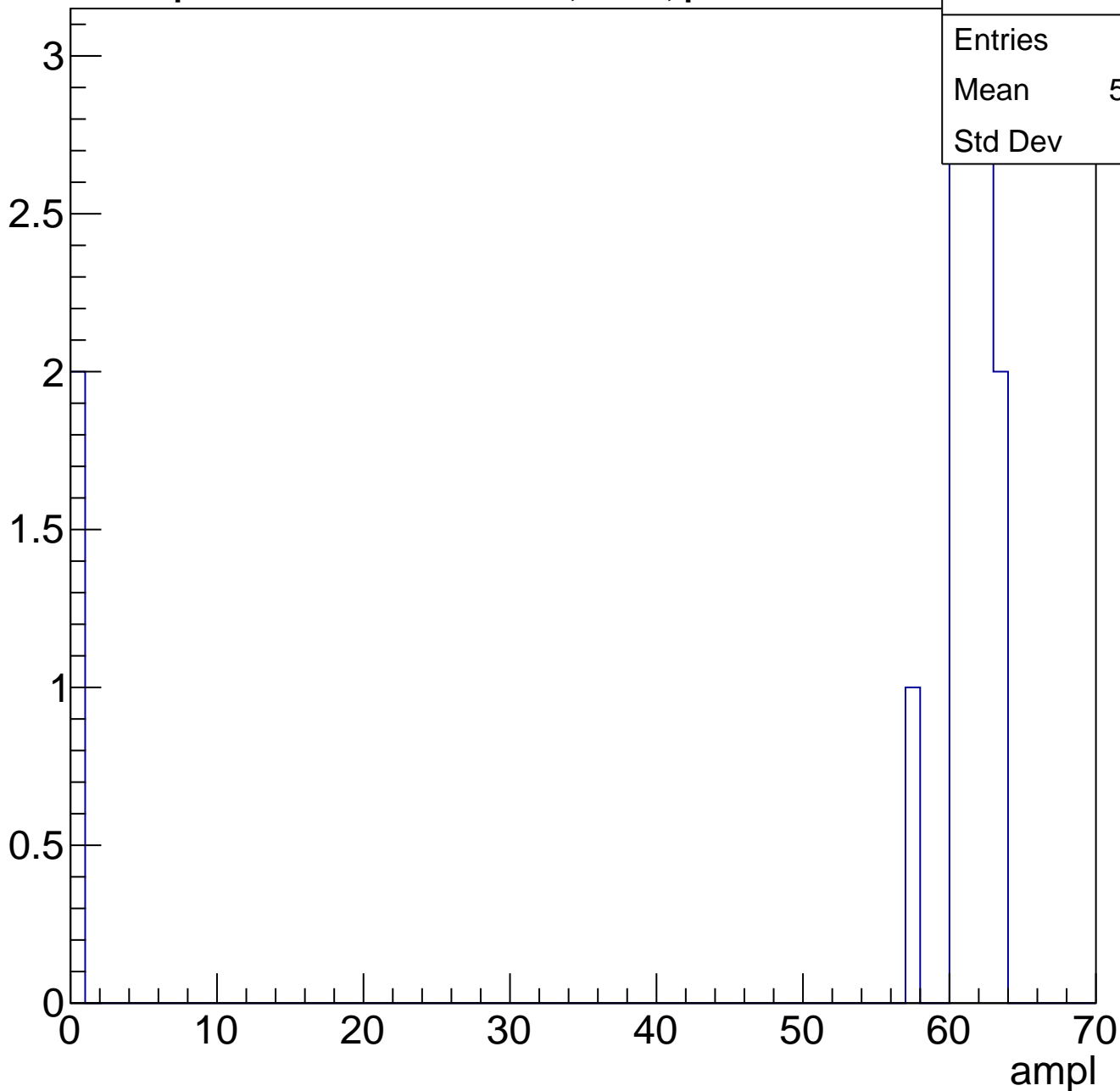
ampl



B0L101S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#1, port C1

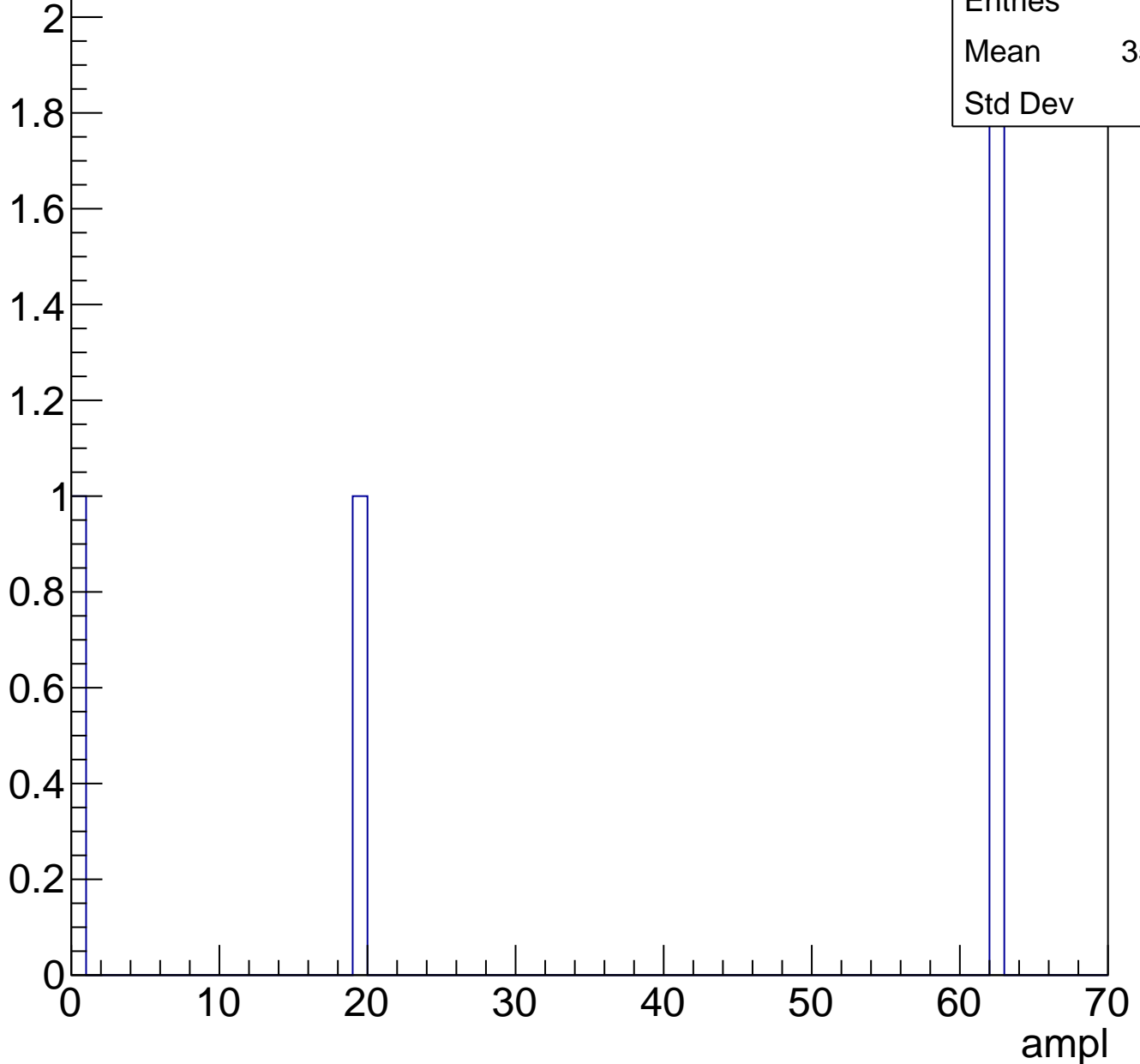
Entry



B0L101S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#1, port C1

Entry

