

B1L103S, U15-ch0, adc0

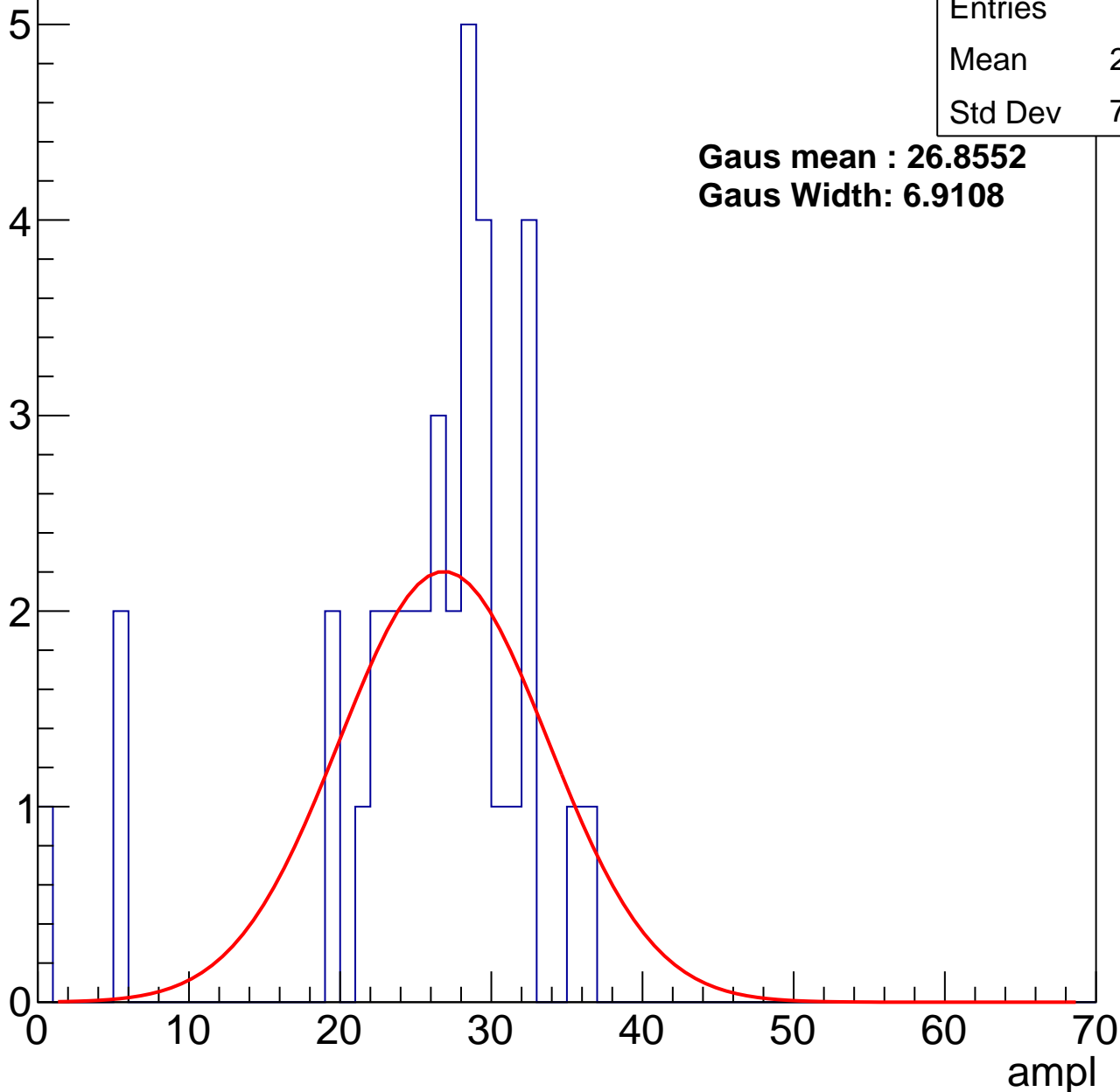
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	25.14
Std Dev	7.707

Gaus mean : 26.8552

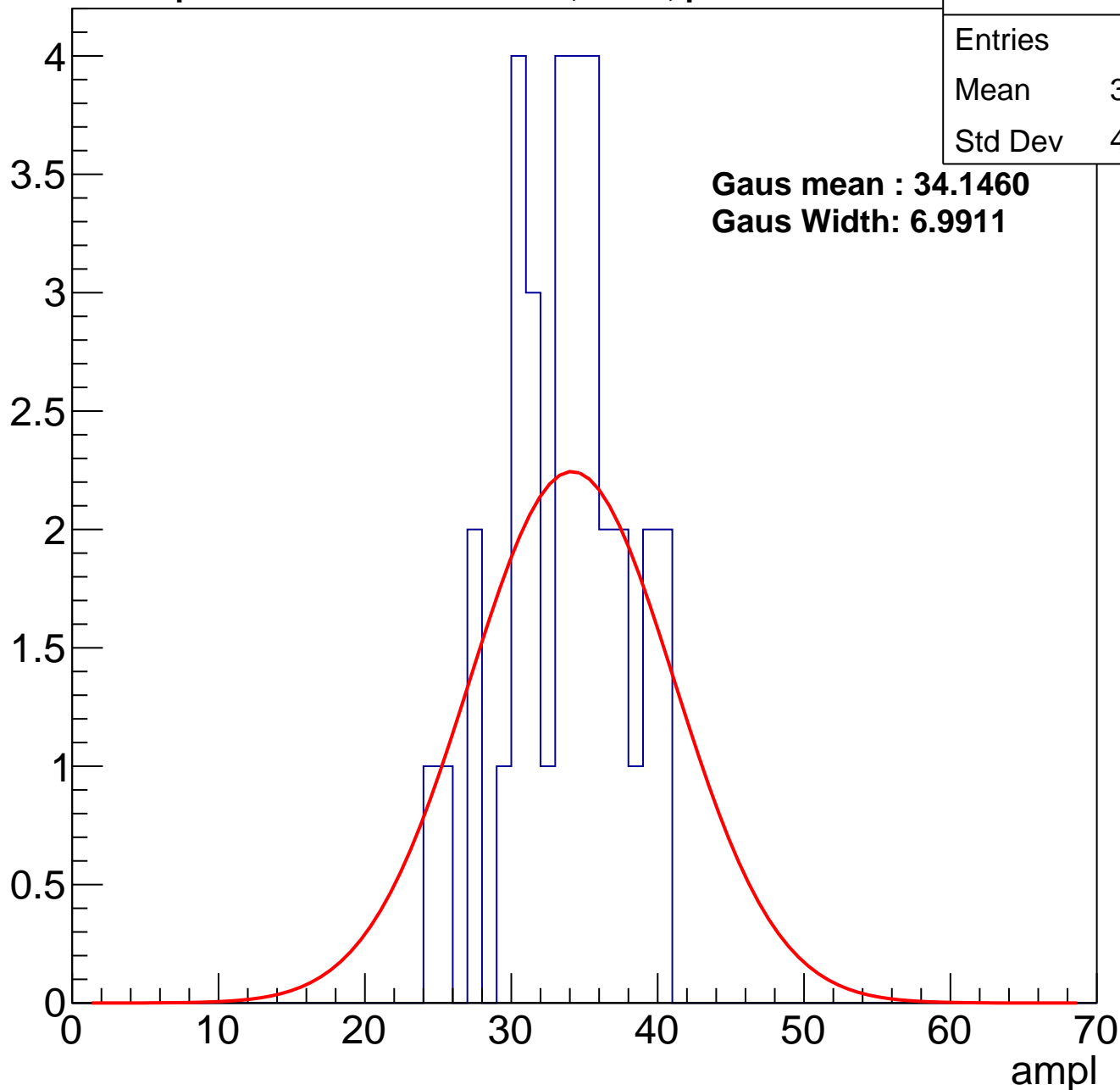
Gaus Width: 6.9108



B1L103S, U15-ch0, adc1

calib_packv5_042523_0143.root, FC#7, port C2

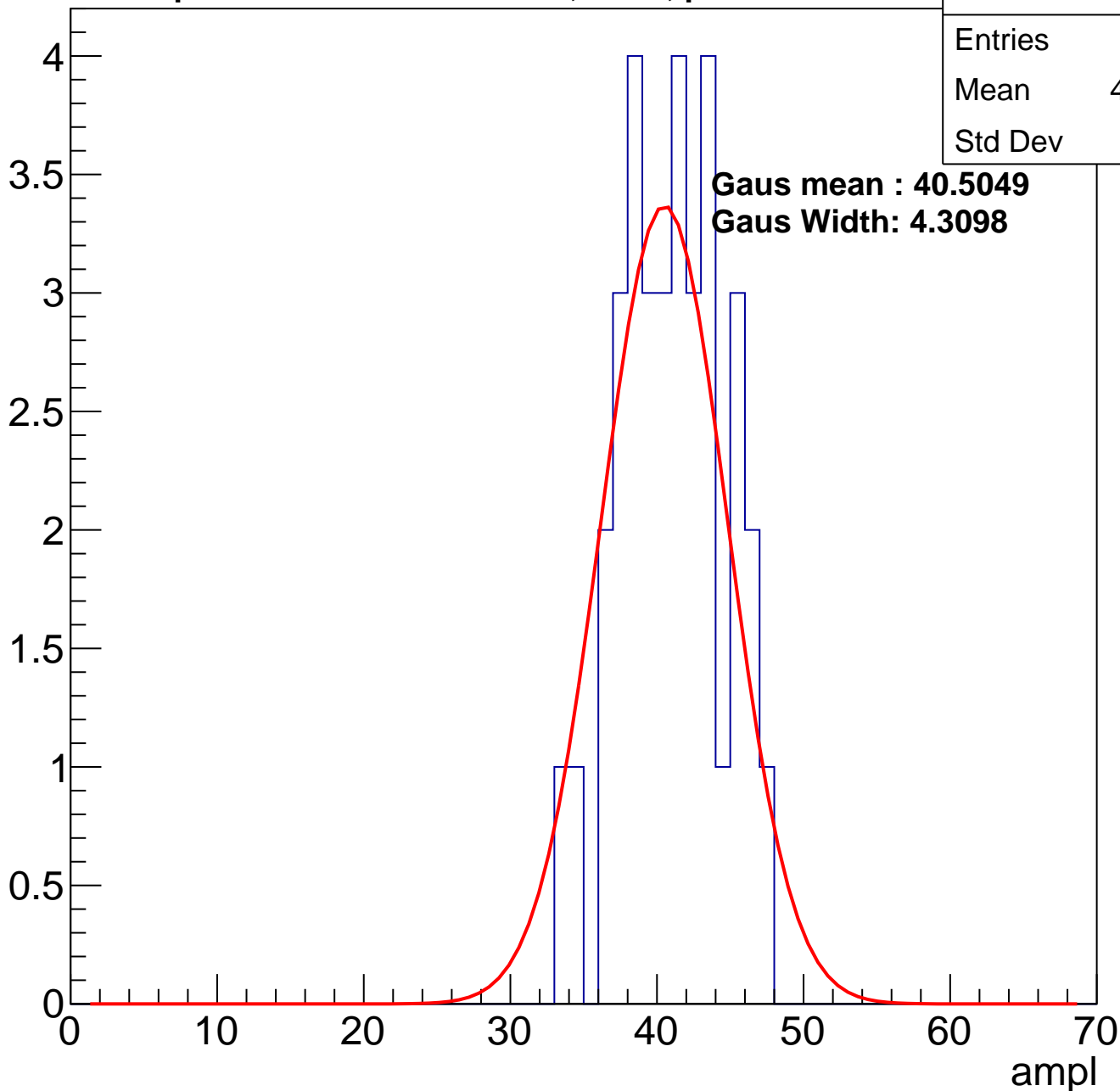
Entry



B1L103S, U15-ch0, adc2

calib_packv5_042523_0143.root, FC#7, port C2

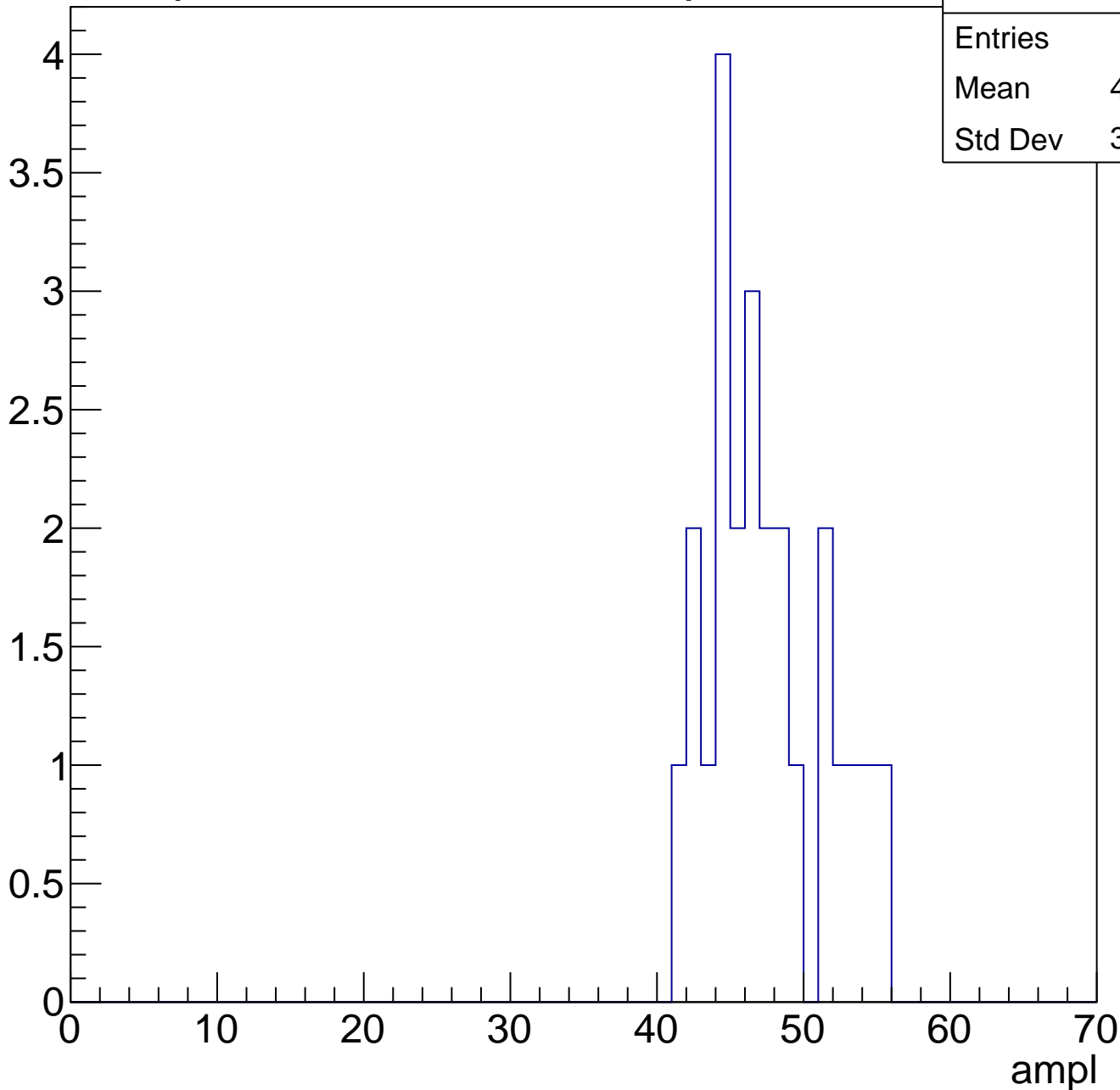
Entry



B1L103S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



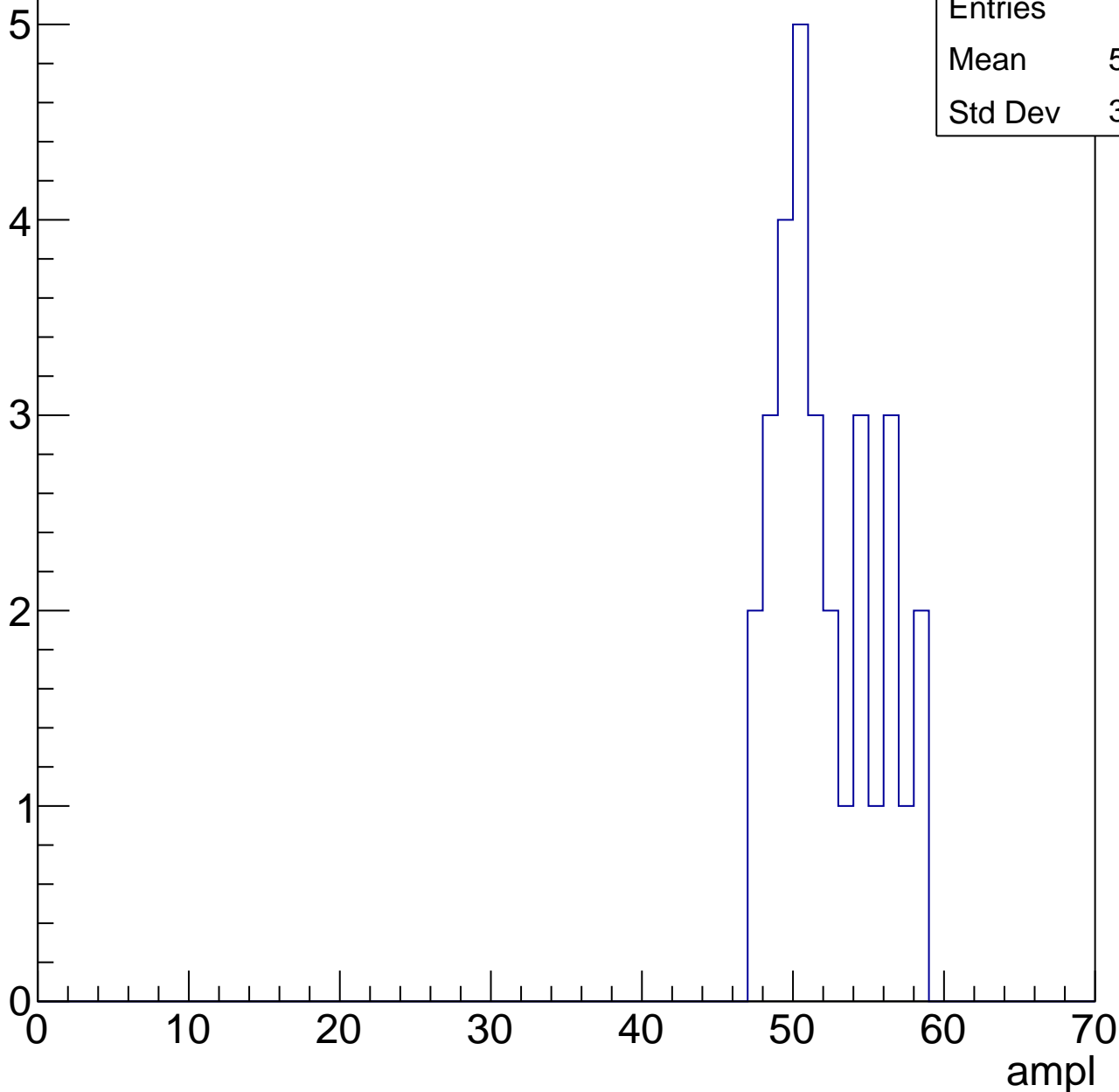
Entries	24
Mean	46.96
Std Dev	3.878

B1L103S, U15-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	51.73
Std Dev	3.275

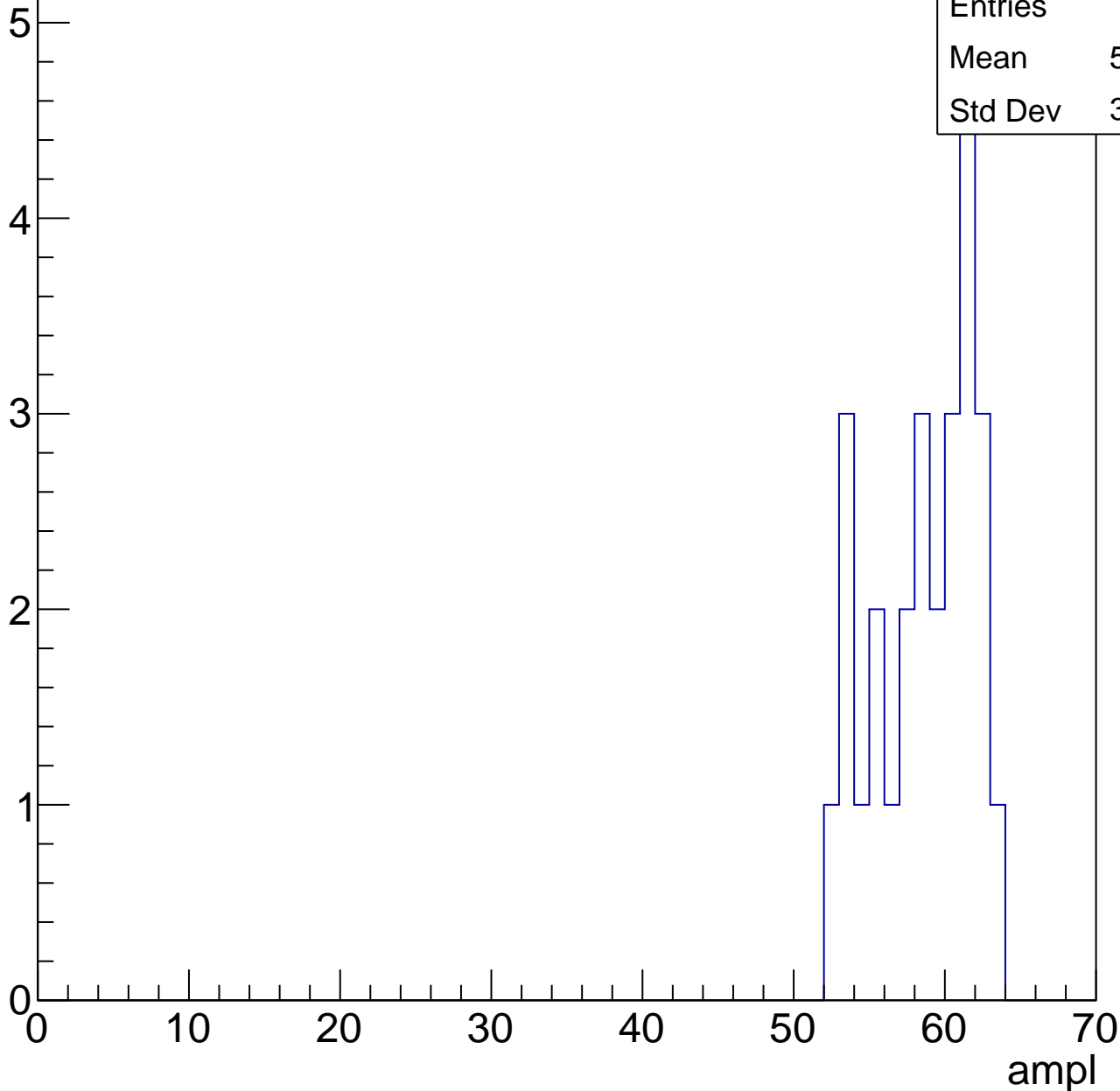


B1L103S, U15-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	58.19
Std Dev	3.232



B1L103S, U15-ch0, adc6

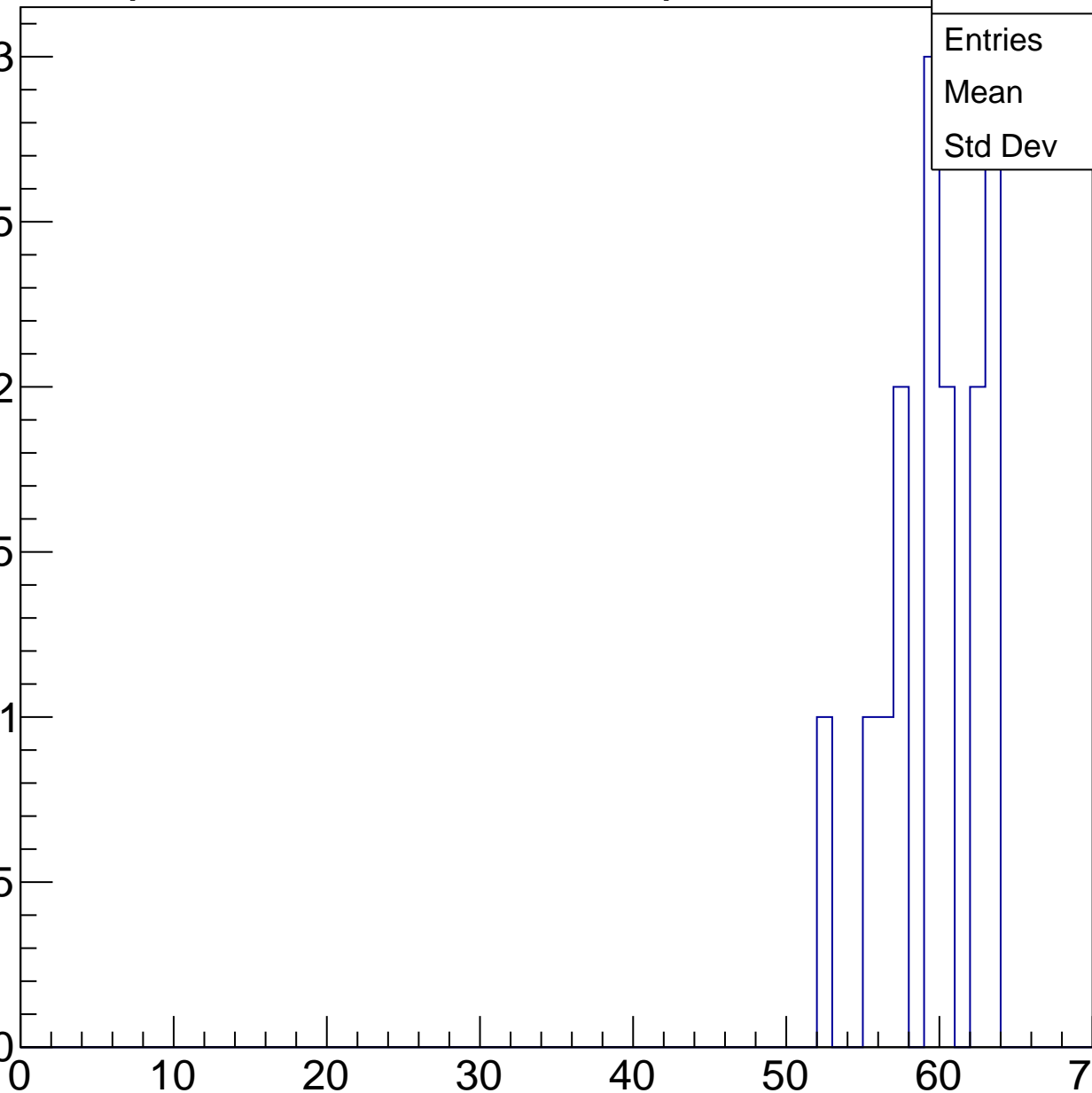
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	15
Mean	59.13
Std Dev	3.159

ampl



B1L103S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

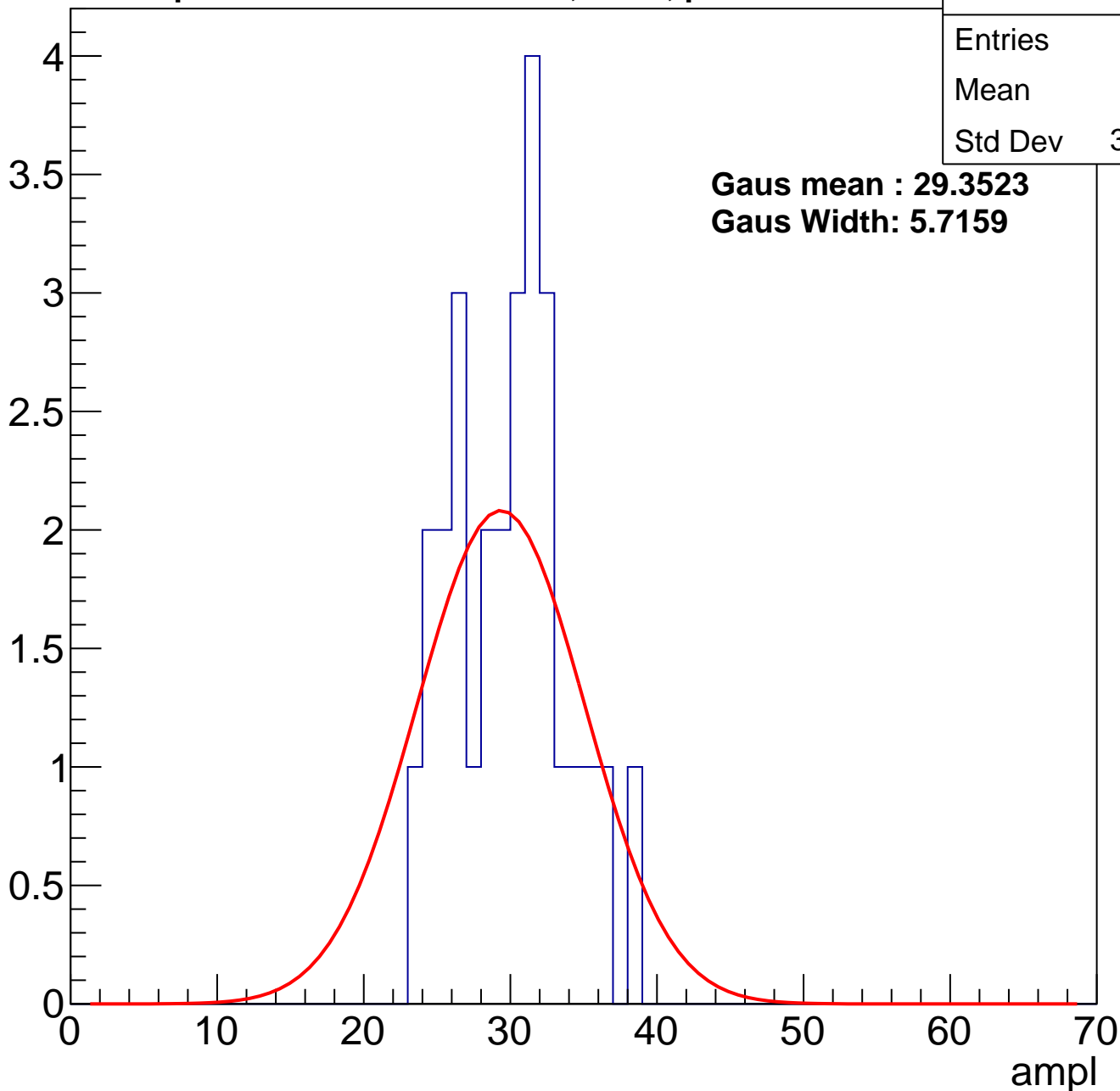
Entries	1
Mean	63
Std Dev	0

ampl

B1L103S, U15-ch1, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch1, adc1

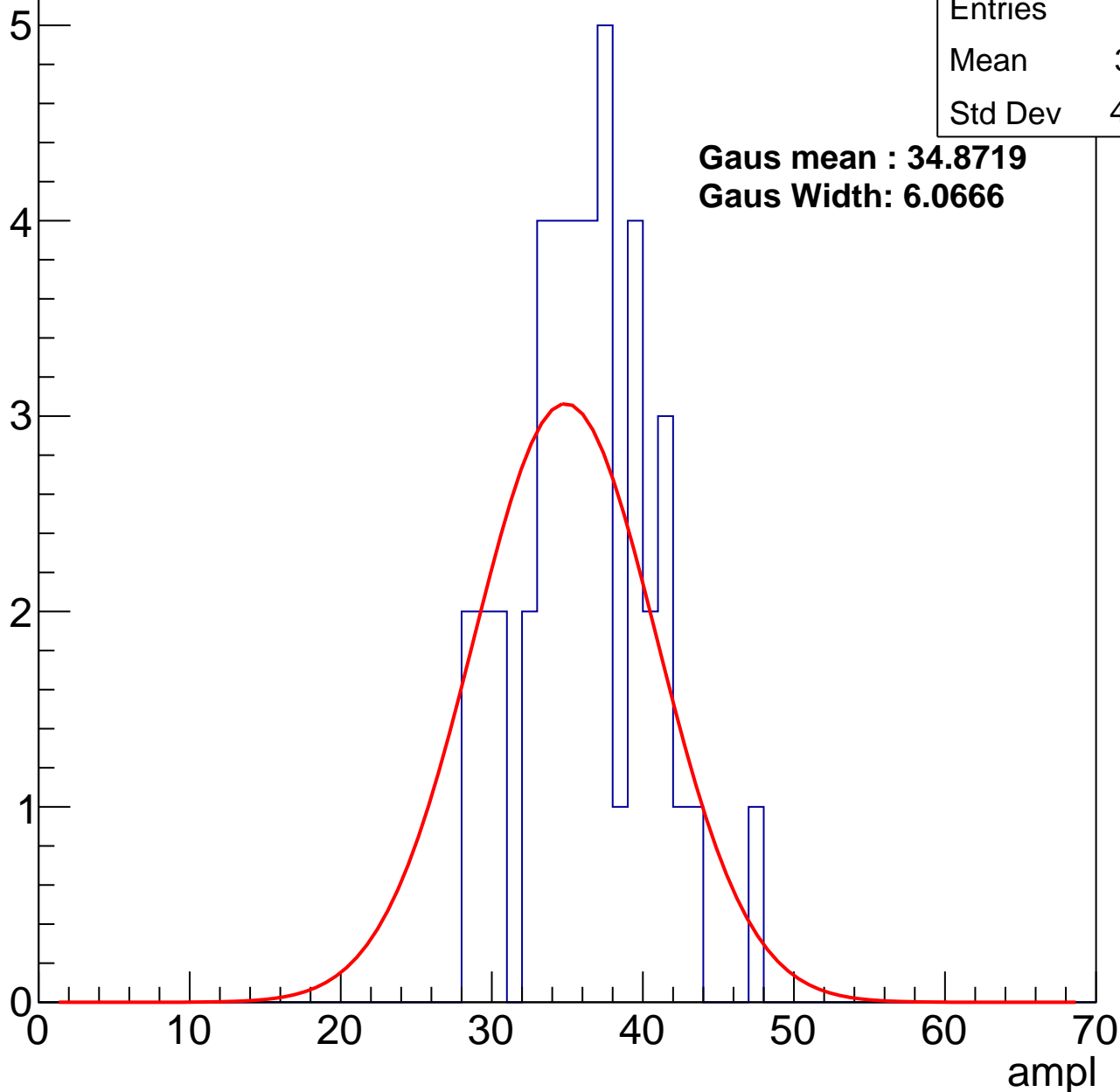
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	35.81
Std Dev	4.205

Gaus mean : 34.8719

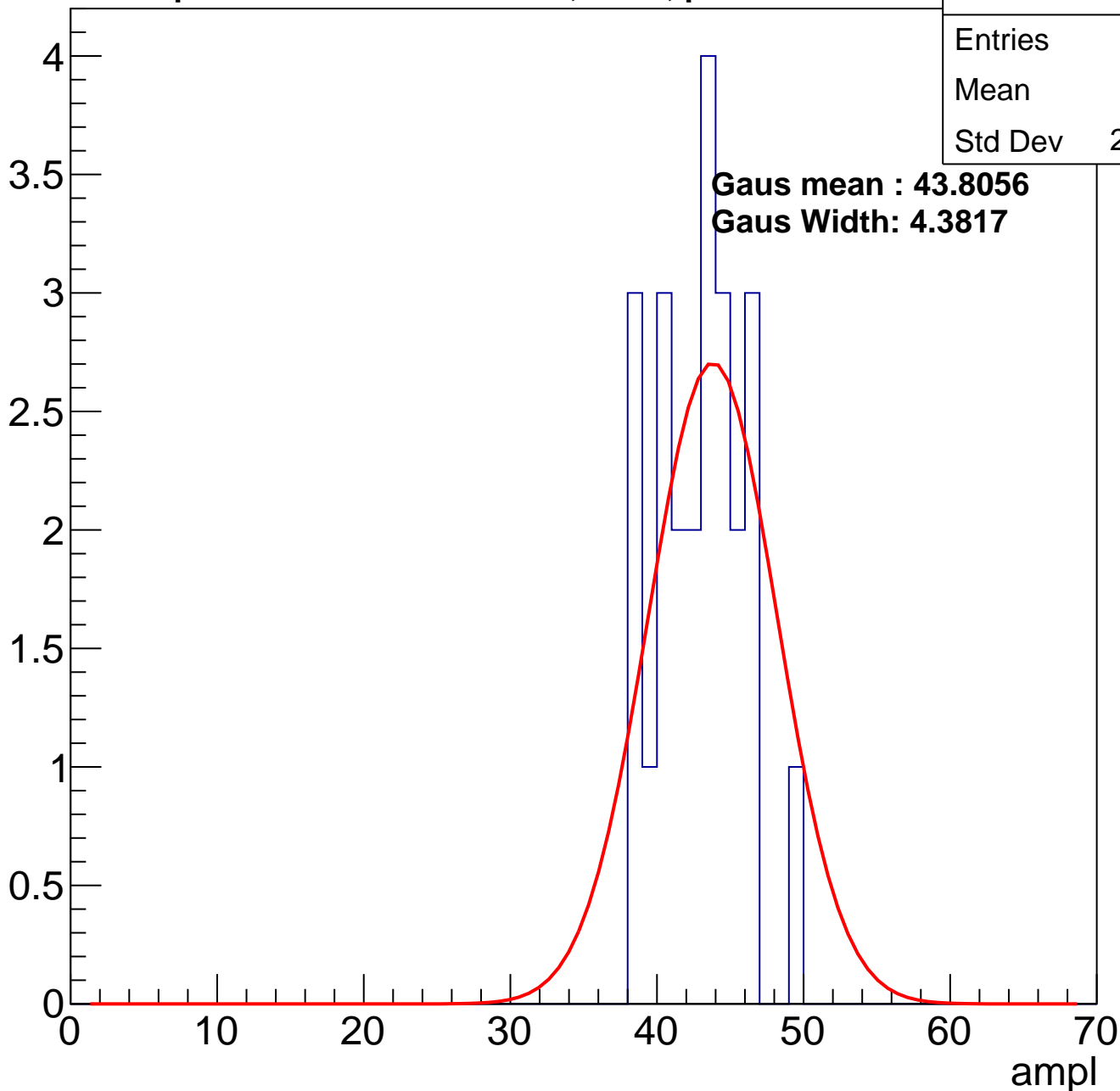
Gaus Width: 6.0666



B1L103S, U15-ch1, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

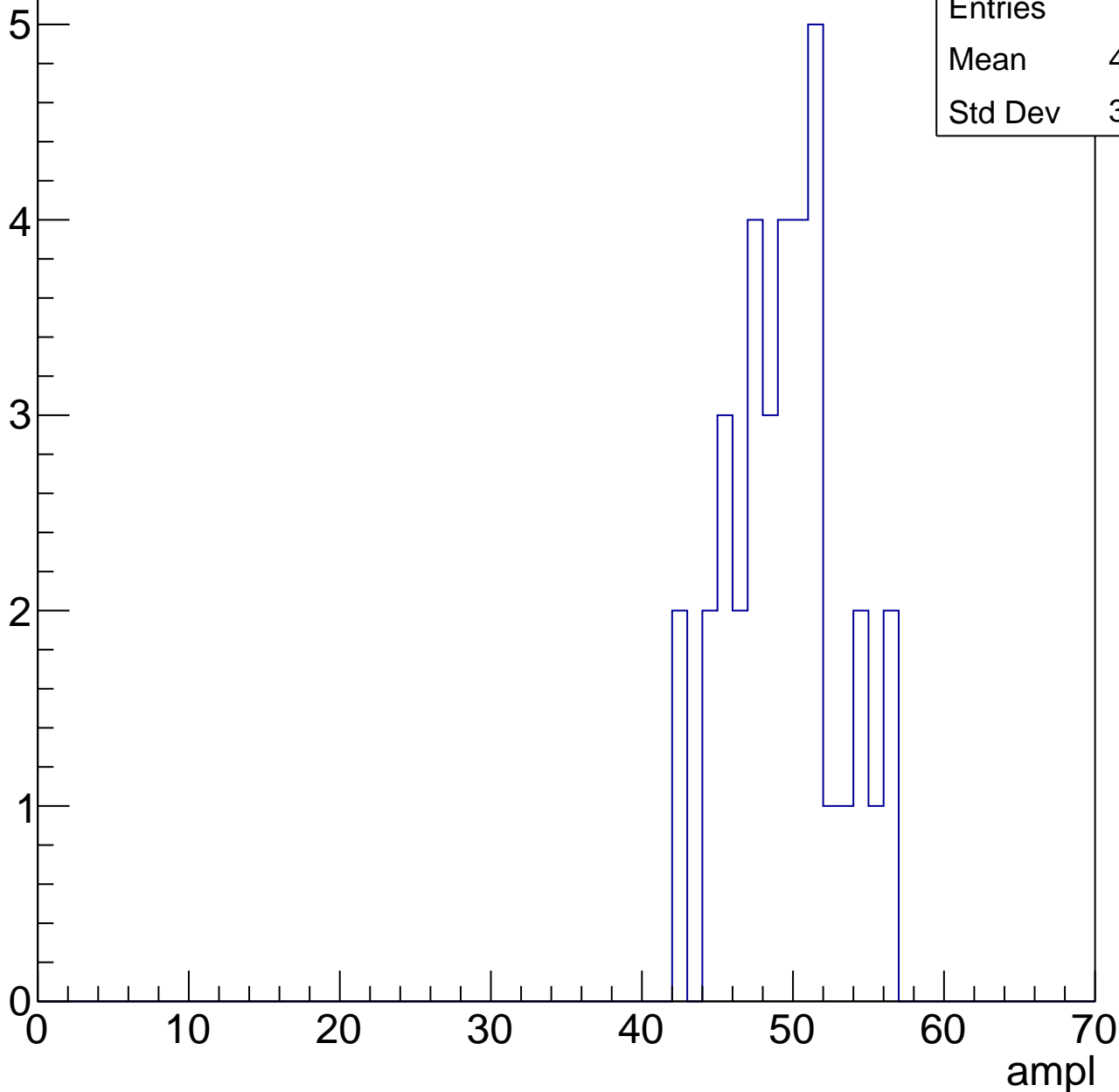


B1L103S, U15-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

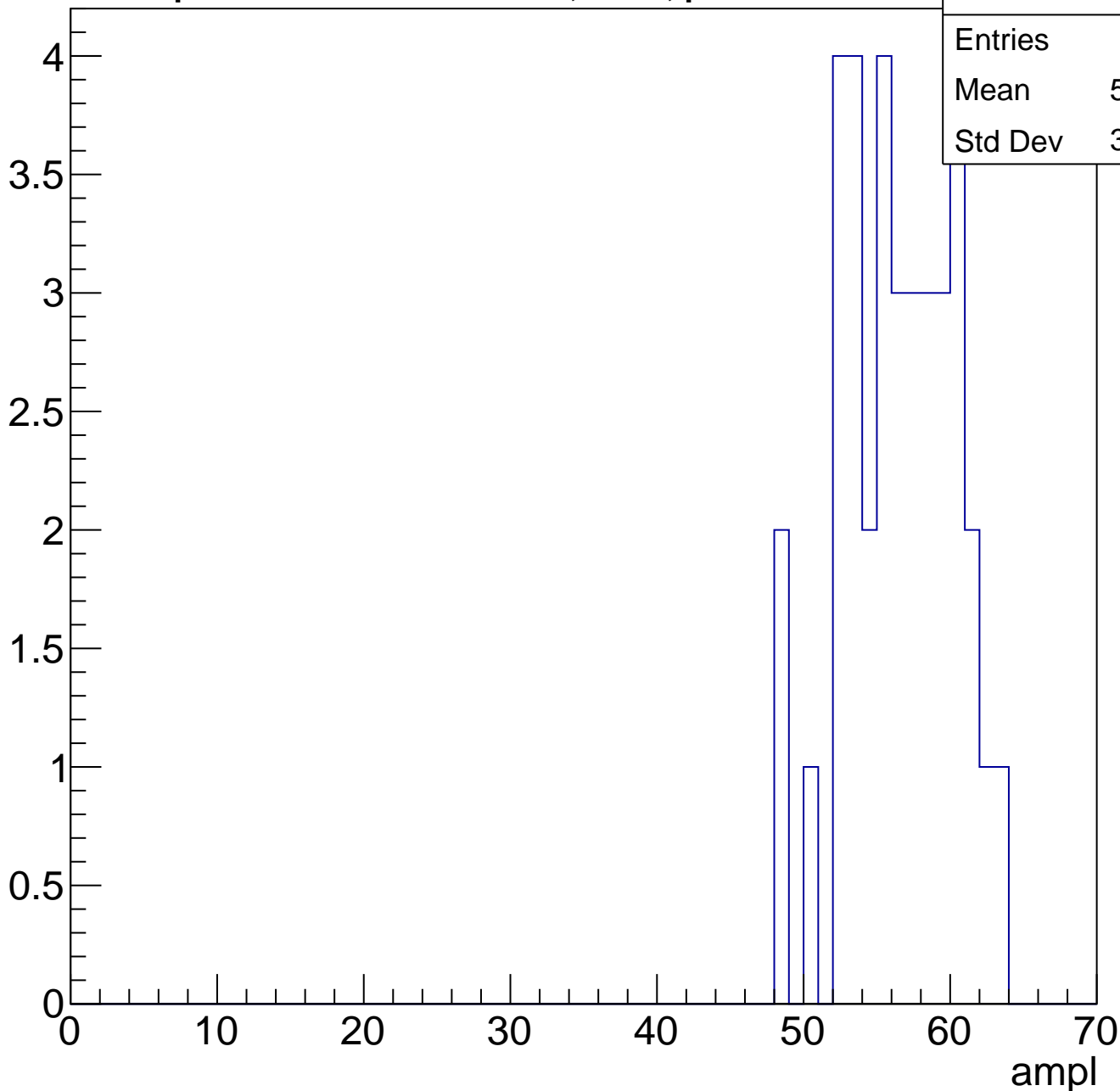
Entries	36
Mean	48.94
Std Dev	3.597



B1L103S, U15-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

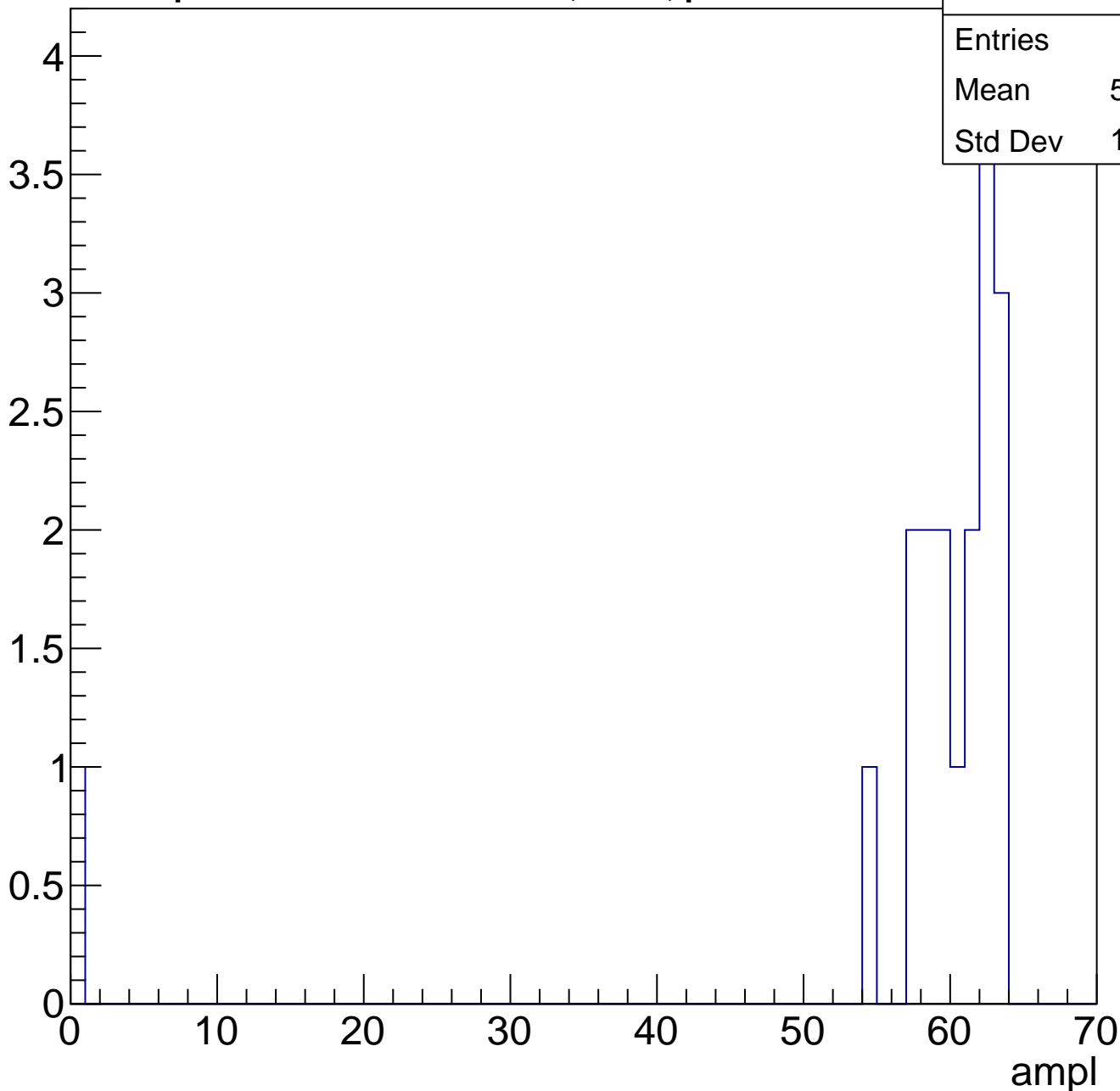


Entries	37
Mean	55.97
Std Dev	3.738

B1L103S, U15-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	18
Mean	56.72
Std Dev	13.98

B1L103S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

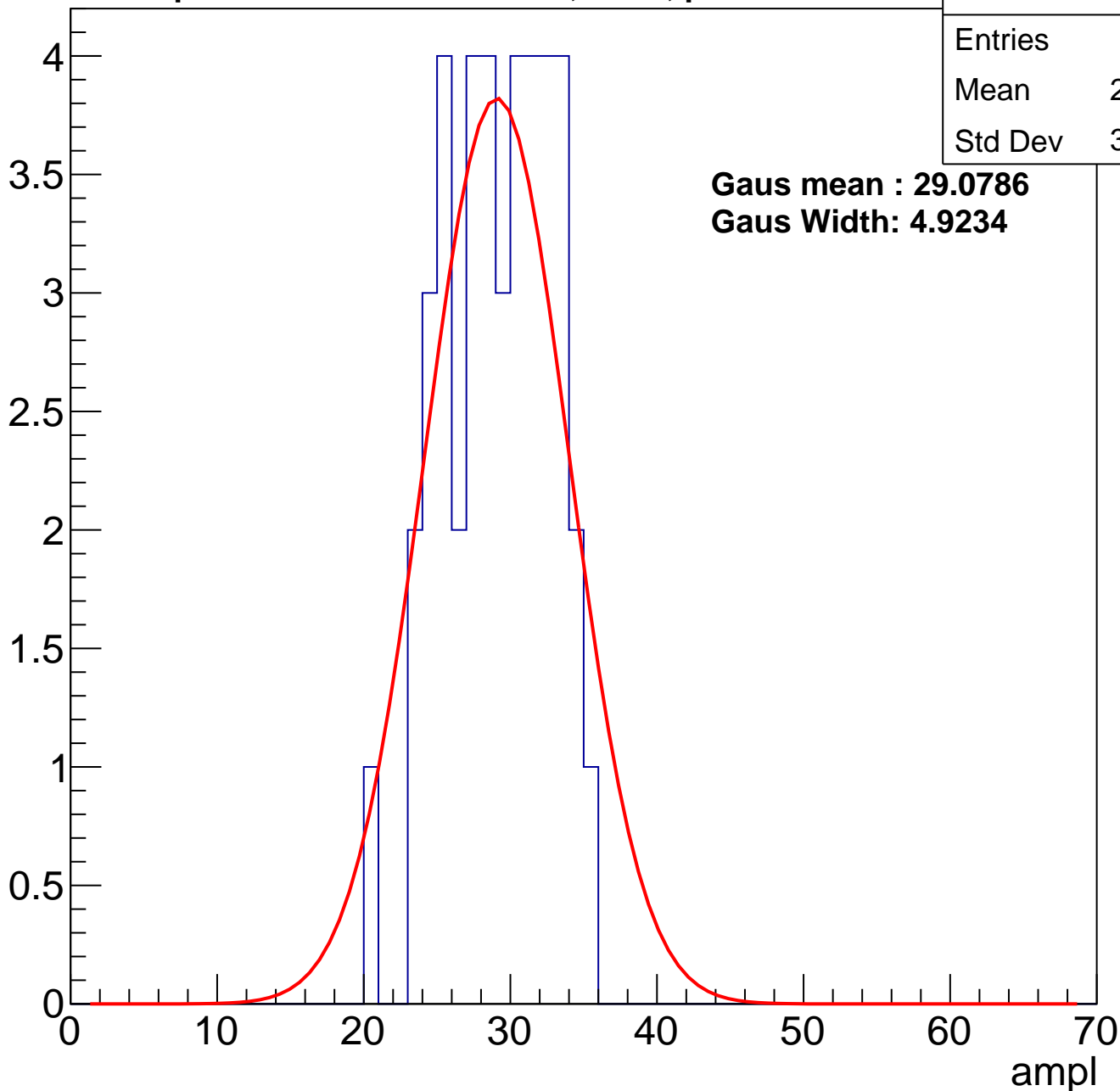


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch2, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch2, adc1

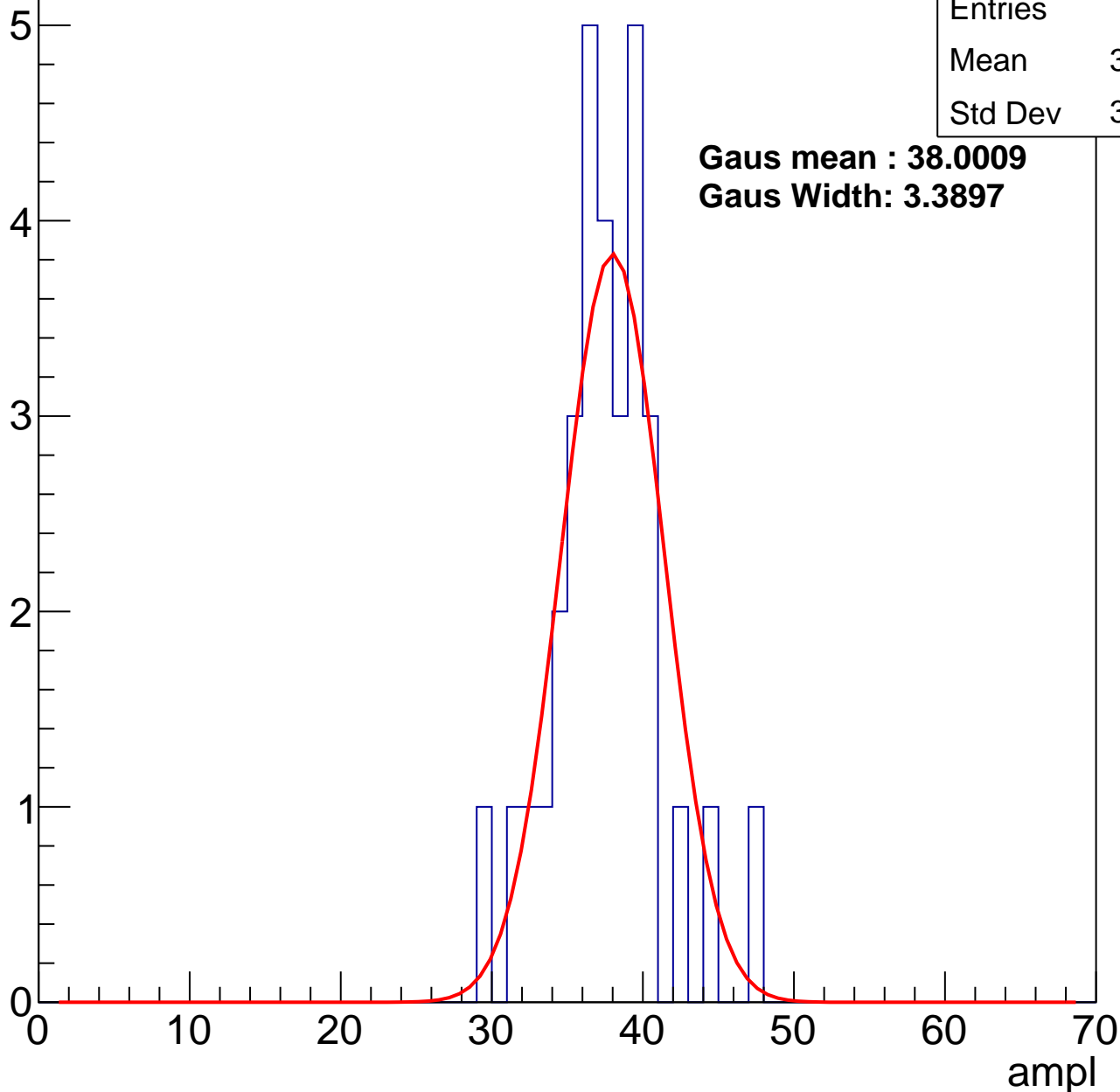
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	37.12
Std Dev	3.533

Gaus mean : 38.0009

Gaus Width: 3.3897



B1L103S, U15-ch2, adc2

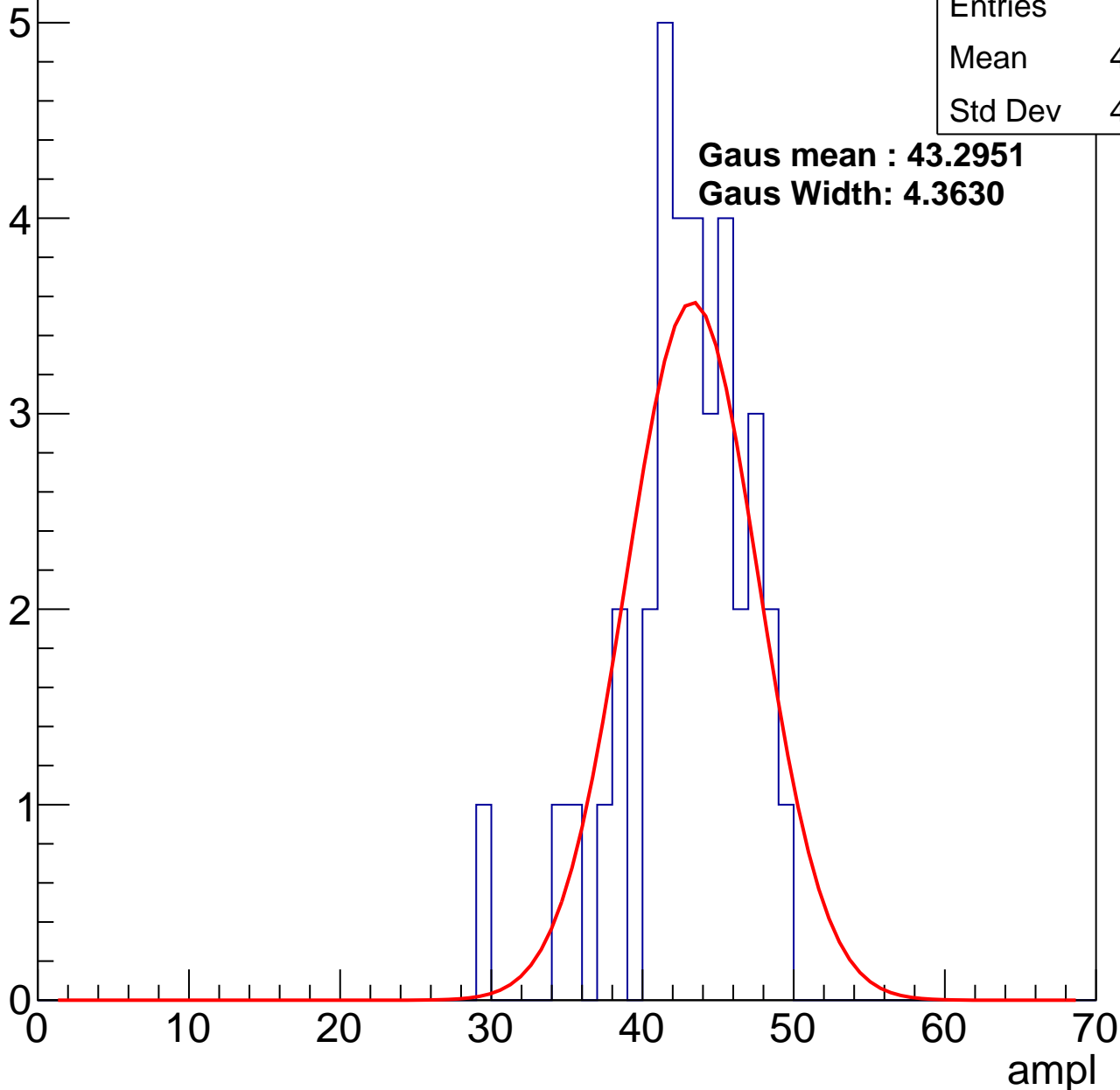
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	42.39
Std Dev	4.172

Gaus mean : 43.2951

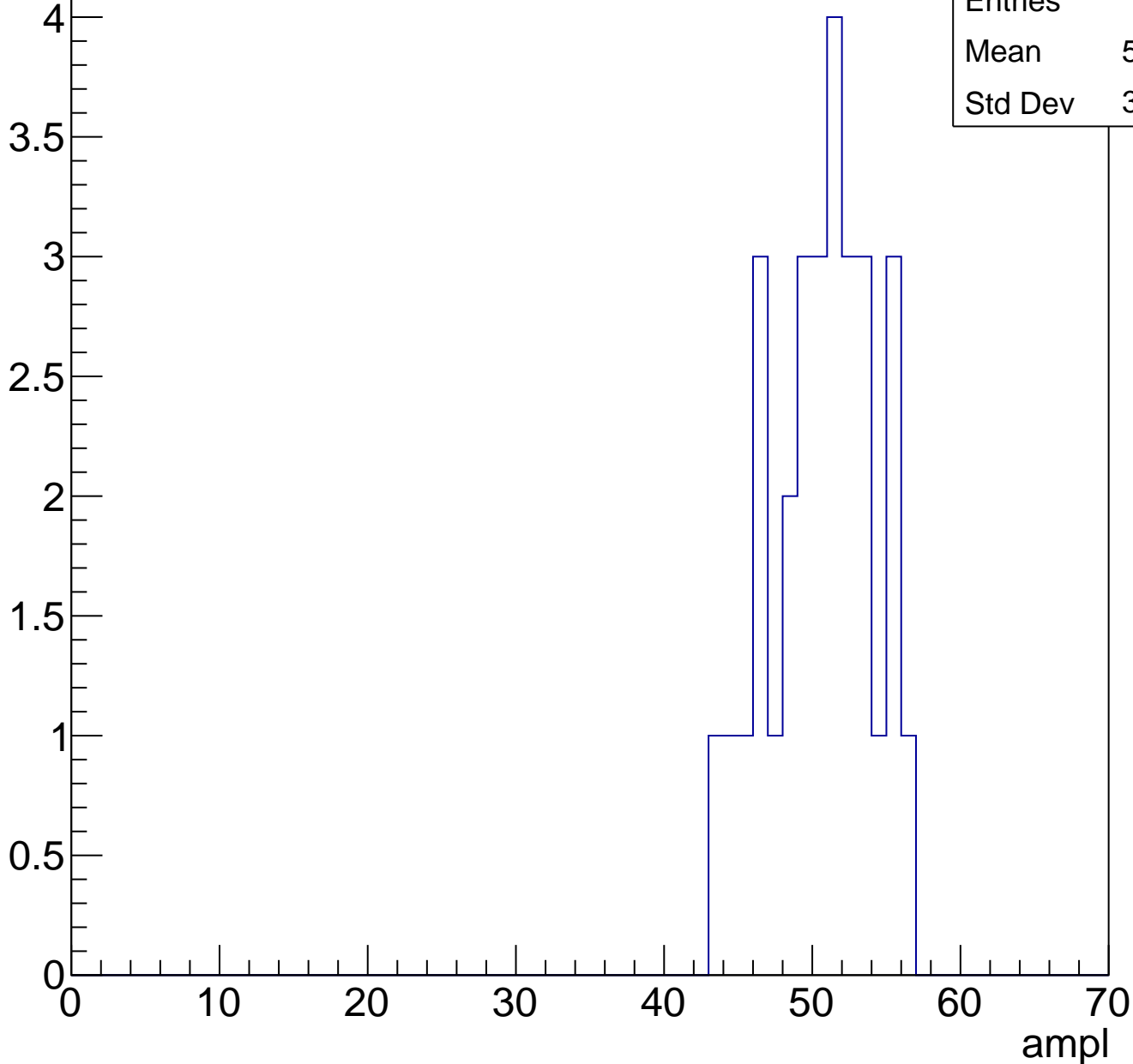
Gaus Width: 4.3630



B1L103S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

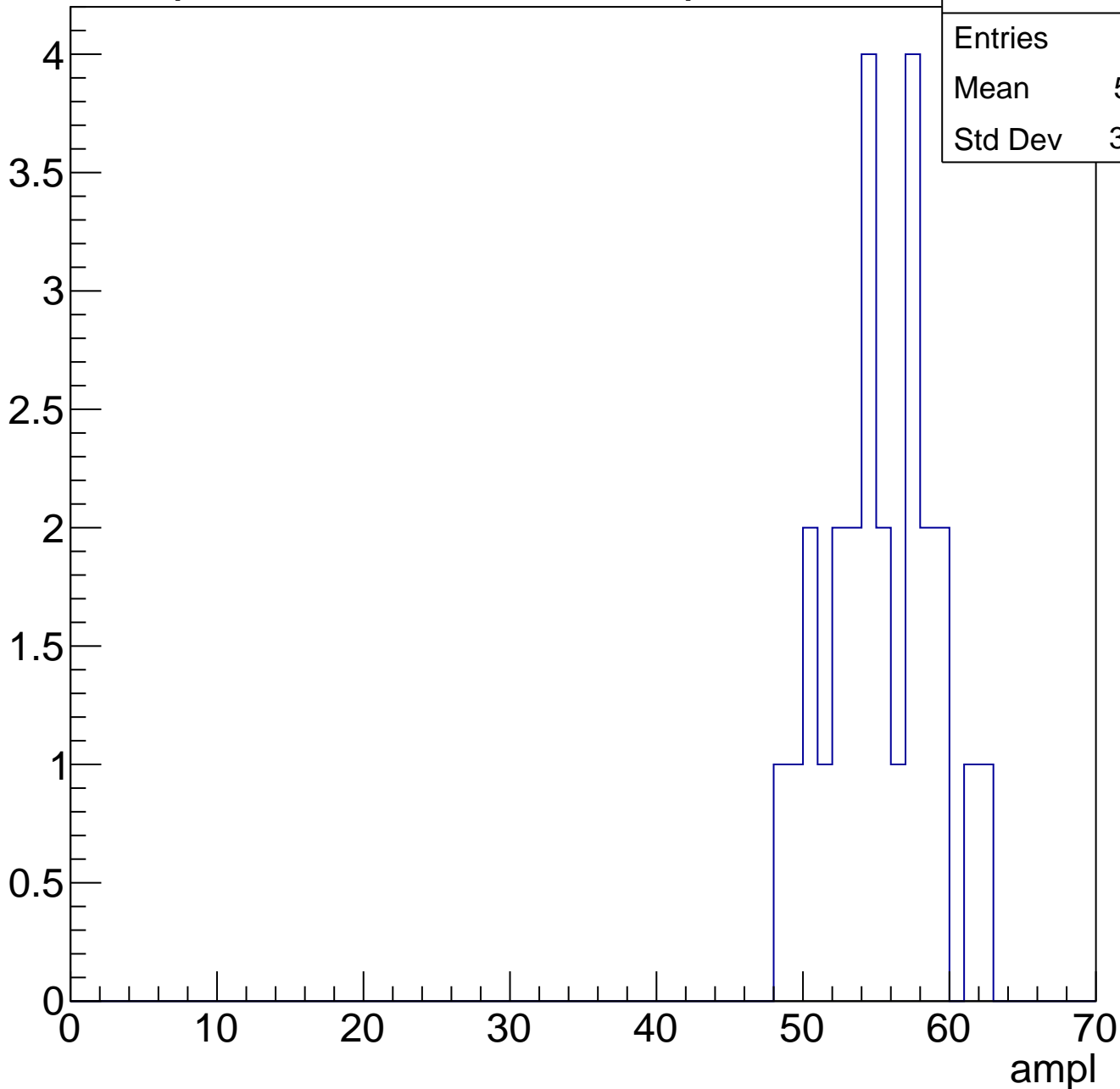
Entry



B1L103S, U15-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

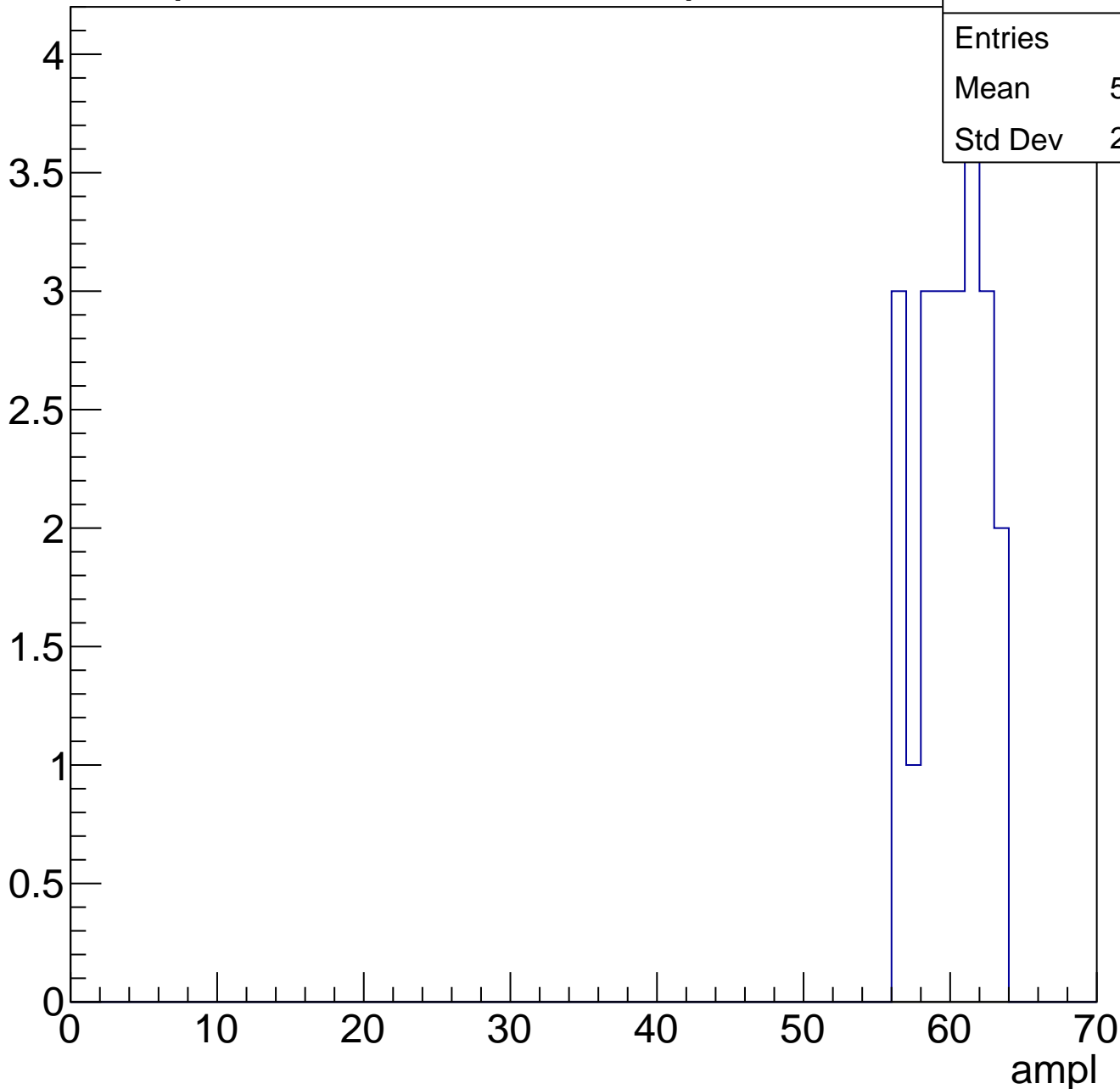
Entry



B1L103S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	22
Mean	59.64
Std Dev	2.165

B1L103S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L103S, U15-ch3, adc0

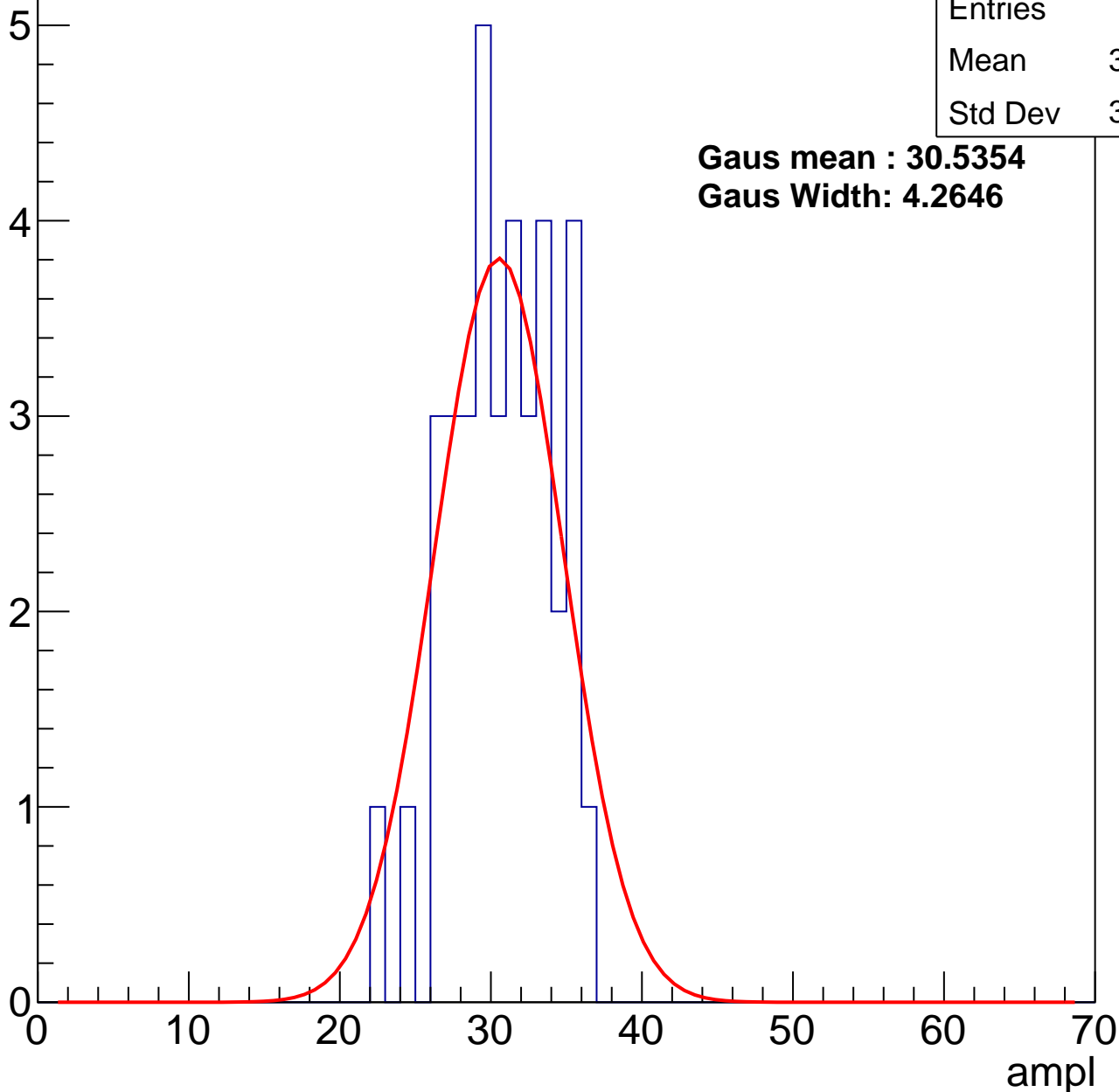
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	30.27
Std Dev	3.326

Gaus mean : 30.5354

Gaus Width: 4.2646



B1L103S, U15-ch3, adc1

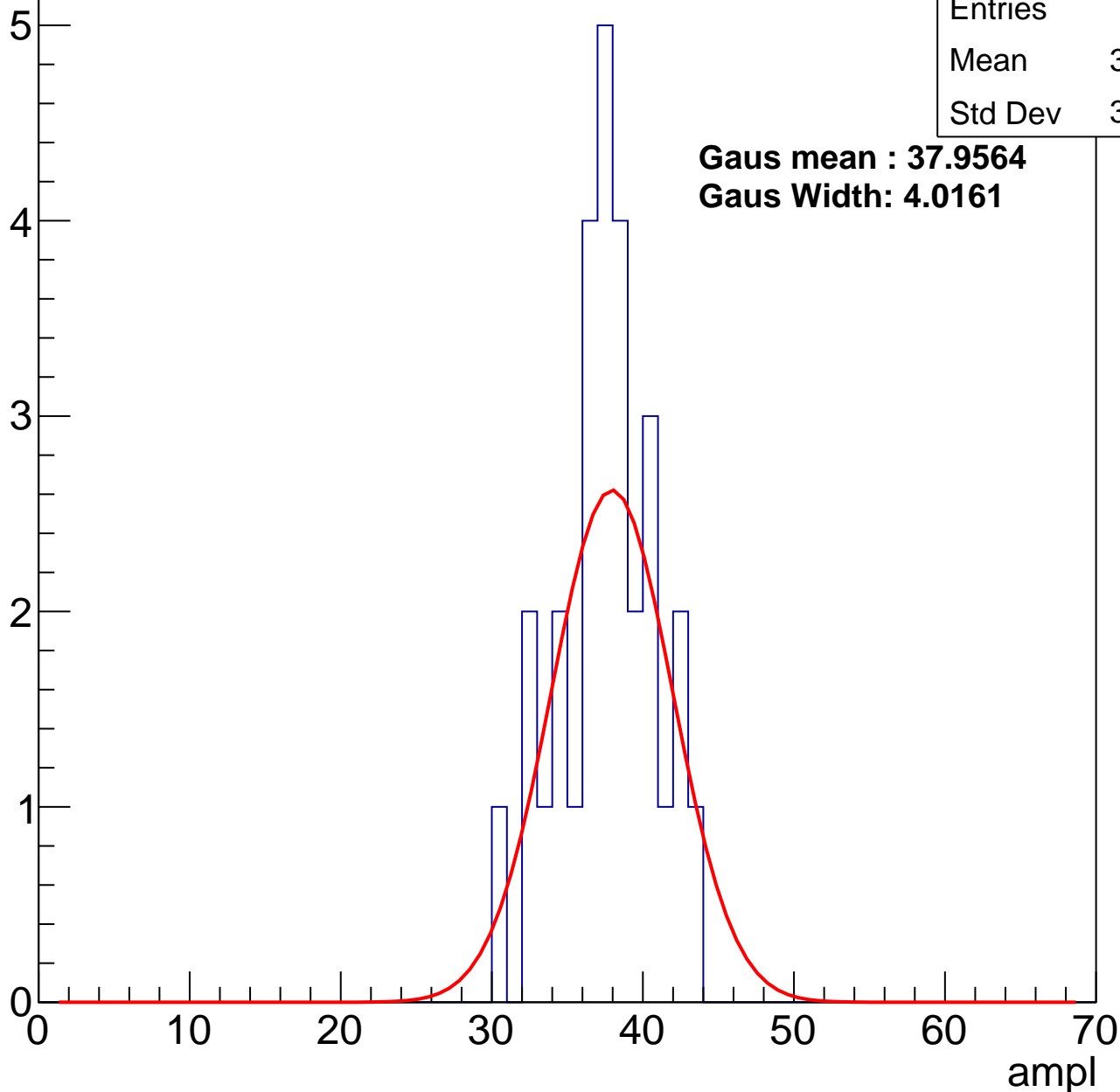
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	37.14
Std Dev	3.115

Gaus mean : 37.9564

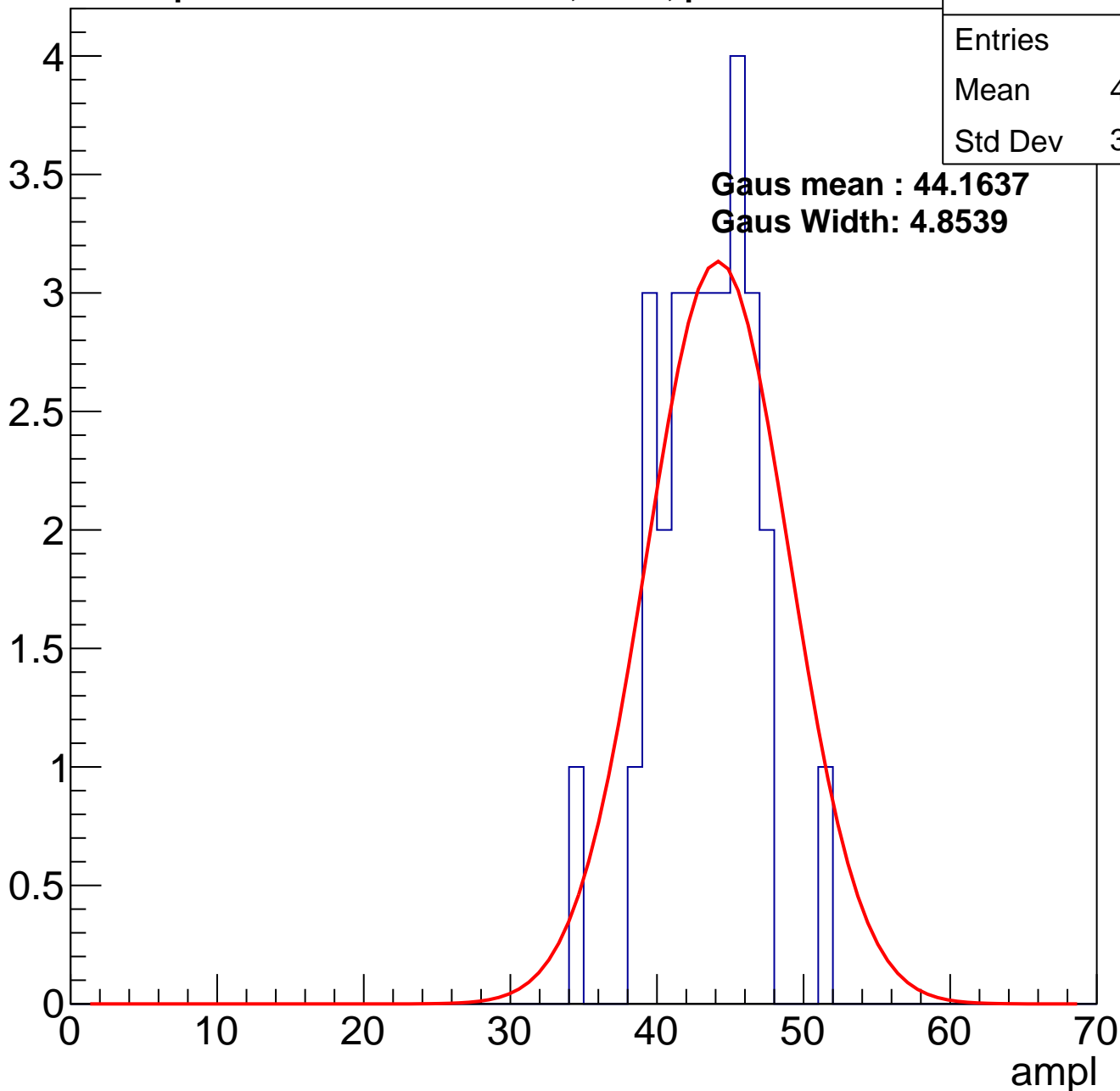
Gaus Width: 4.0161



B1L103S, U15-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



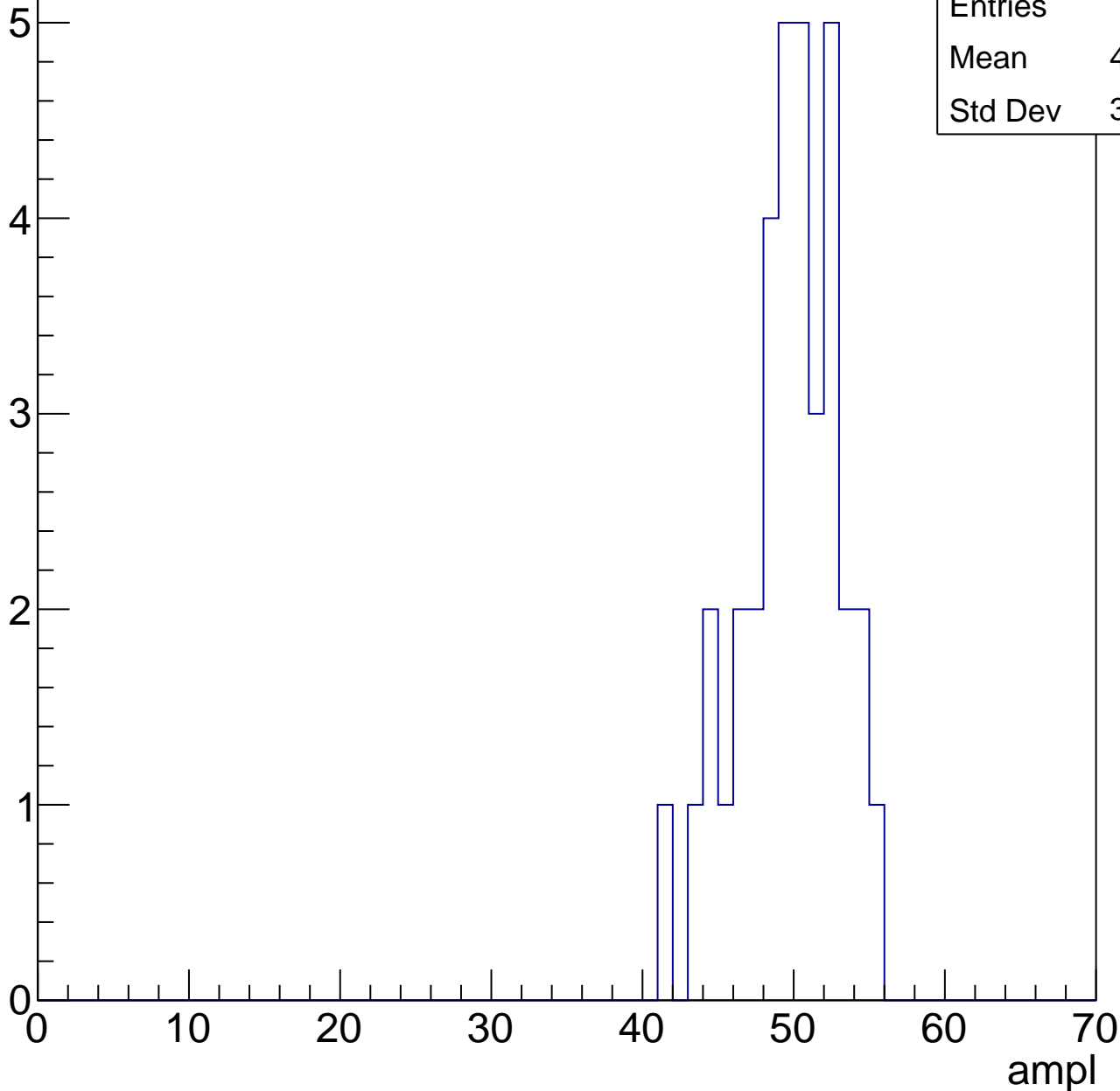
Entries	29
Mean	42.83
Std Dev	3.364

B1L103S, U15-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	49.22
Std Dev	3.224

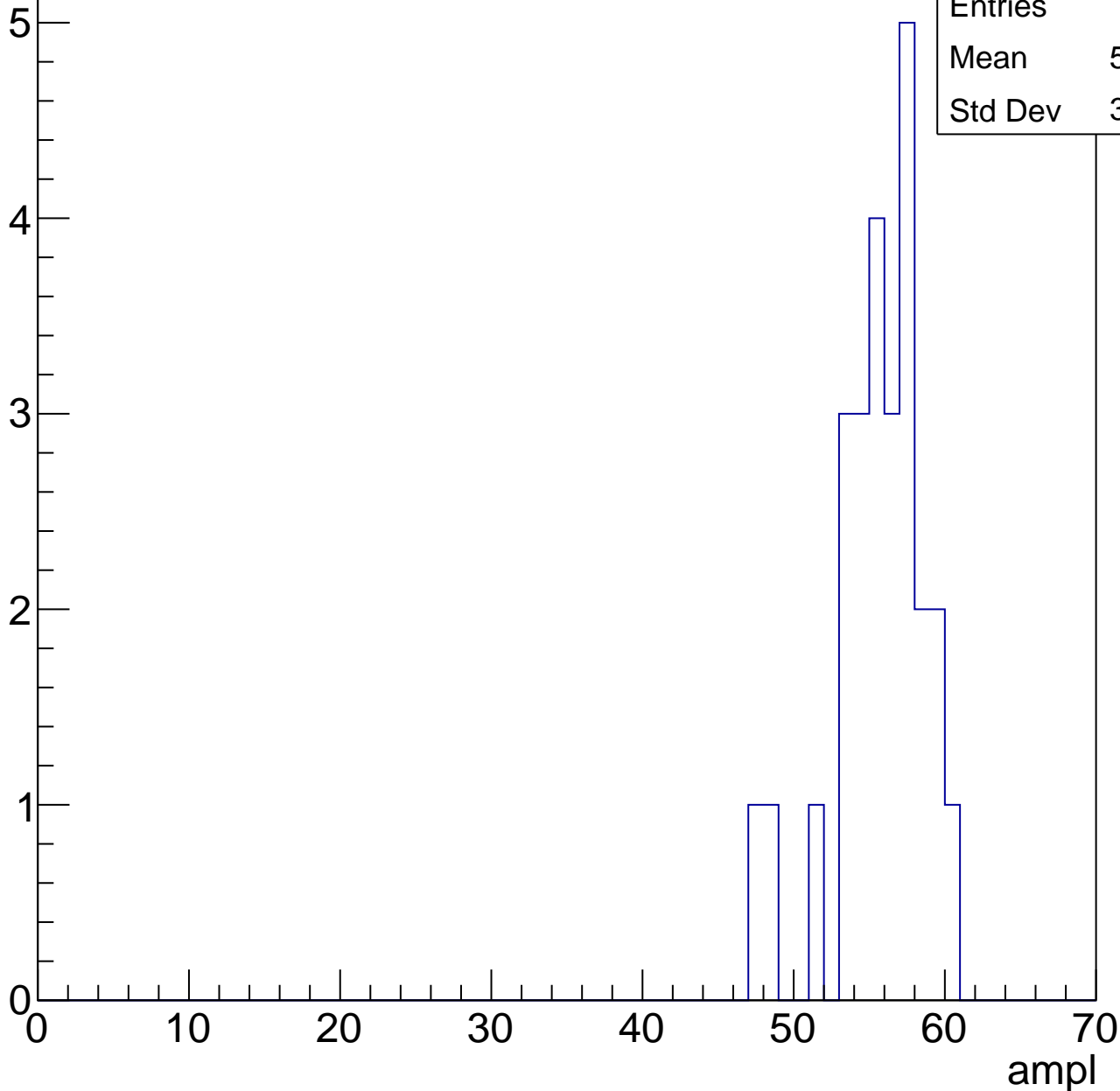


B1L103S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

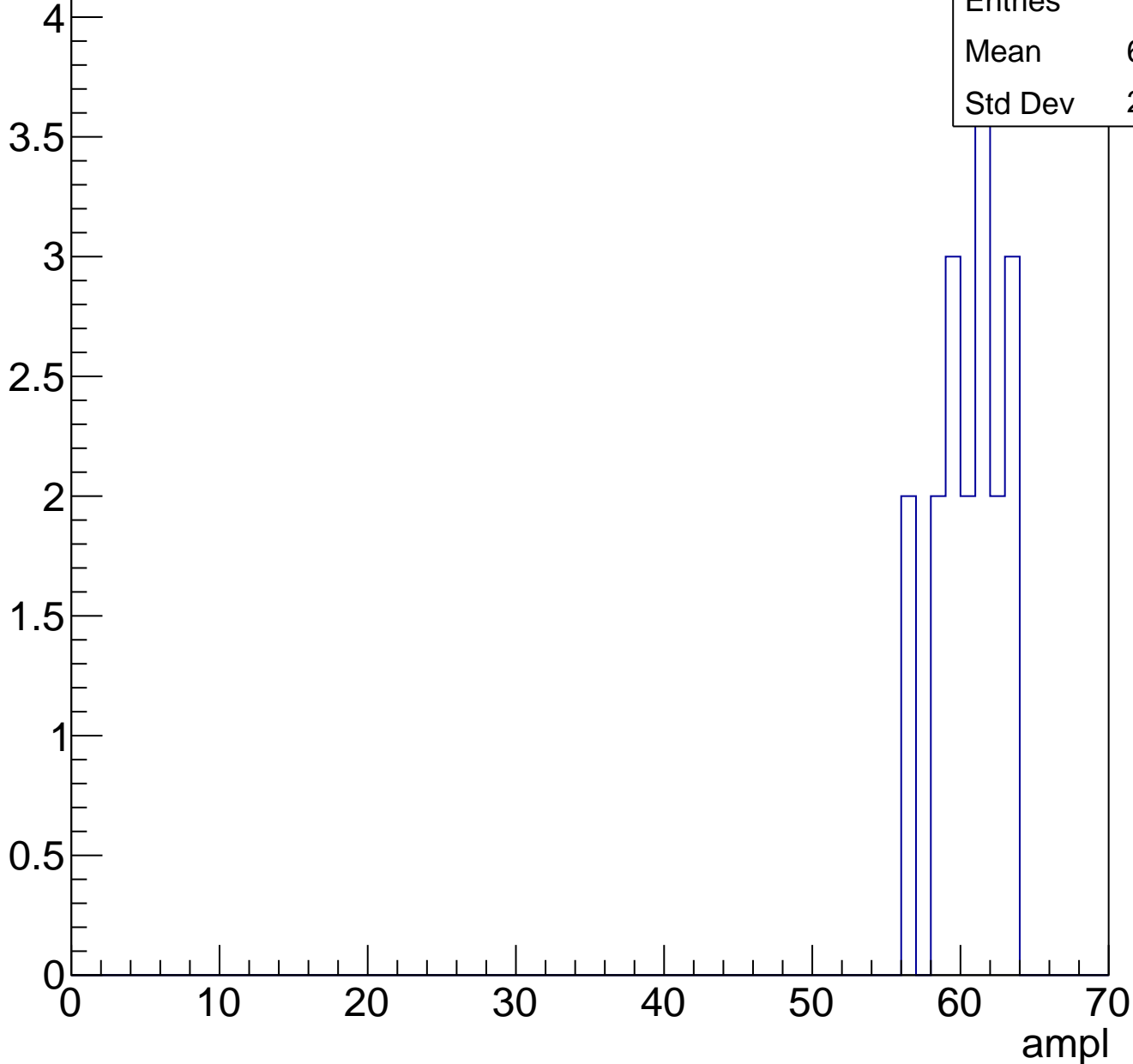
Entries	26
Mean	55.15
Std Dev	3.047



B1L103S, U15-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

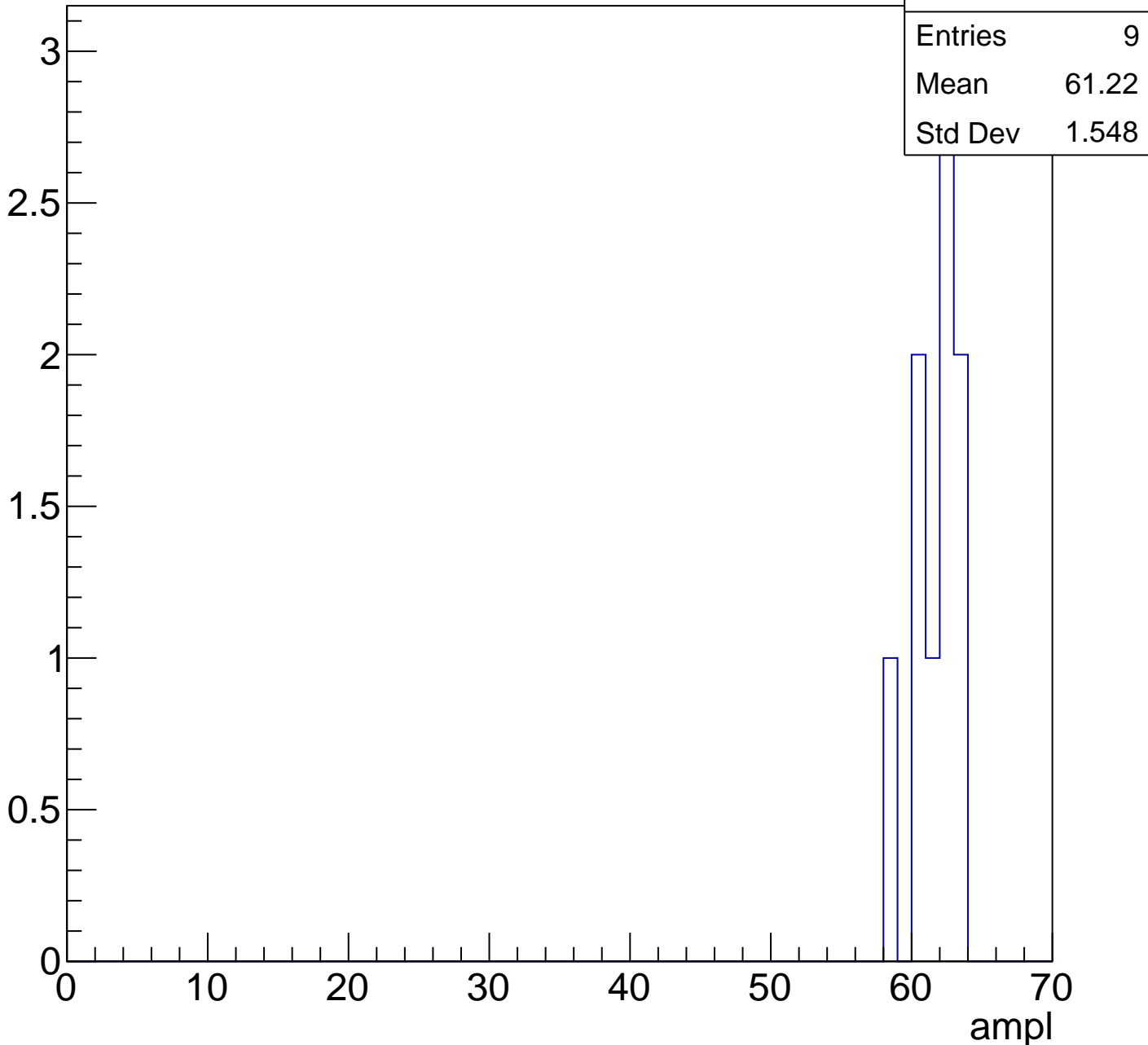
9

Mean

61.22

Std Dev

1.548



B1L103S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch4, adc0

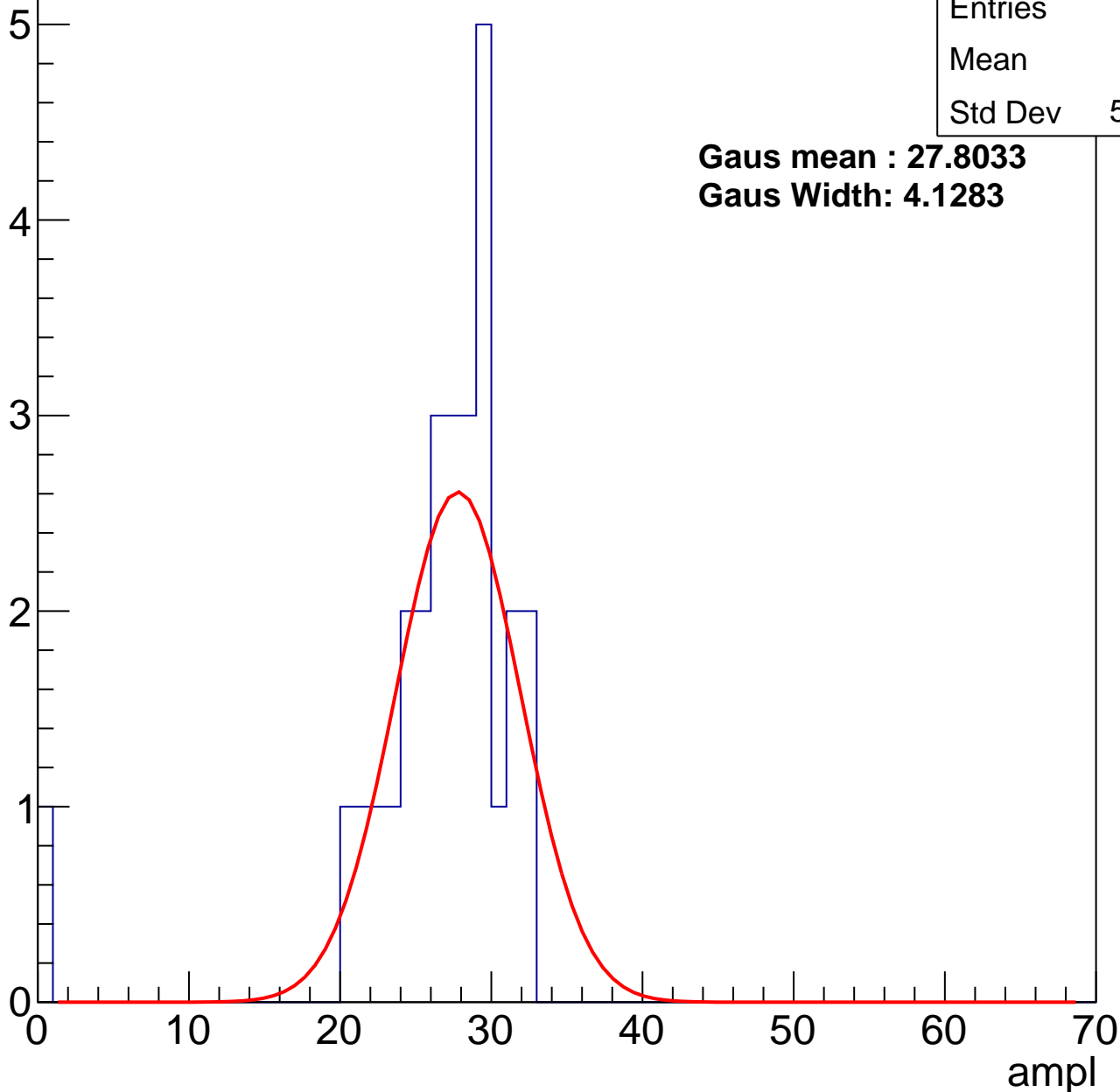
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	26
Std Dev	5.886

Gaus mean : 27.8033

Gaus Width: 4.1283



B1L103S, U15-ch4, adc1

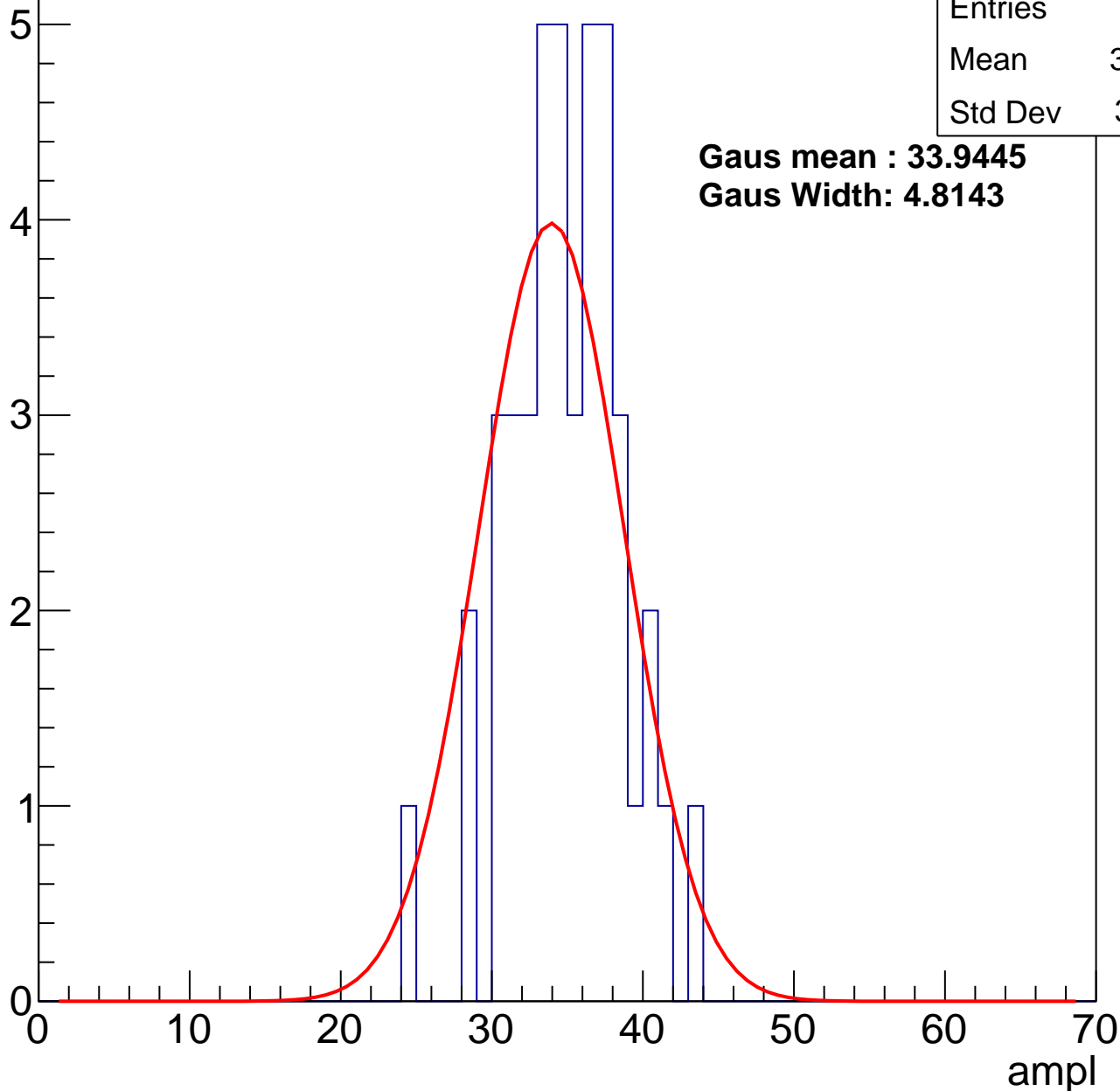
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	34.44
Std Dev	3.731

Gaus mean : 33.9445

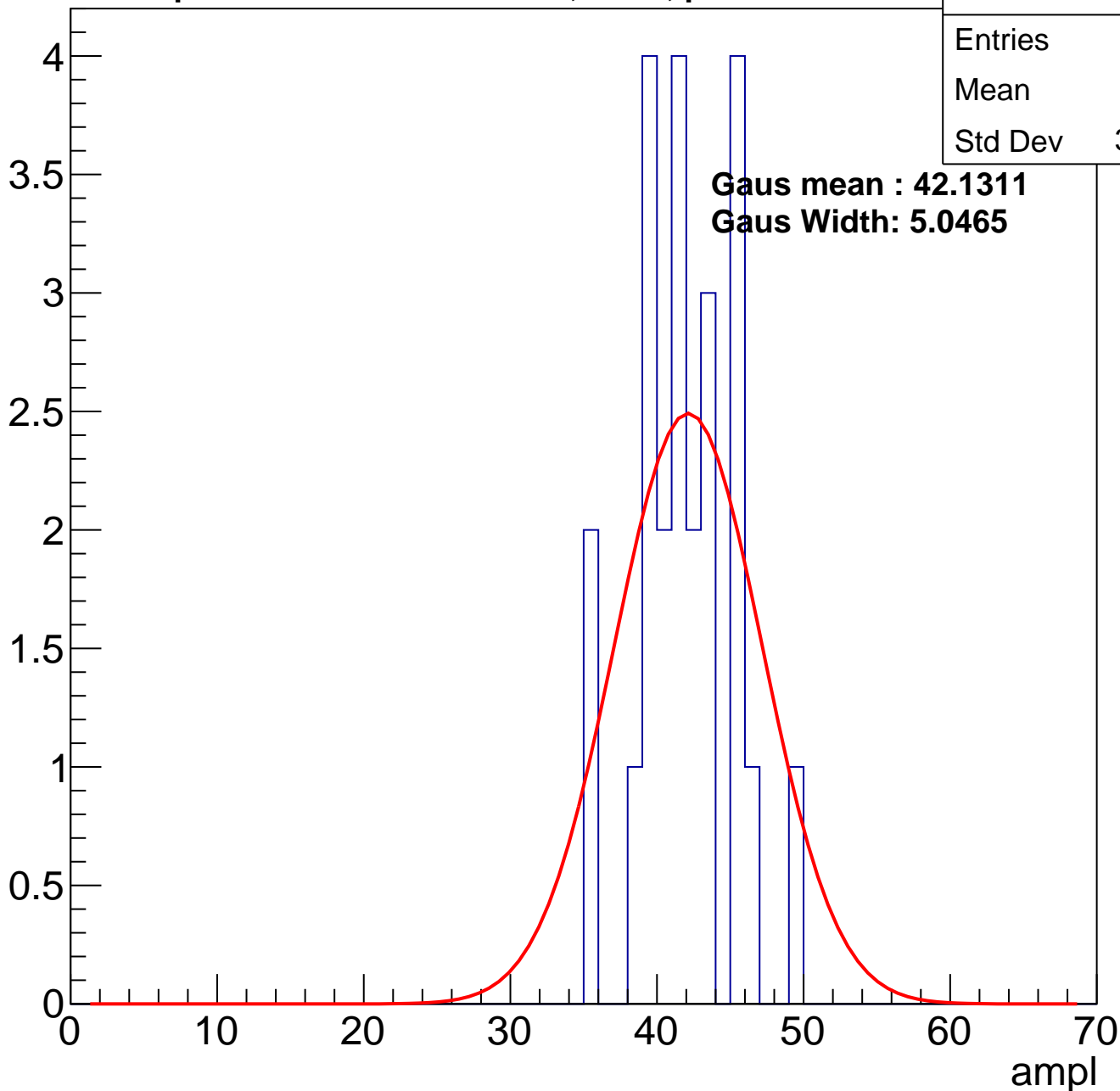
Gaus Width: 4.8143



B1L103S, U15-ch4, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

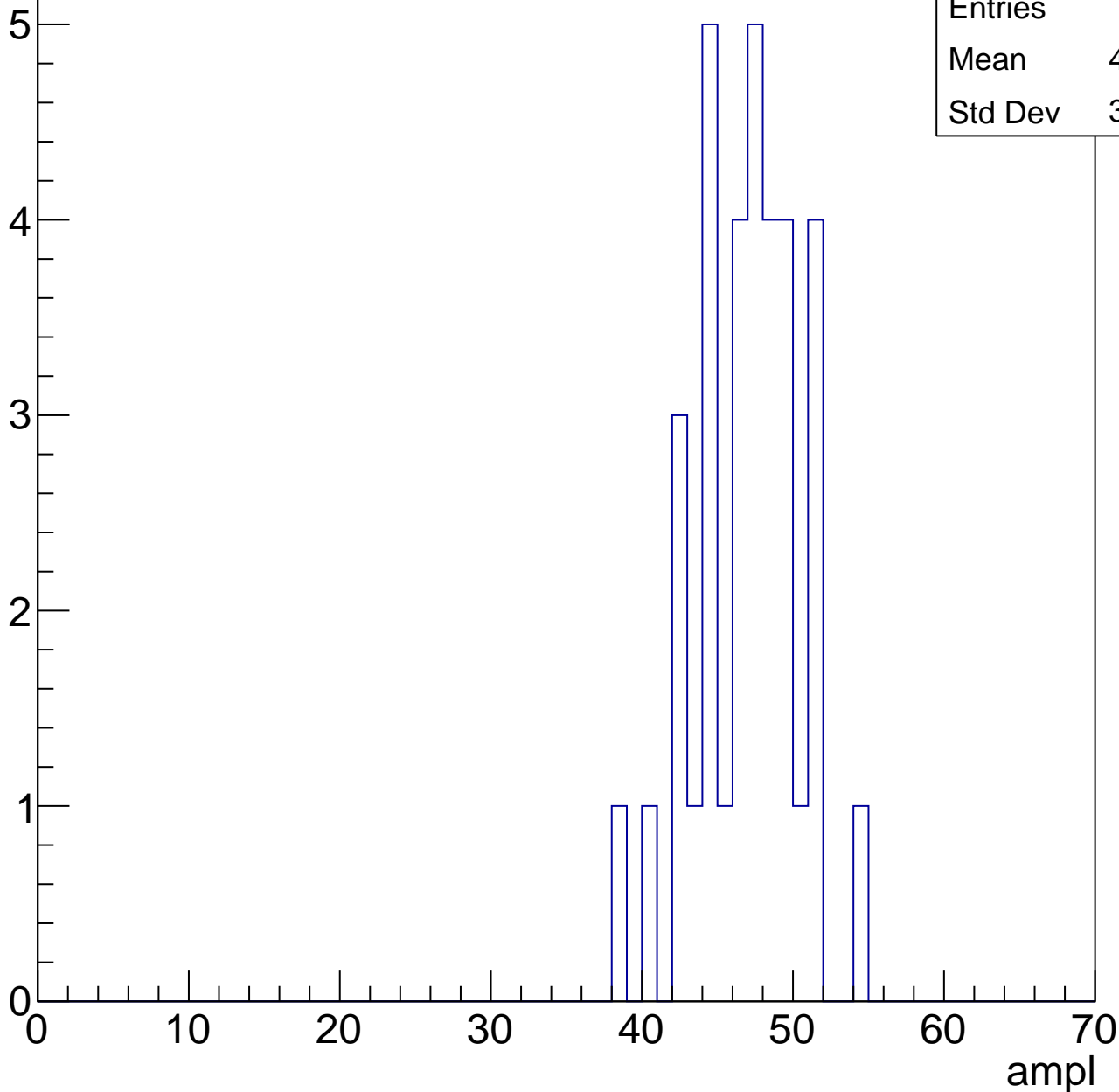


B1L103S, U15-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	46.49
Std Dev	3.409

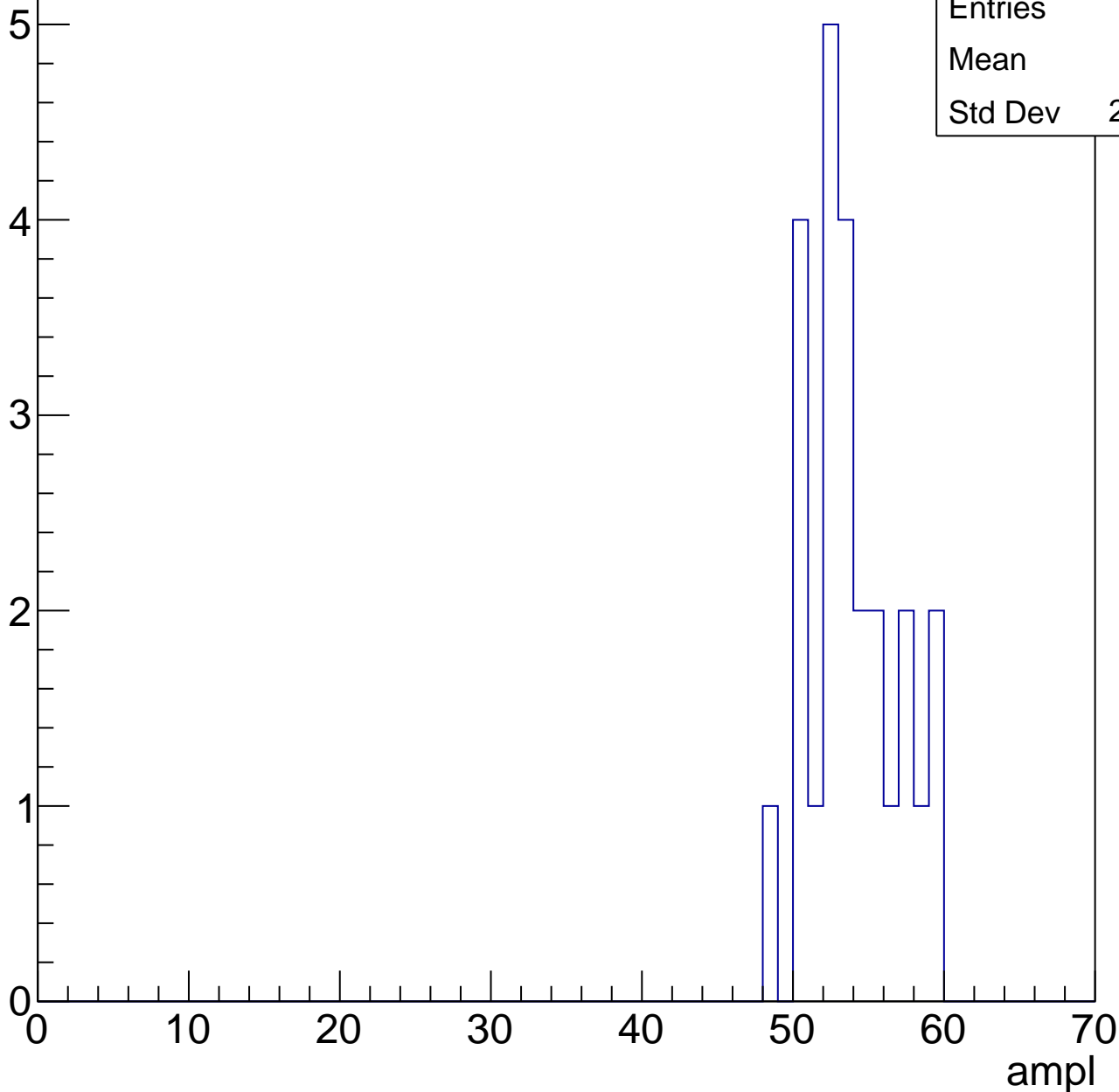


B1L103S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	53.4
Std Dev	2.926



B1L103S, U15-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	30
Mean	58.3
Std Dev	2.734

ampl

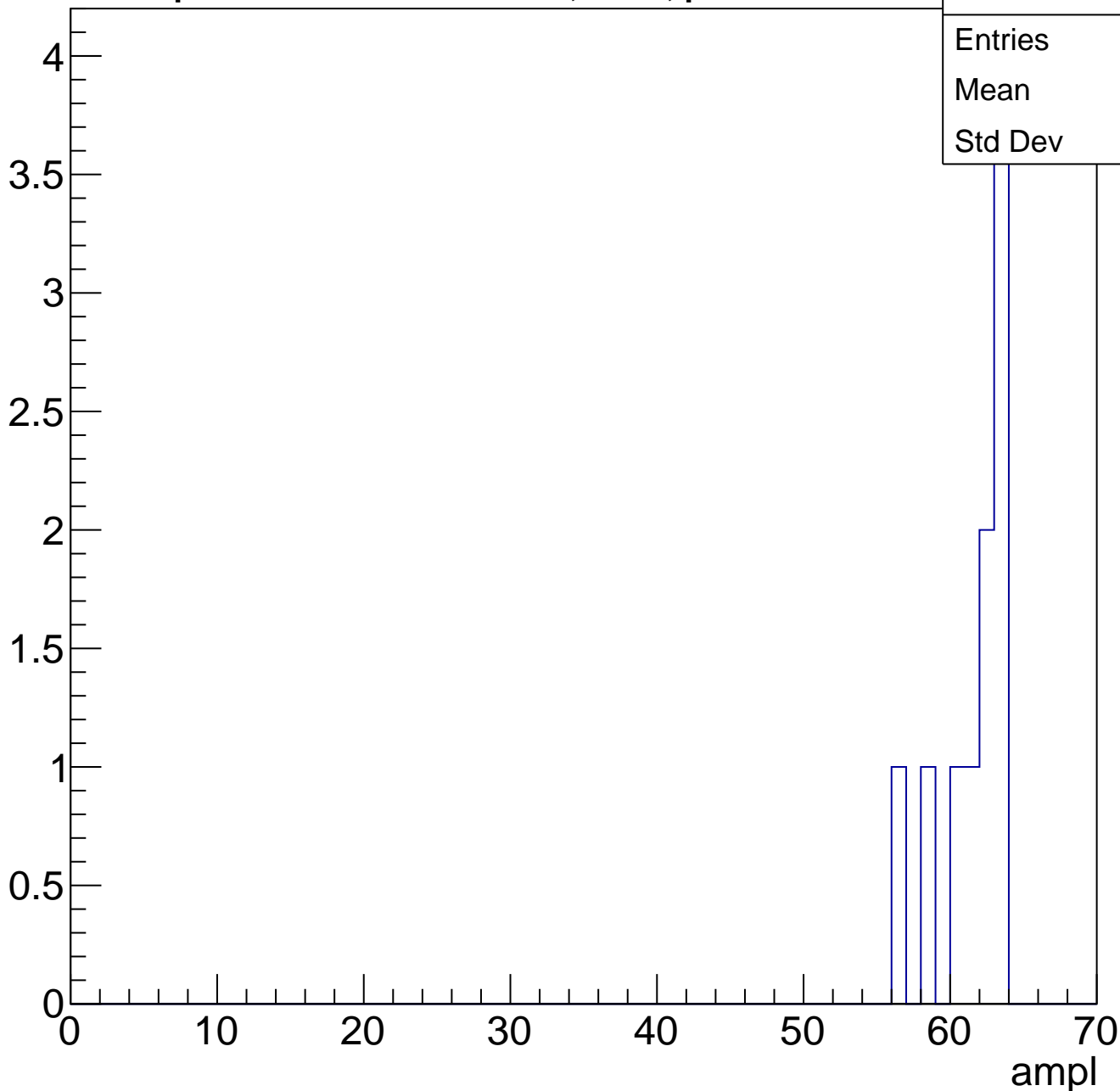
0 10 20 30 40 50 60 70

0 0.5 1 1.5 2 2.5 3 3.5 4

B1L103S, U15-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.1
Std Dev	2.3

B1L103S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch5, adc0

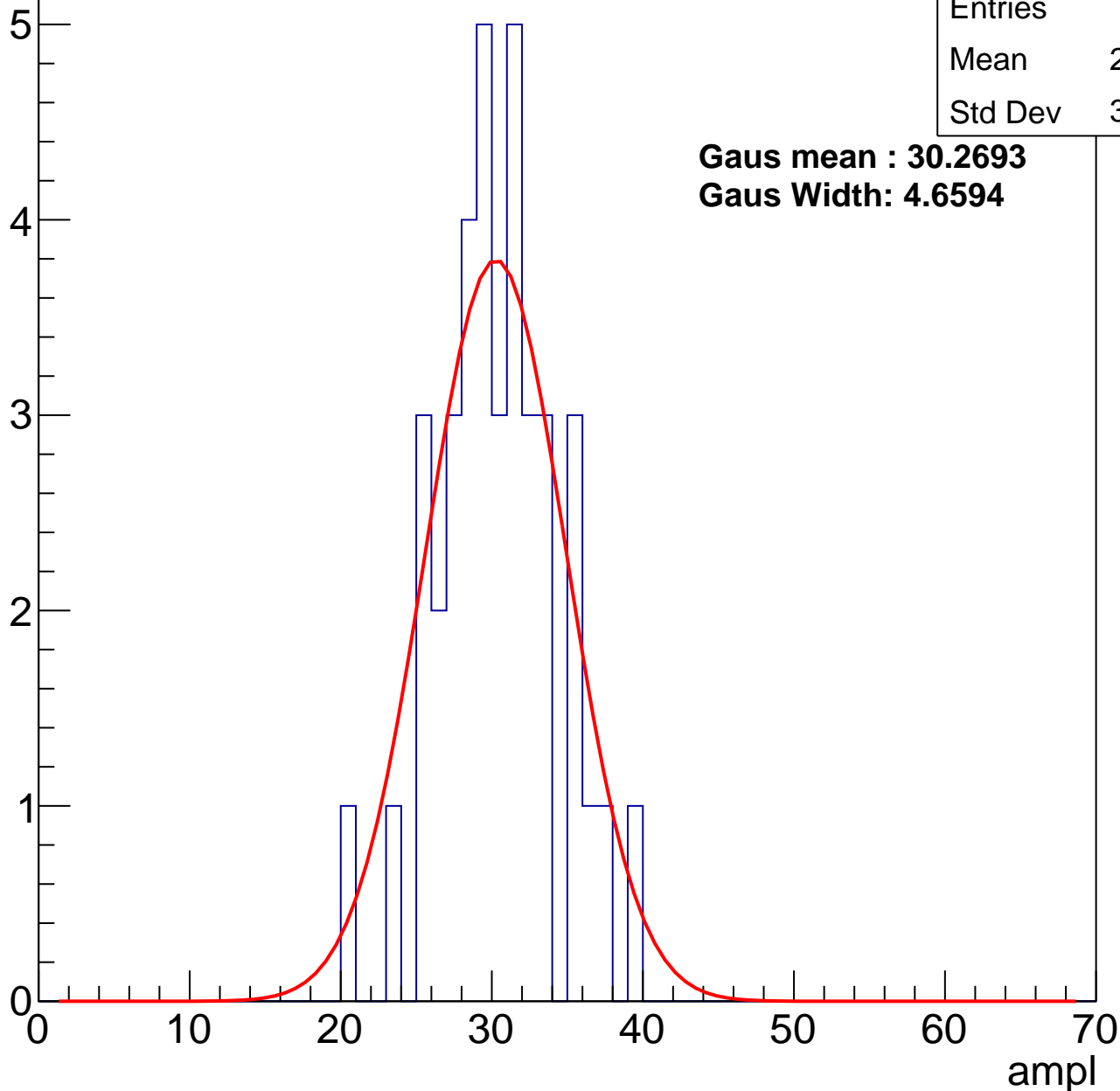
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	29.87
Std Dev	3.884

Gaus mean : 30.2693

Gaus Width: 4.6594



B1L103S, U15-ch5, adc1

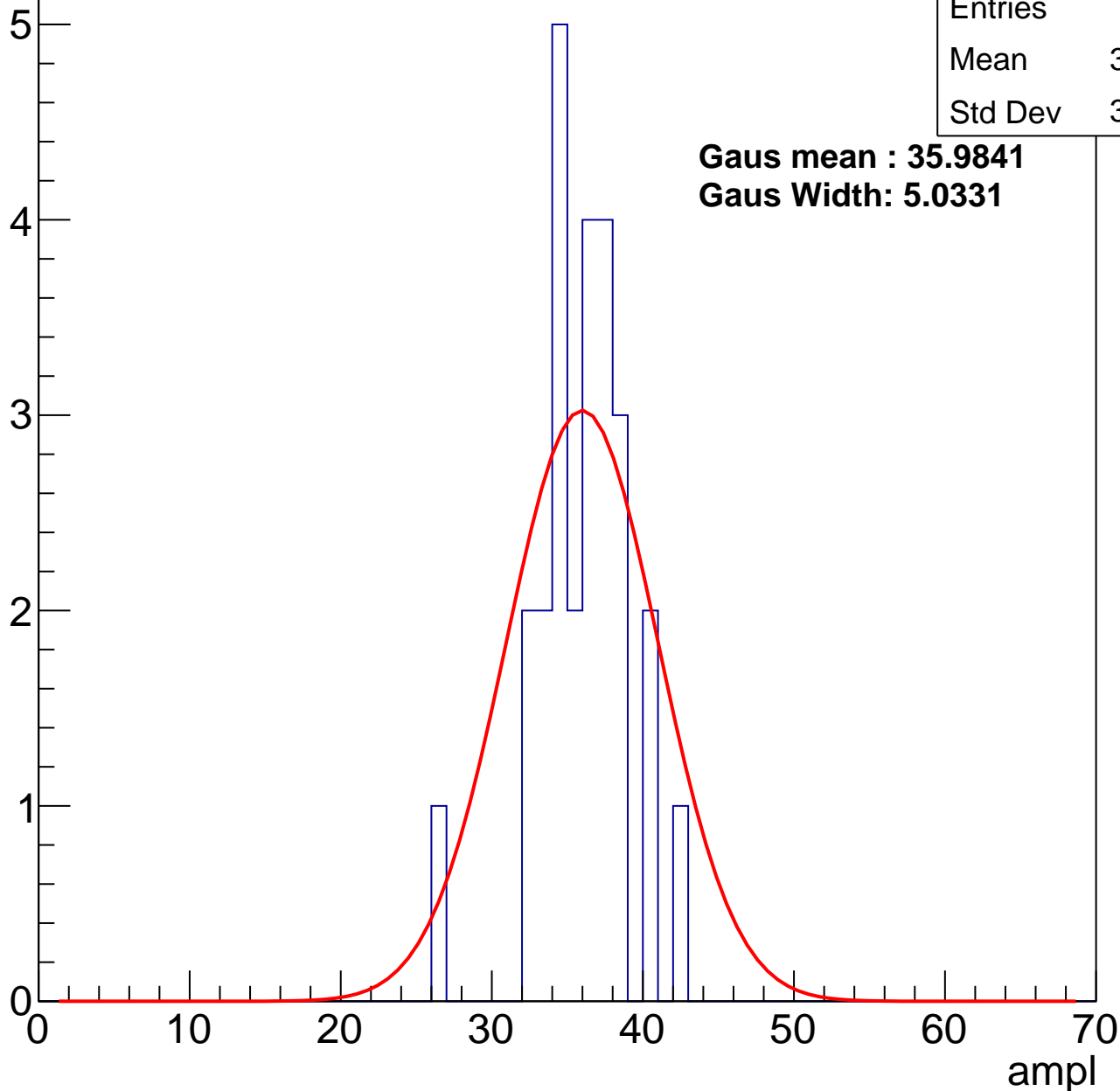
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	35.54
Std Dev	3.104

Gaus mean : 35.9841

Gaus Width: 5.0331



B1L103S, U15-ch5, adc2

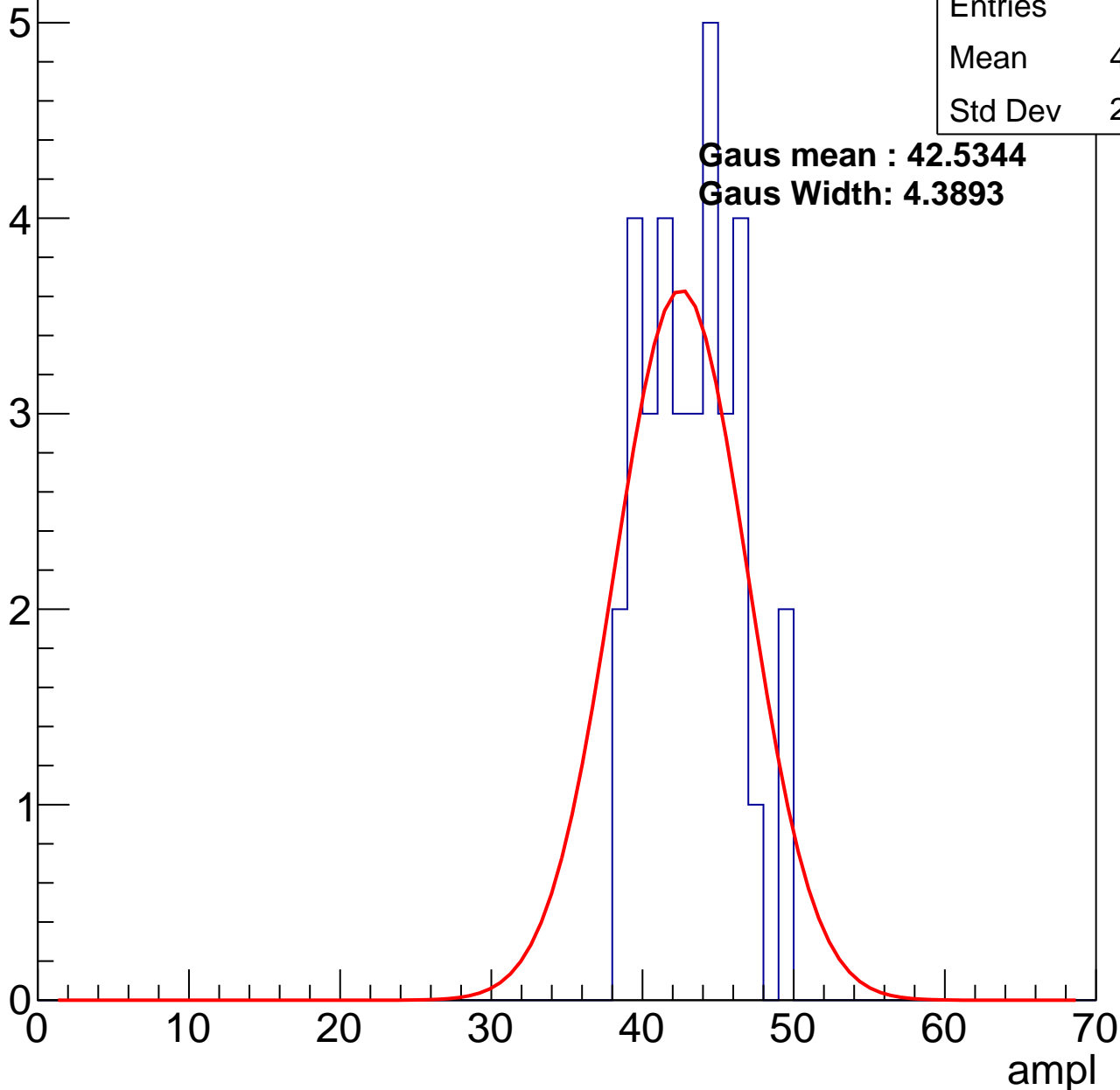
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	42.79
Std Dev	2.968

Gaus mean : 42.5344

Gaus Width: 4.3893

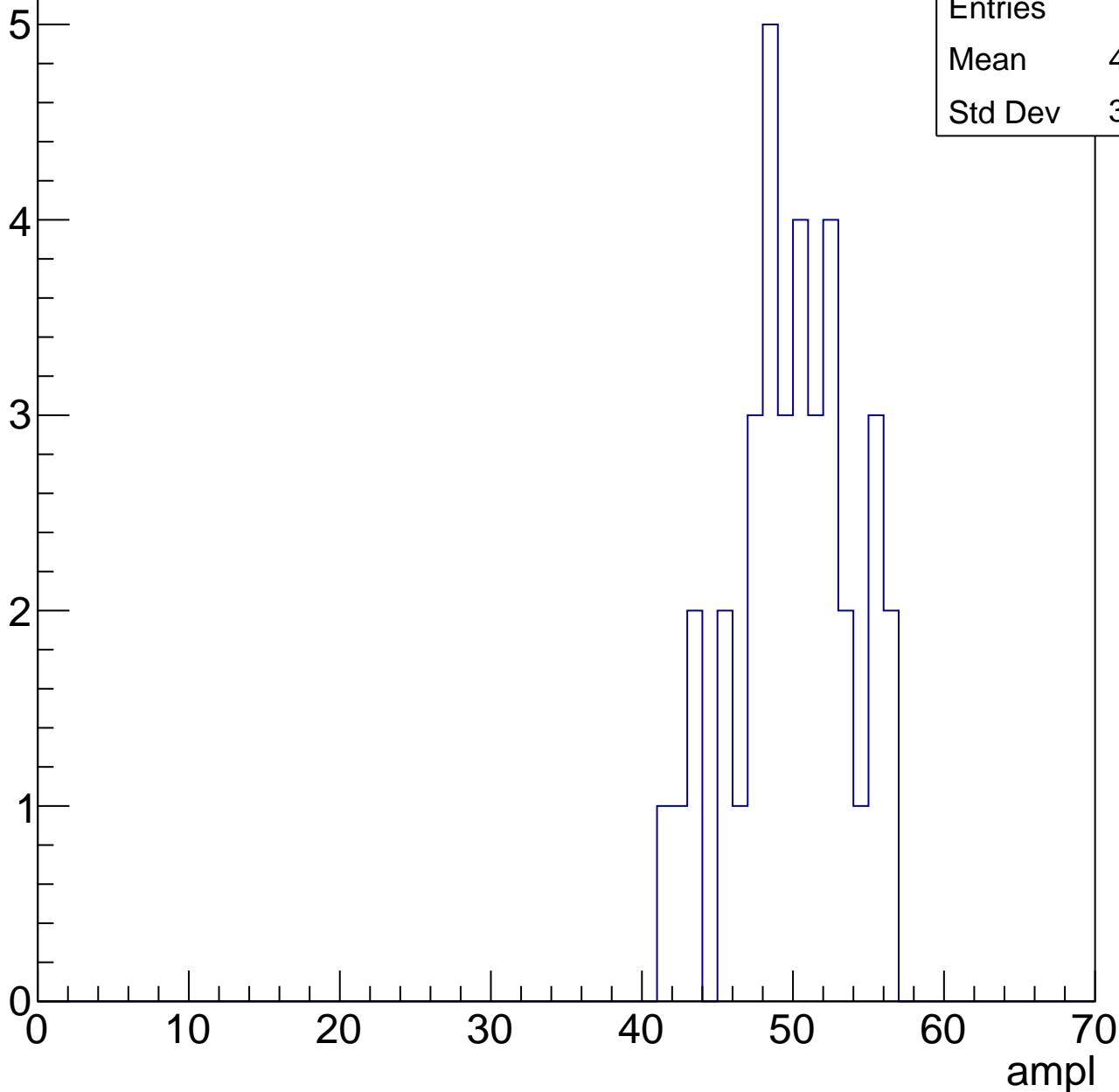


B1L103S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

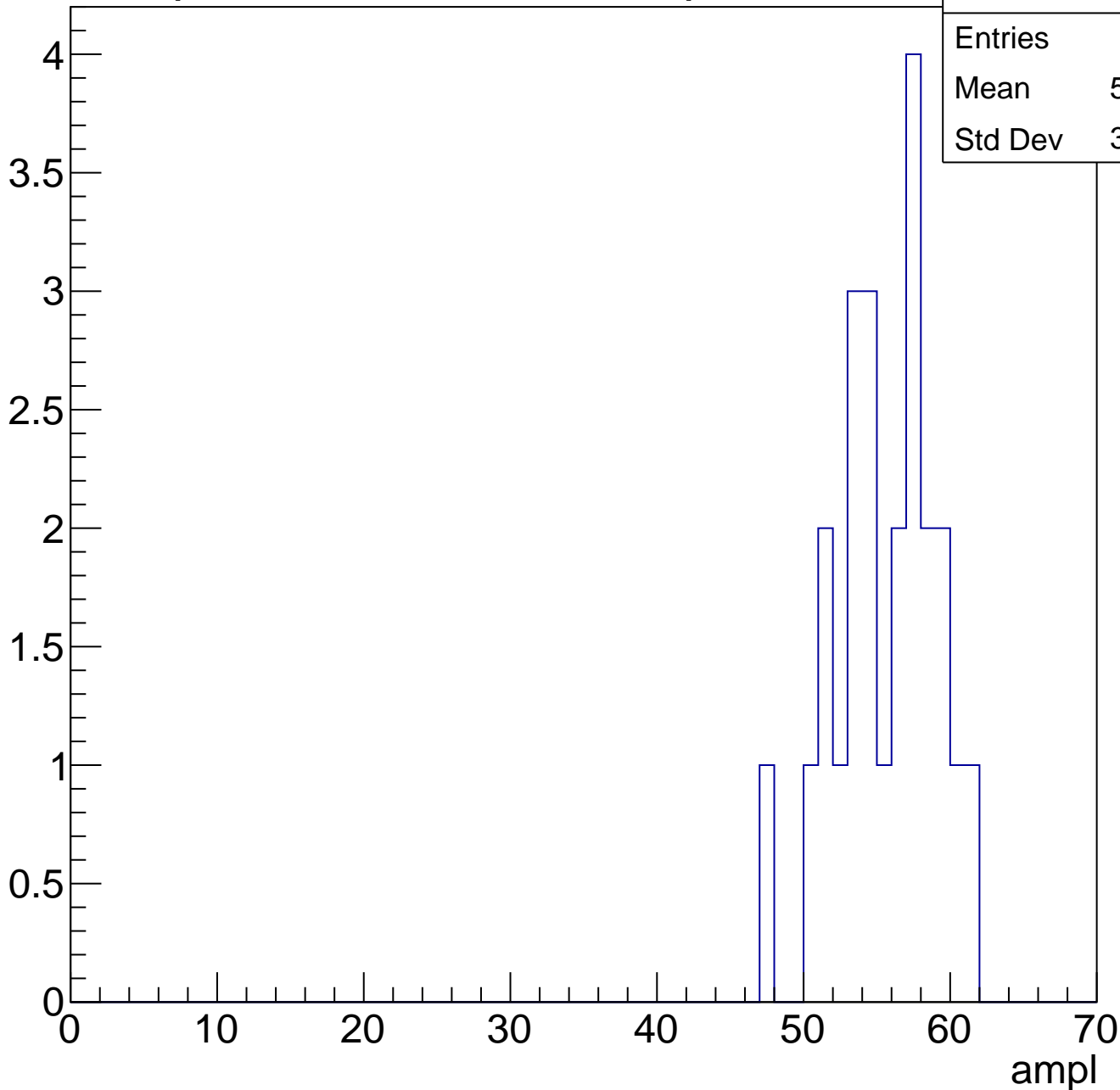
Entries	37
Mean	49.49
Std Dev	3.853



B1L103S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

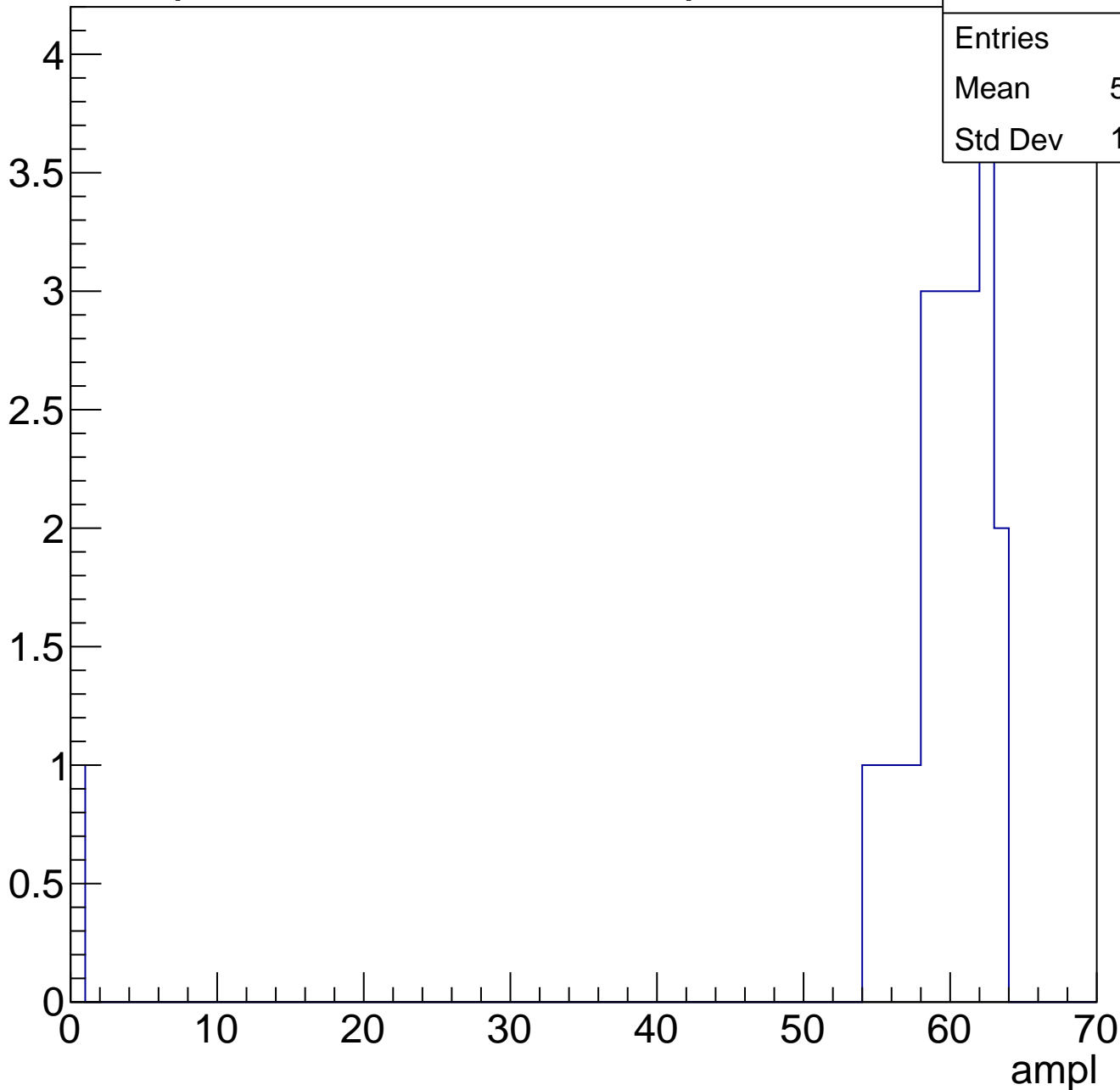
Entry



B1L103S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

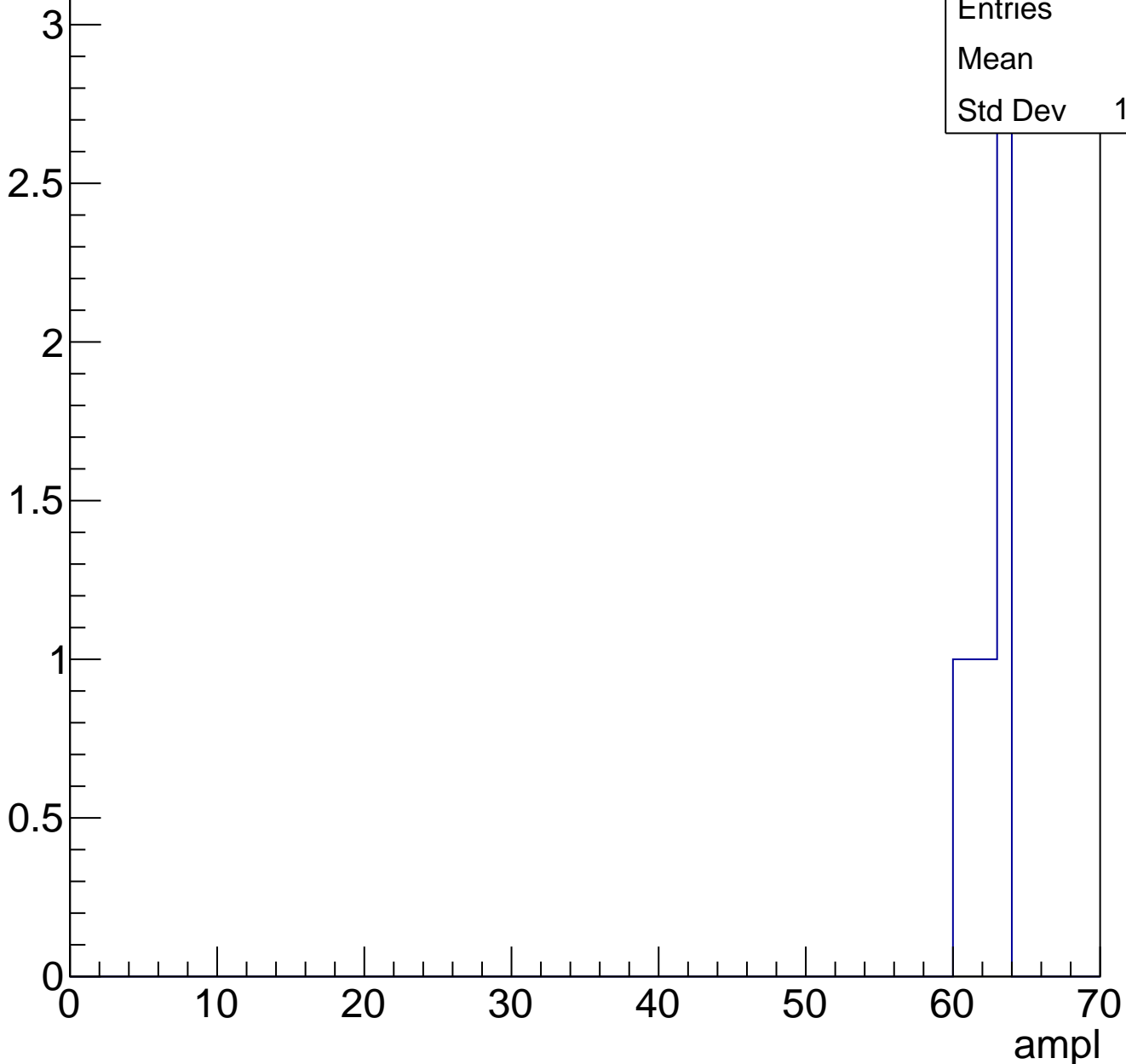
Entry



B1L103S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch6, adc0

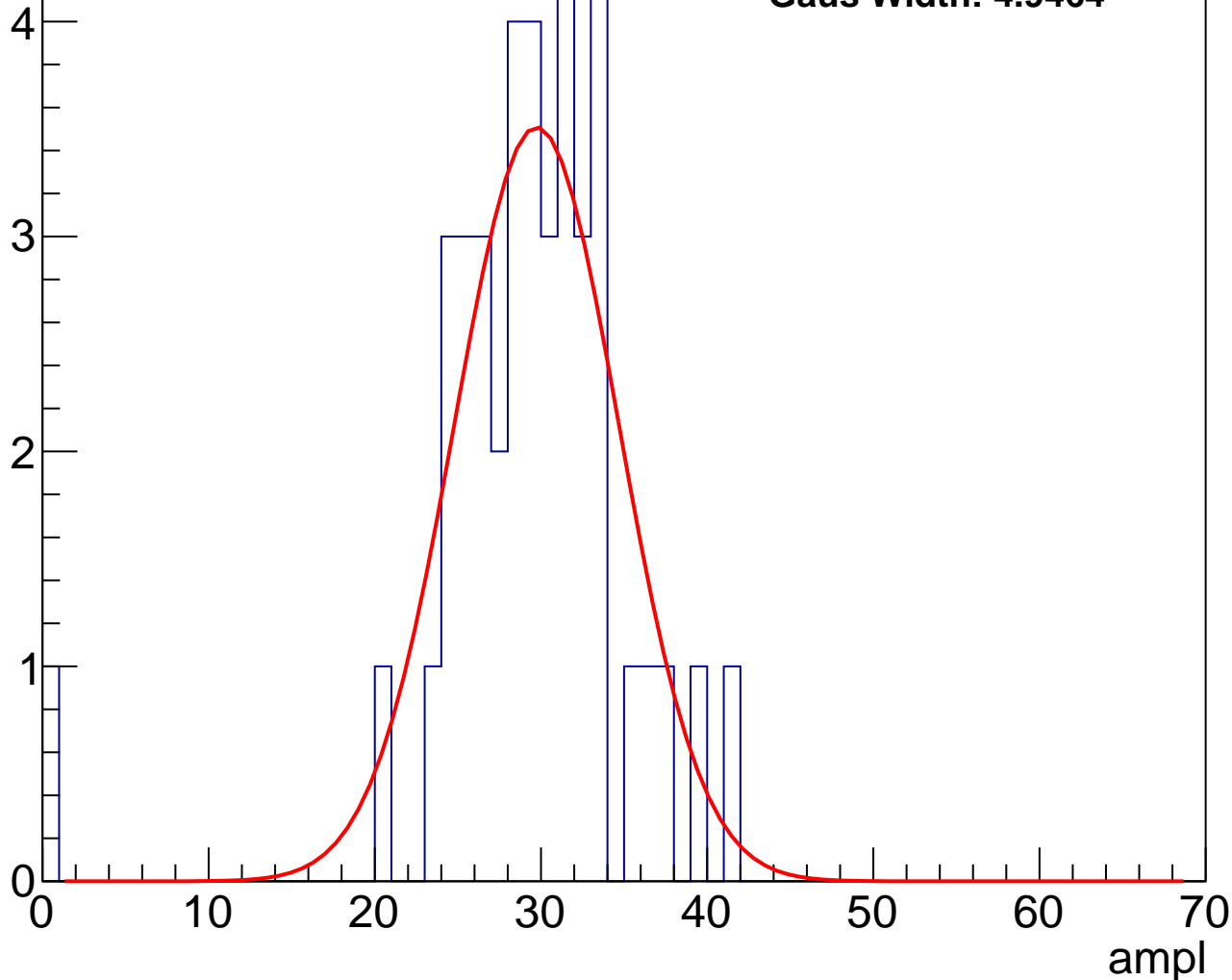
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	28.93
Std Dev	6.181

Gaus mean : 29.7351

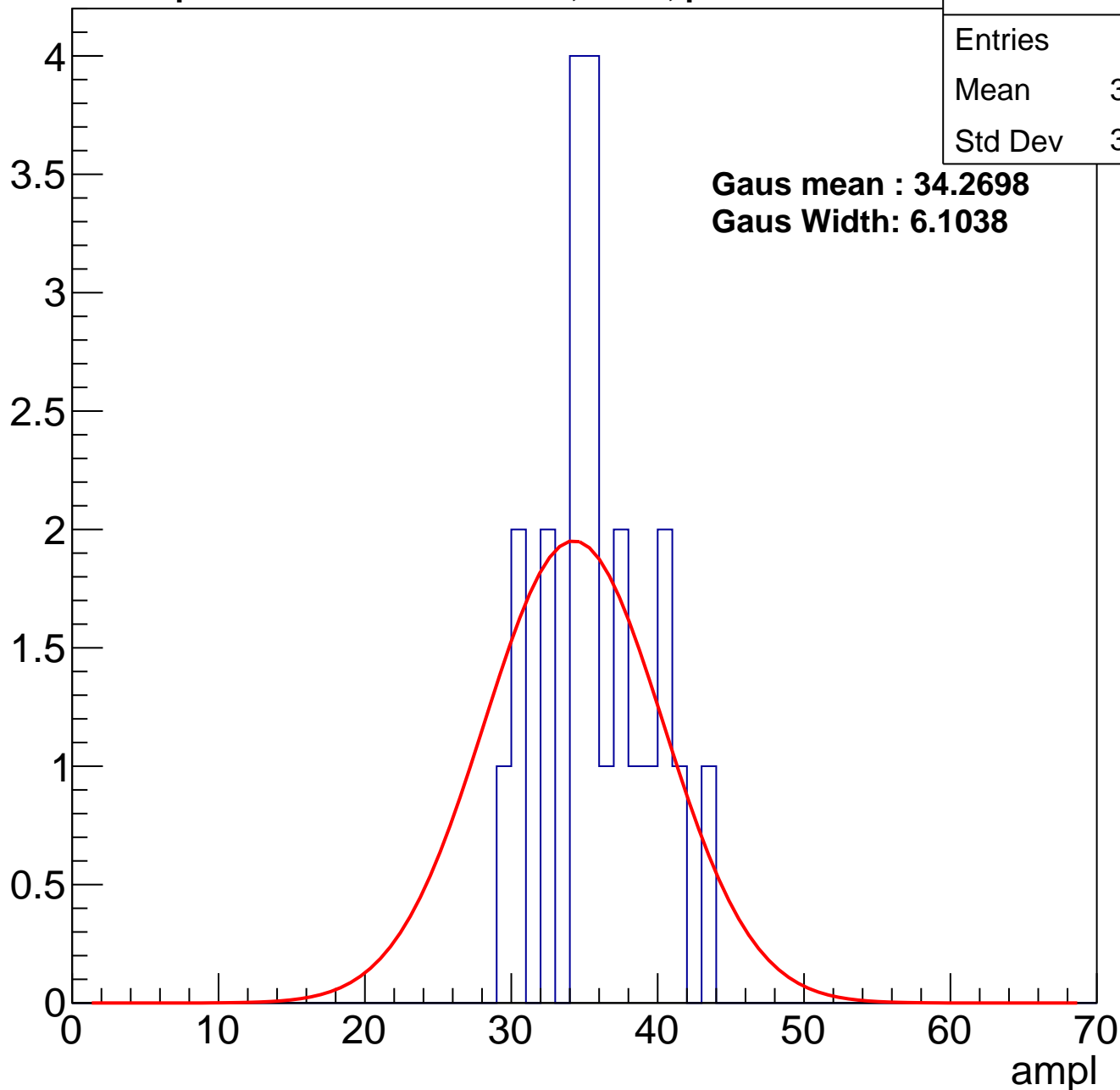
Gaus Width: 4.9464



B1L103S, U15-ch6, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch6, adc2

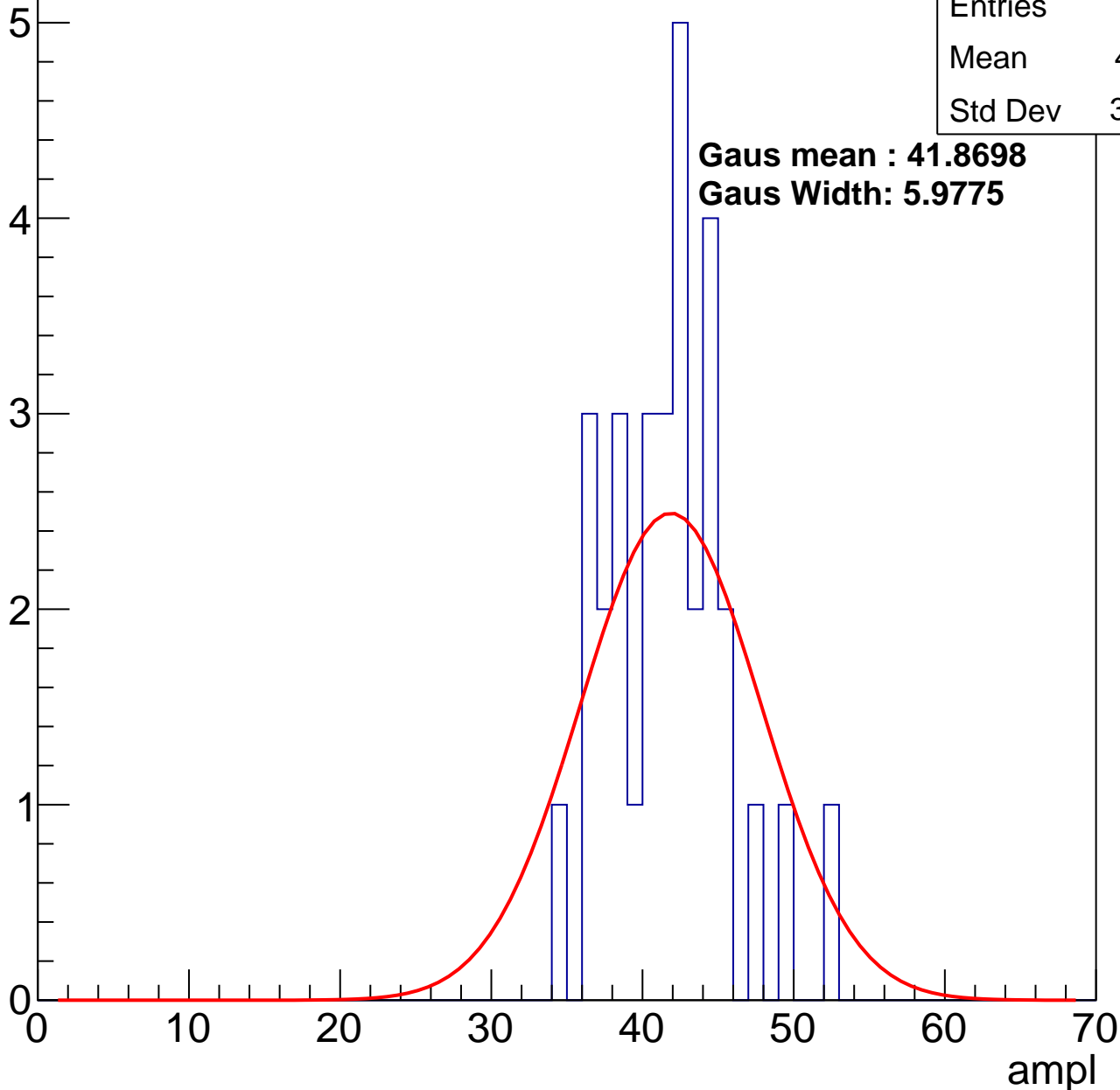
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	41.31
Std Dev	3.909

Gaus mean : 41.8698

Gaus Width: 5.9775

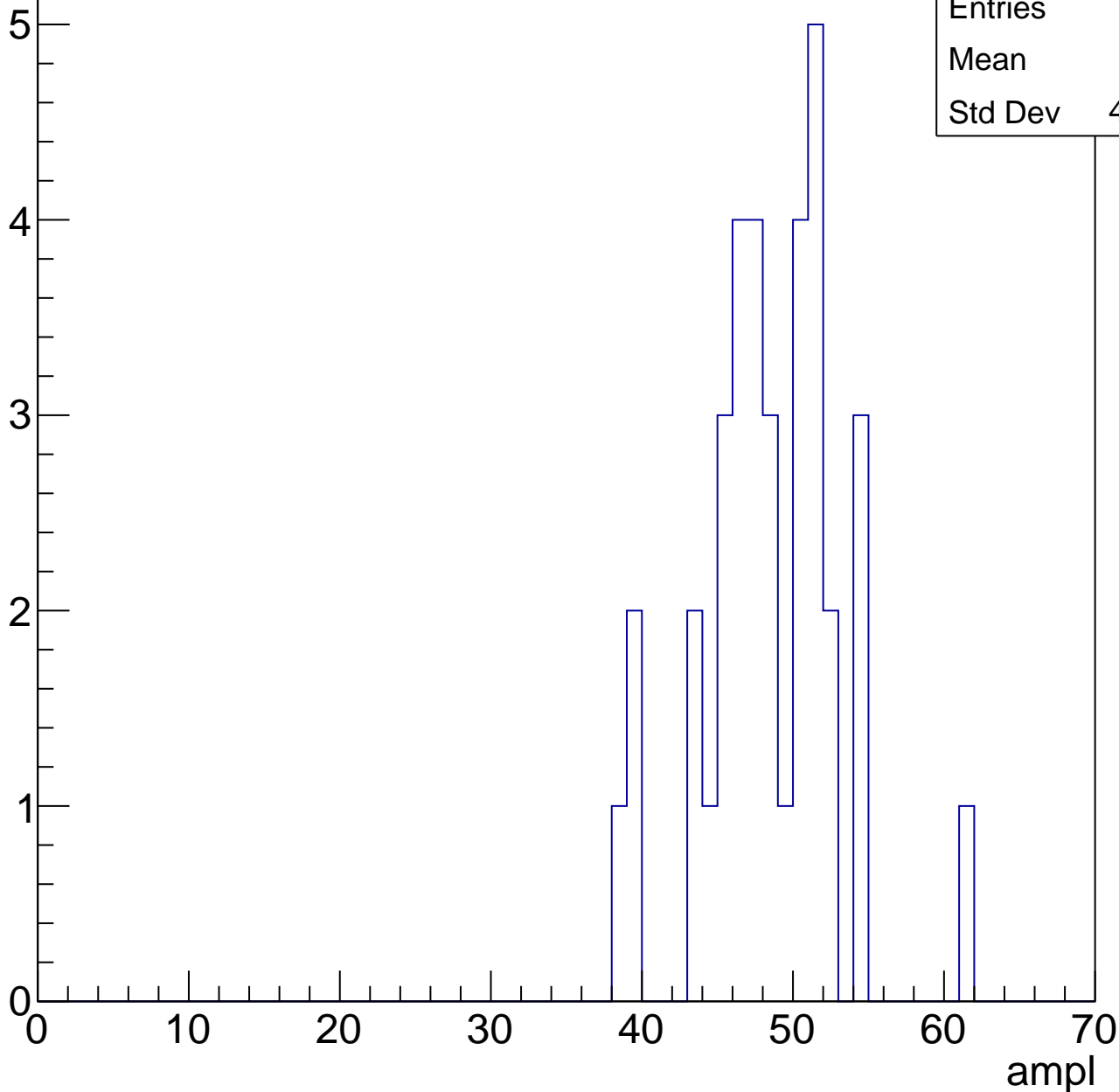


B1L103S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

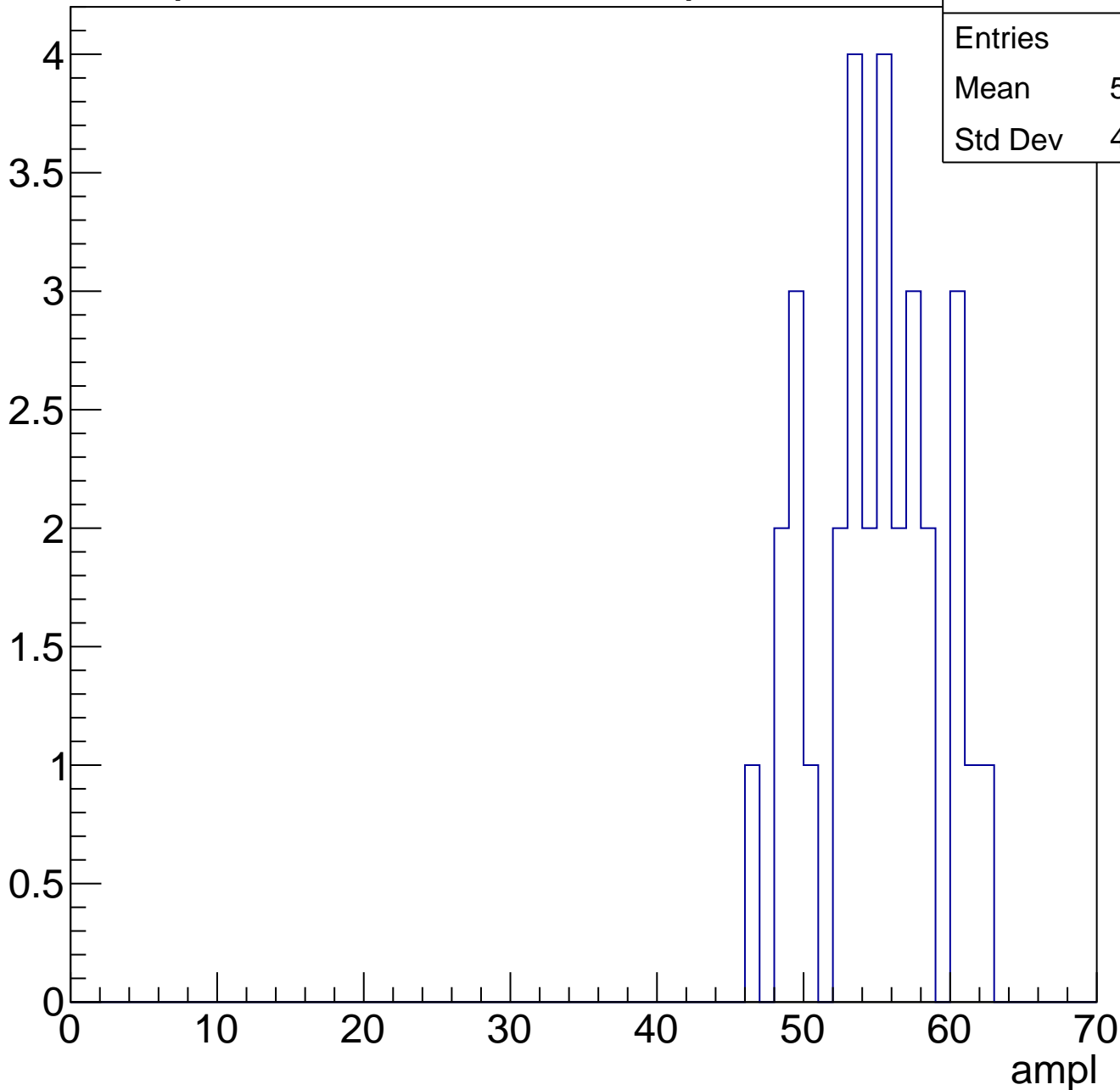
Entries	36
Mean	48
Std Dev	4.552



B1L103S, U15-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	20
Mean	58.8
Std Dev	2.694

B1L103S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

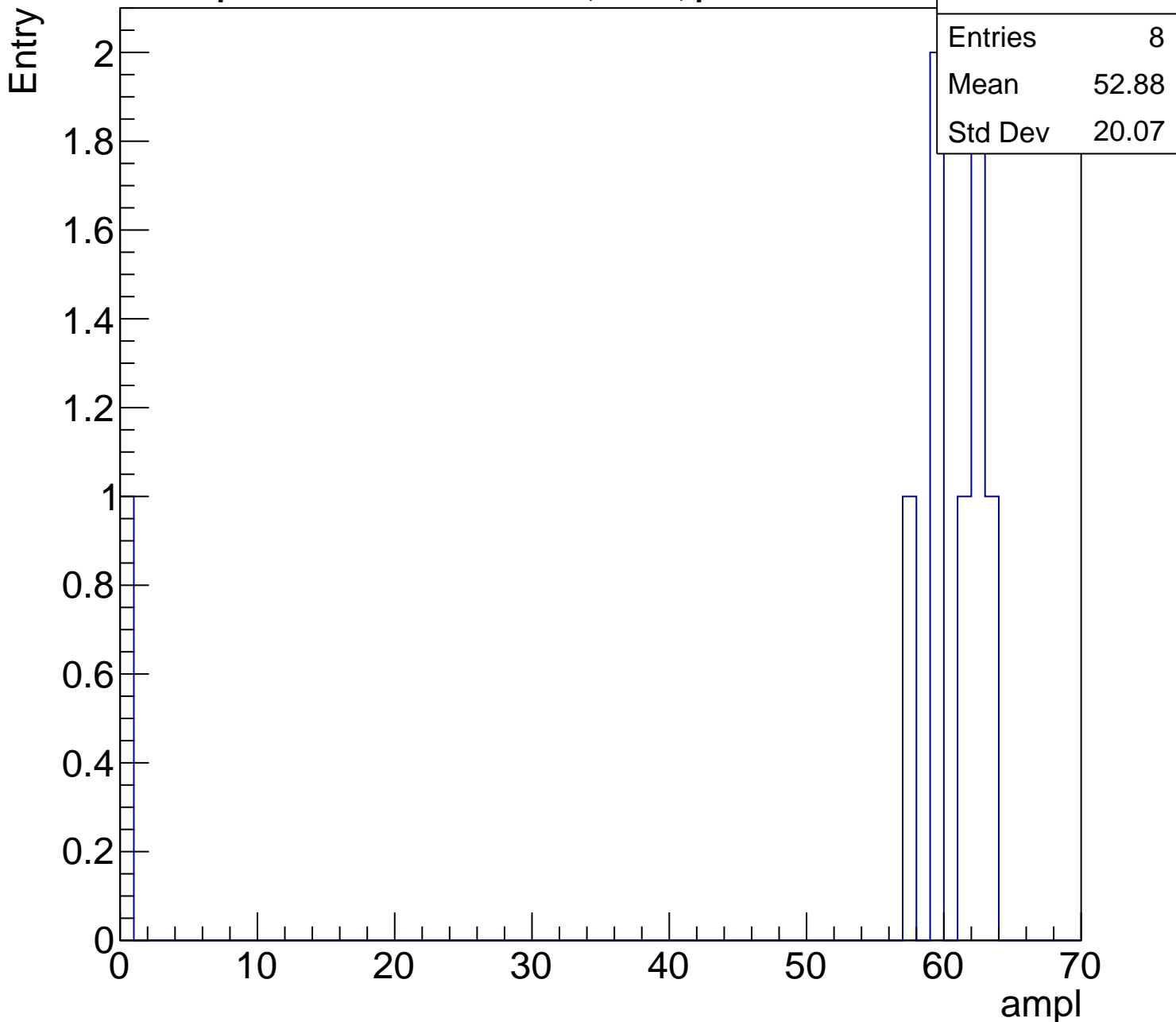
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	52.88
Std Dev	20.07

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L103S, U15-ch7, adc0

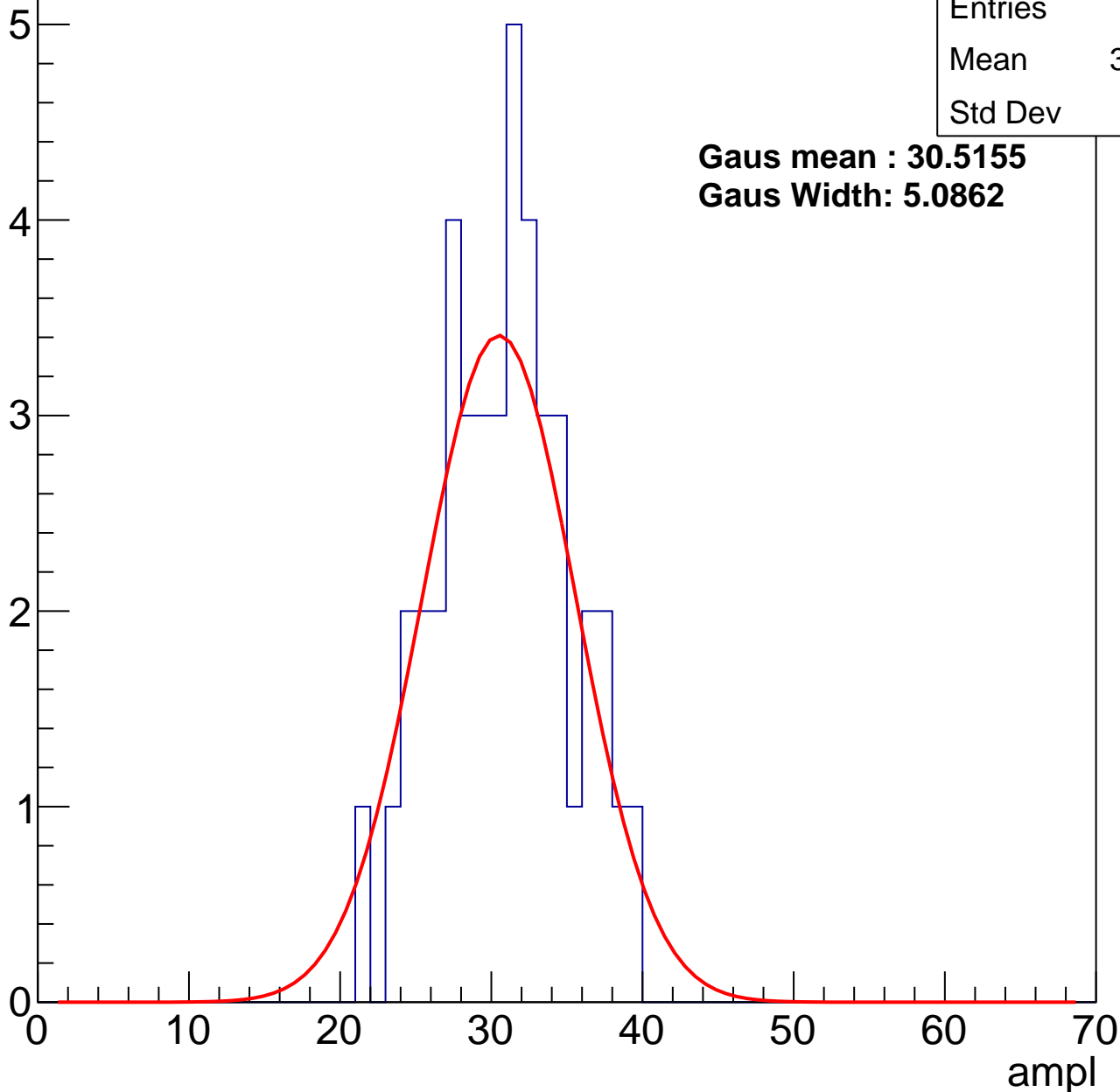
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	30.35
Std Dev	4.22

Gaus mean : 30.5155

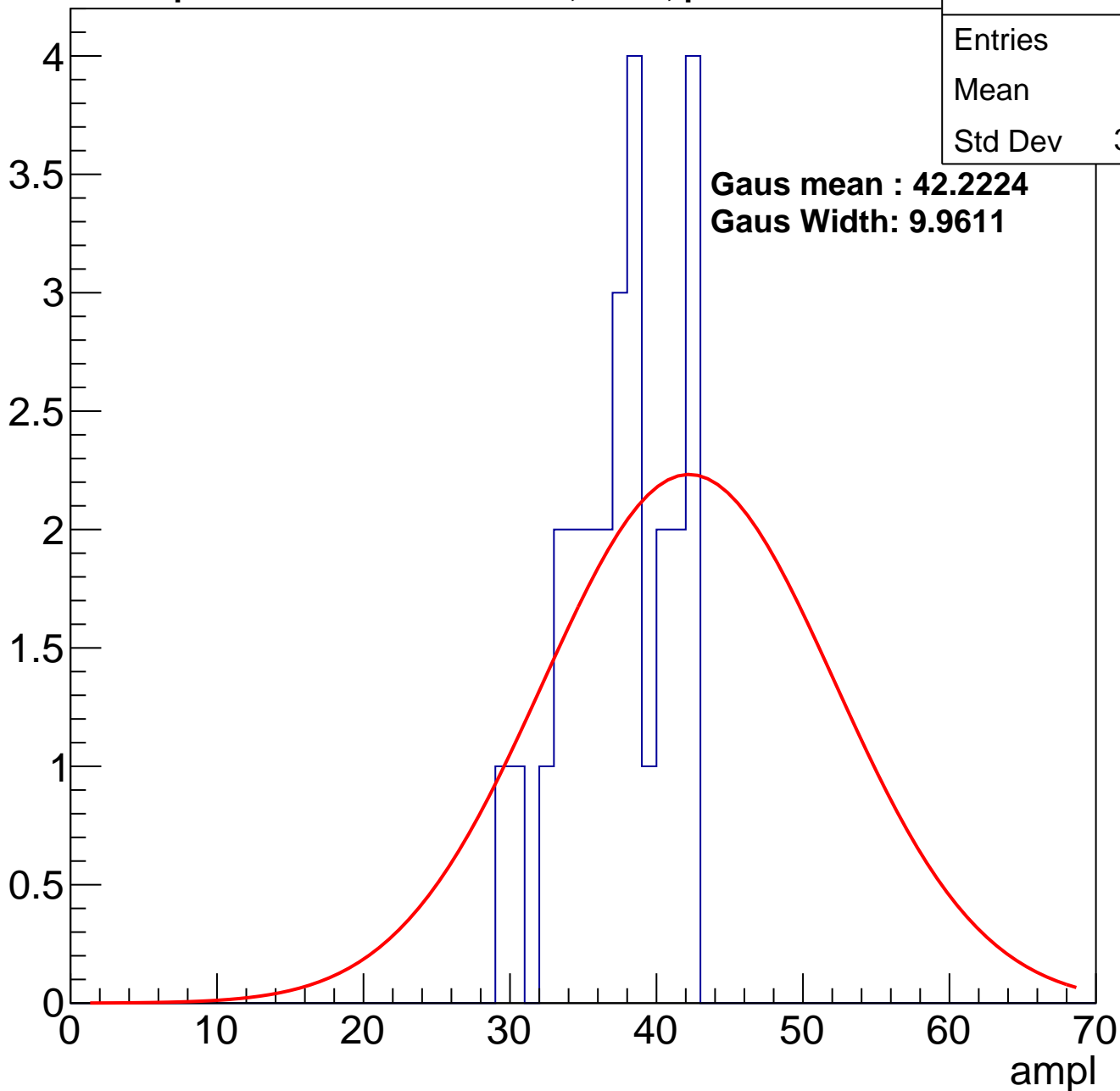
Gaus Width: 5.0862



B1L103S, U15-ch7, adc1

calib_packv5_042523_0143.root, FC#7, port C2

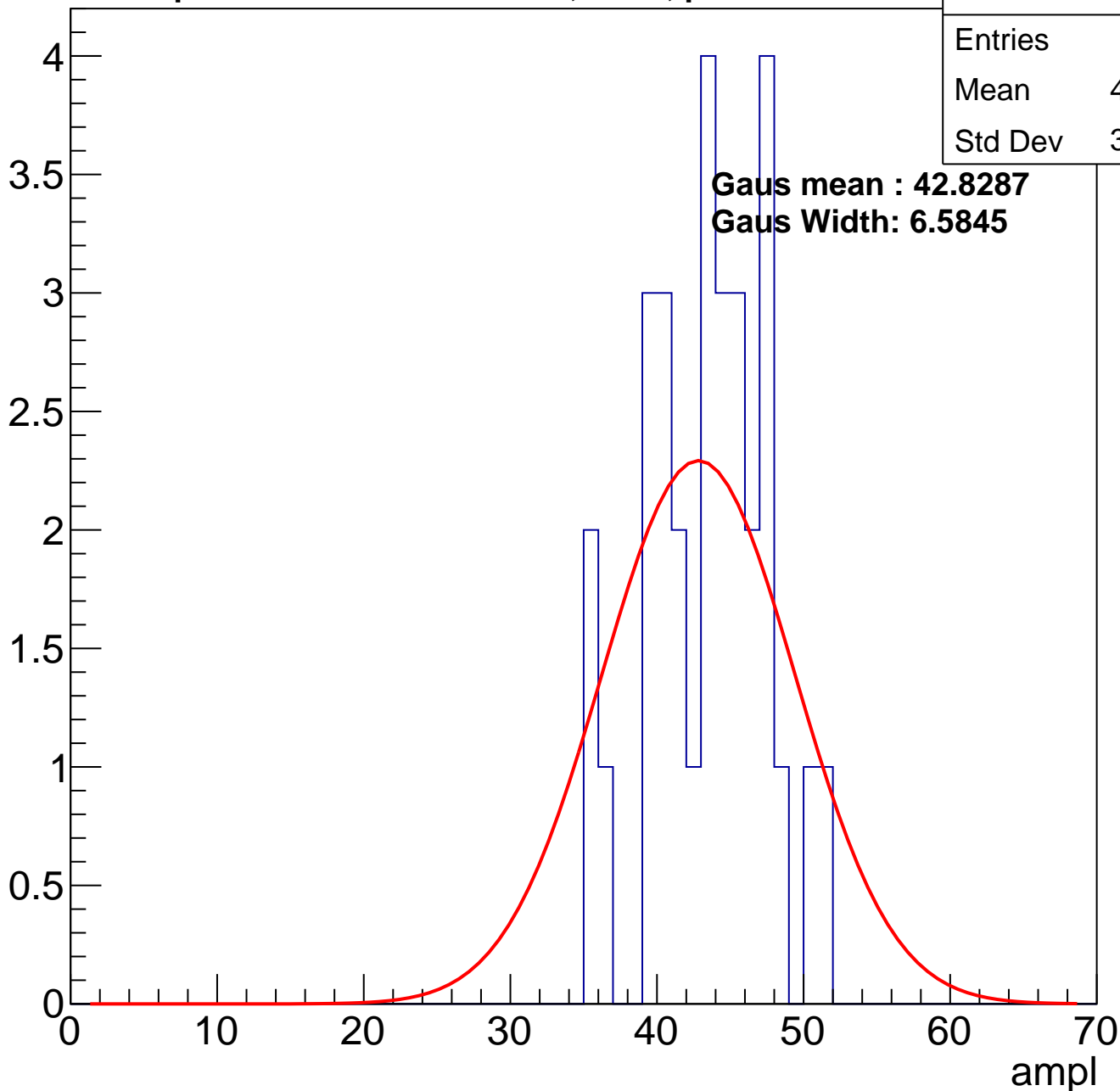
Entry



B1L103S, U15-ch7, adc2

calib_packv5_042523_0143.root, FC#7, port C2

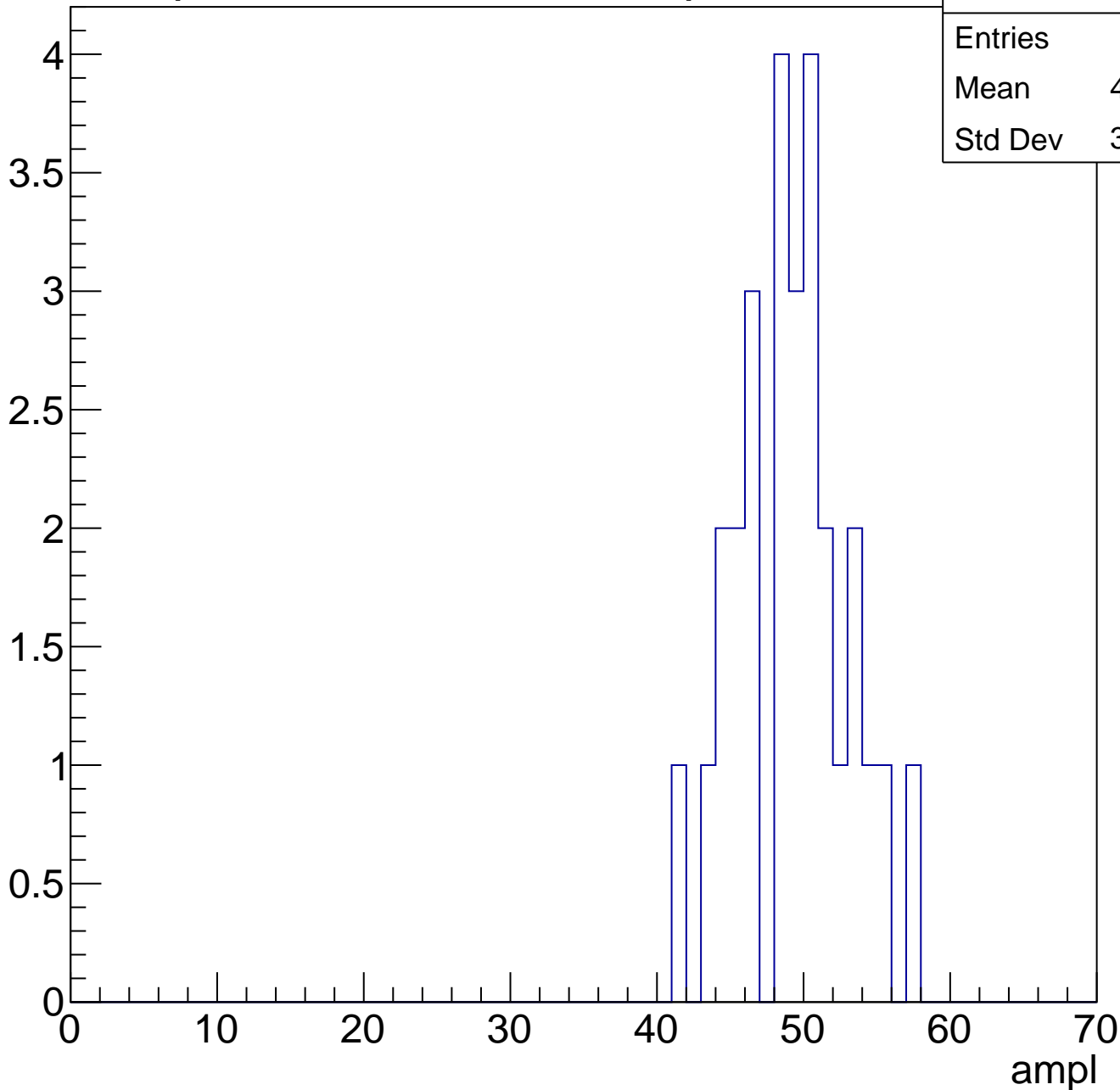
Entry



B1L103S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

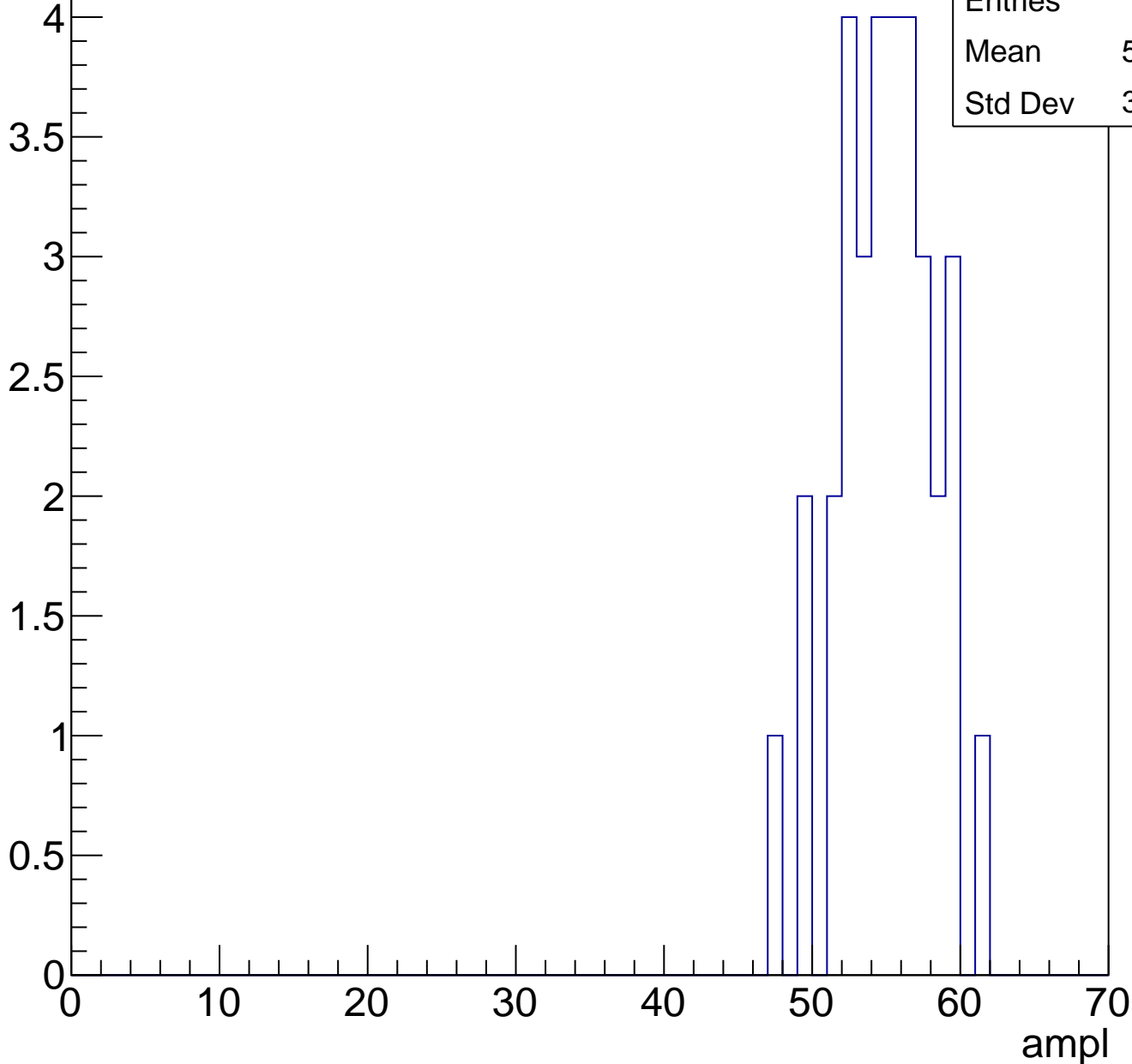


Entries	28
Mean	48.75
Std Dev	3.729

B1L103S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

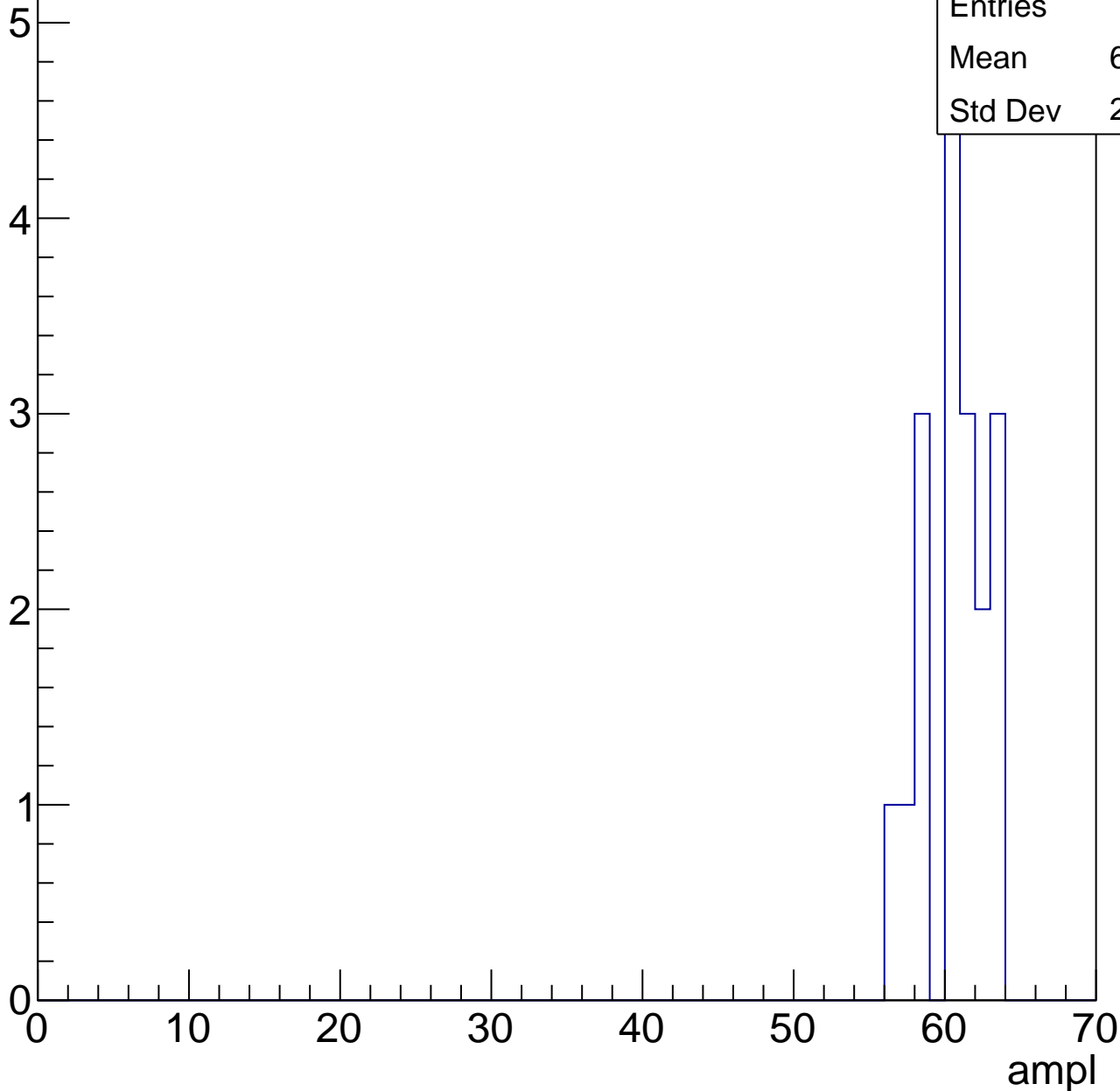


B1L103S, U15-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

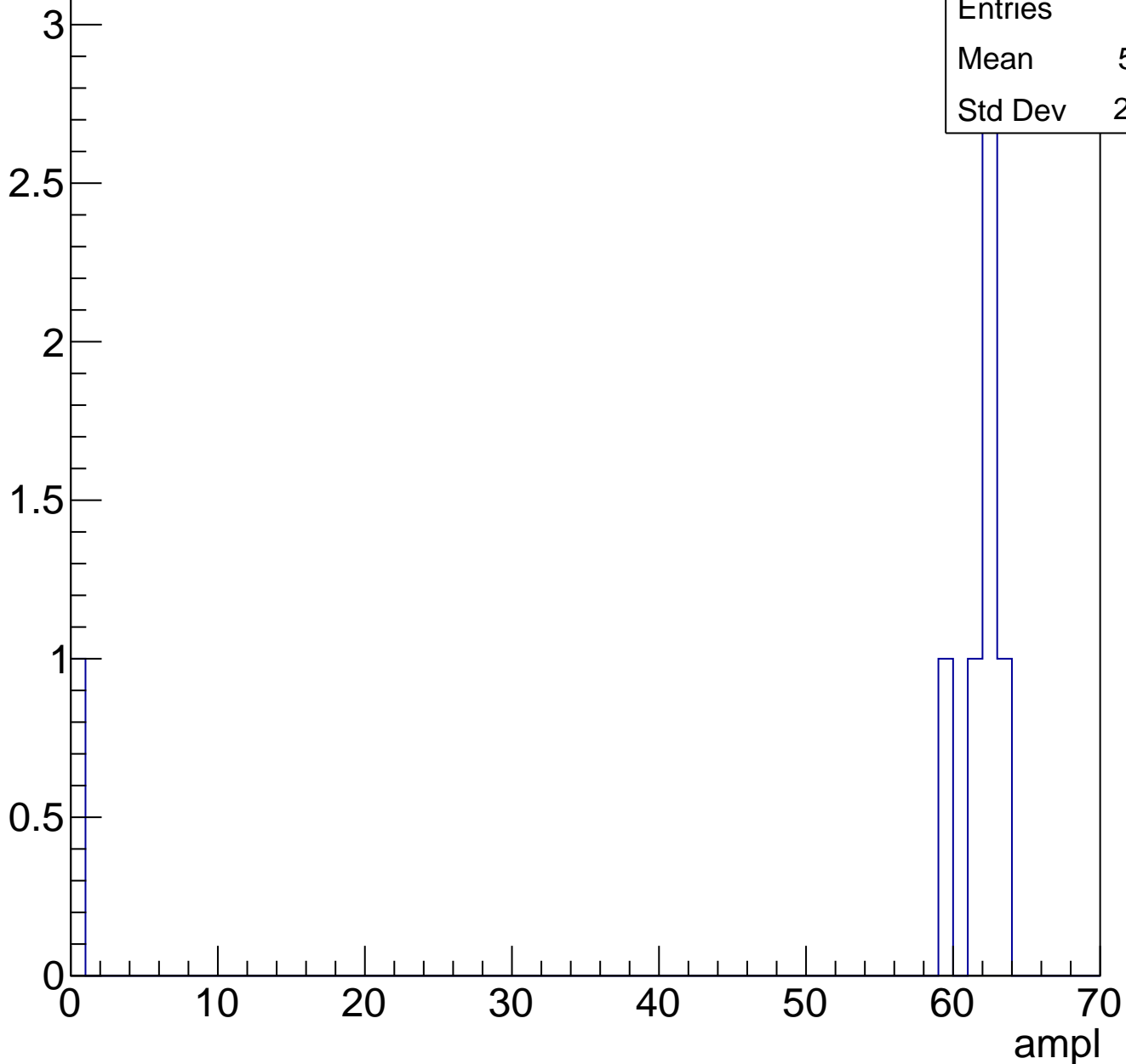
Entries	18
Mean	60.17
Std Dev	2.034



B1L103S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

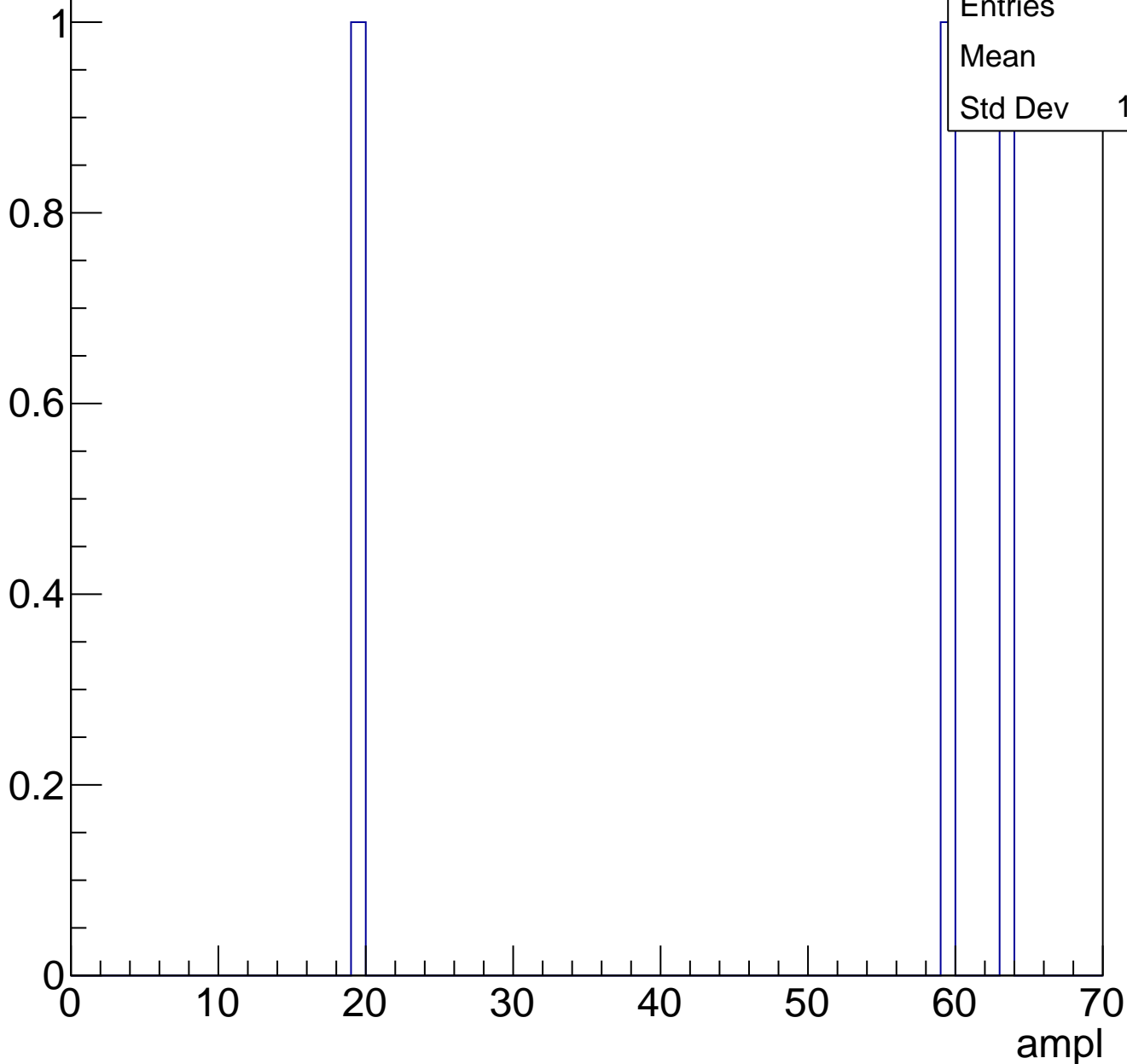
Entry



B1L103S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch8, adc0

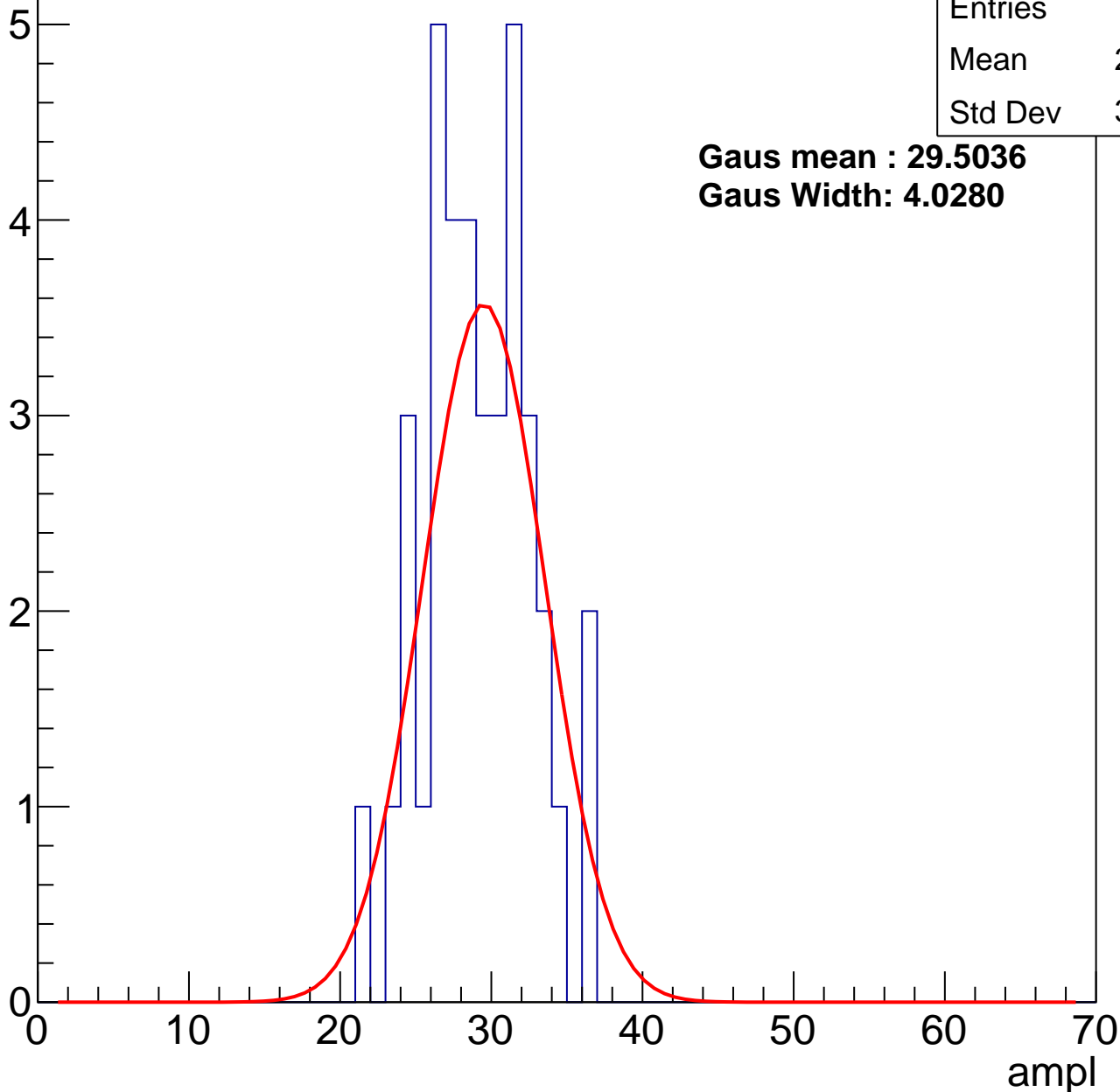
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	28.71
Std Dev	3.471

Gaus mean : 29.5036

Gaus Width: 4.0280



B1L103S, U15-ch8, adc1

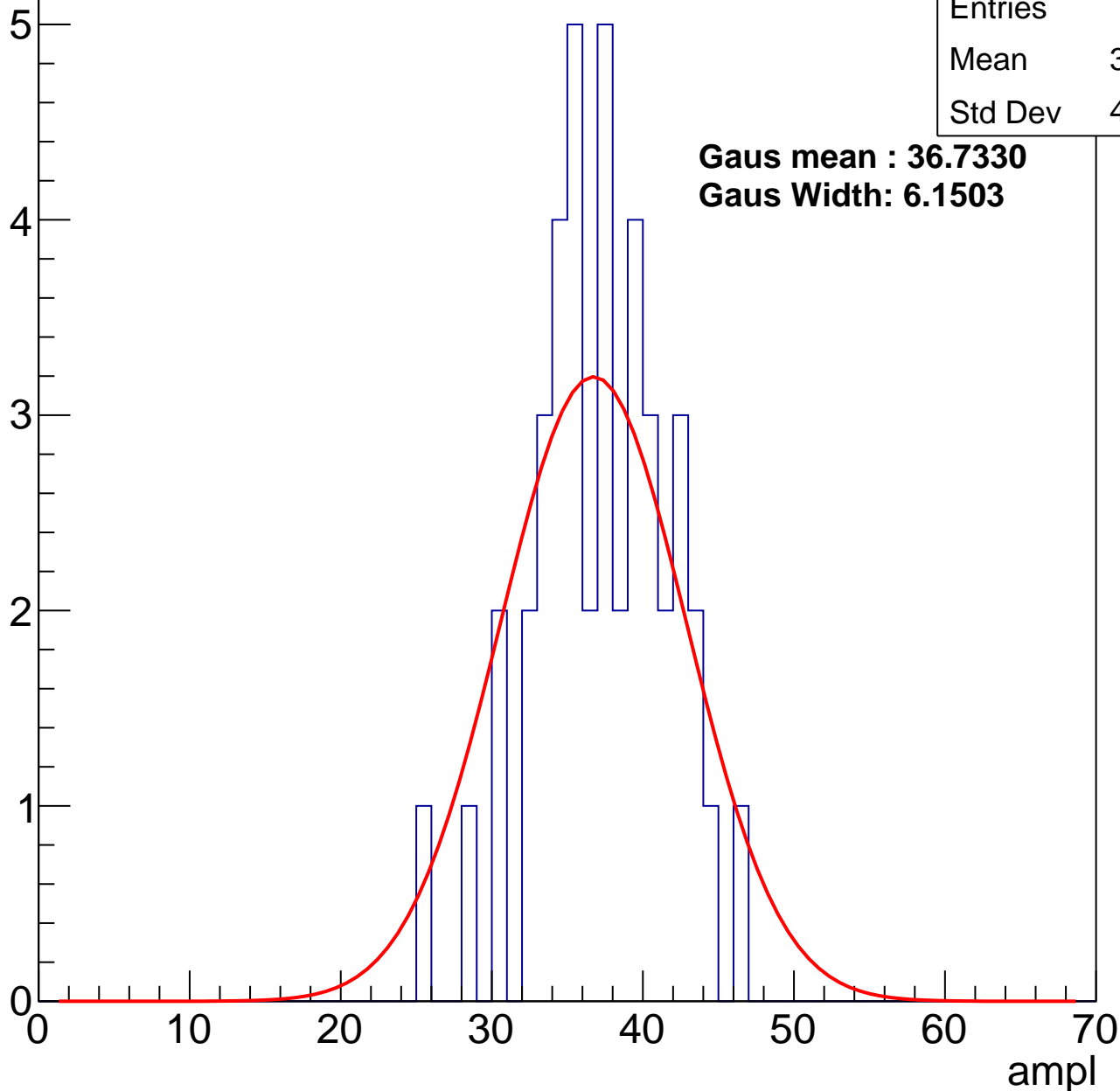
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	36.74
Std Dev	4.394

Gaus mean : 36.7330

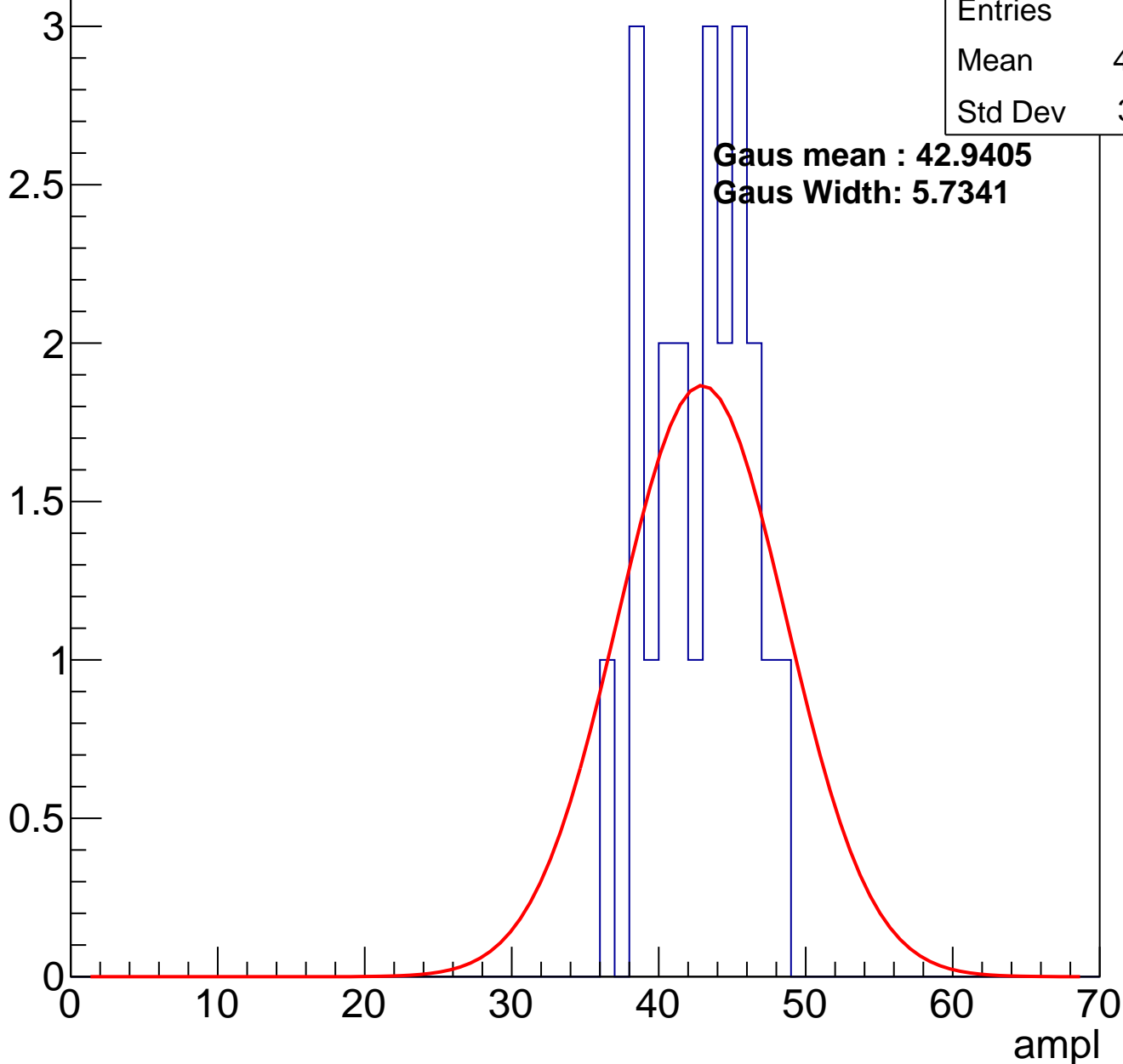
Gaus Width: 6.1503



B1L103S, U15-ch8, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

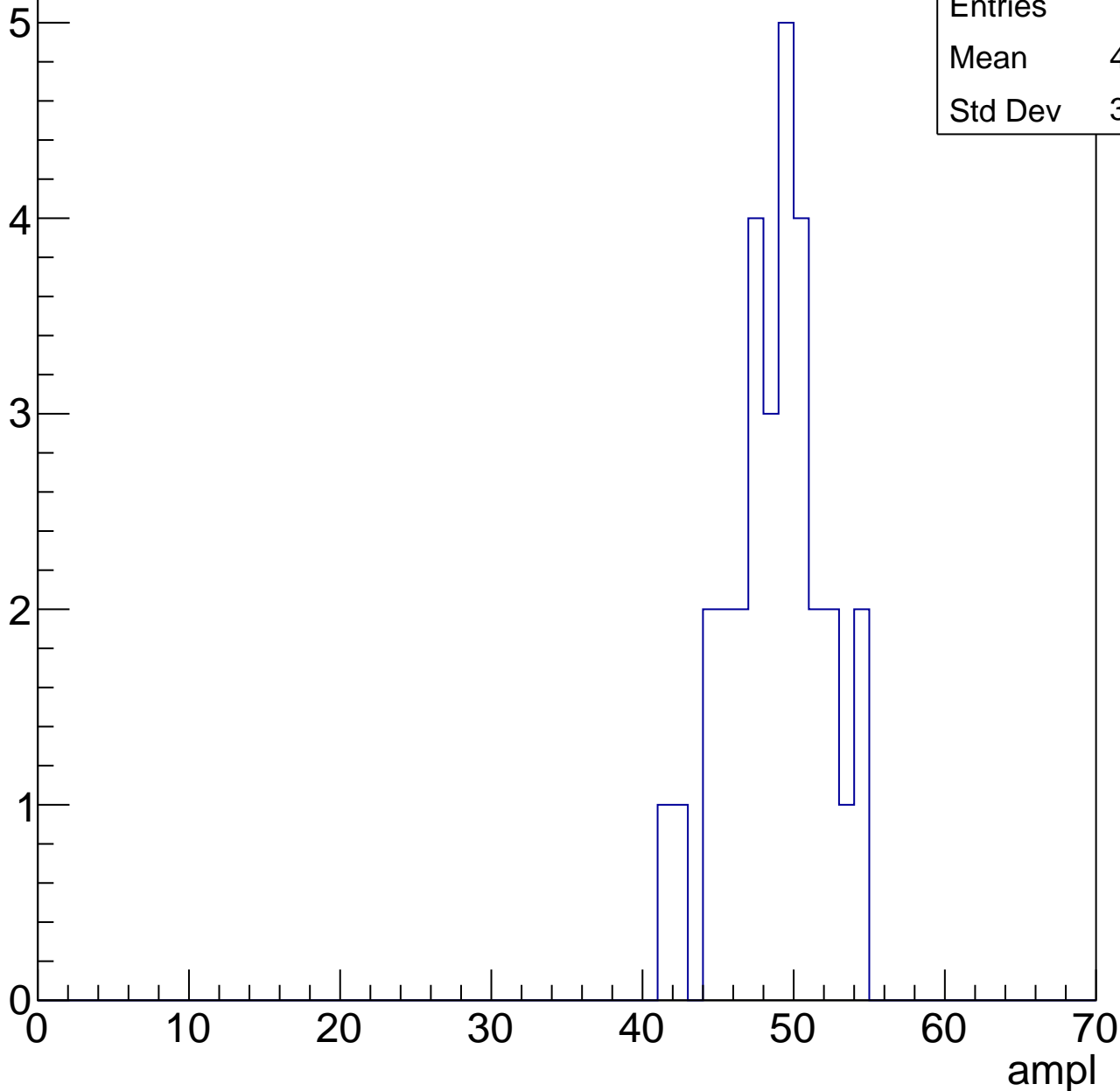


B1L103S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

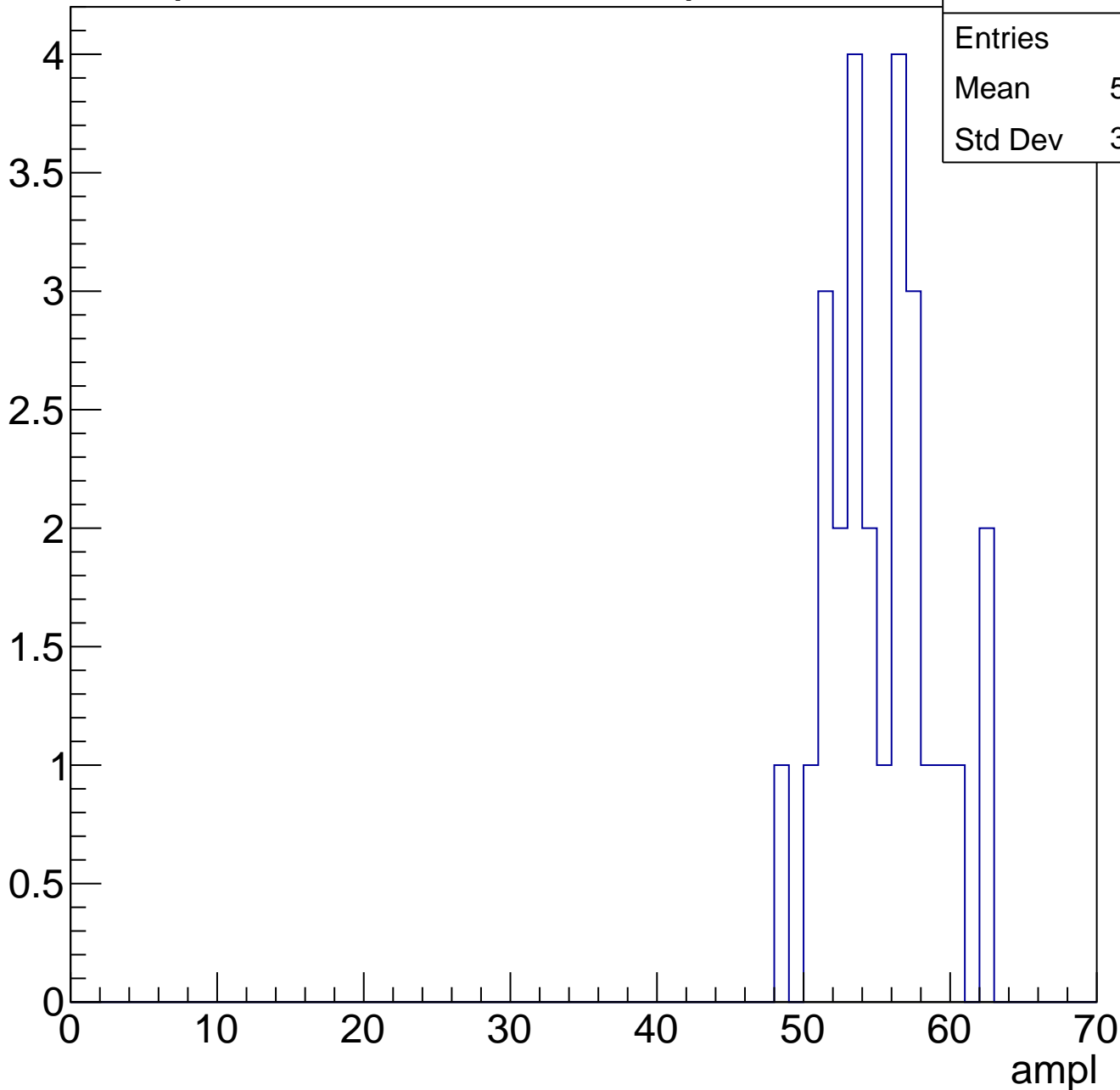
Entries	31
Mean	48.29
Std Dev	3.174



B1L103S, U15-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

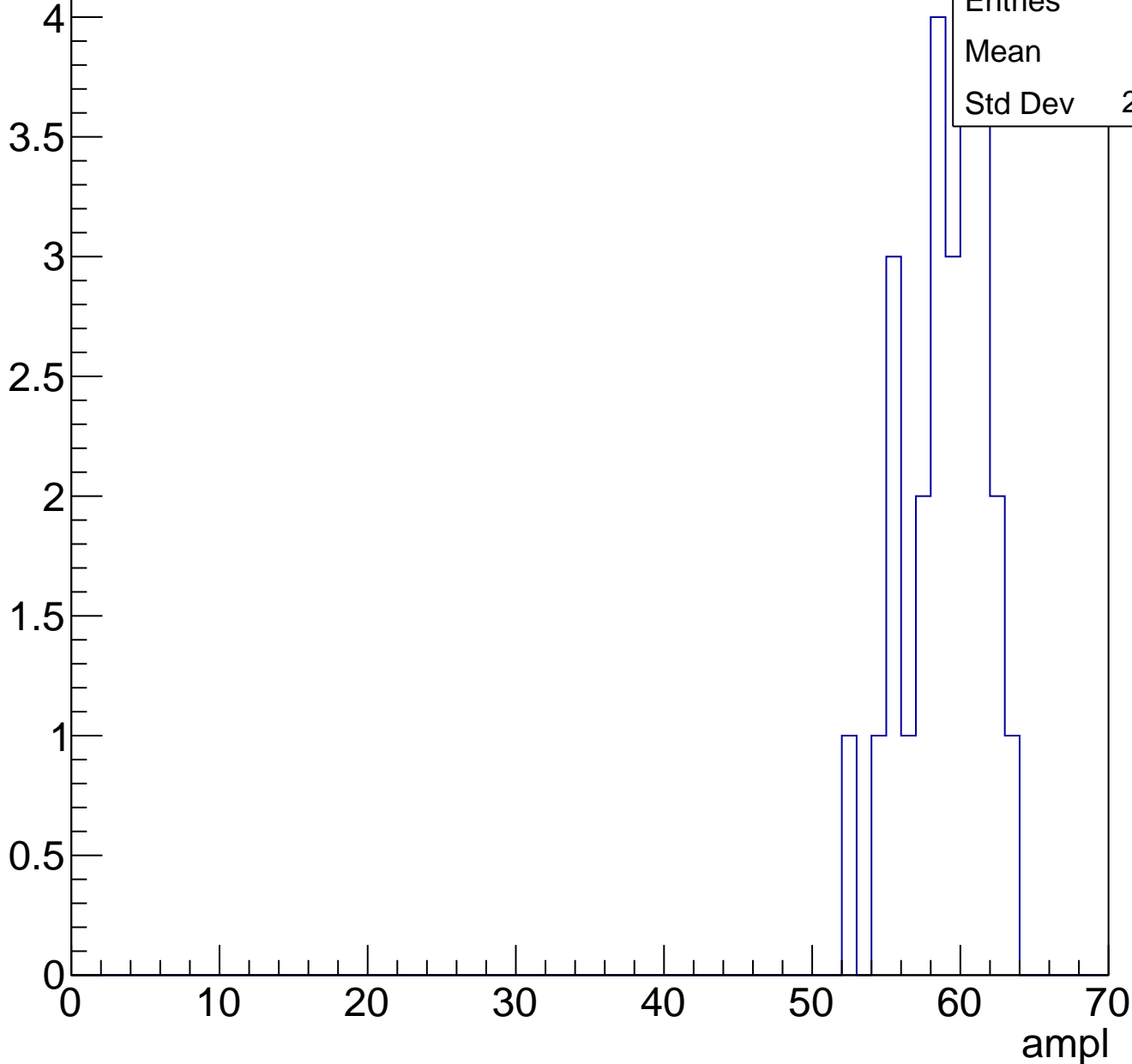
Entry



B1L103S, U15-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

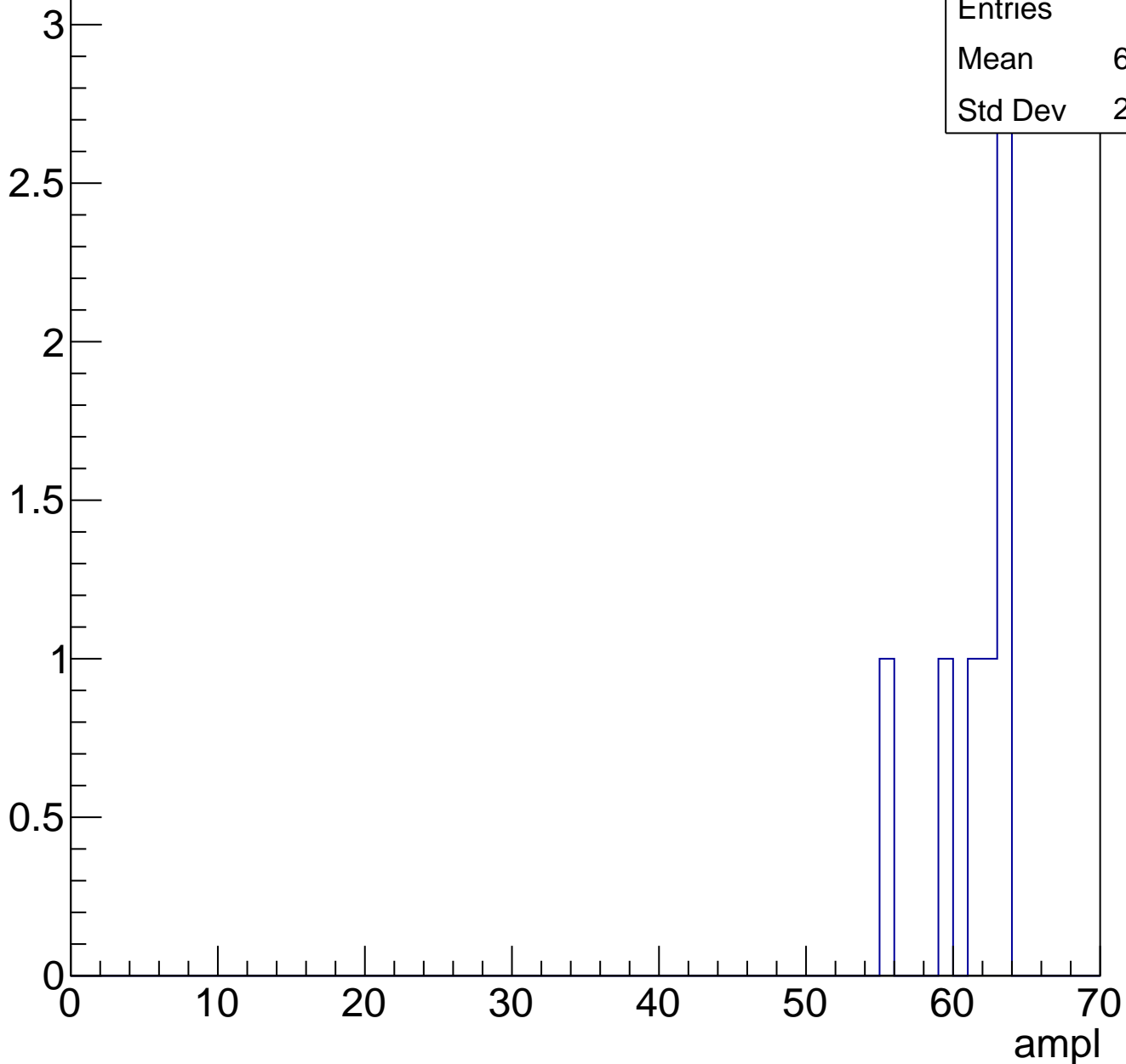
Entry



B1L103S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

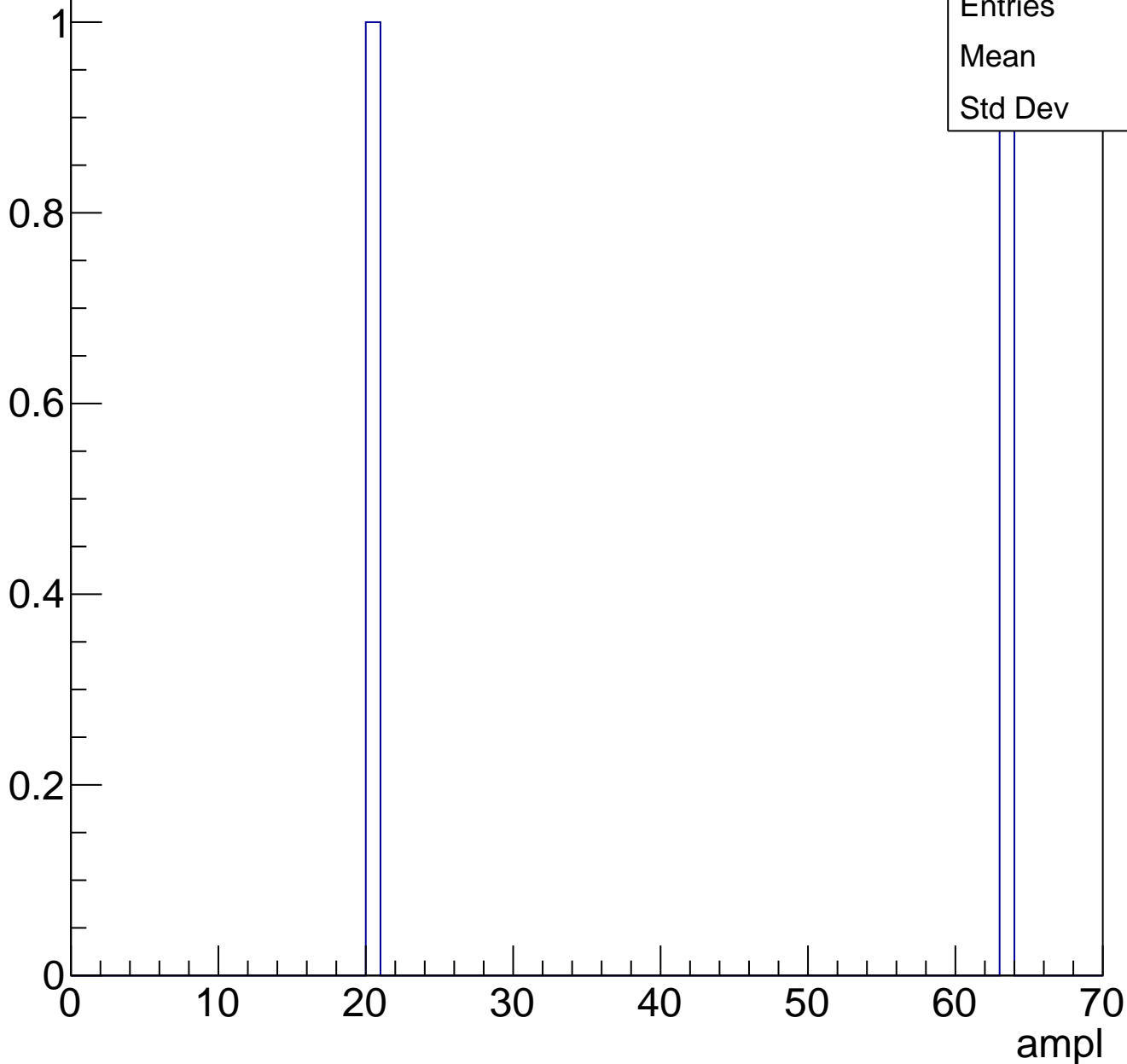
Entry



B1L103S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

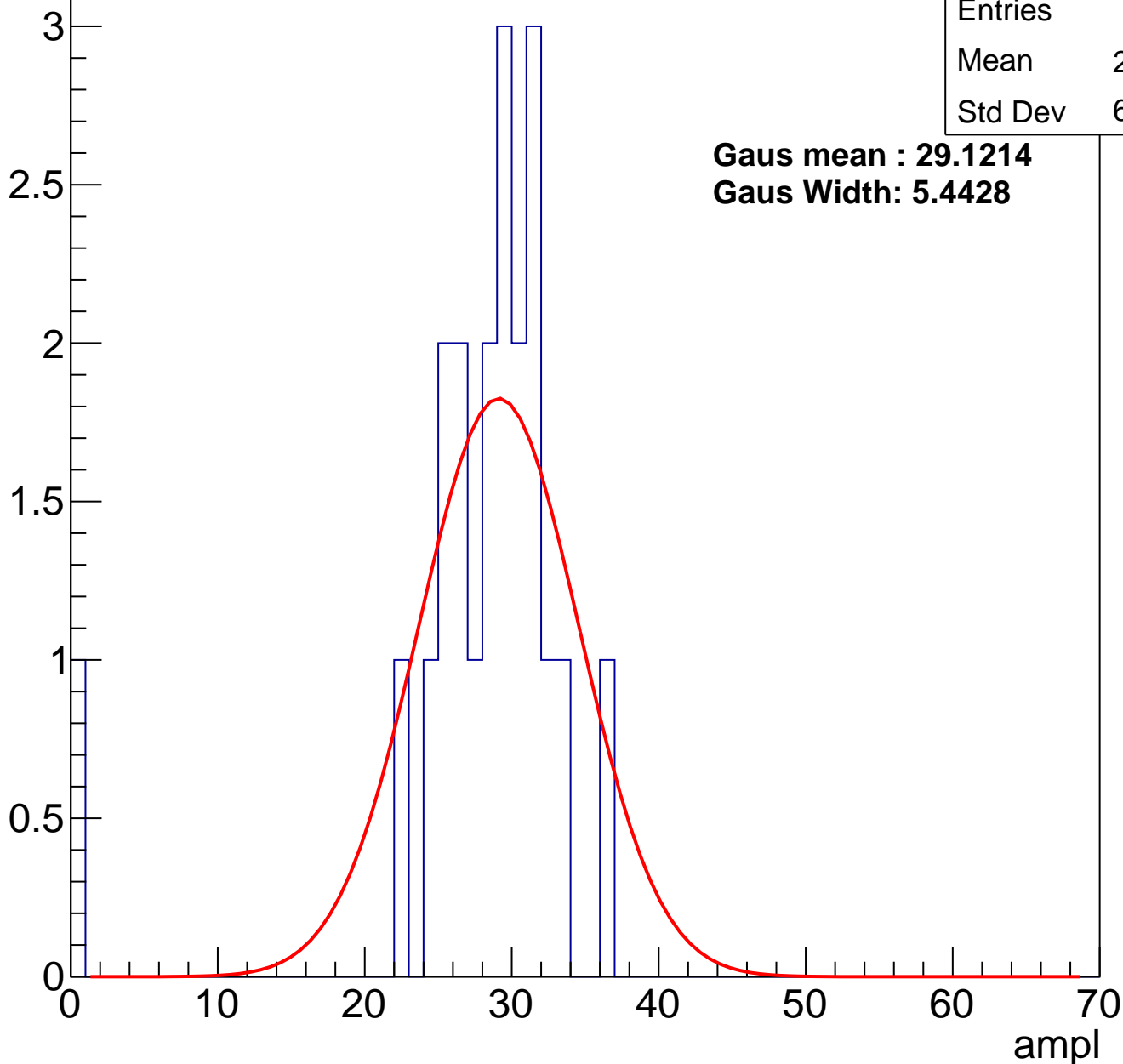
Entry



B1L103S, U15-ch9, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch9, adc1

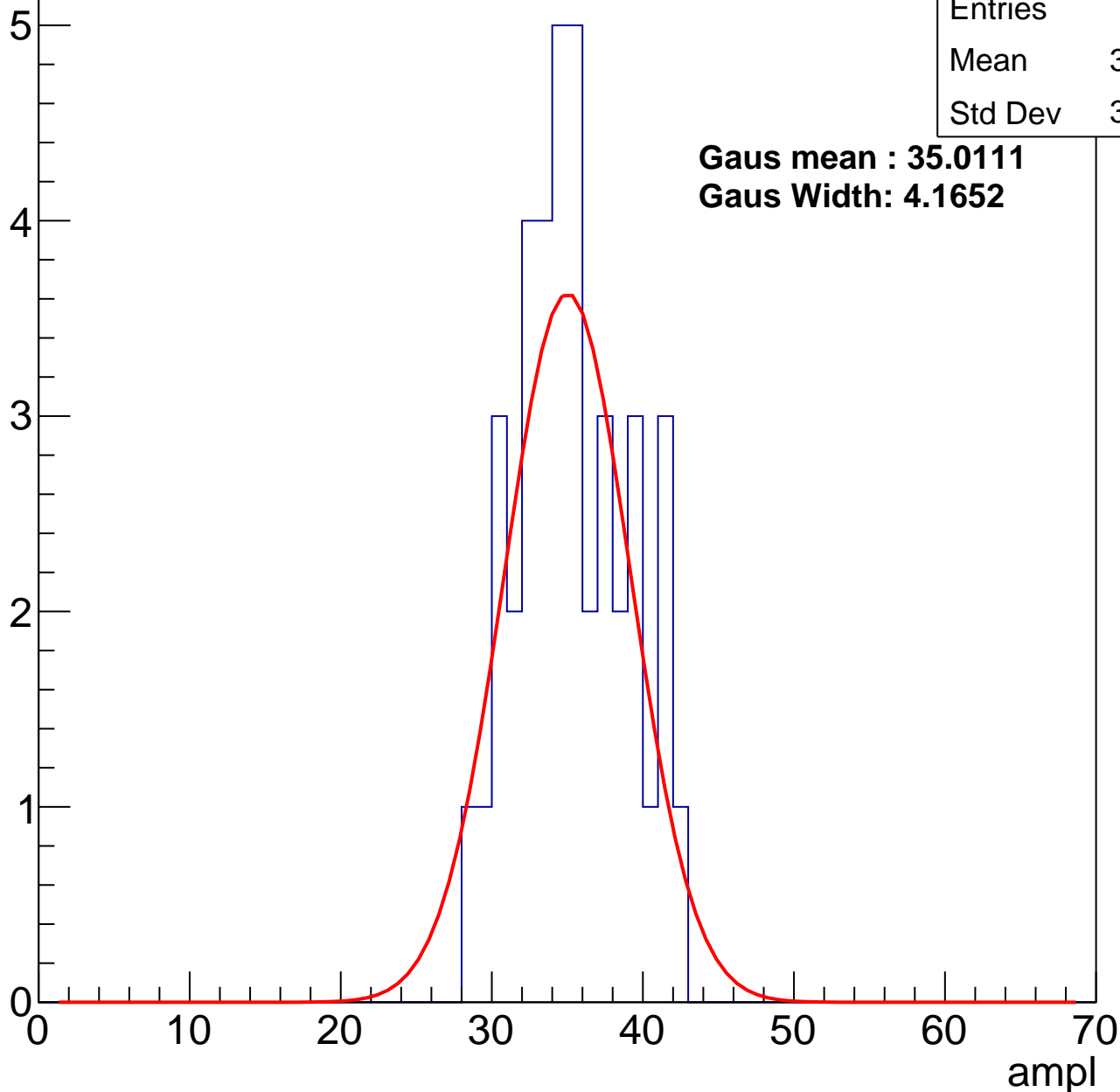
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	34.88
Std Dev	3.572

Gaus mean : 35.0111

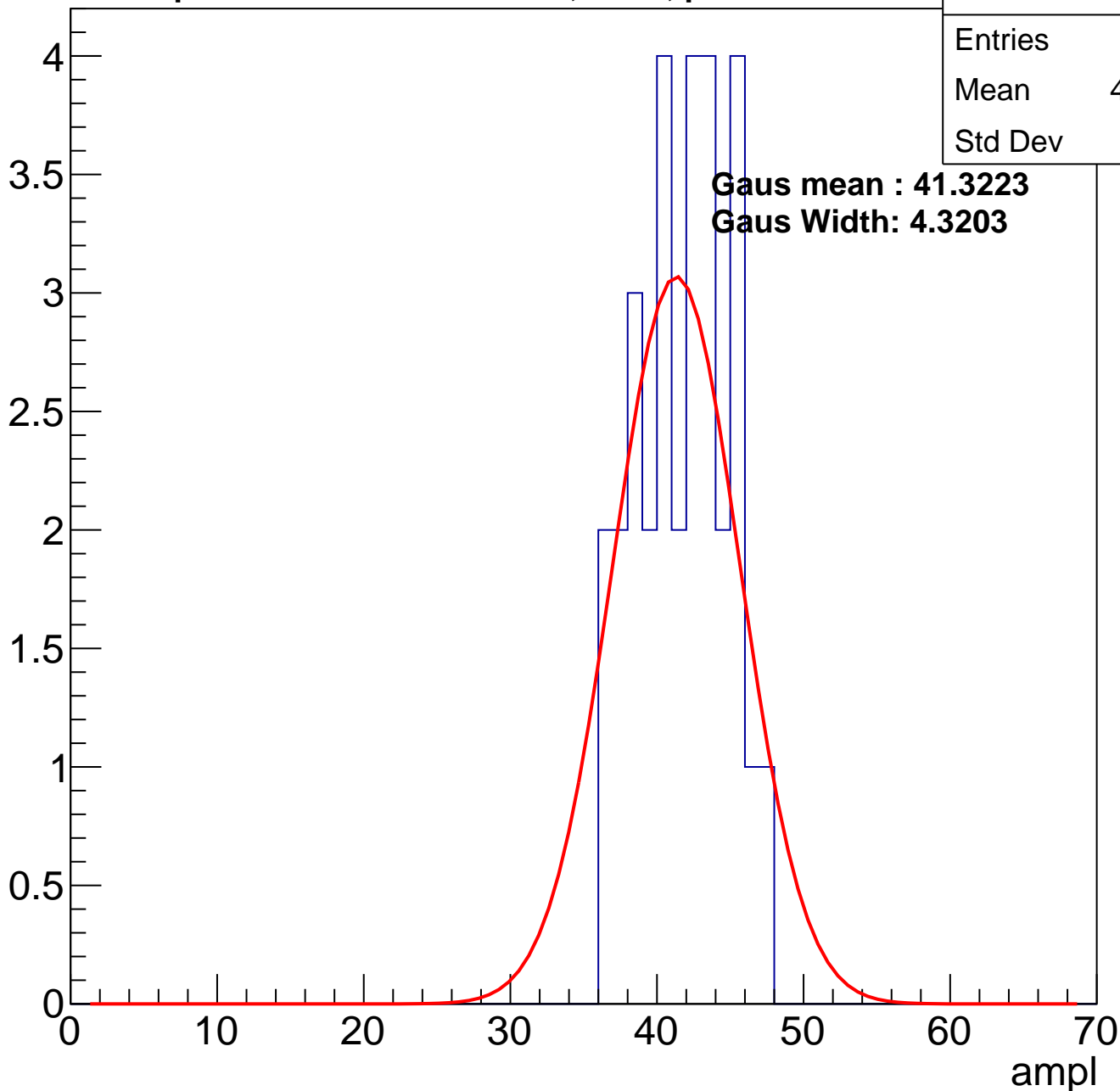
Gaus Width: 4.1652



B1L103S, U15-ch9, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

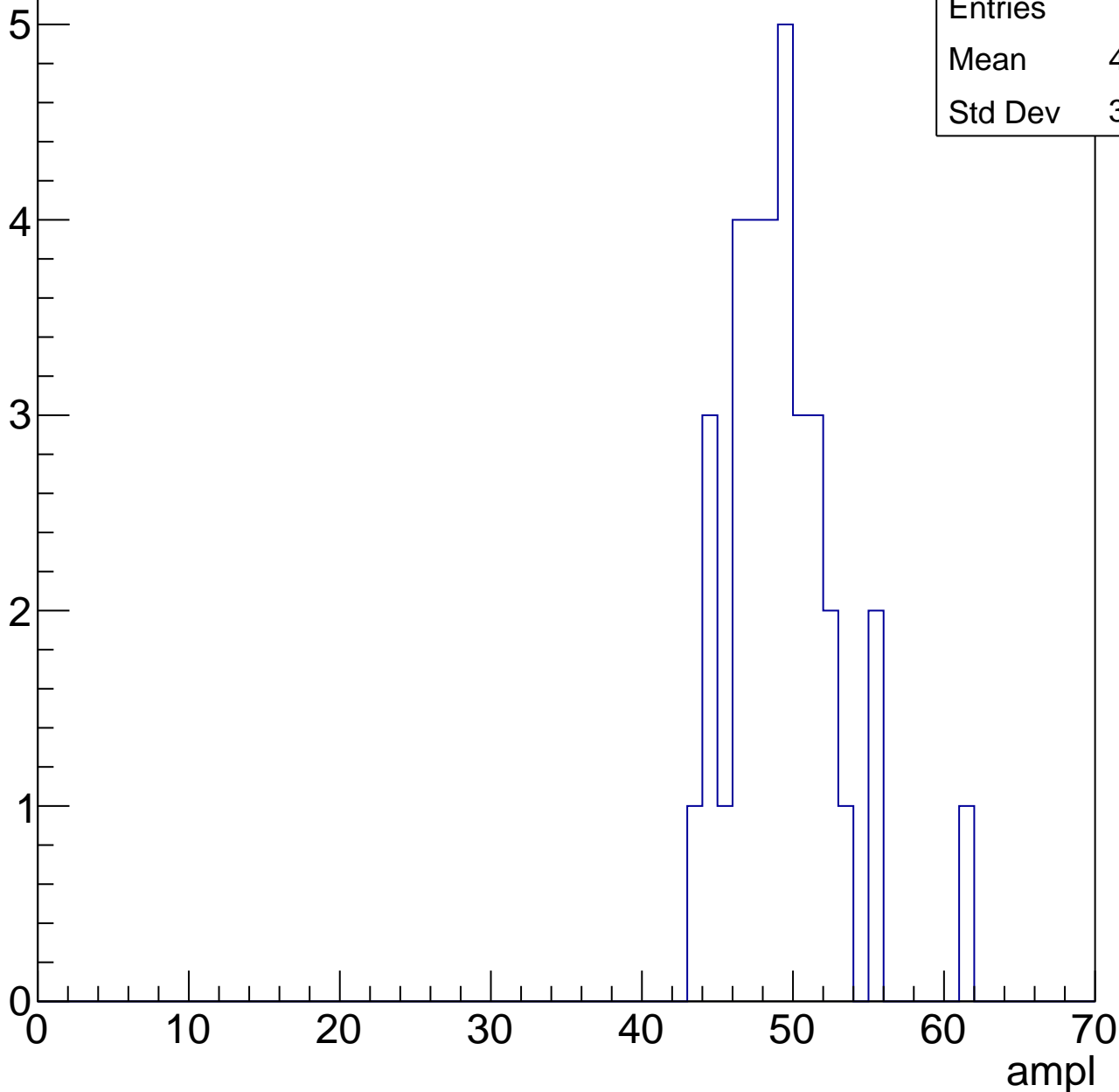


B1L103S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	48.82
Std Dev	3.626

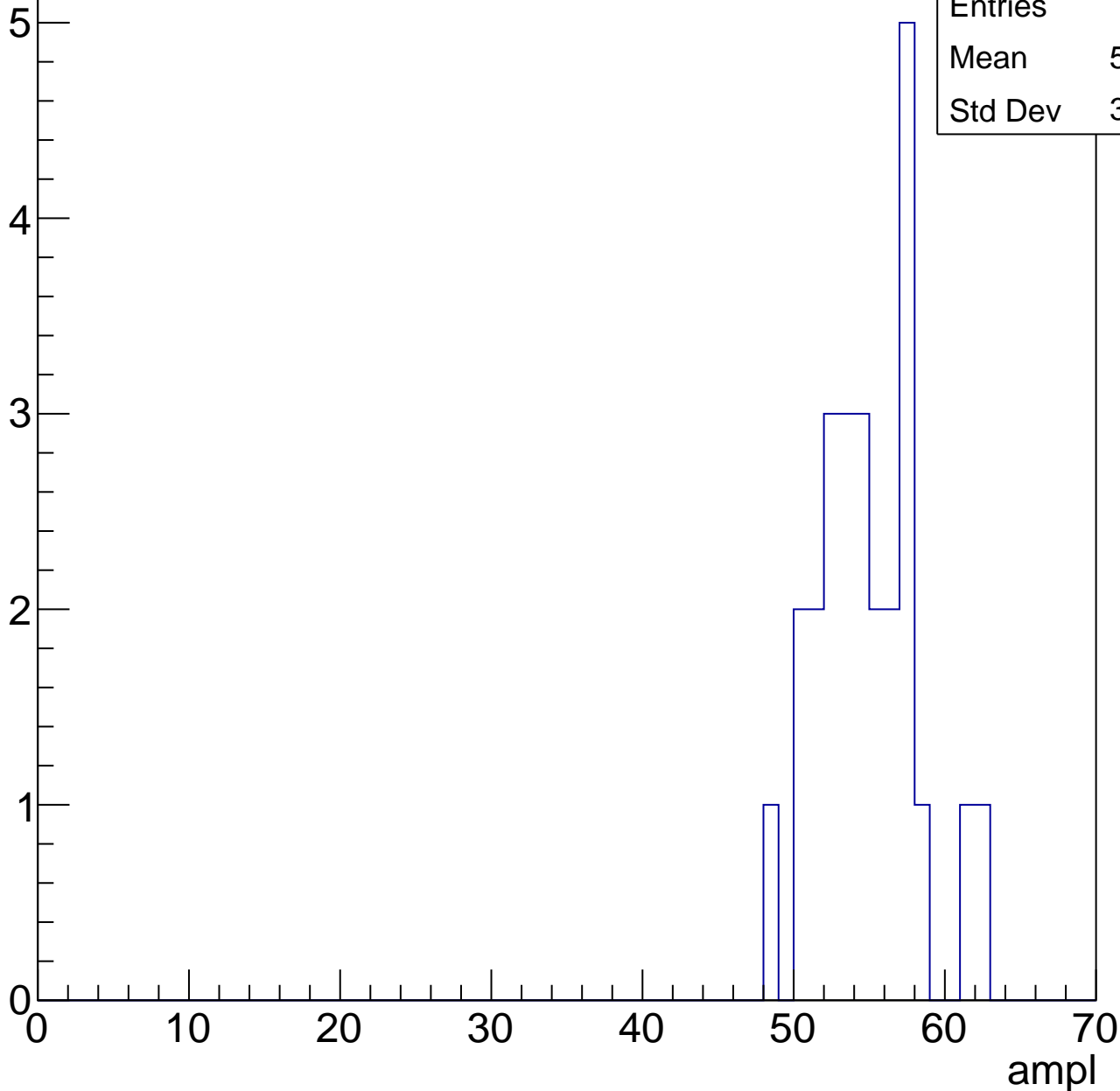


B1L103S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	54.42
Std Dev	3.284

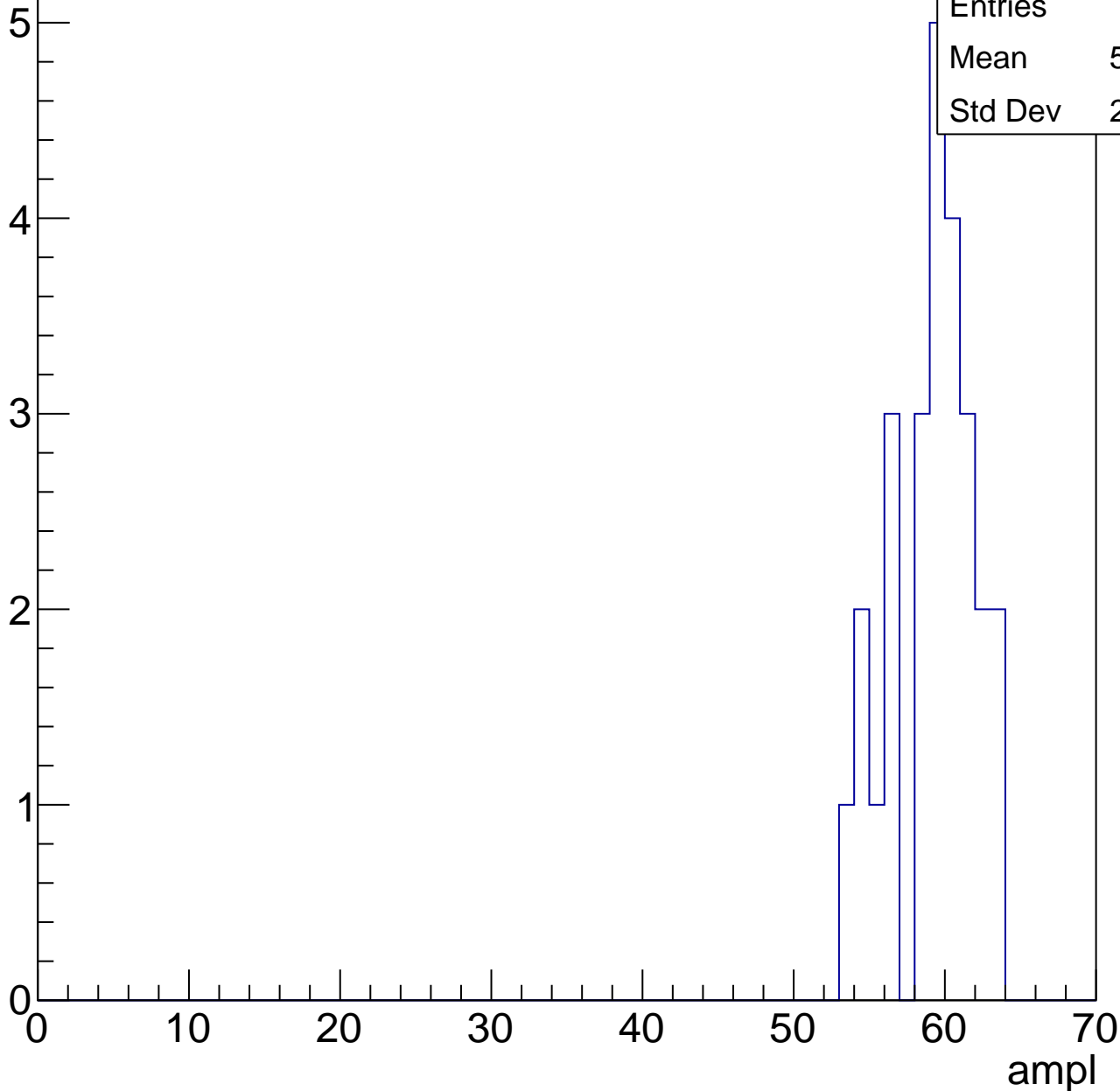


B1L103S, U15-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

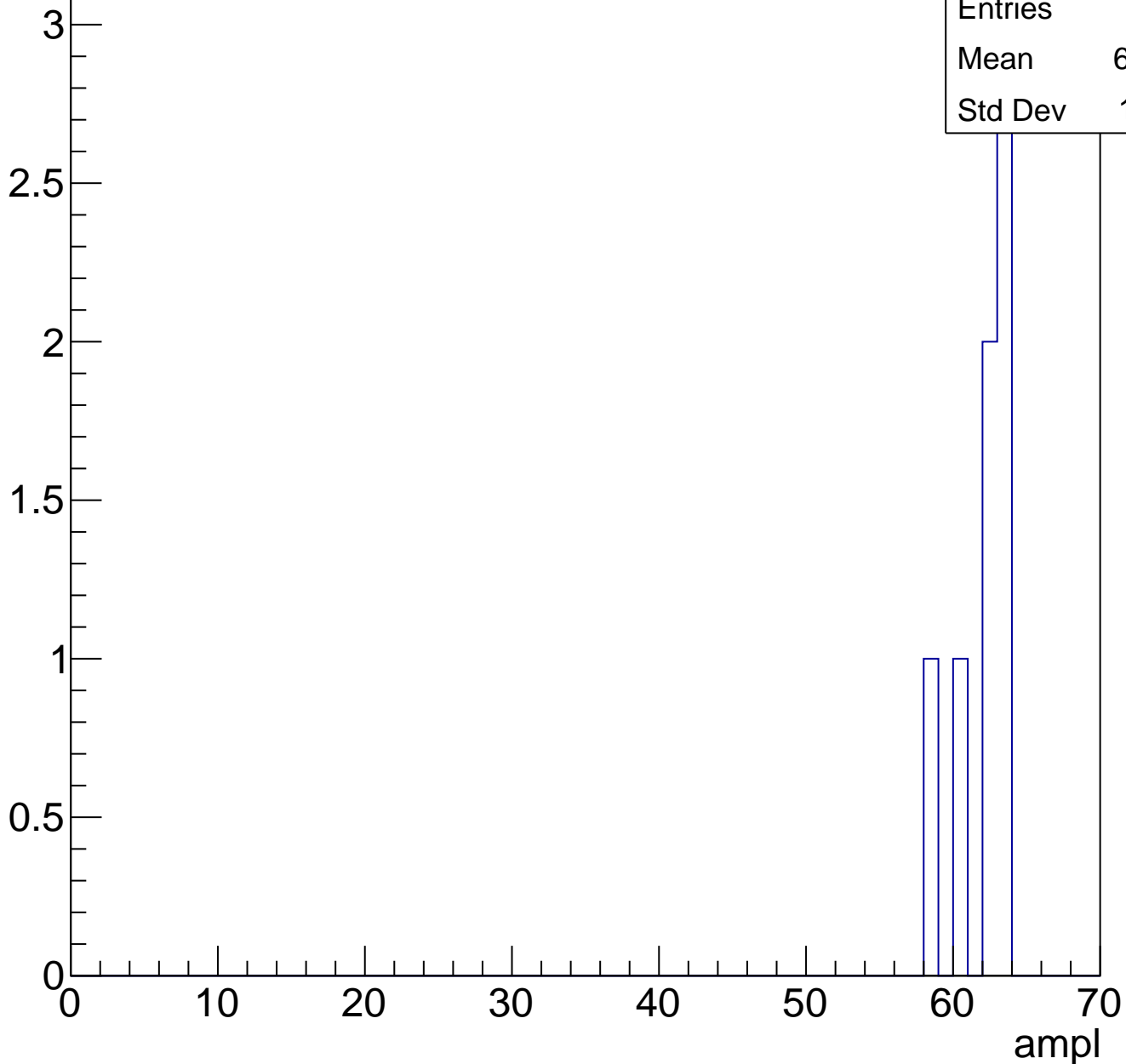
Entries	26
Mean	58.69
Std Dev	2.742



B1L103S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch10, adc0

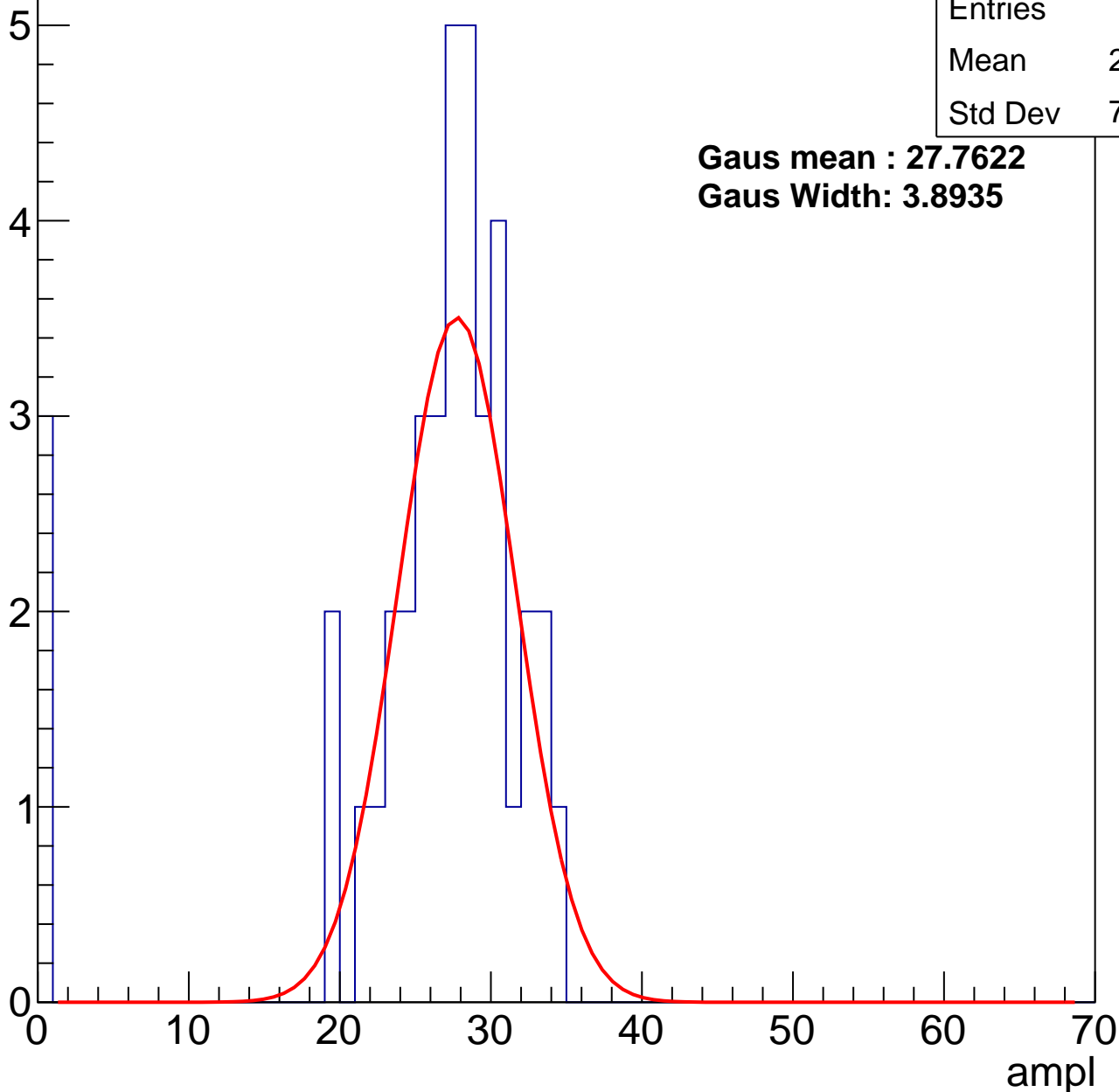
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	25.12
Std Dev	7.969

Gaus mean : 27.7622

Gaus Width: 3.8935



B1L103S, U15-ch10, adc1

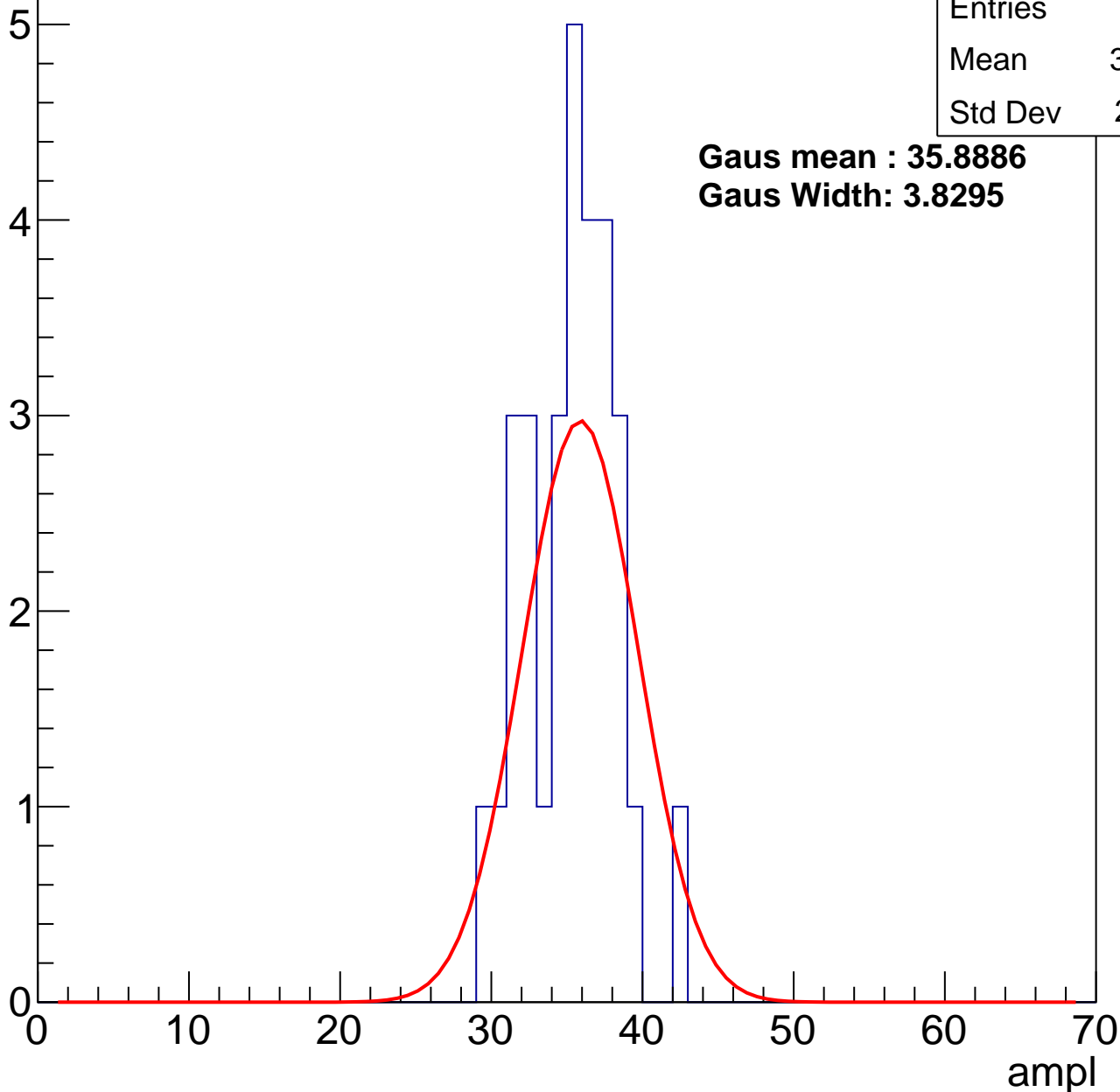
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	34.83
Std Dev	2.911

Gaus mean : 35.8886

Gaus Width: 3.8295



B1L103S, U15-ch10, adc2

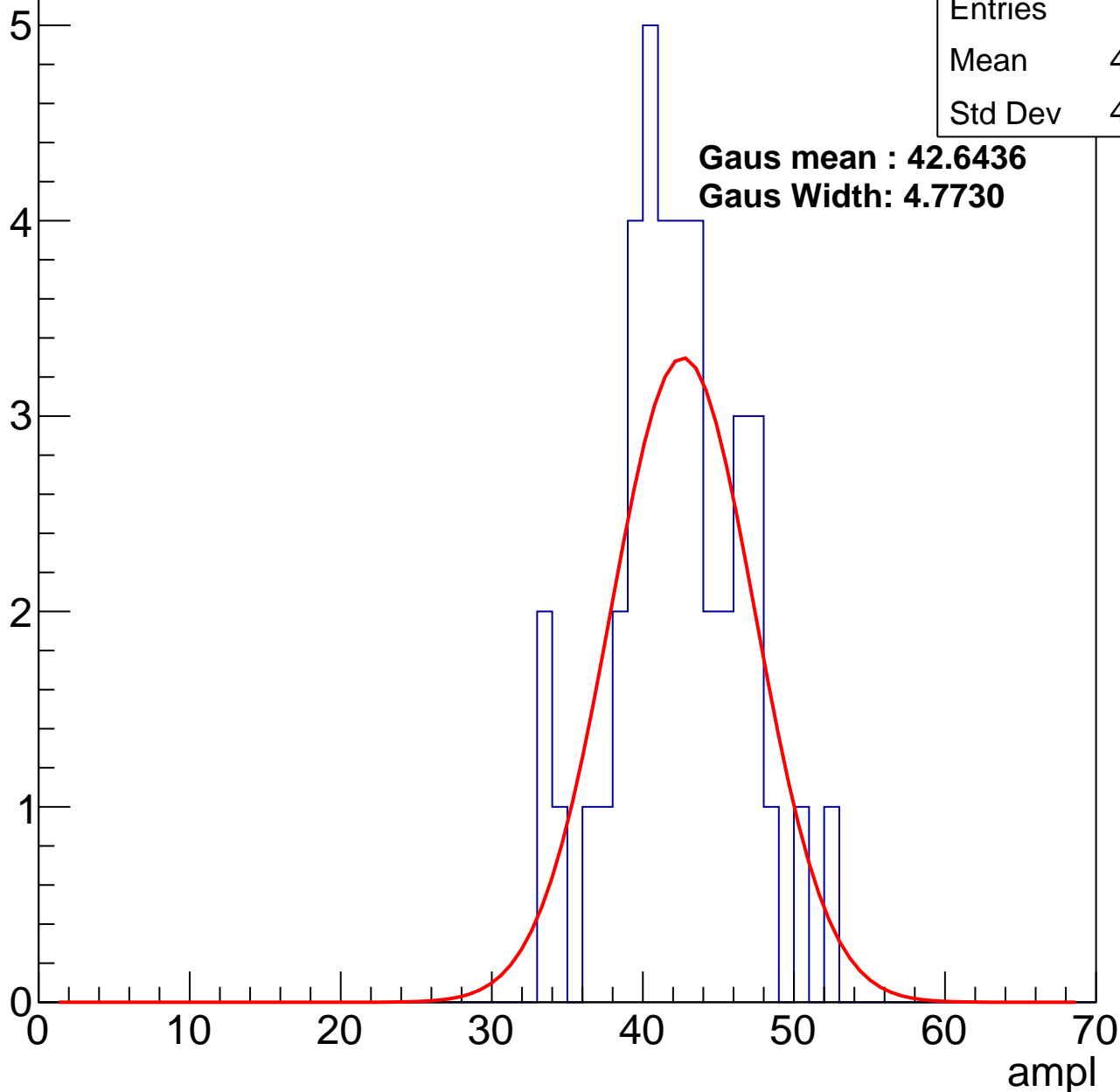
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	41.85
Std Dev	4.223

Gaus mean : 42.6436

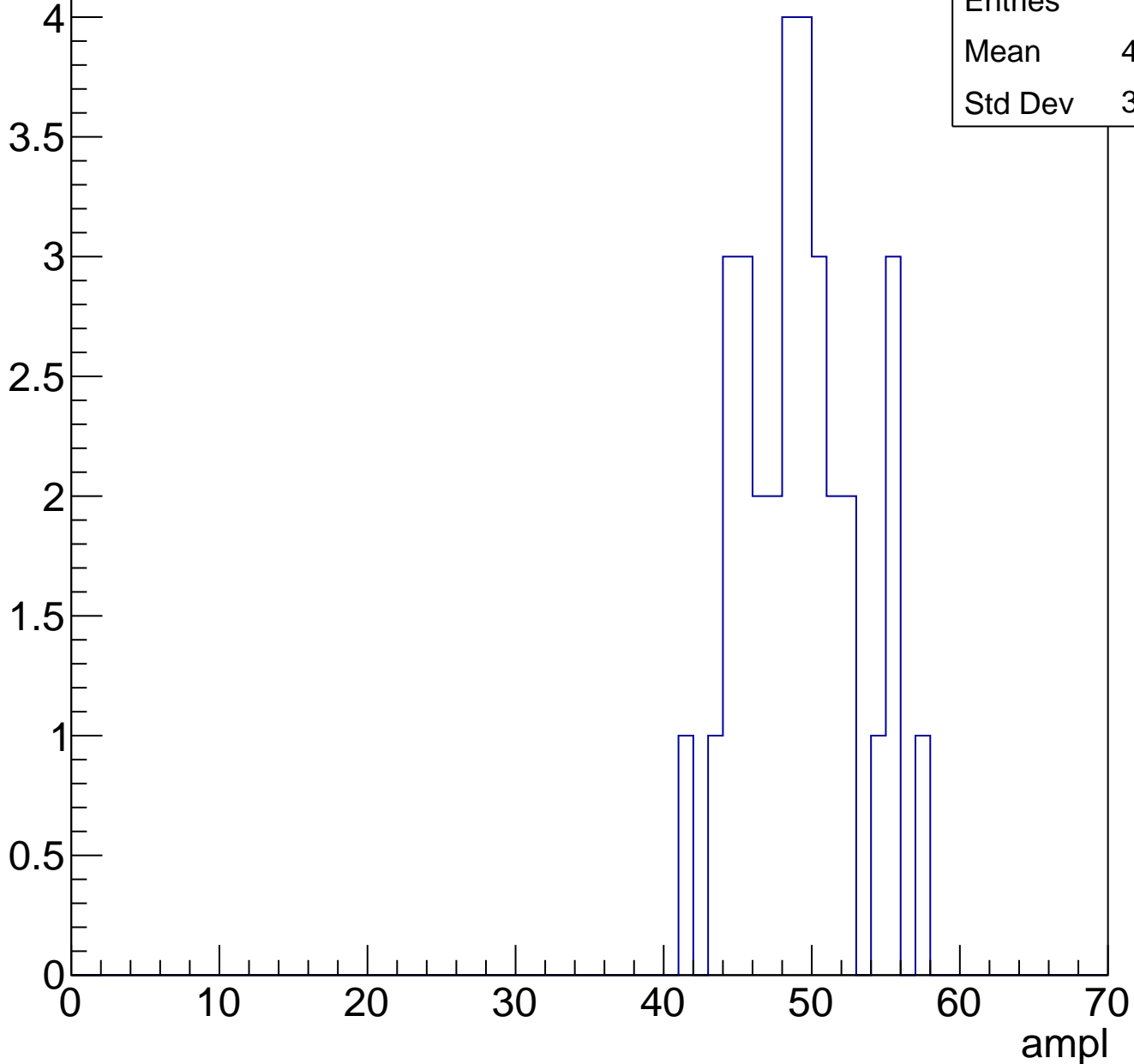
Gaus Width: 4.7730



B1L103S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

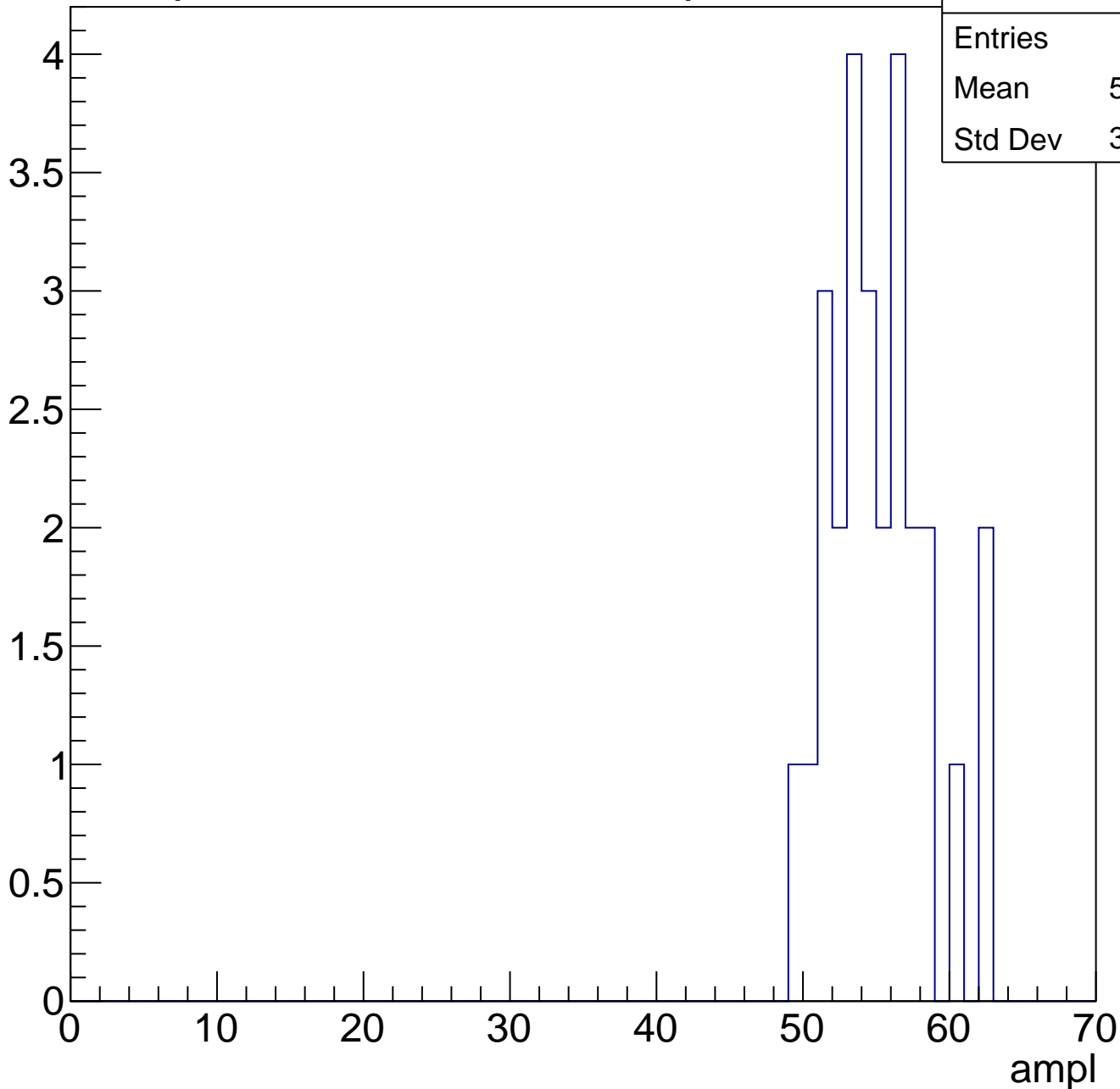


Entries	32
Mean	48.66
Std Dev	3.854

B1L103S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

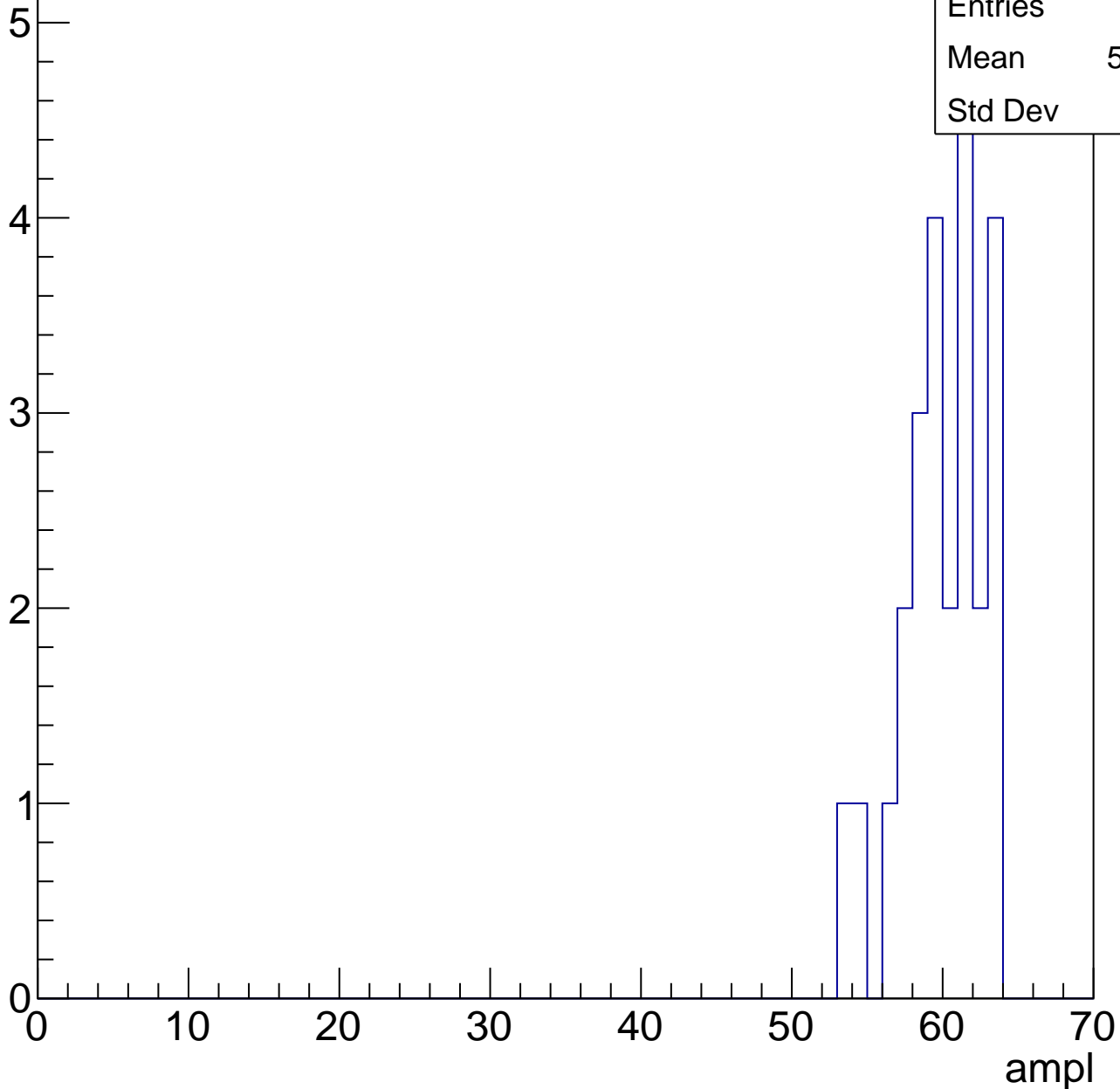


B1L103S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

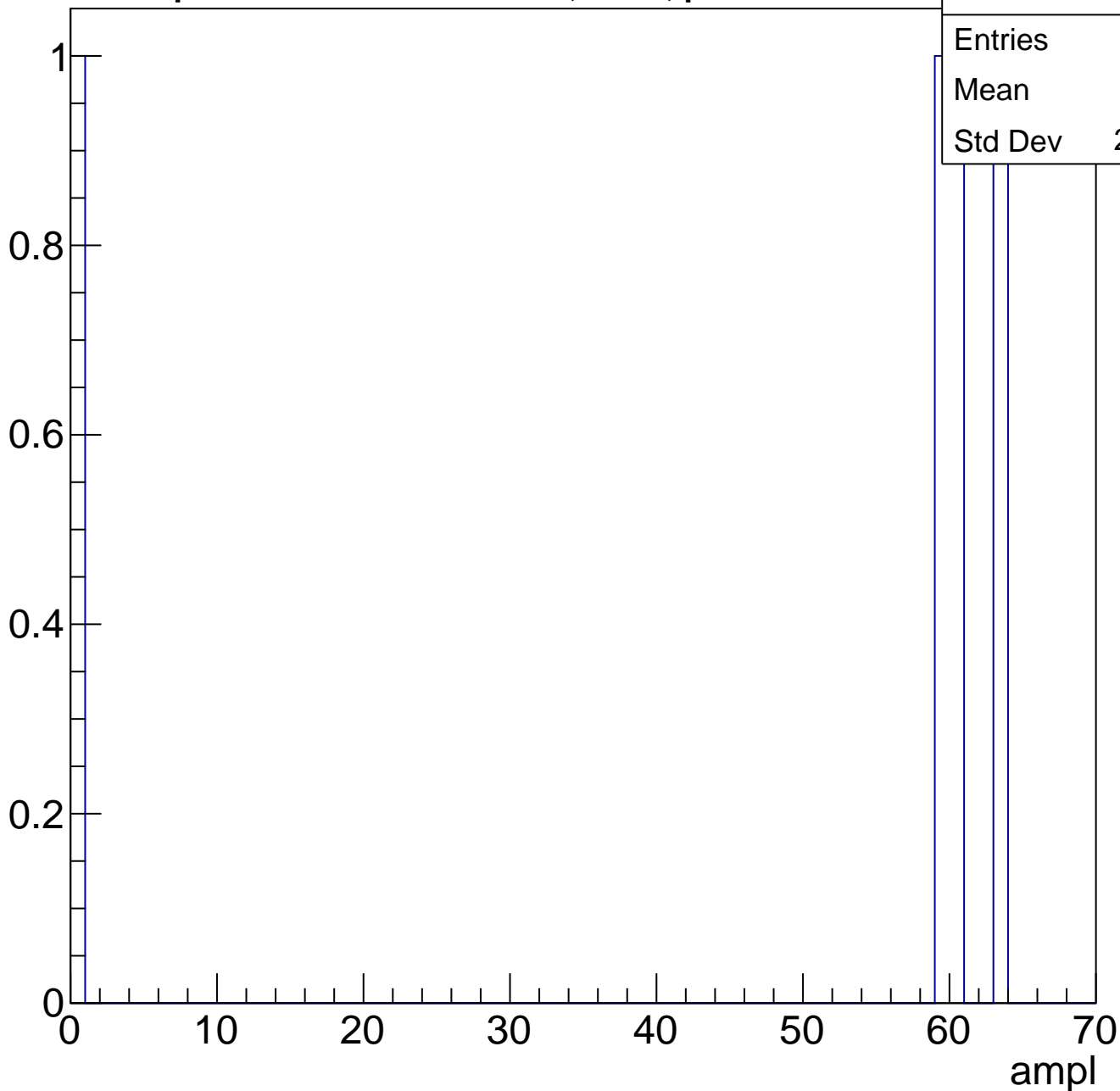
Entries	25
Mean	59.52
Std Dev	2.67



B1L103S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

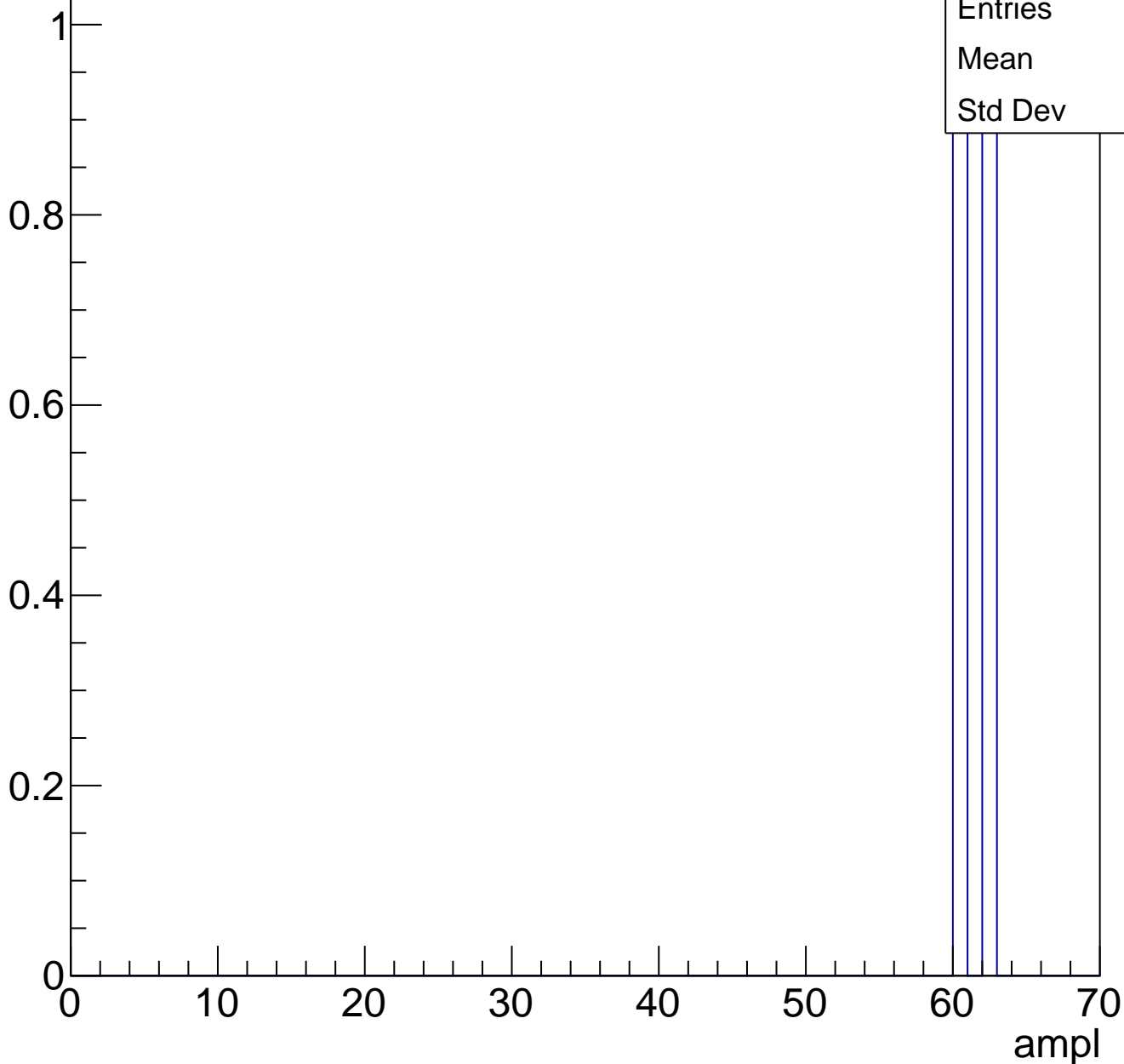
Entry



B1L103S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch11, adc0

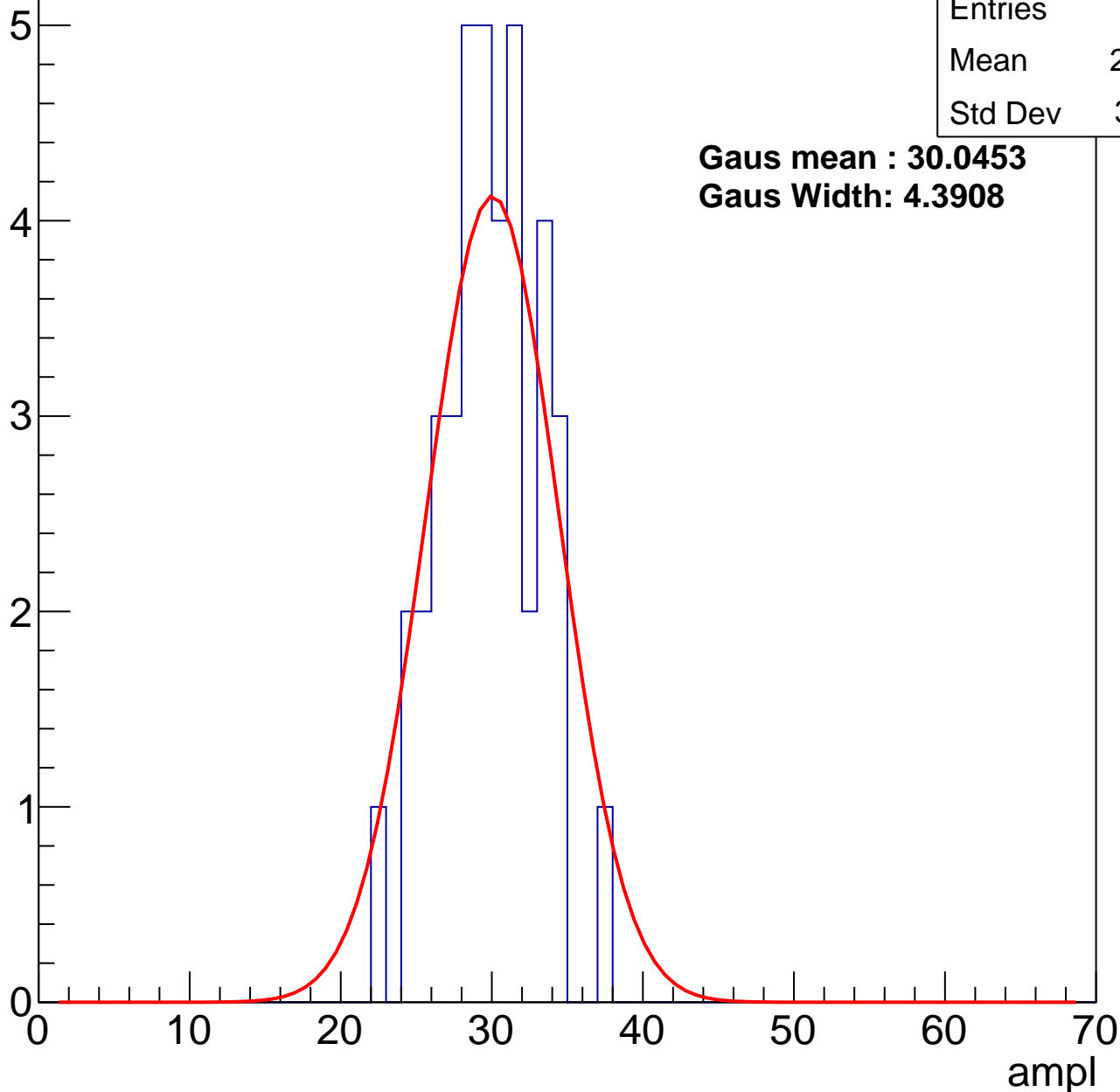
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	29.35
Std Dev	3.221

Gaus mean : 30.0453

Gaus Width: 4.3908



B1L103S, U15-ch11, adc1

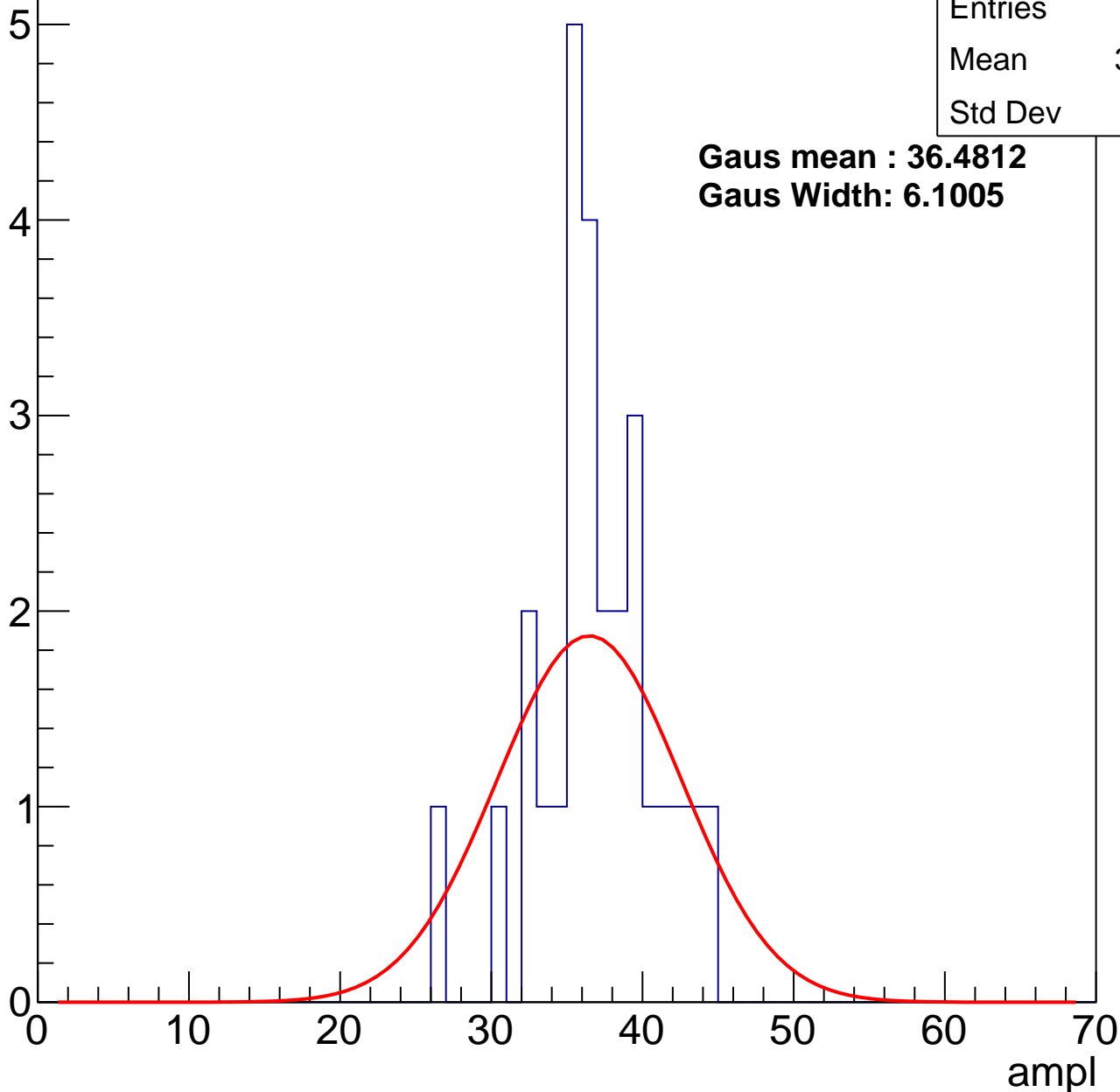
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	36.41
Std Dev	3.89

Gaus mean : 36.4812

Gaus Width: 6.1005



B1L103S, U15-ch11, adc2

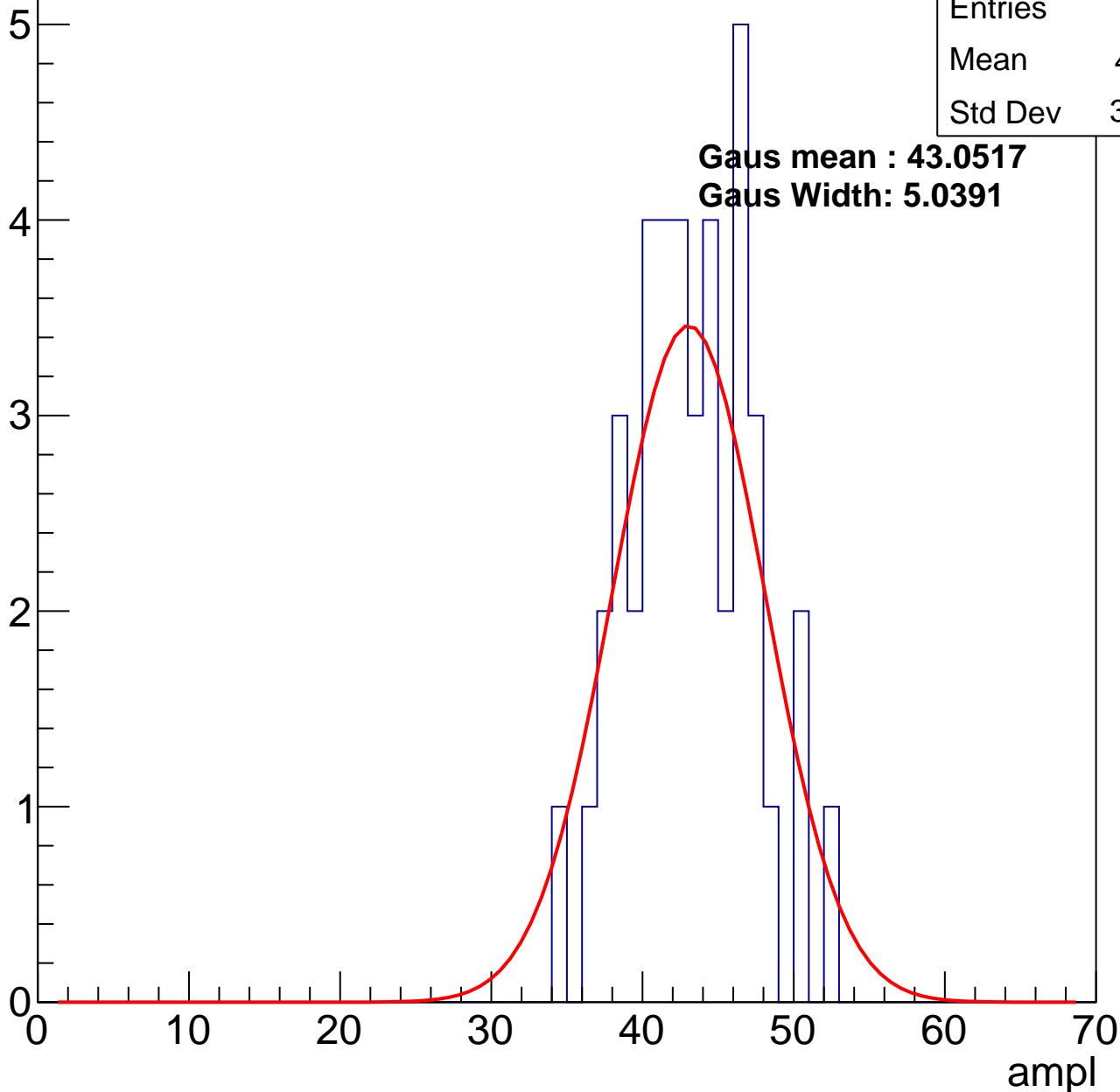
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	42.71
Std Dev	3.996

Gaus mean : 43.0517

Gaus Width: 5.0391

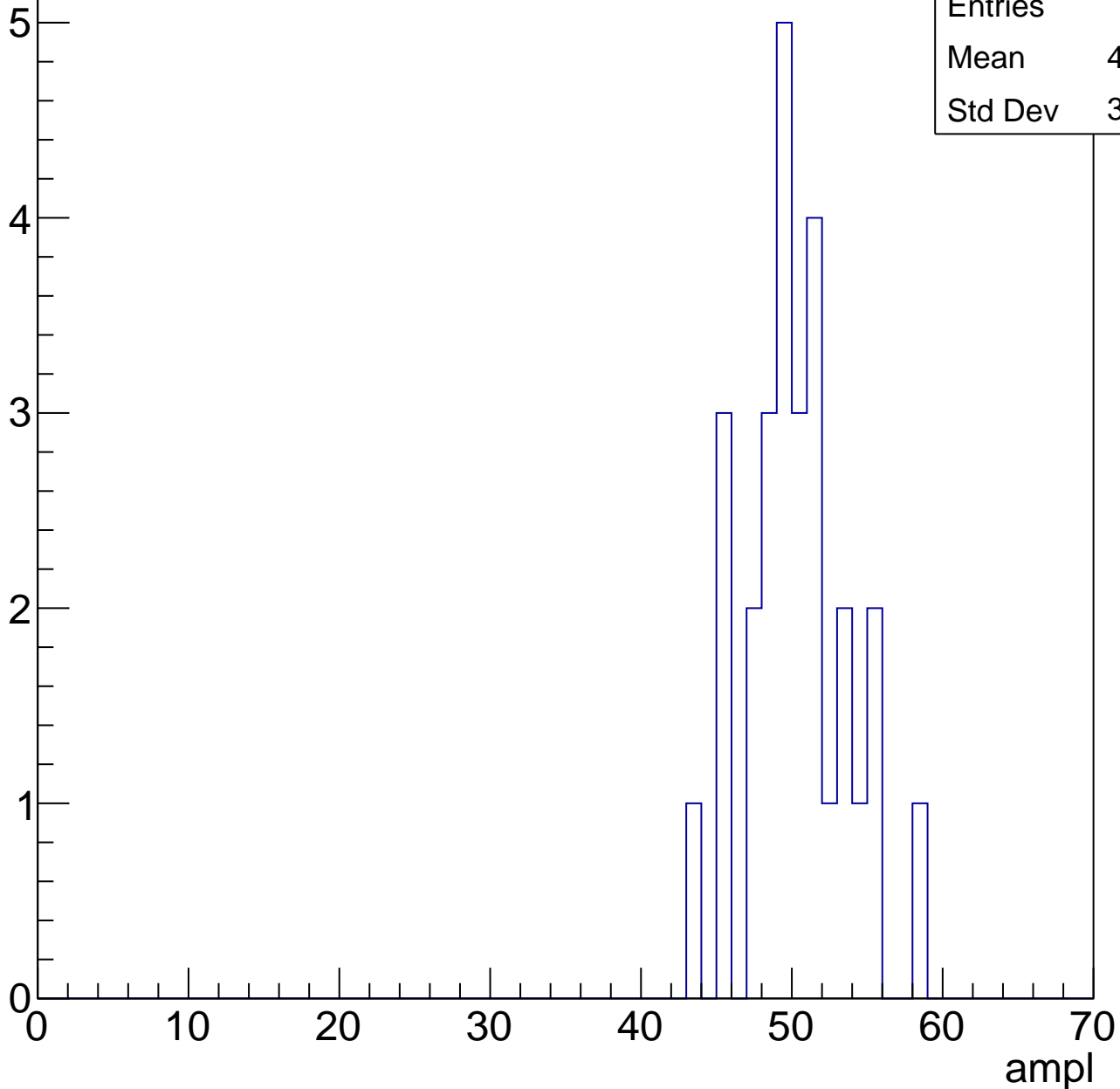


B1L103S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

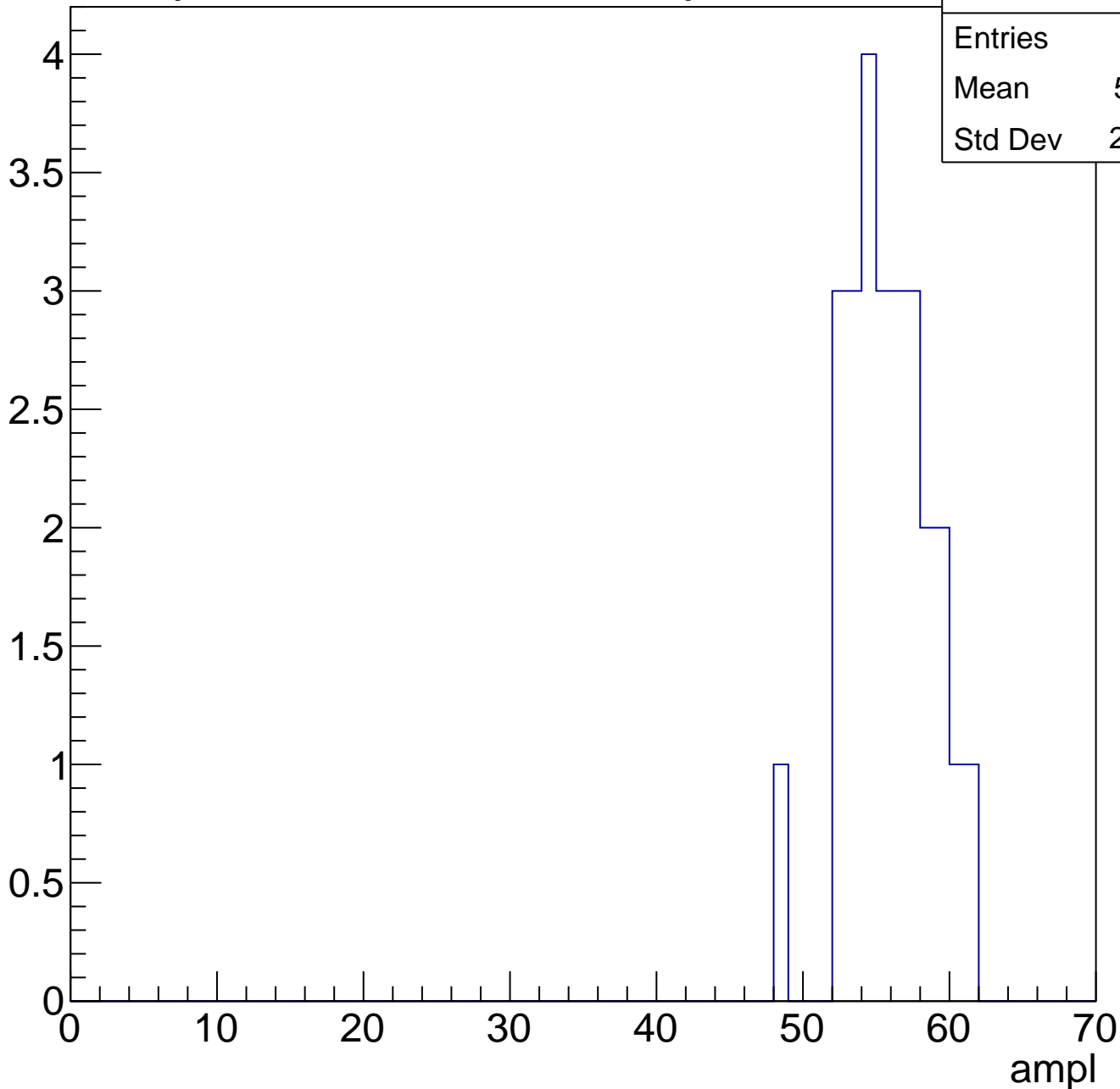
Entries	28
Mean	49.82
Std Dev	3.349



B1L103S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

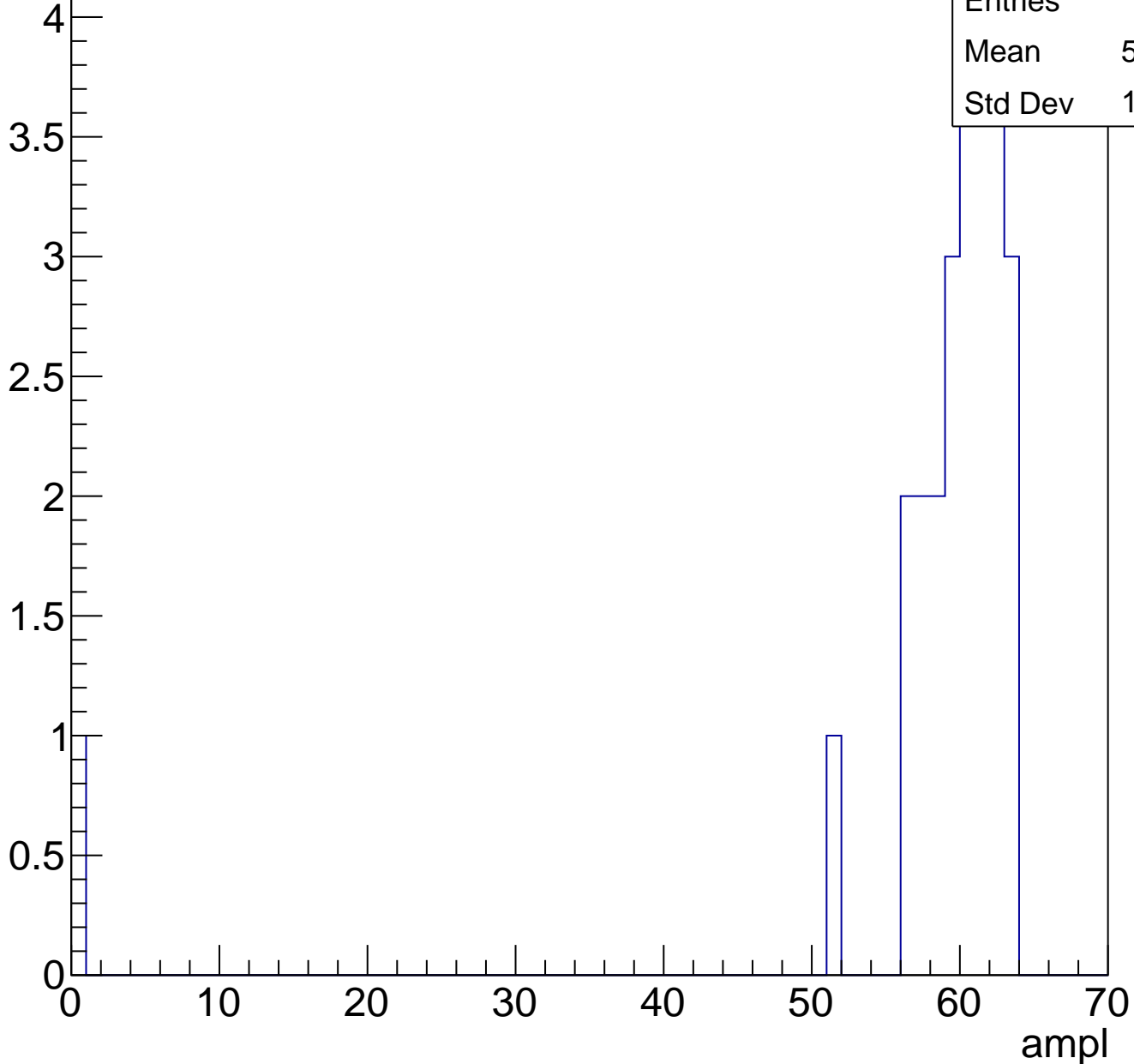
Entry



B1L103S, U15-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U15-ch12, adc0

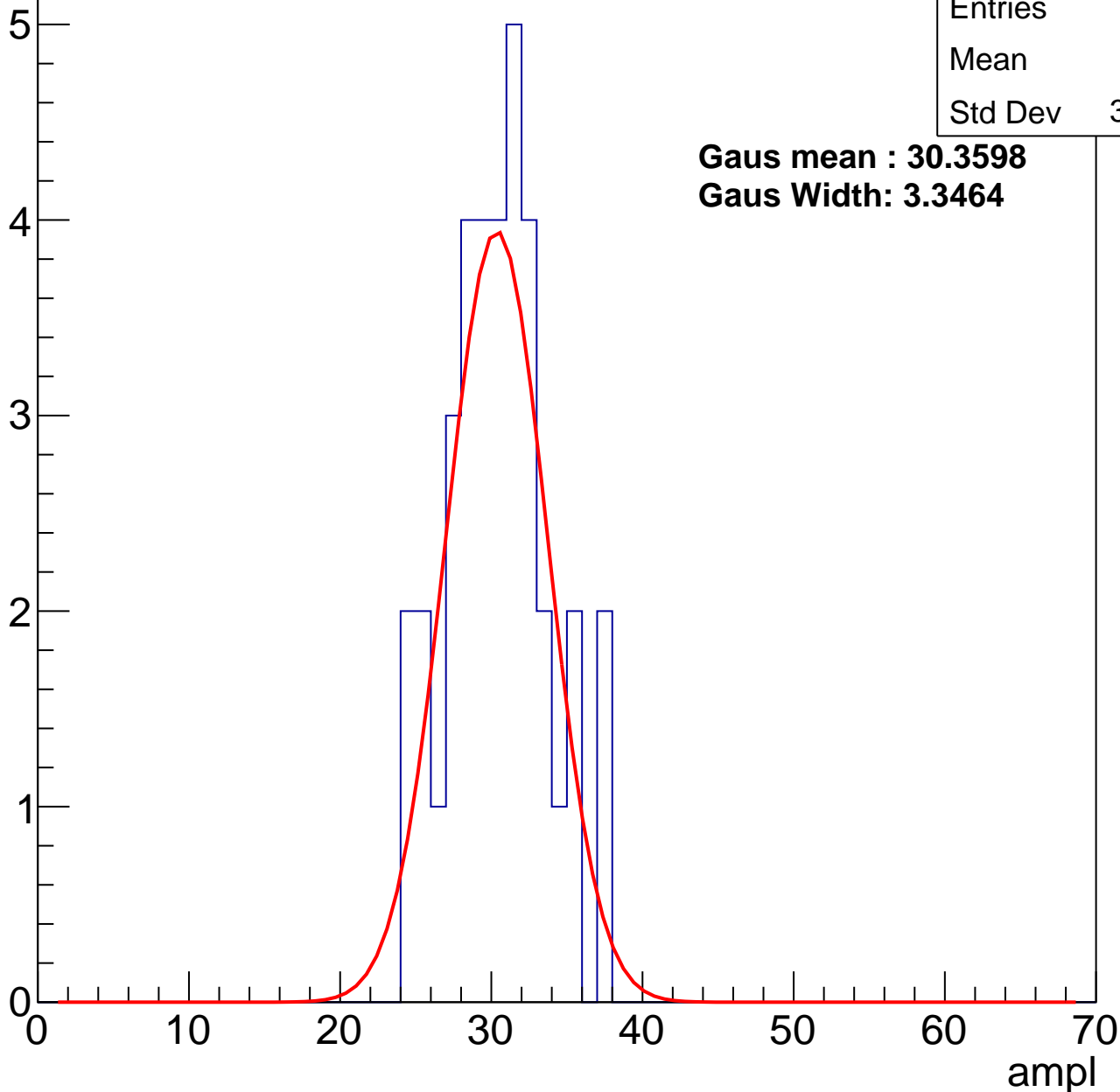
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	30
Std Dev	3.283

Gaus mean : 30.3598

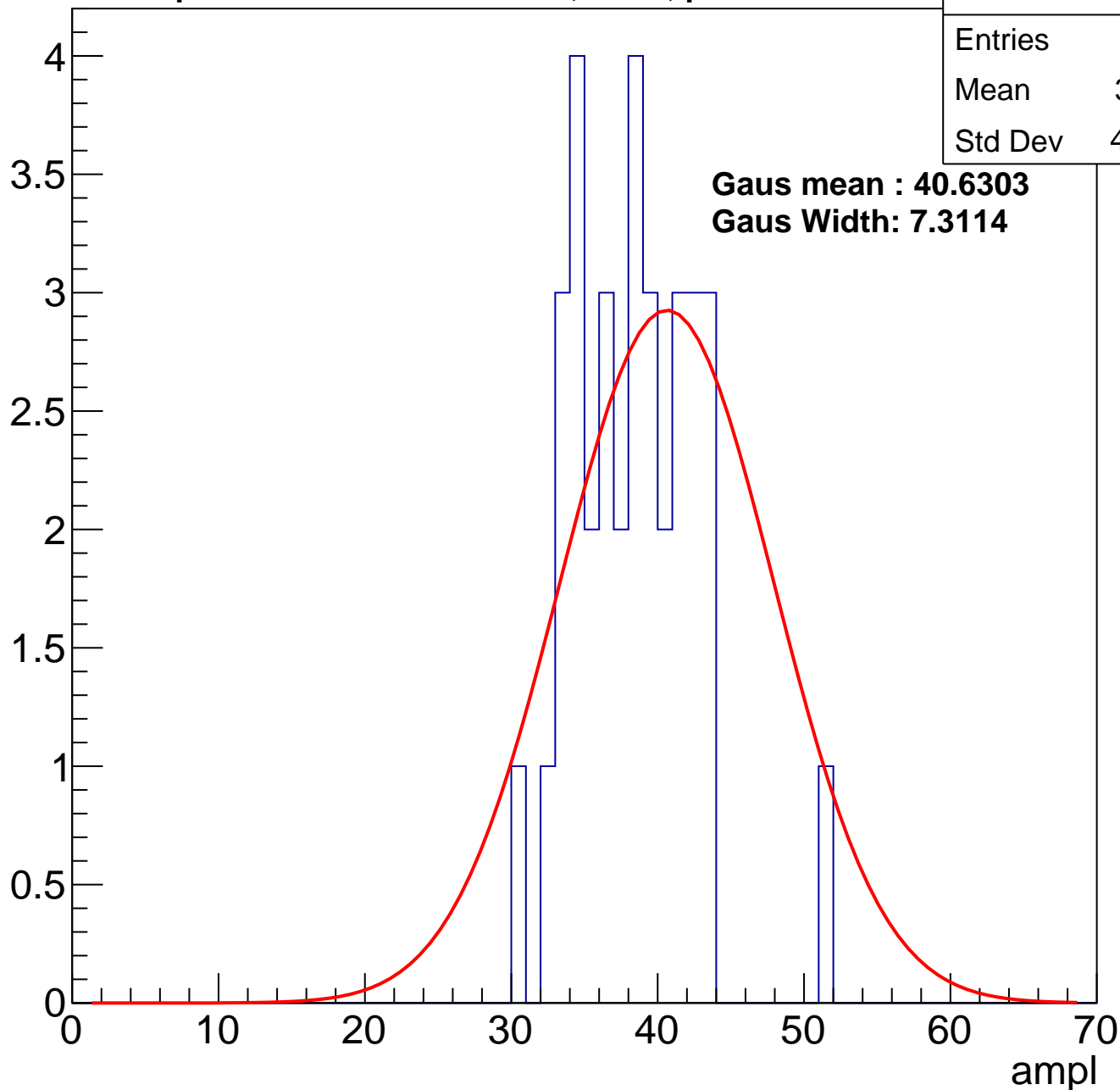
Gaus Width: 3.3464



B1L103S, U15-ch12, adc1

calib_packv5_042523_0143.root, FC#7, port C2

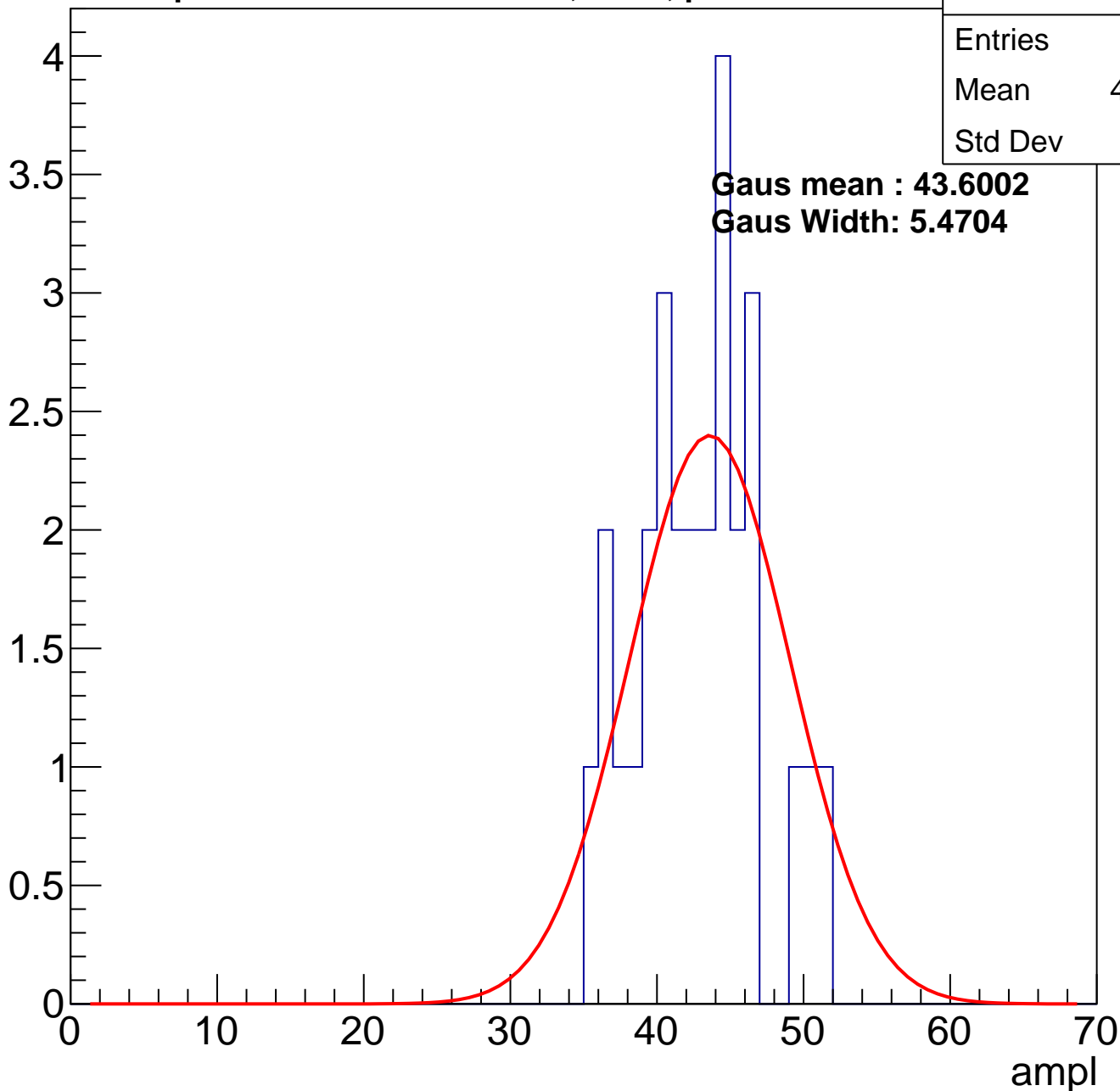
Entry



B1L103S, U15-ch12, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

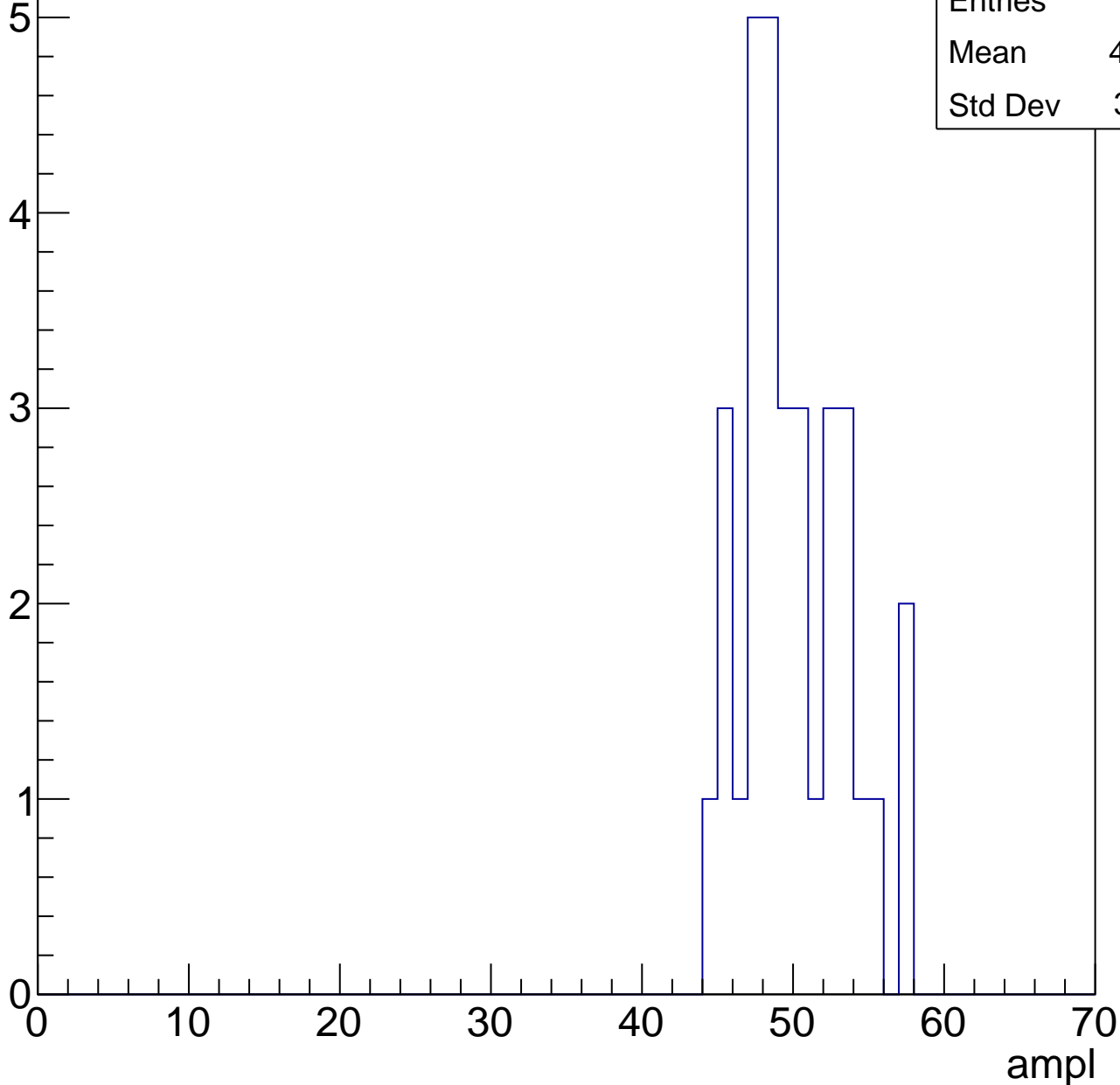


B1L103S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

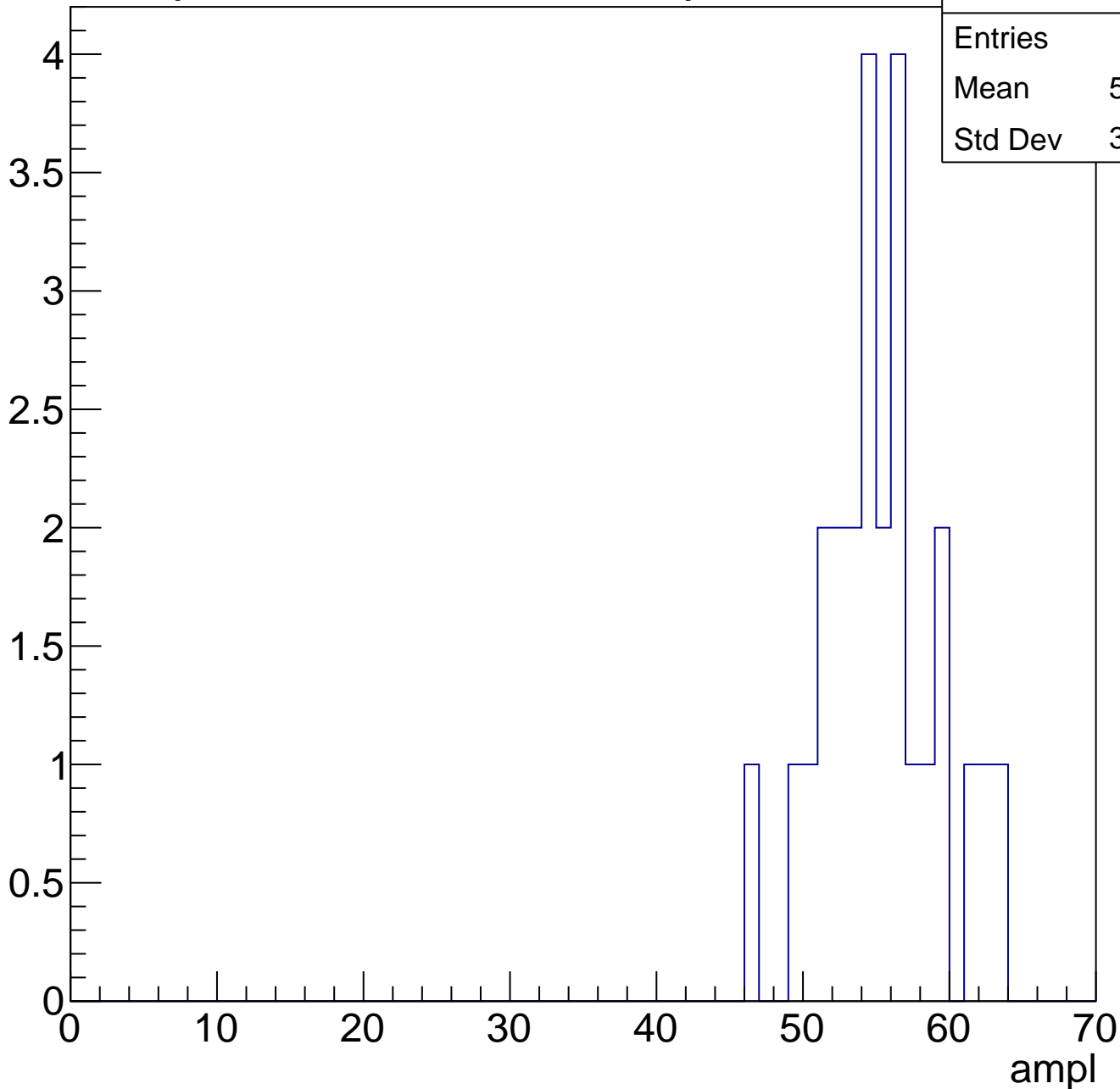
Entries	32
Mean	49.56
Std Dev	3.391



B1L103S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

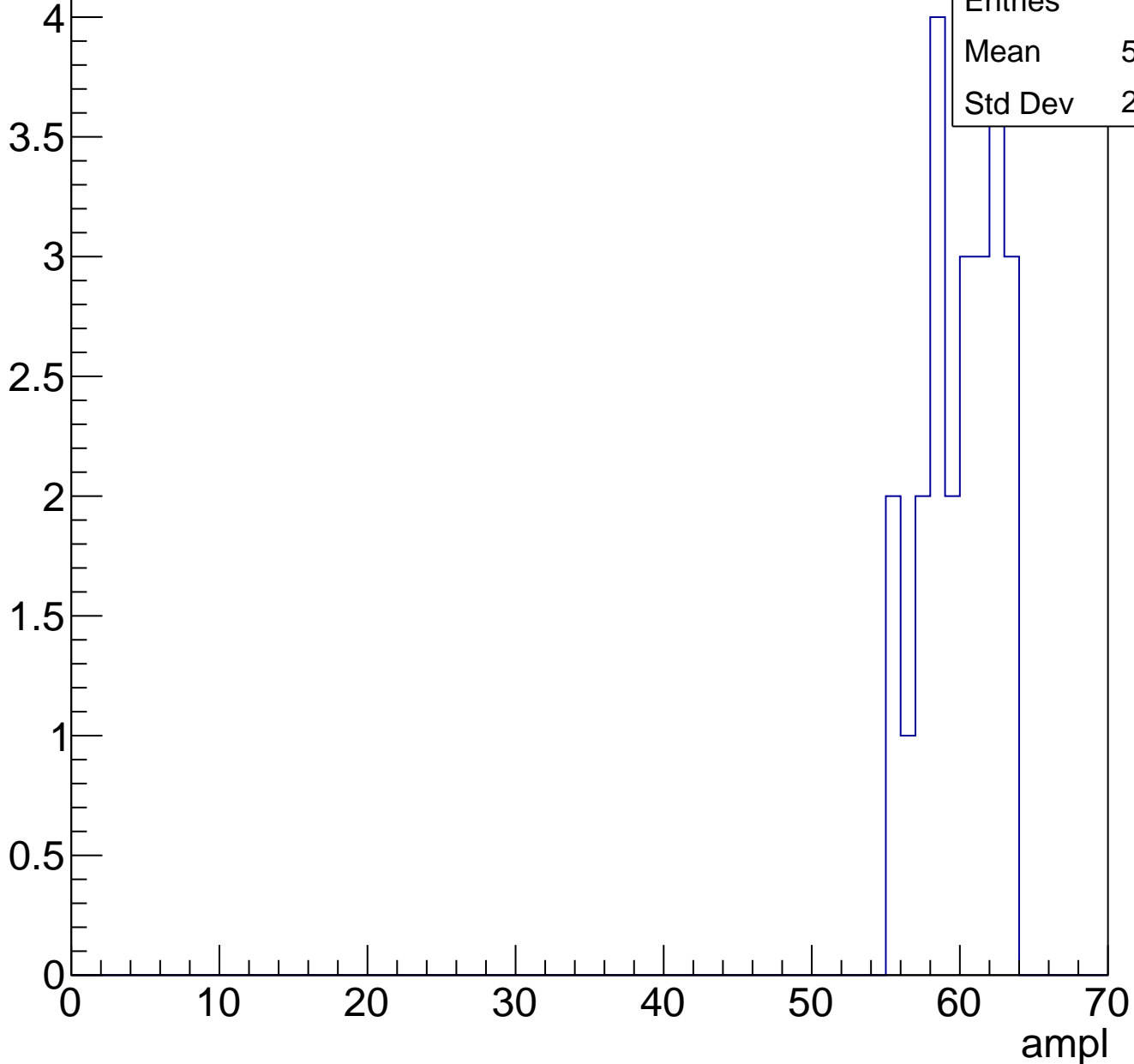


Entries	26
Mean	54.85
Std Dev	3.929

B1L103S, U15-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

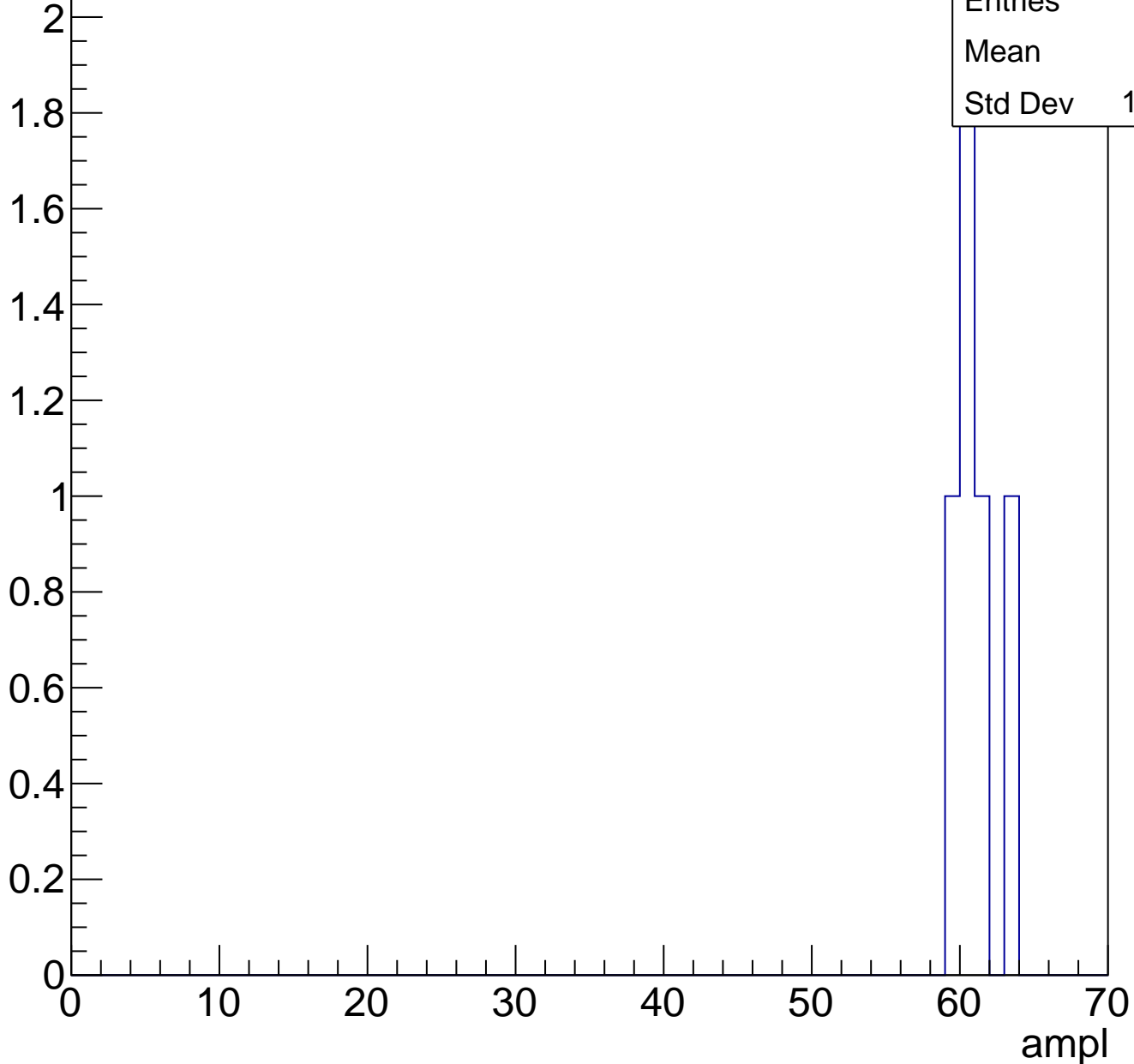


Entries	24
Mean	59.58
Std Dev	2.448

B1L103S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

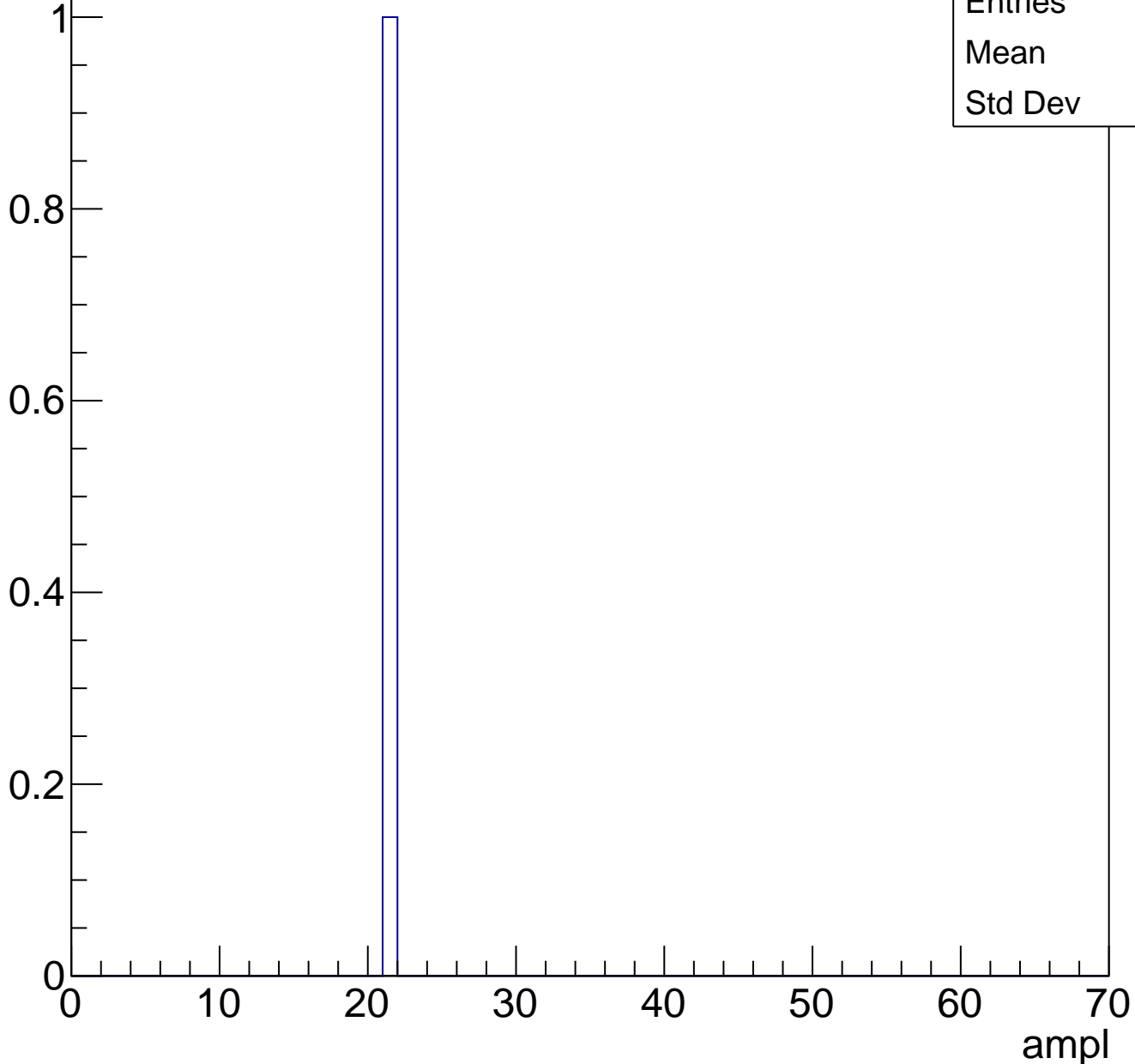


Entries	5
Mean	60.6
Std Dev	1.356

B1L103S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

B1L103S, U15-ch13, adc0

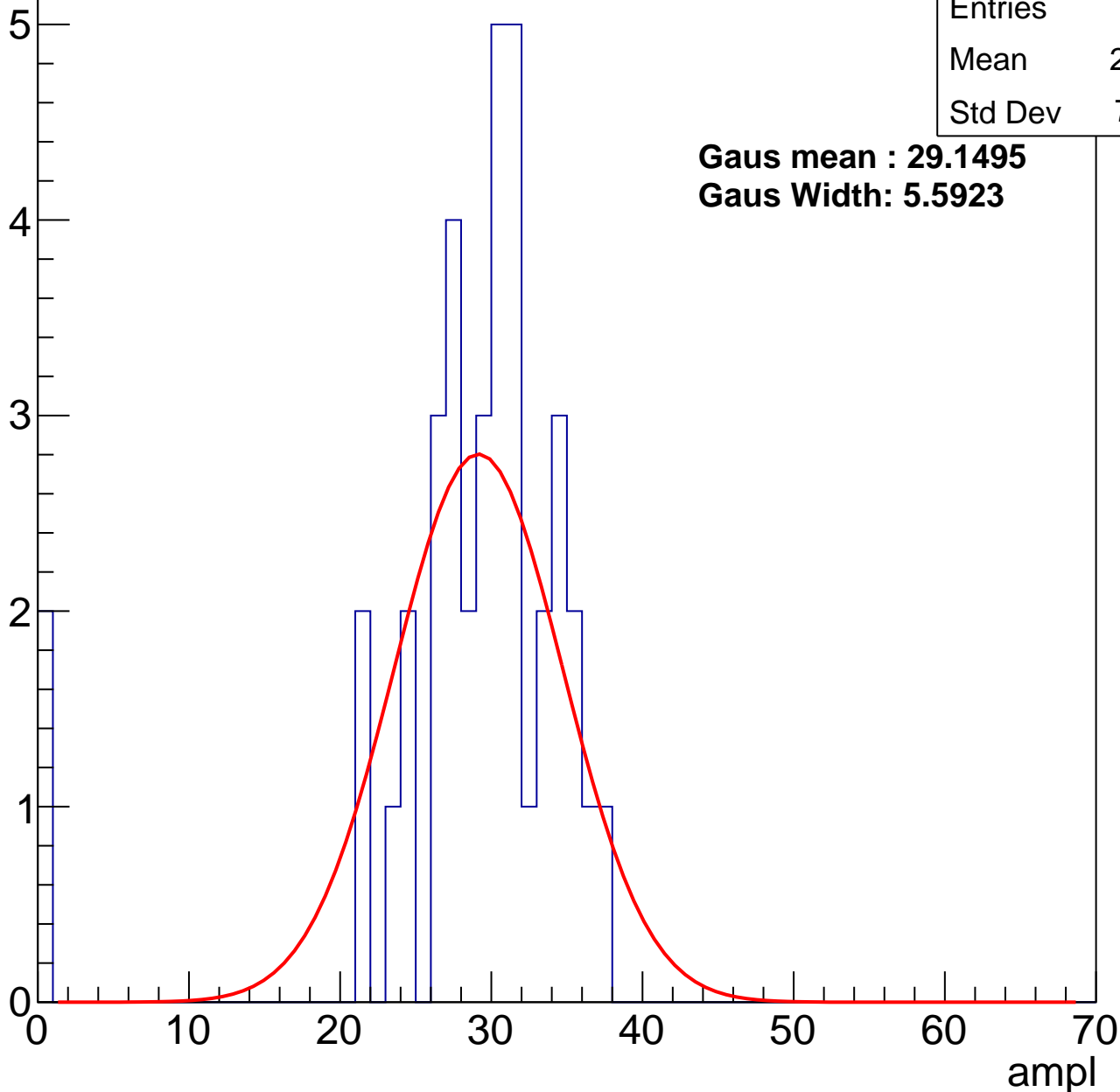
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	27.95
Std Dev	7.551

Gaus mean : 29.1495

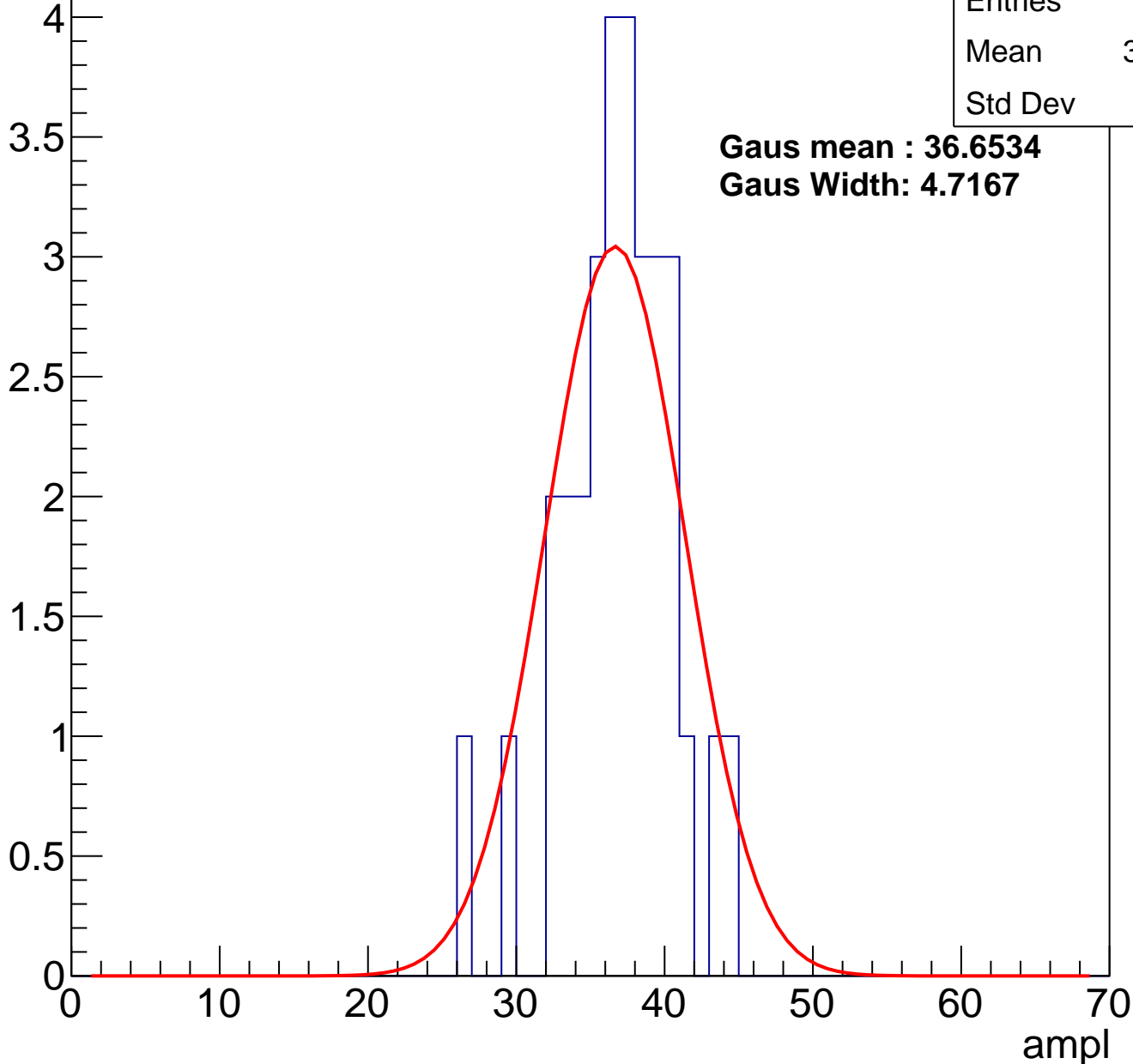
Gaus Width: 5.5923



B1L103S, U15-ch13, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	31
Mean	36.42
Std Dev	3.74

B1L103S, U15-ch13, adc2

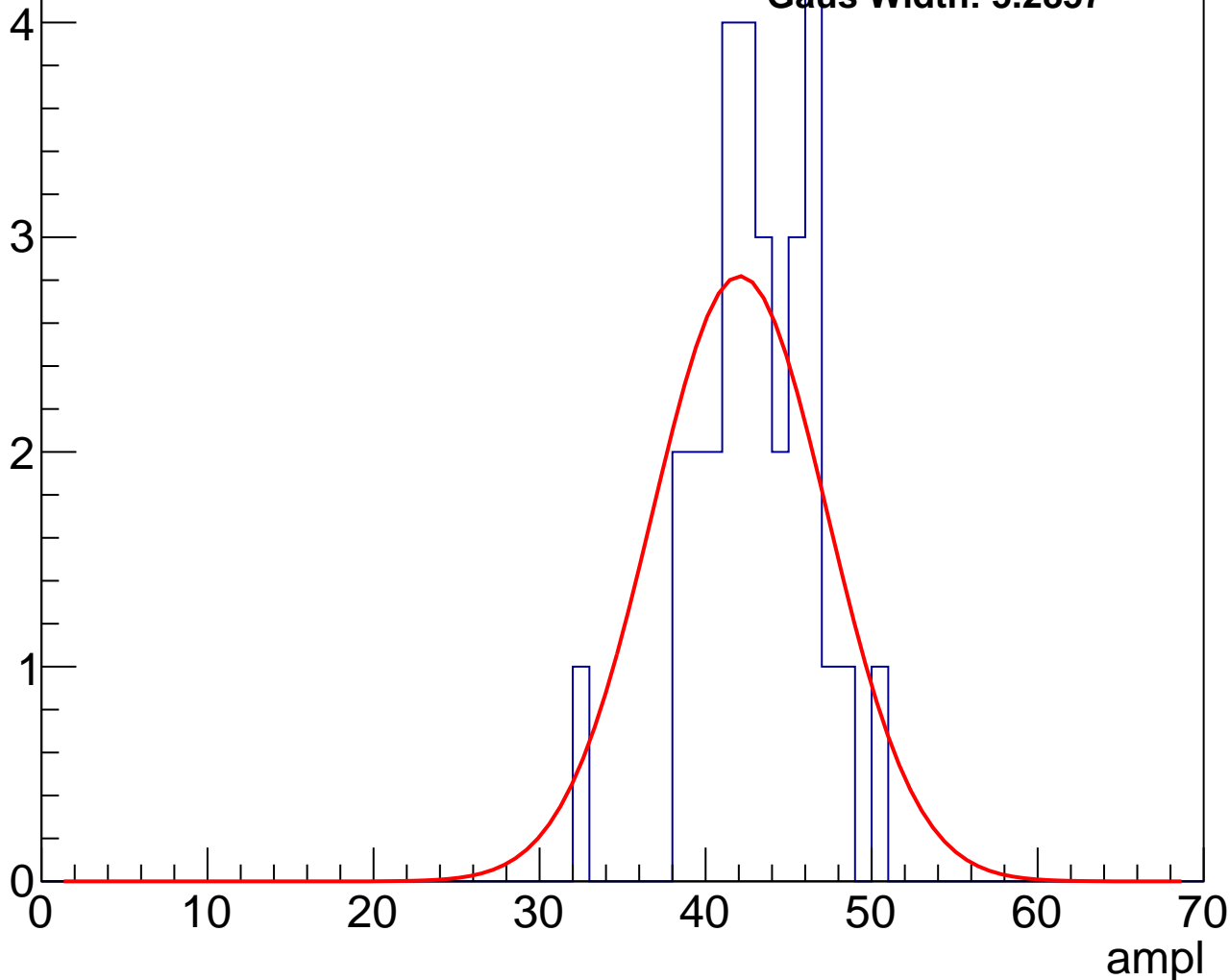
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	42.74
Std Dev	3.538

Gaus mean : 42.0615

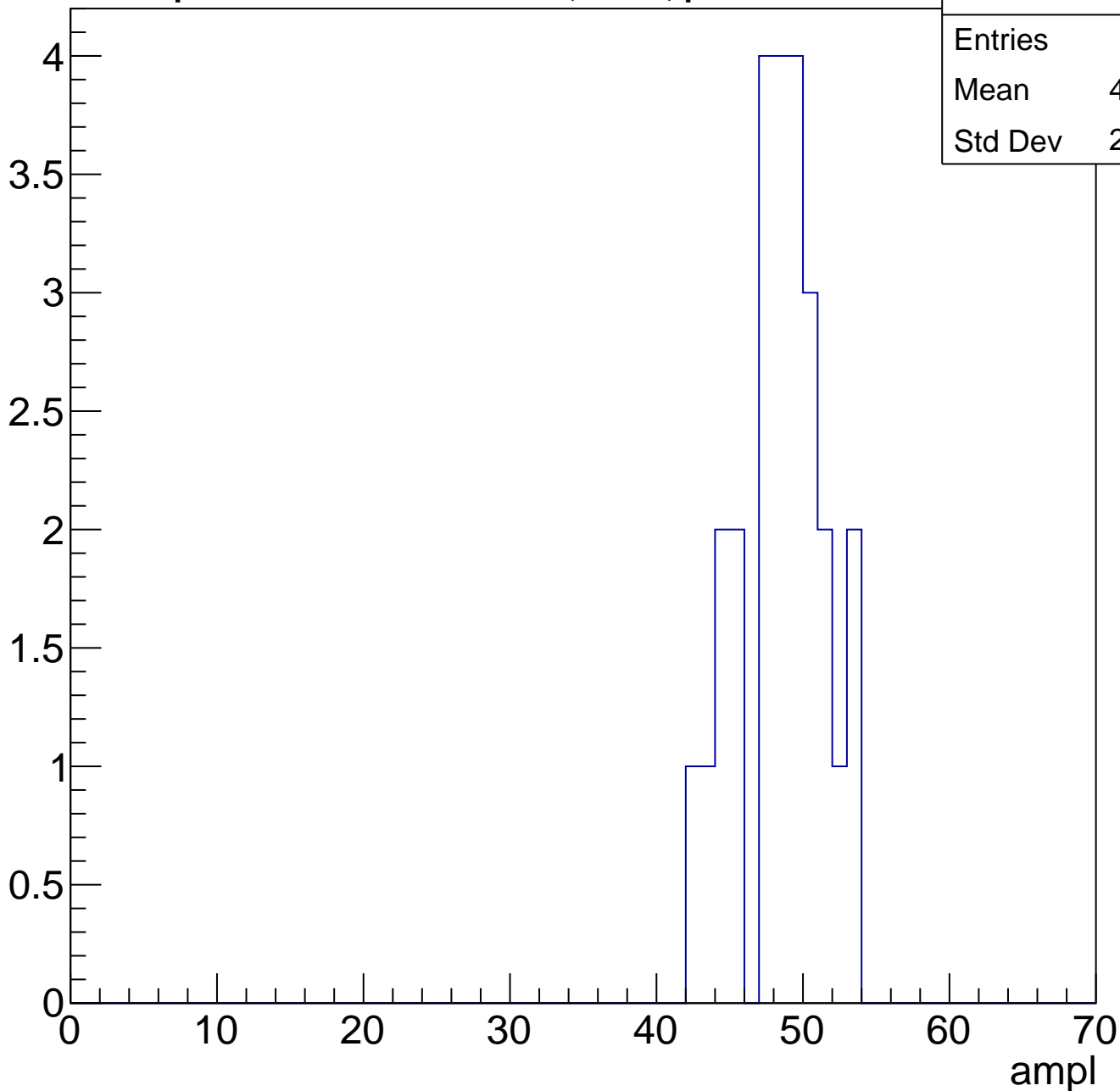
Gaus Width: 5.2857



B1L103S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



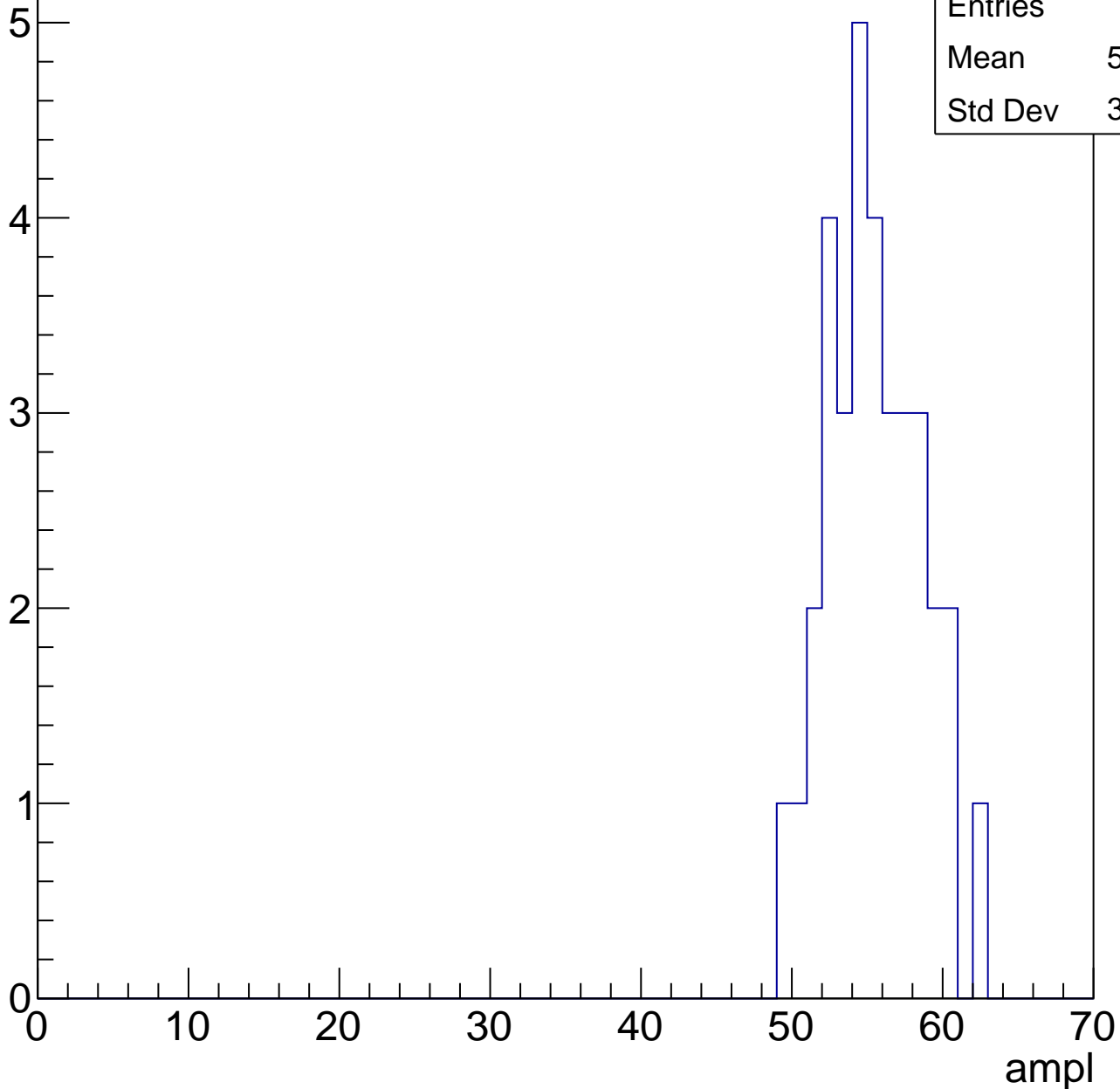
Entries	26
Mean	48.04
Std Dev	2.875

B1L103S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

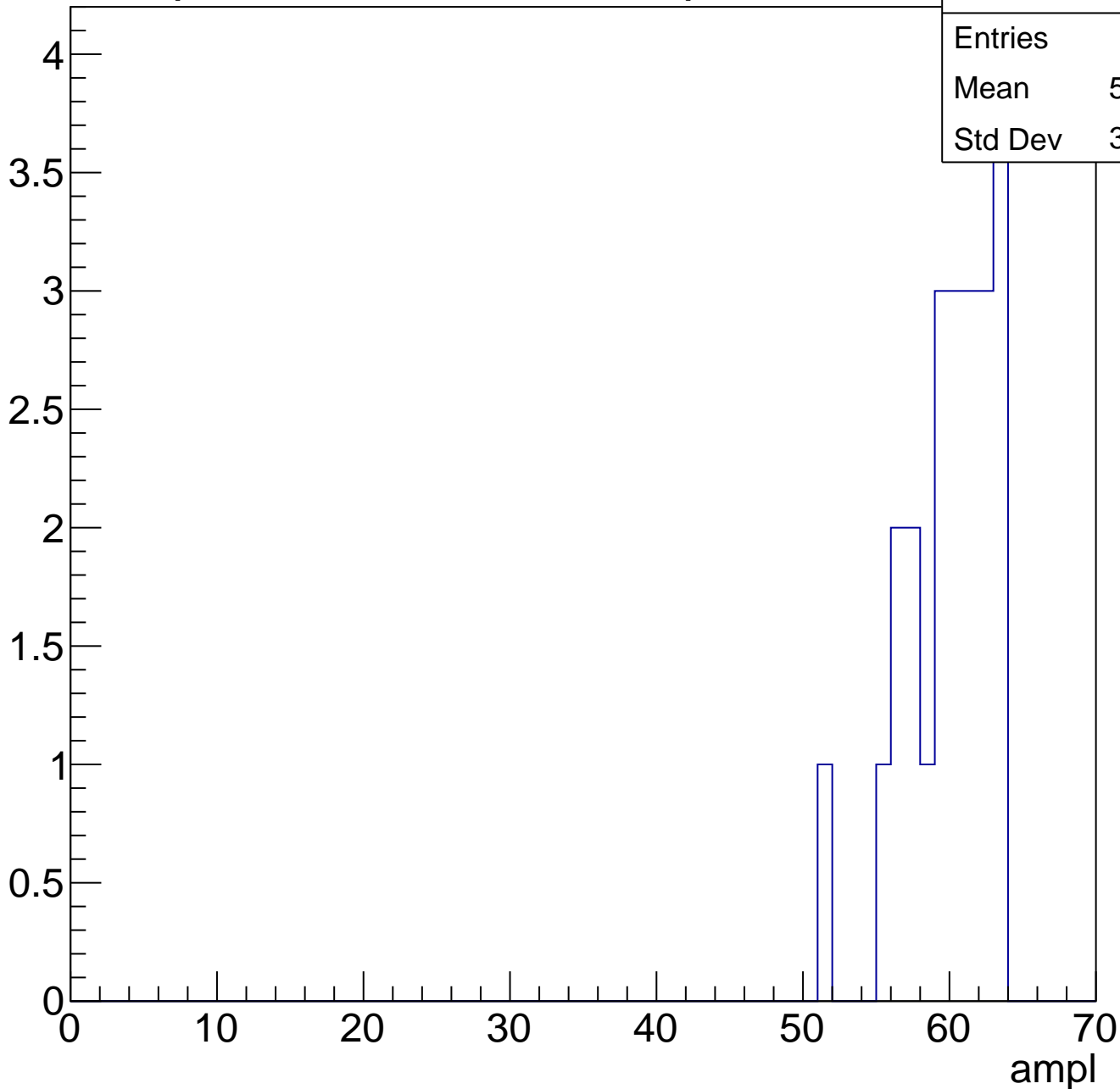
Entries	34
Mean	55.03
Std Dev	3.063



B1L103S, U15-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

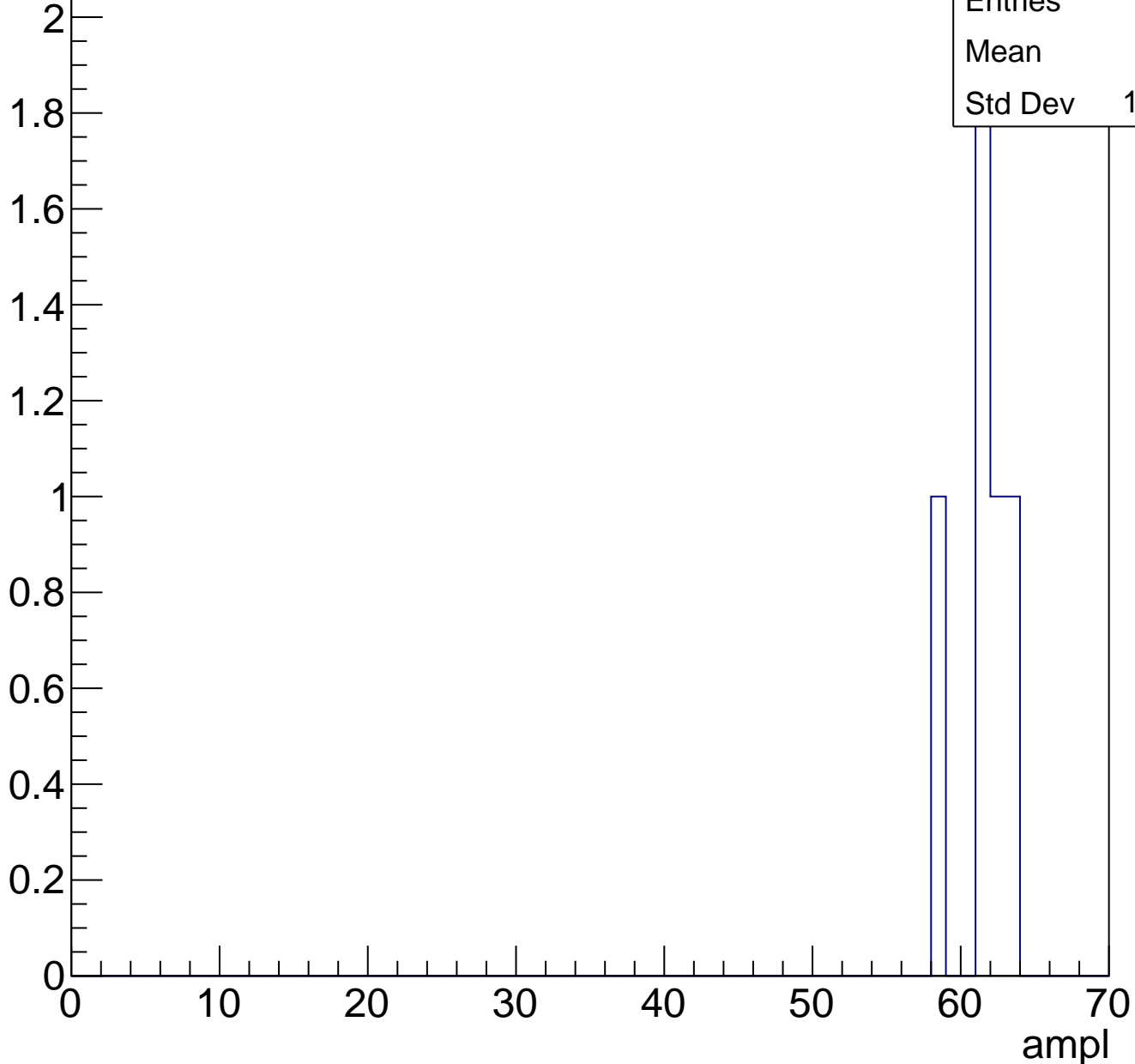
Entry



B1L103S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	61
Std Dev	1.673

B1L103S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch14, adc0

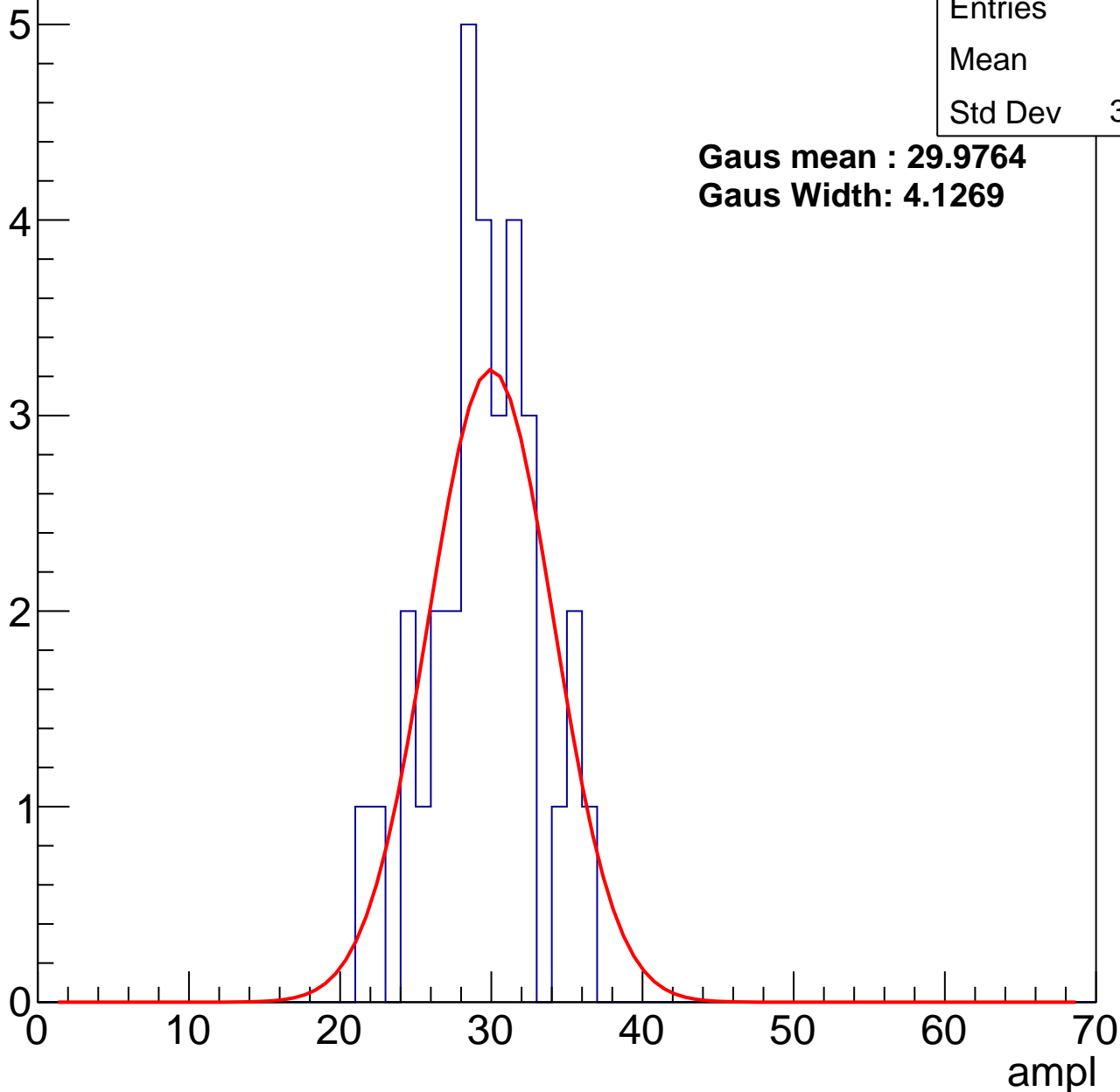
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	29
Std Dev	3.544

Gaus mean : 29.9764

Gaus Width: 4.1269



B1L103S, U15-ch14, adc1

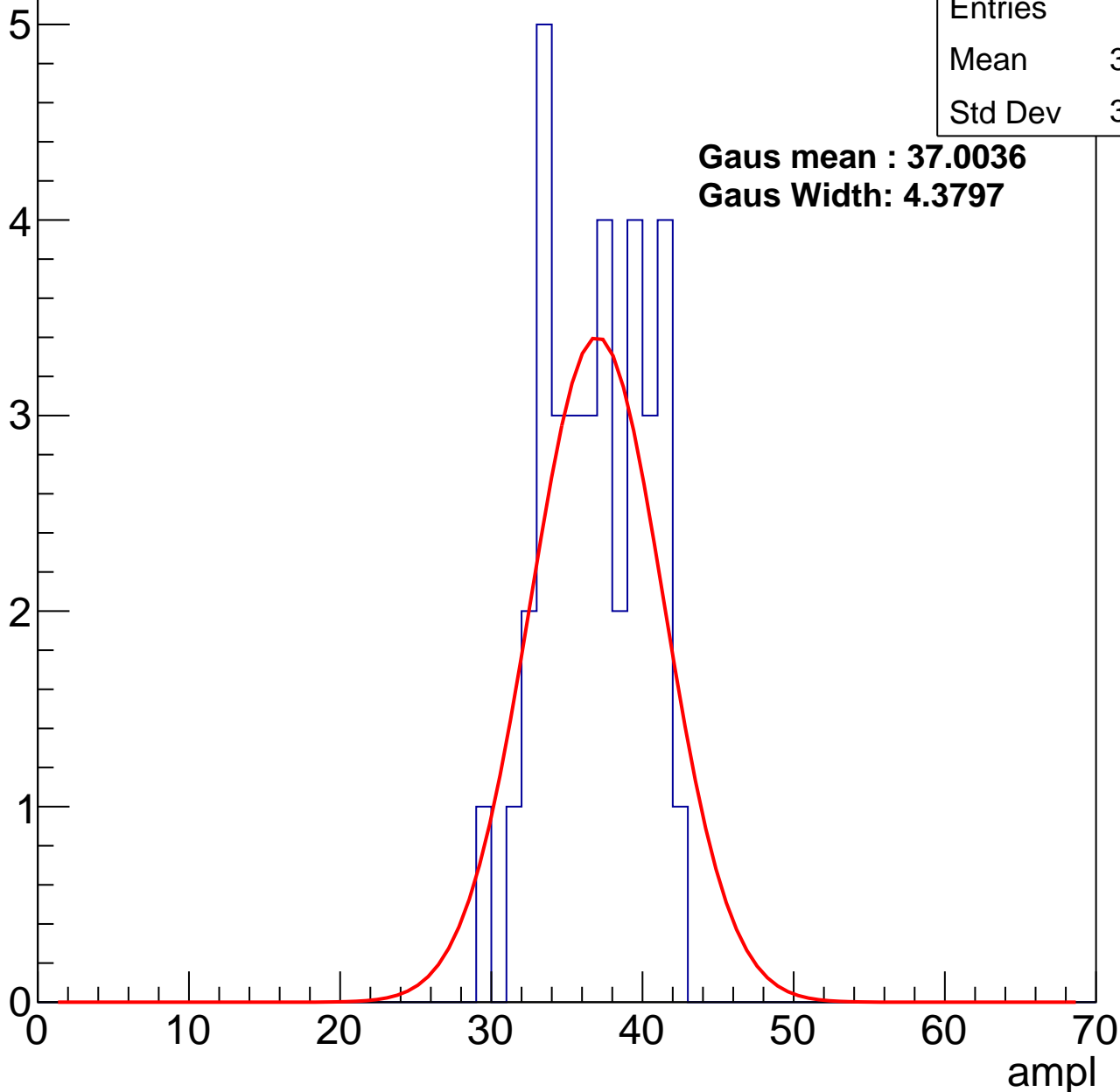
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	36.39
Std Dev	3.302

Gaus mean : 37.0036

Gaus Width: 4.3797



B1L103S, U15-ch14, adc2

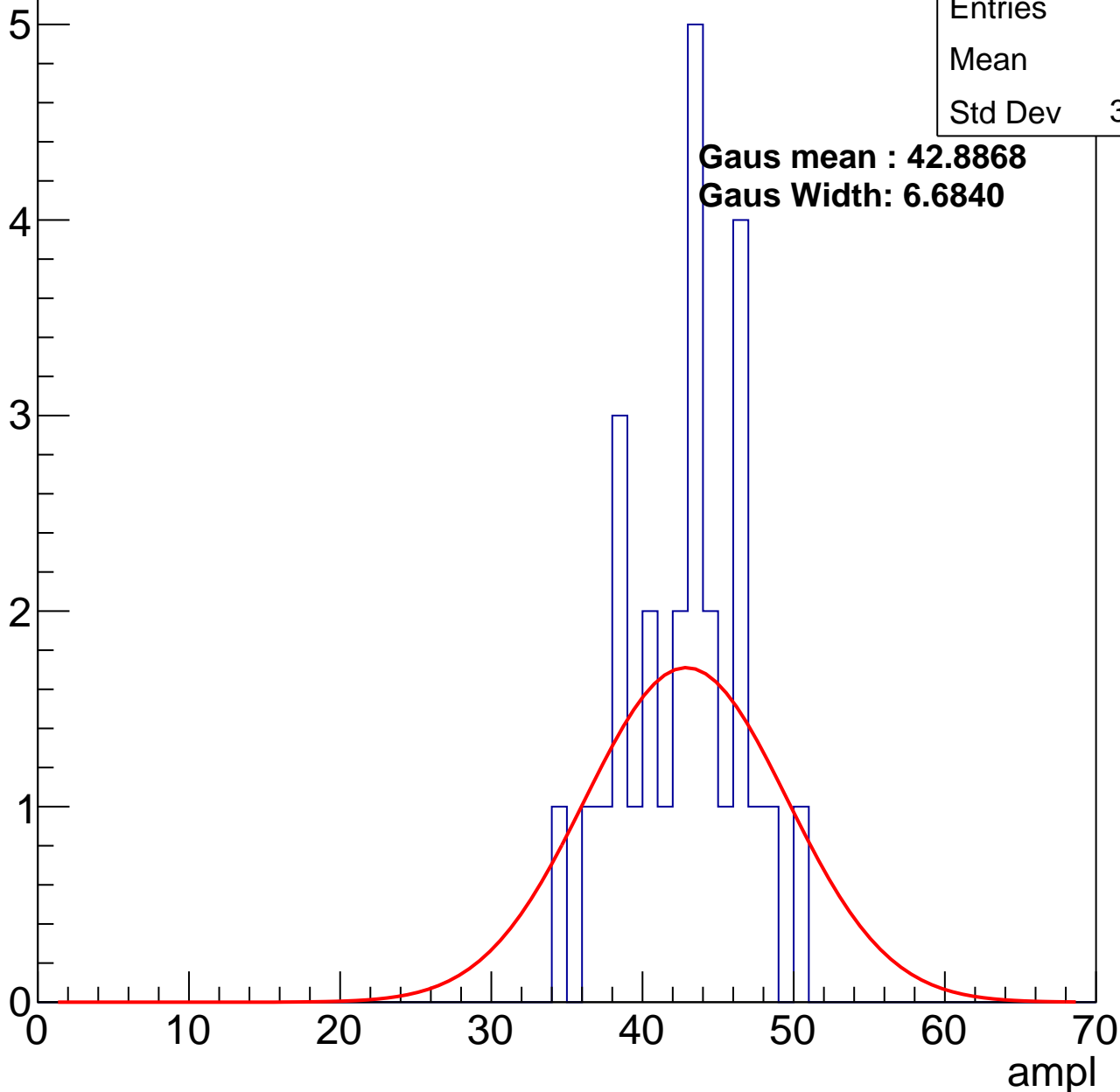
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	42.3
Std Dev	3.847

Gaus mean : 42.8868

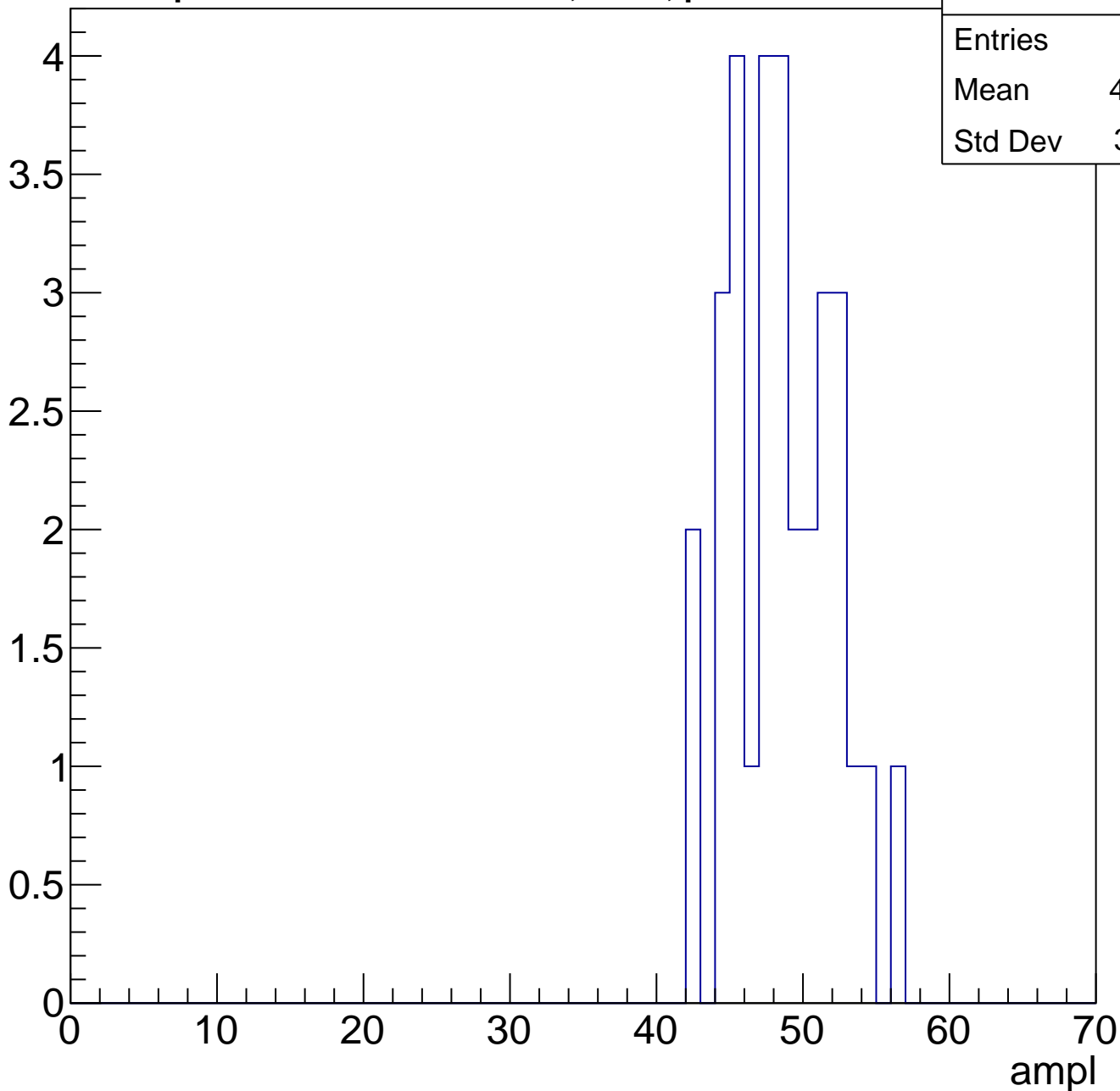
Gaus Width: 6.6840



B1L103S, U15-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



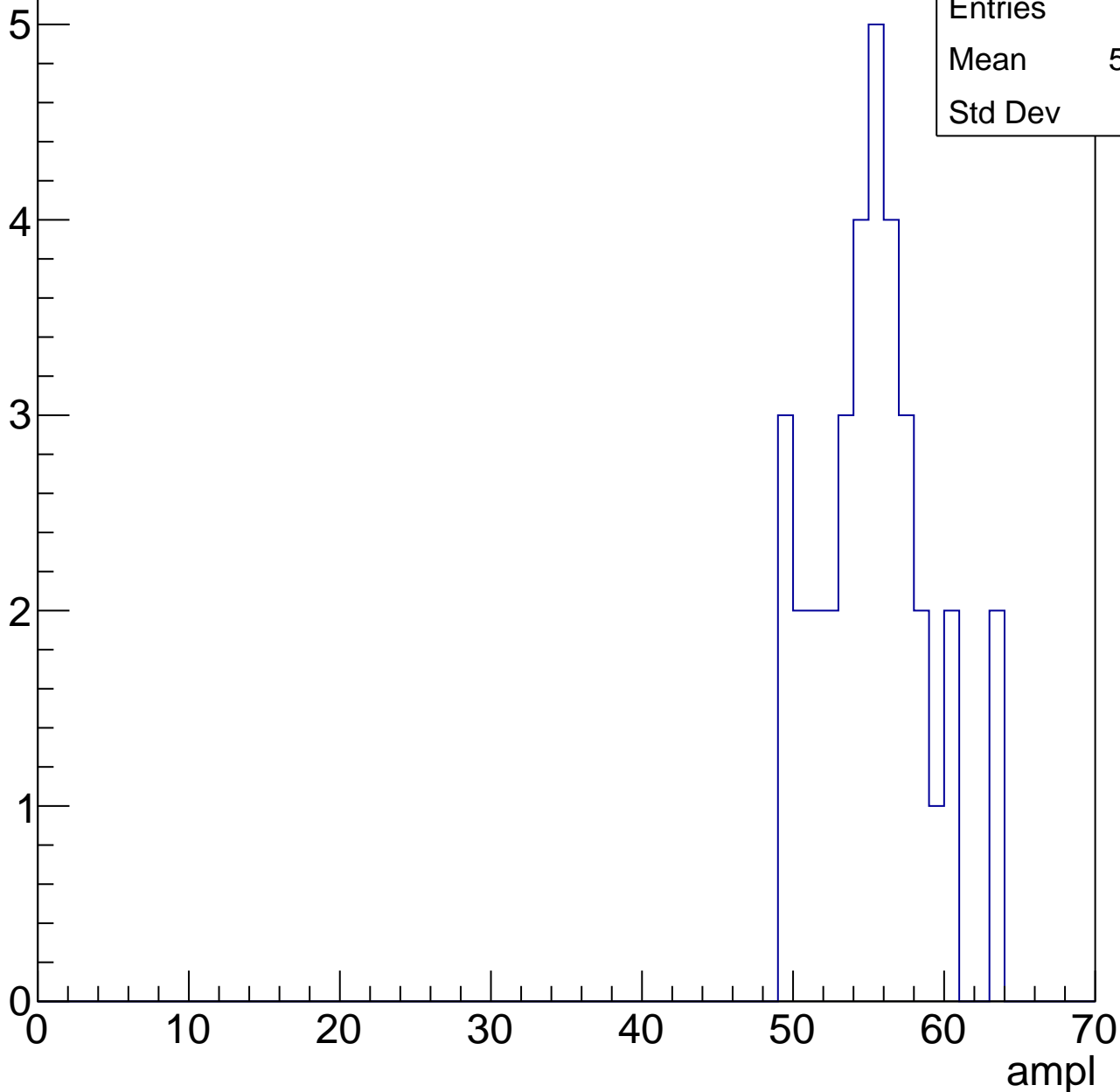
Entries	31
Mean	48.13
Std Dev	3.471

B1L103S, U15-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

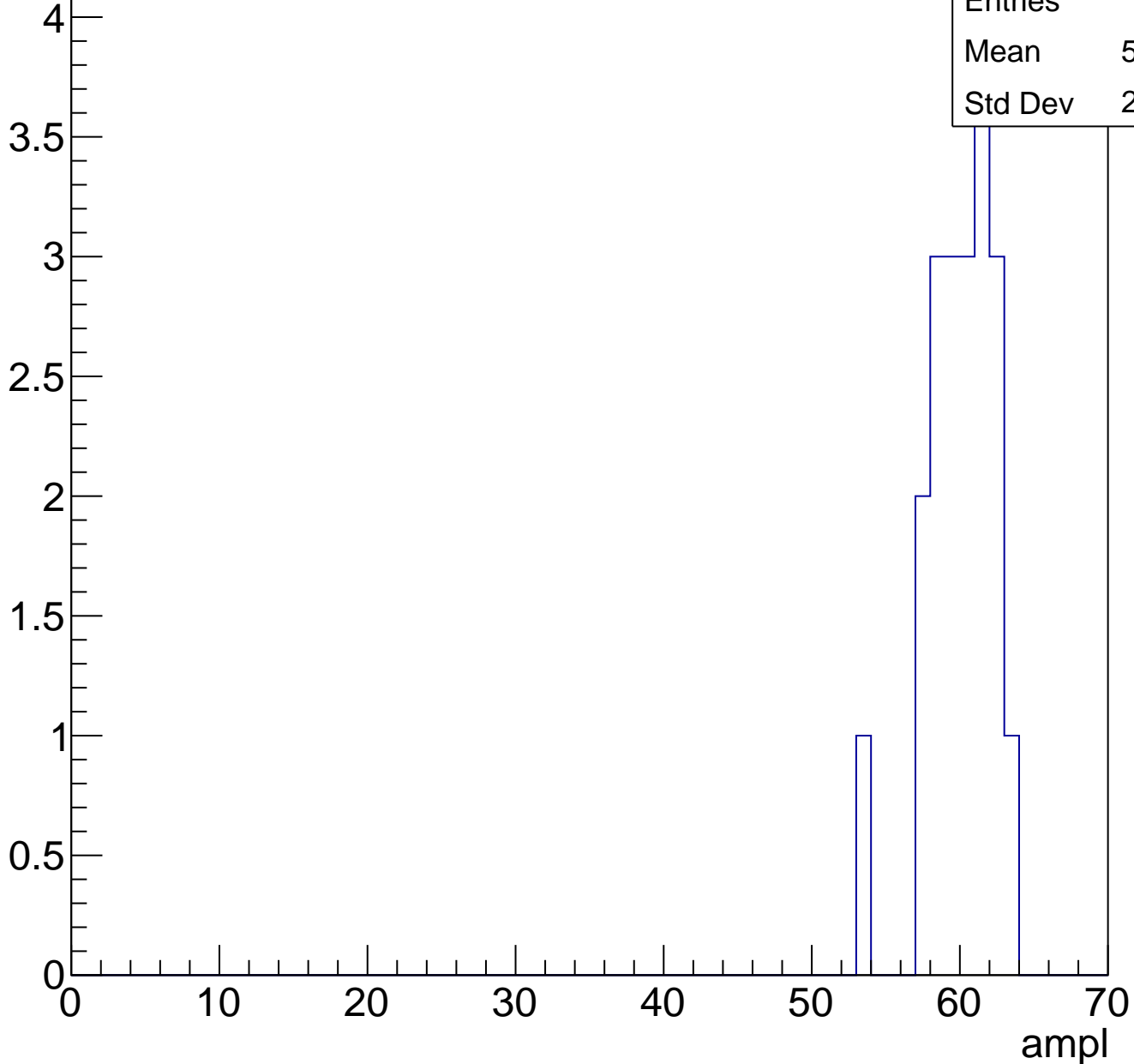
Entries	35
Mean	54.83
Std Dev	3.59



B1L103S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

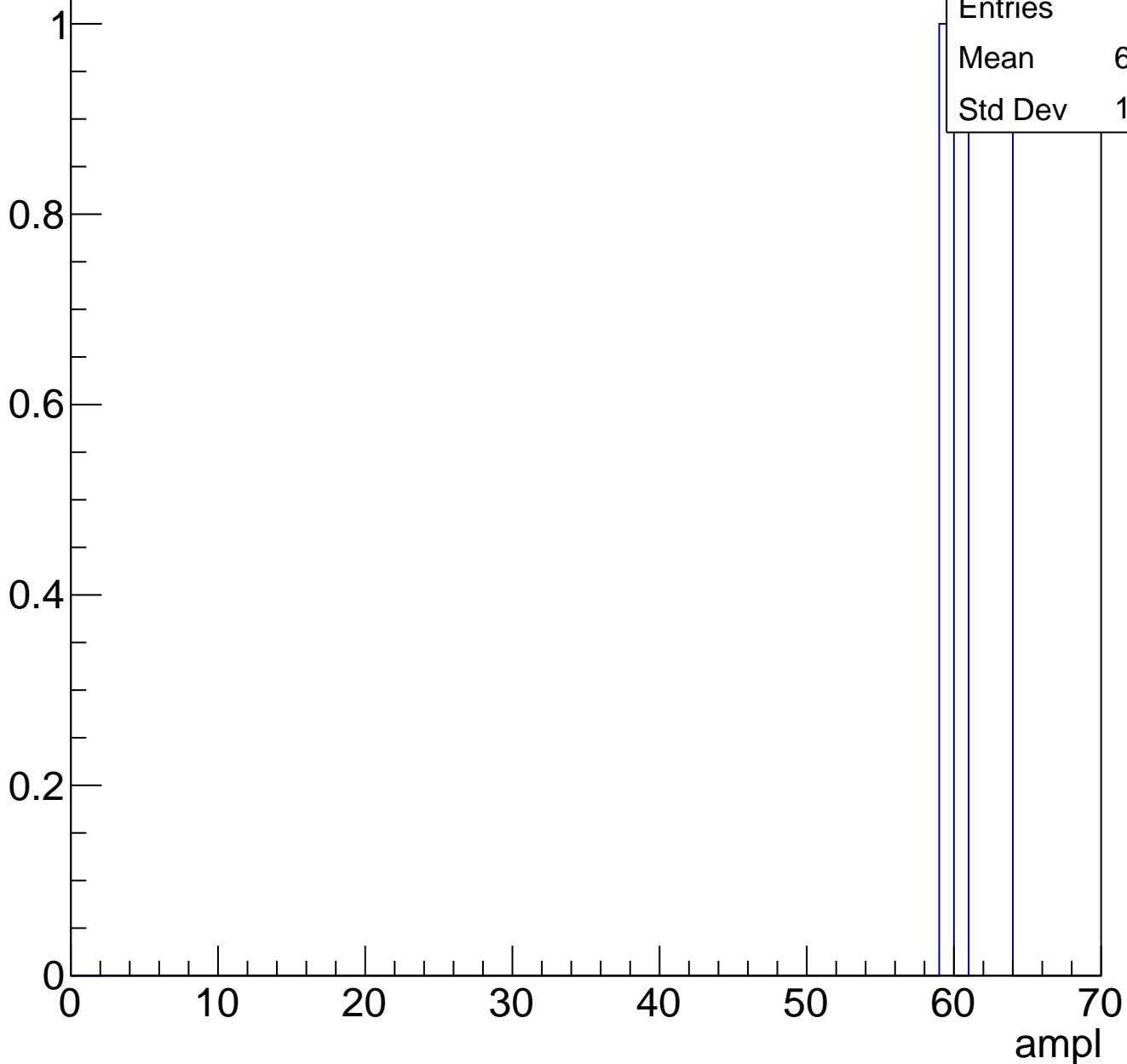


Entries	20
Mean	59.55
Std Dev	2.269

B1L103S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

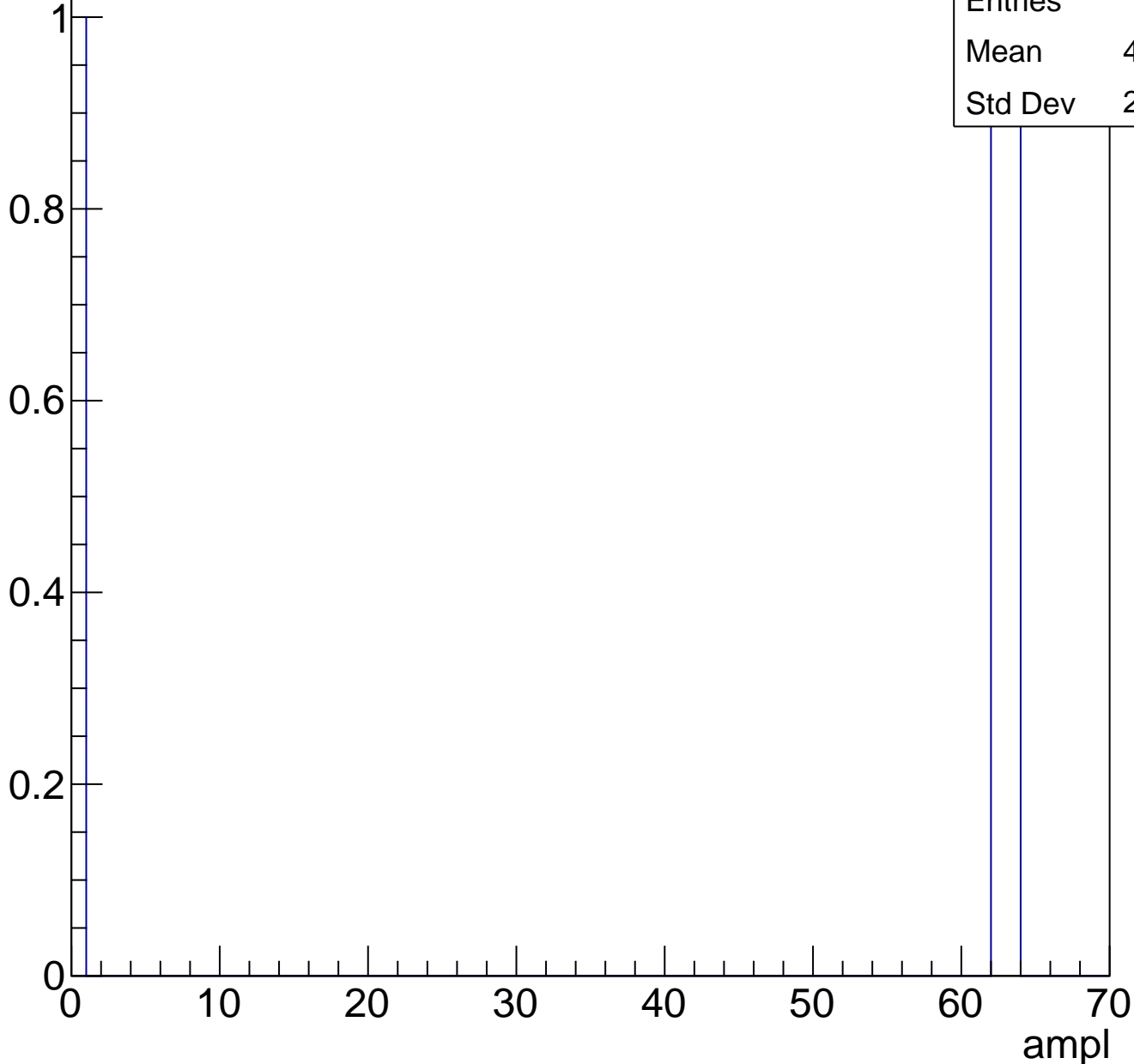
Entry



B1L103S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

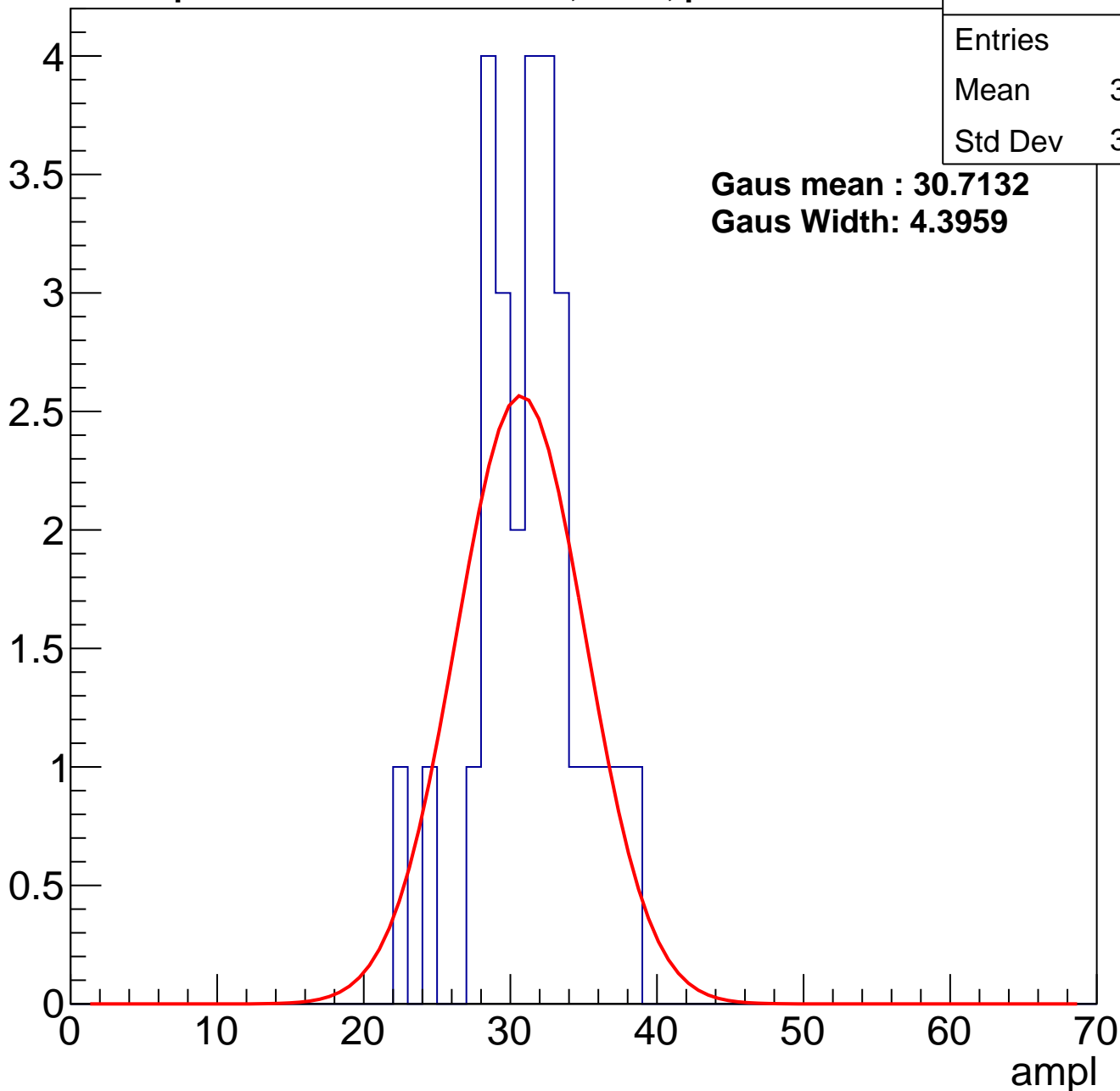
Entry



B1L103S, U15-ch15, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch15, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

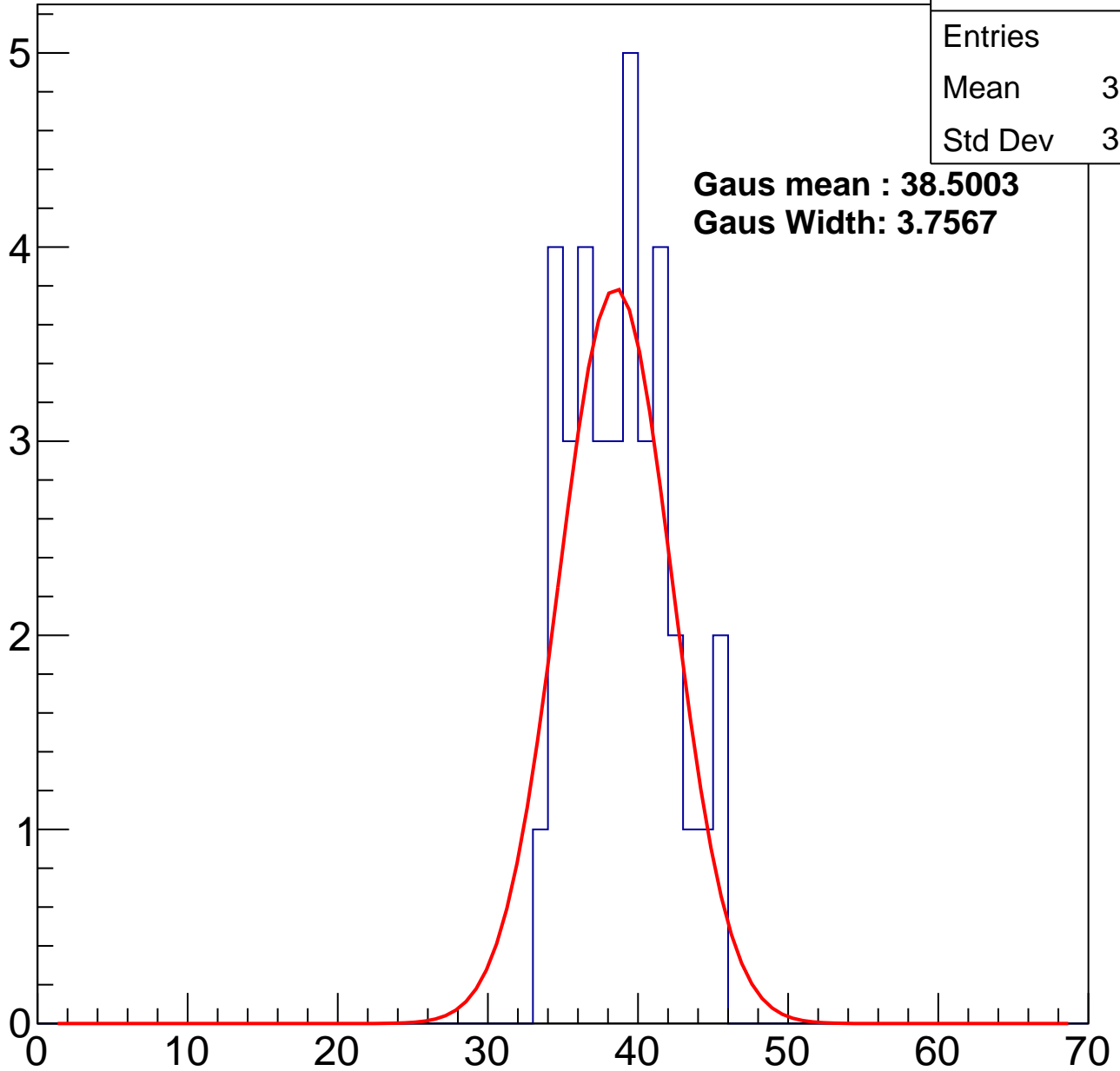
Entries	36
Mean	38.42
Std Dev	3.218

Gaus mean : 38.5003

Gaus Width: 3.7567

5
4
3
2
1
0

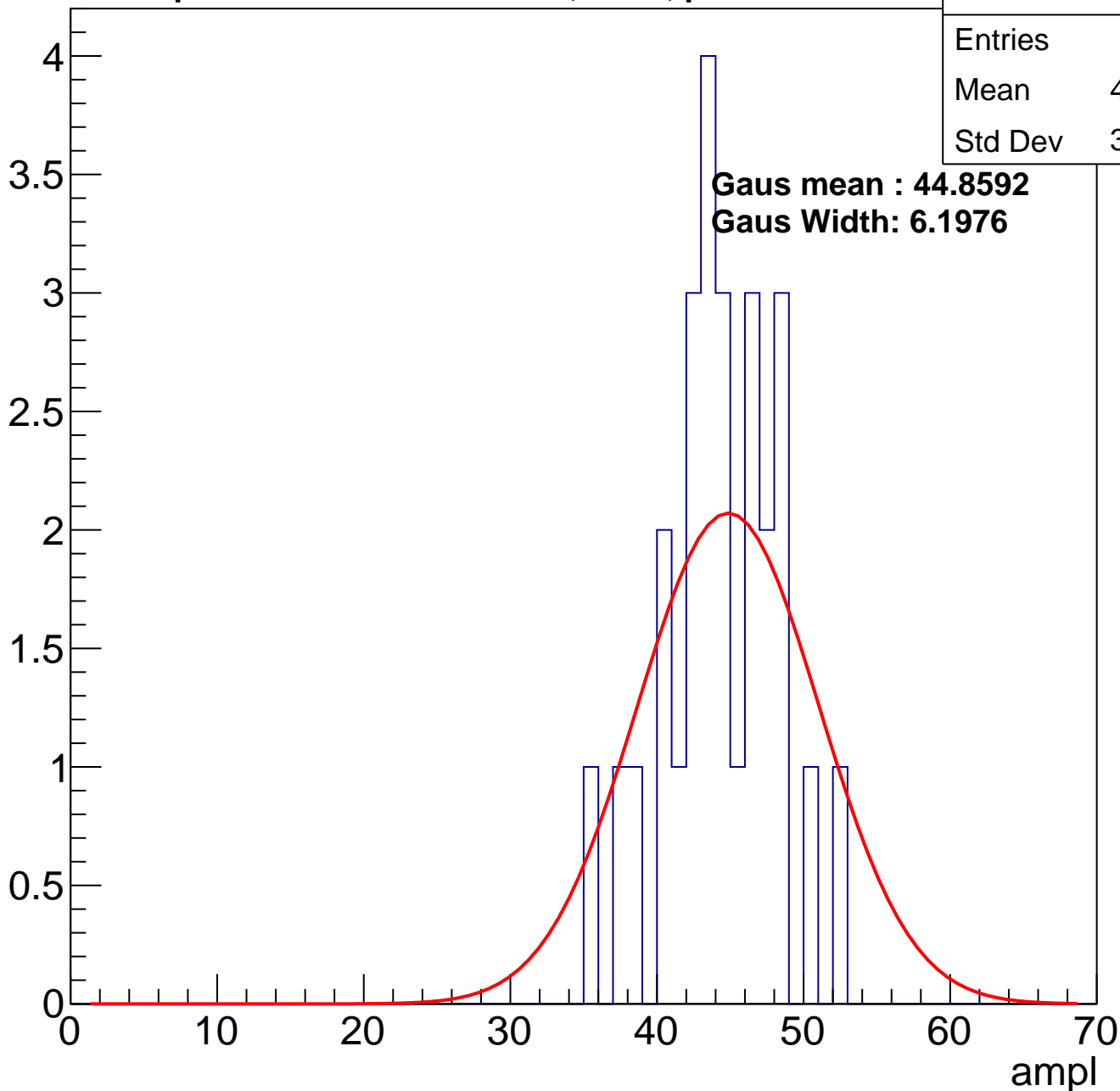
ampl



B1L103S, U15-ch15, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

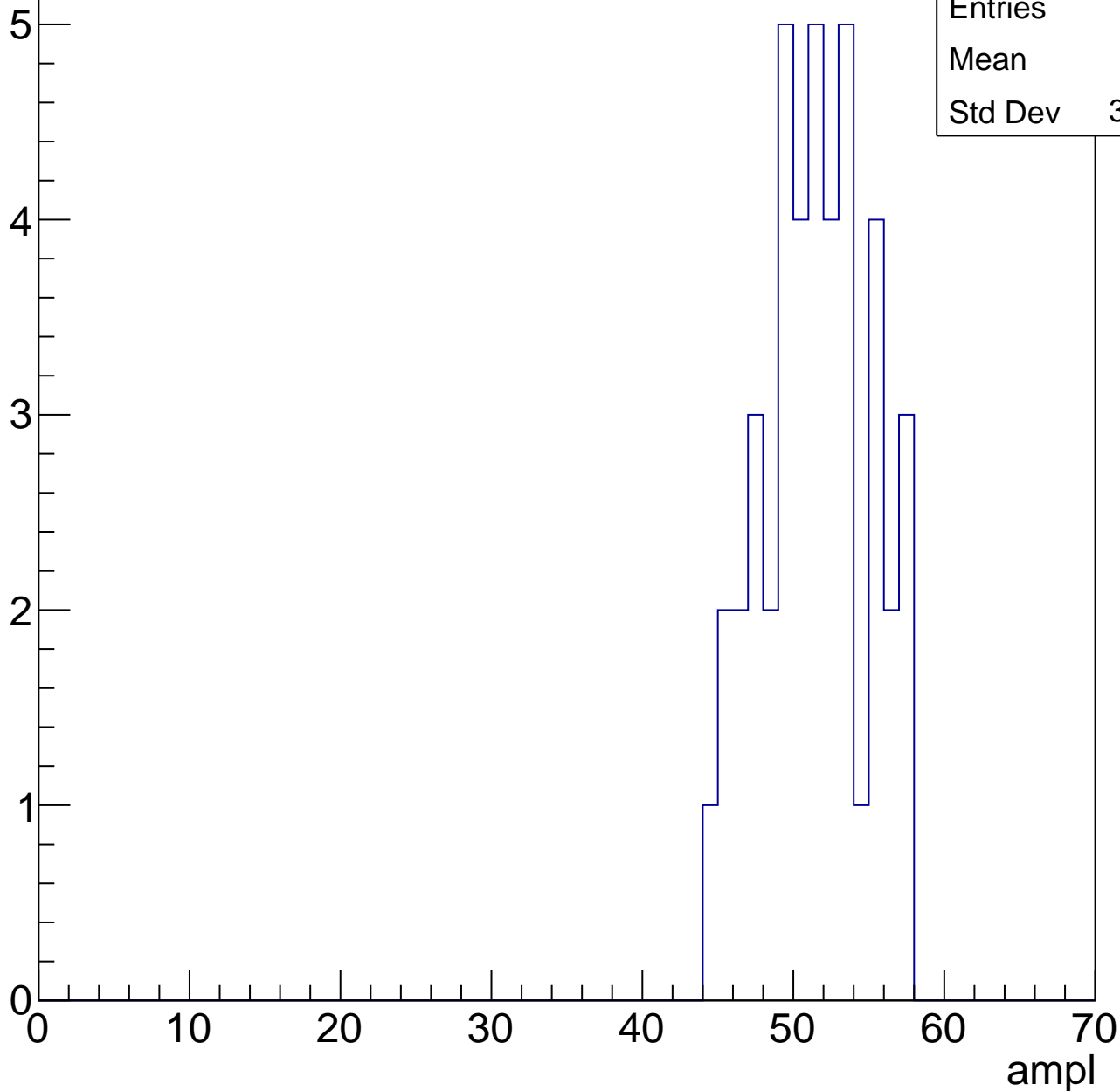


B1L103S, U15-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

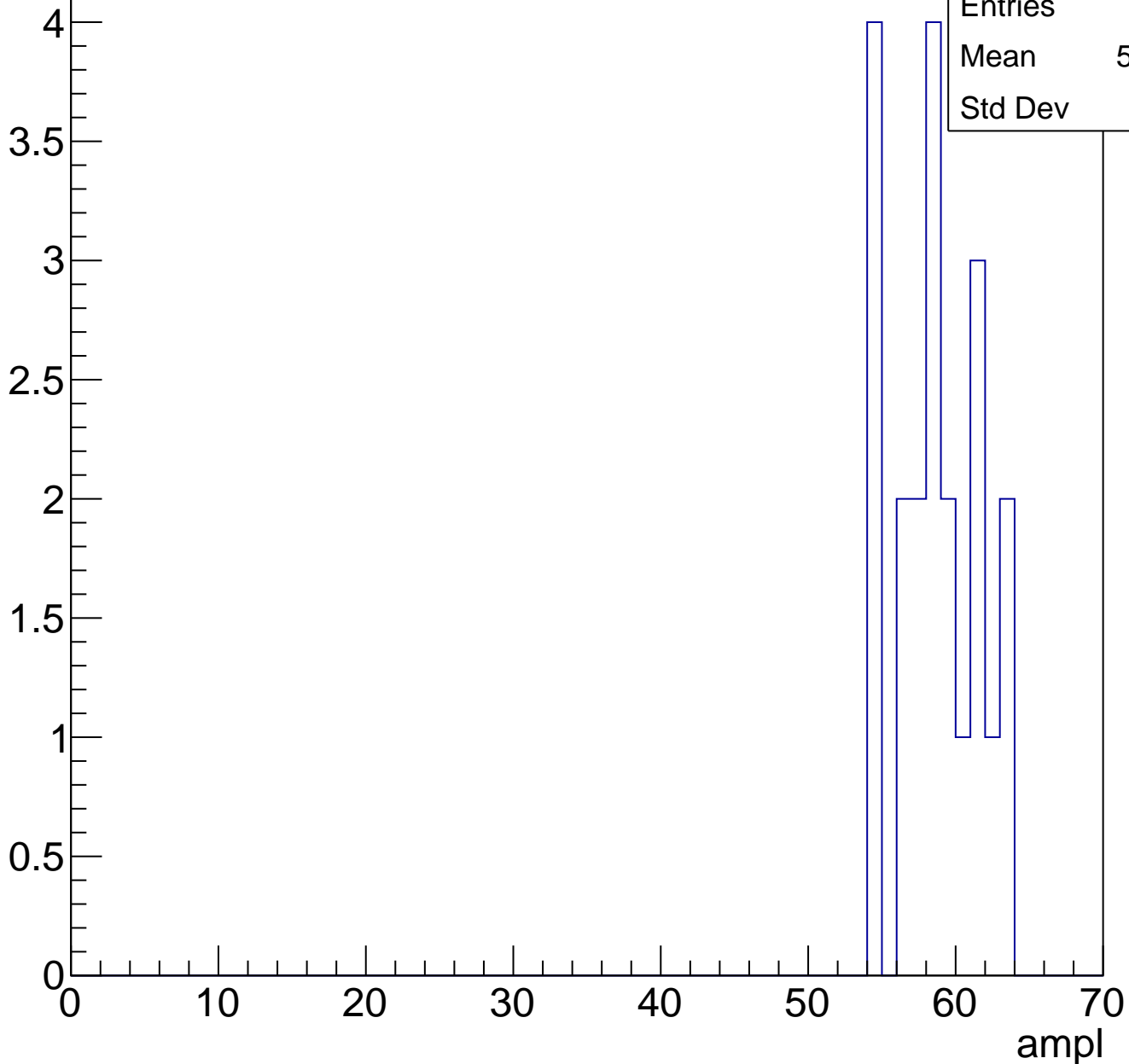
Entries	43
Mean	51
Std Dev	3.464



B1L103S, U15-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

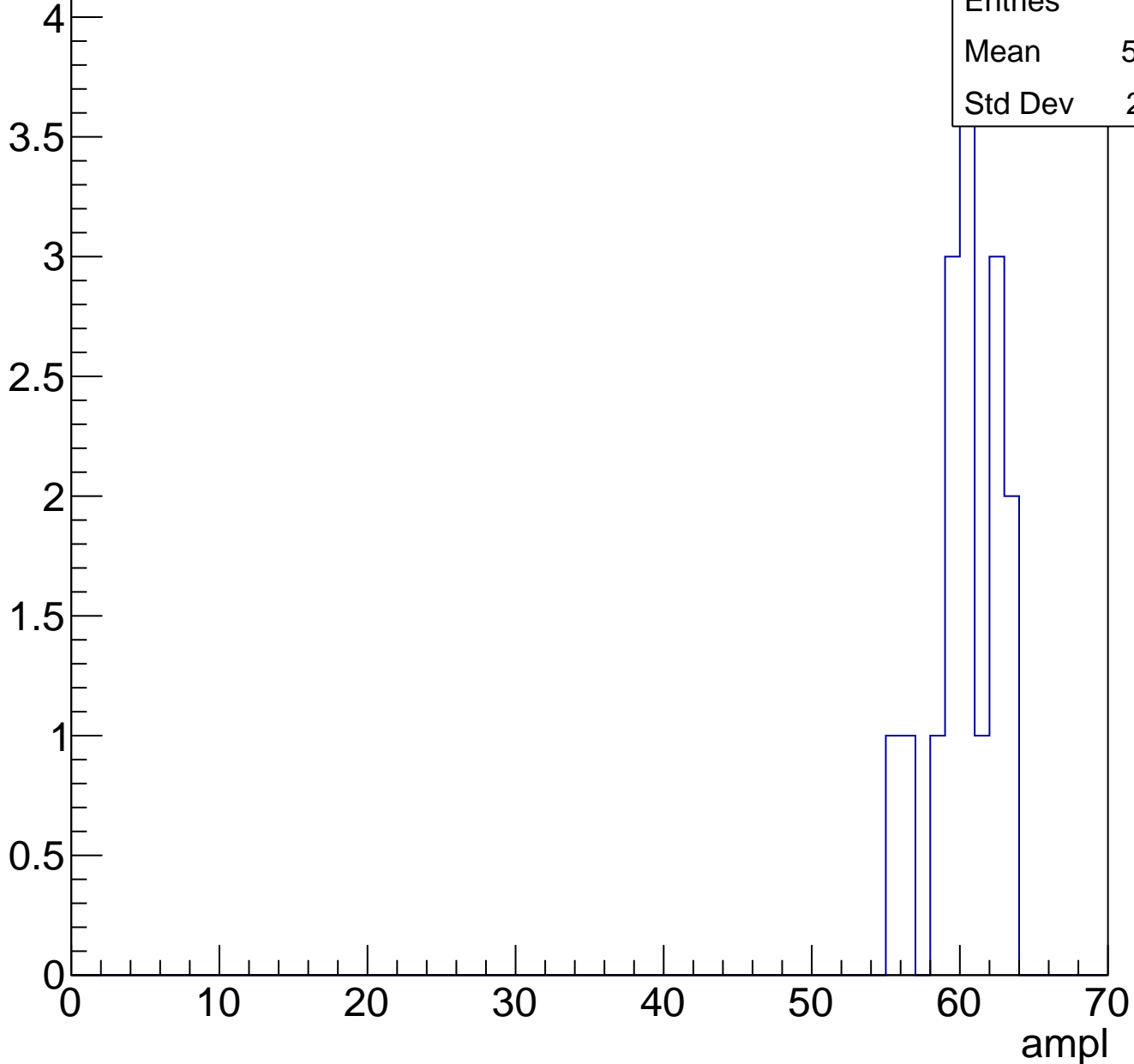
Entry



B1L103S, U15-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

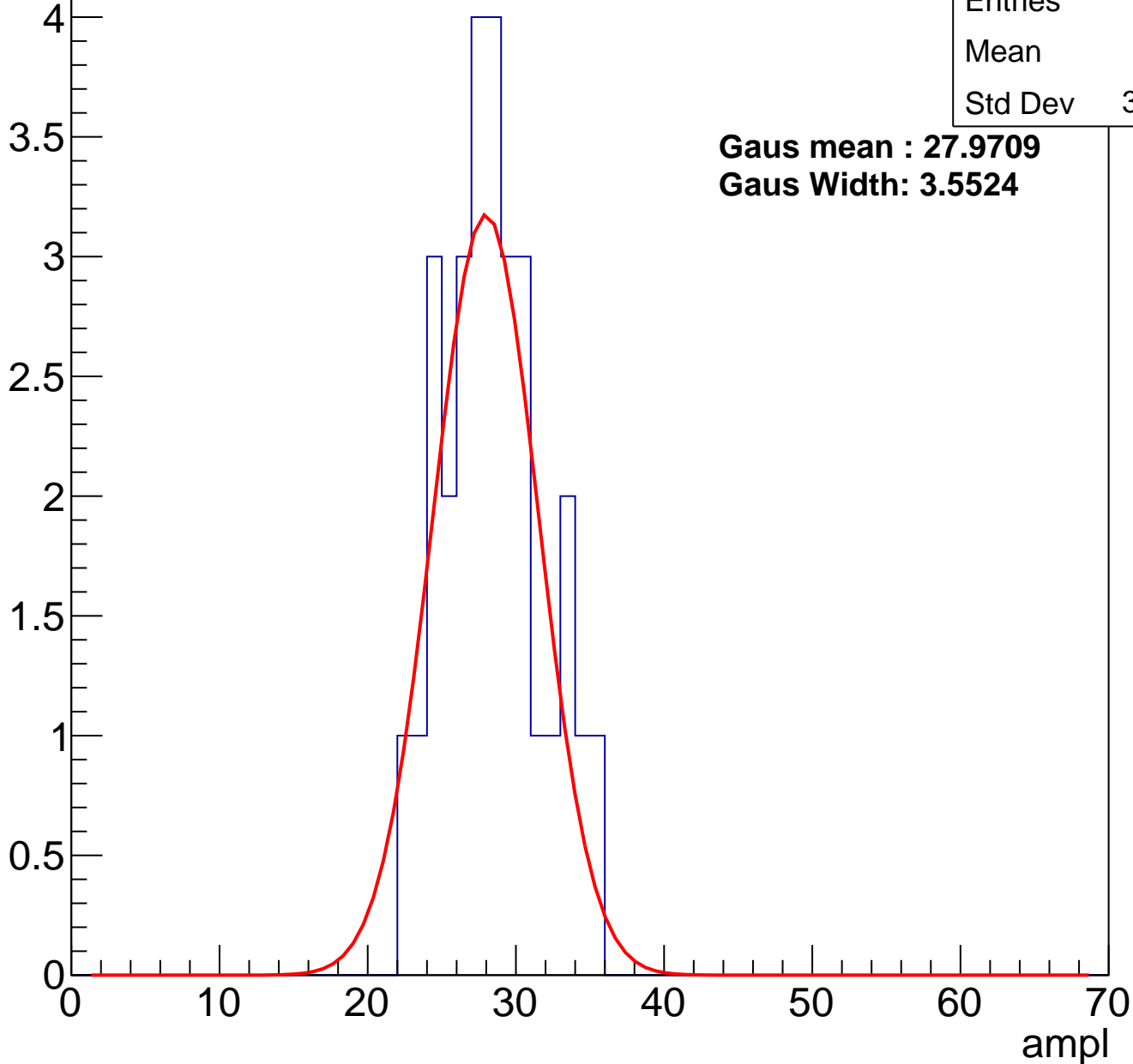


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch16, adc0

calib_packv5_042523_0143.root, FC#7, port C2

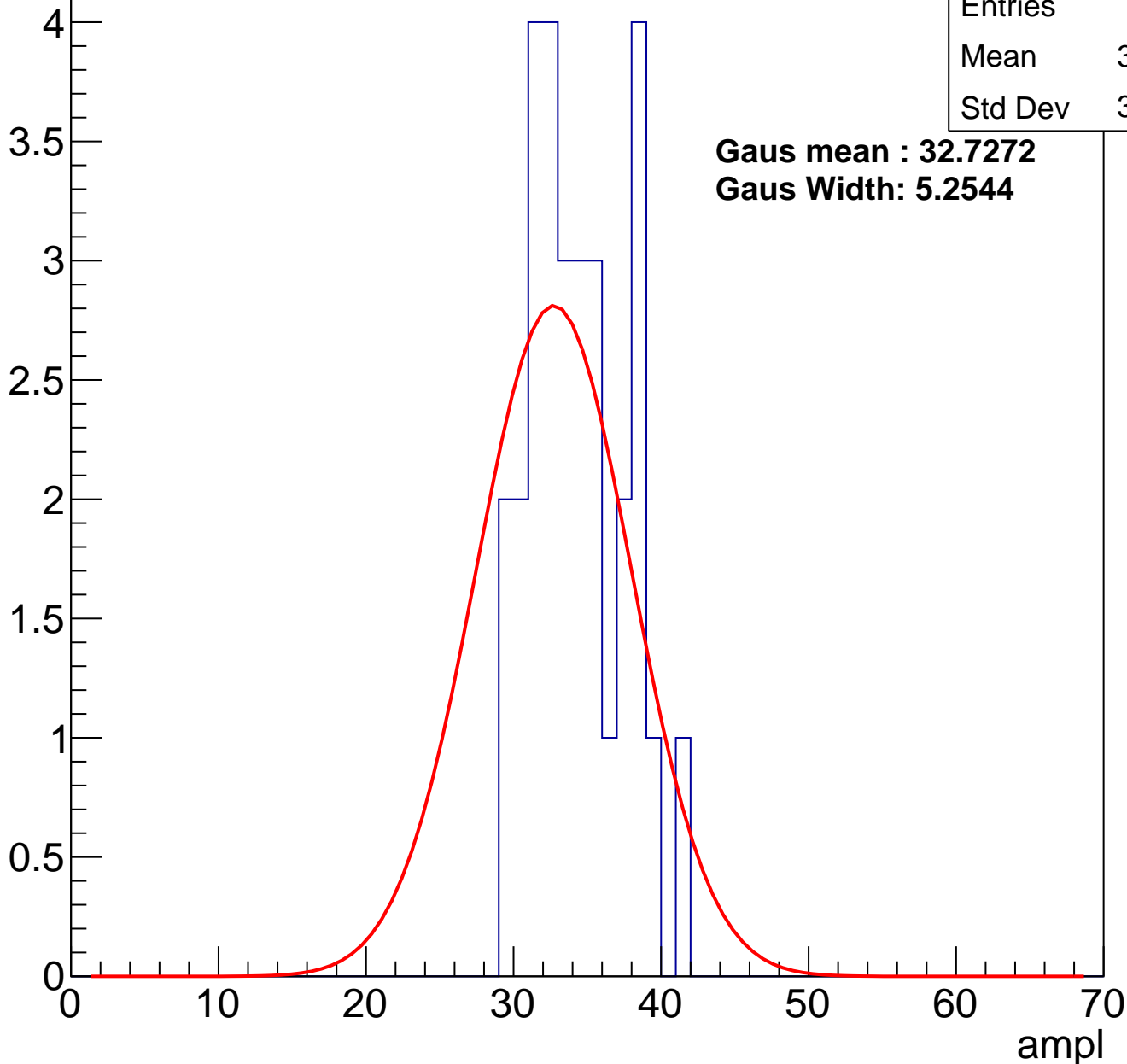
Entry



B1L103S, U15-ch16, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch16, adc2

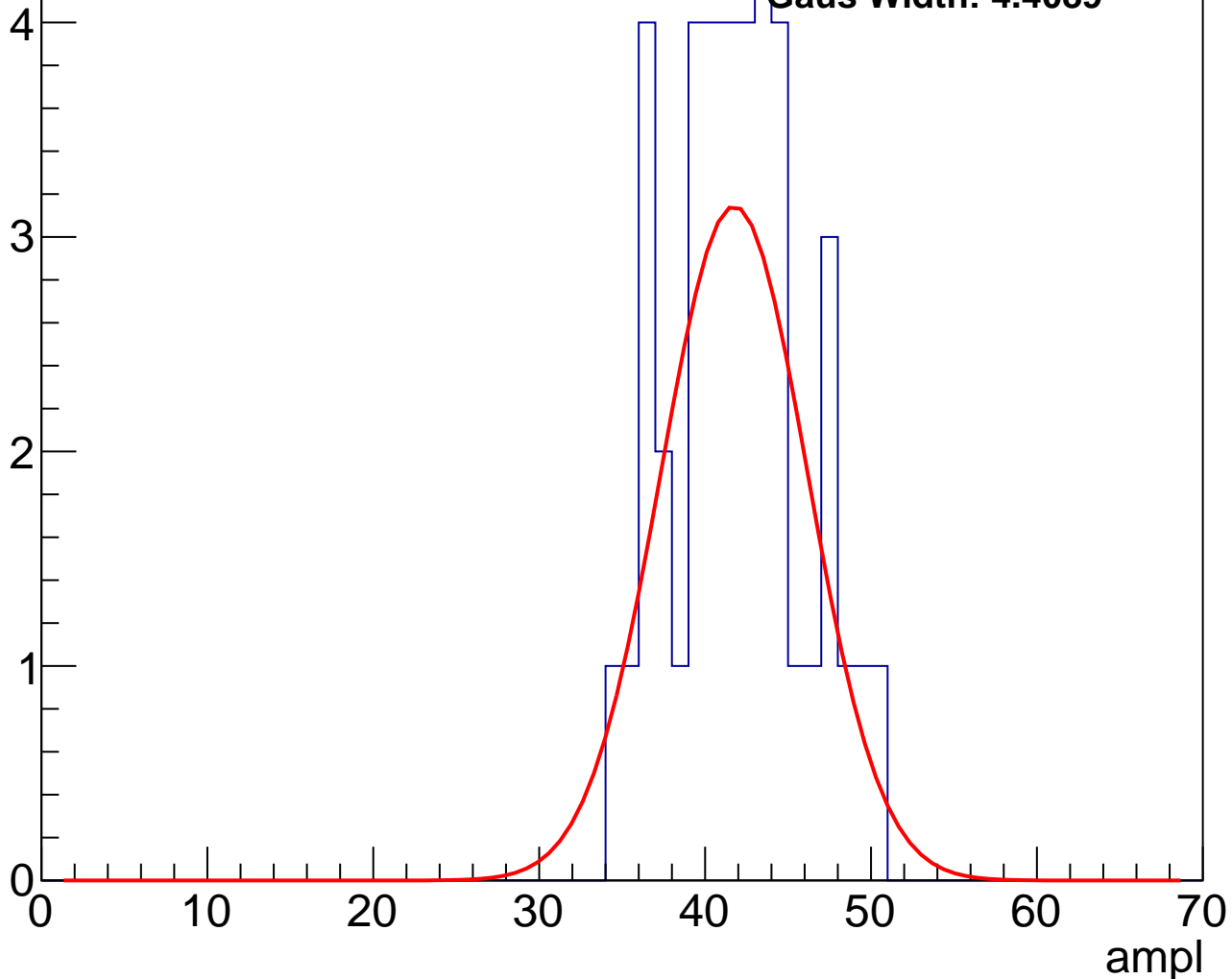
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	41.5
Std Dev	3.899

Gaus mean : 41.7543

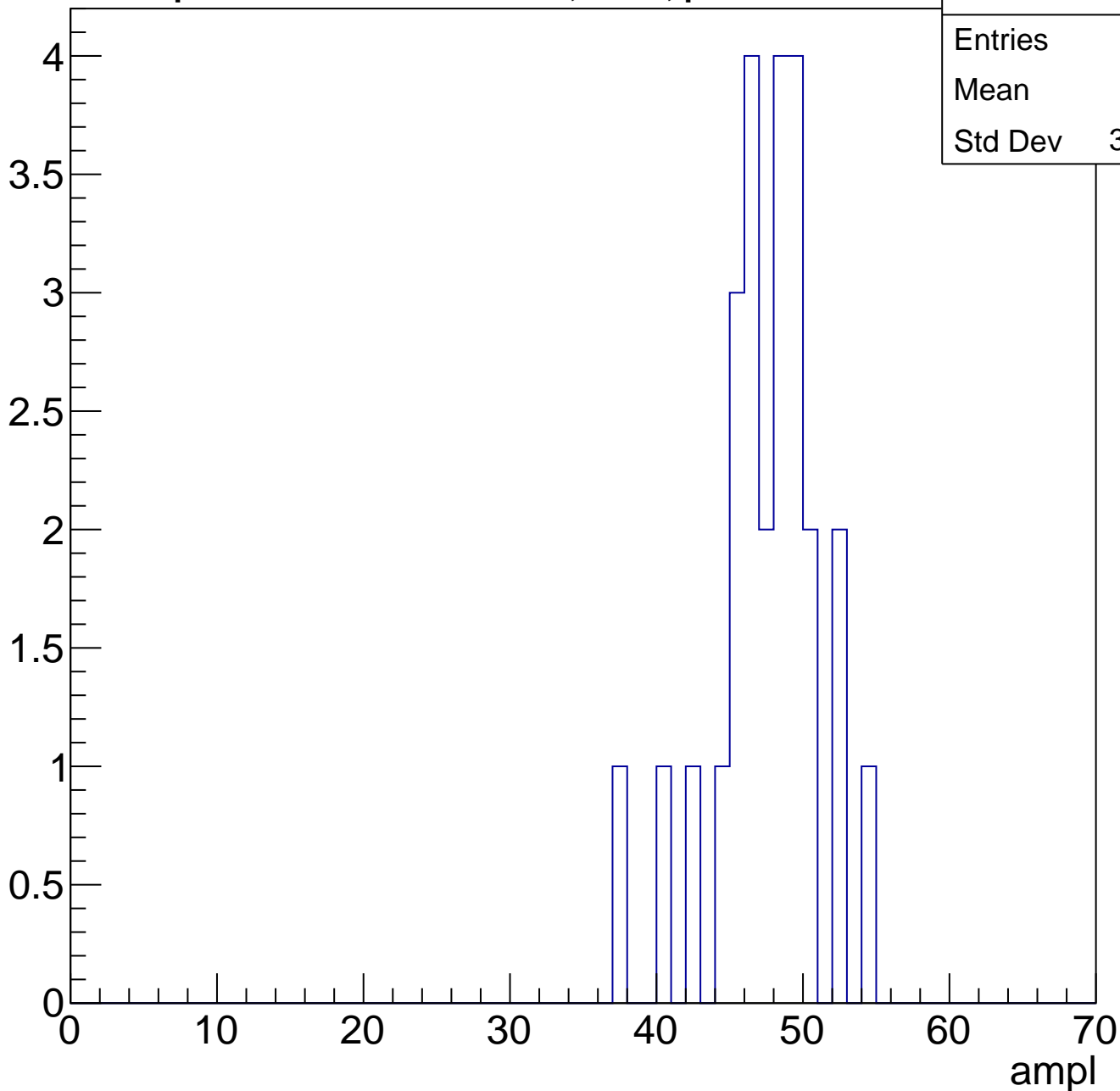
Gaus Width: 4.4089



B1L103S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



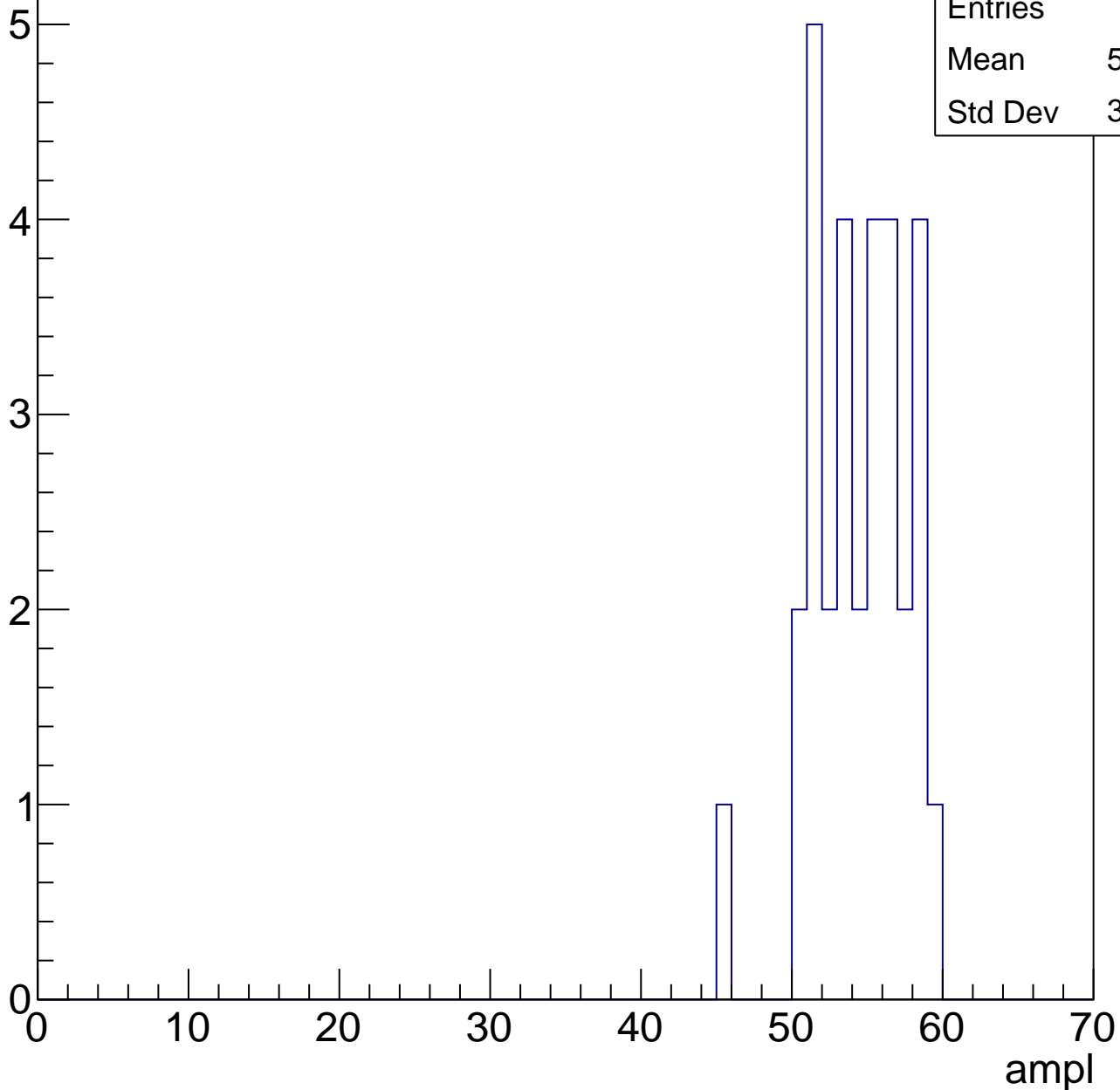
Entries	26
Mean	47
Std Dev	3.595

B1L103S, U15-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

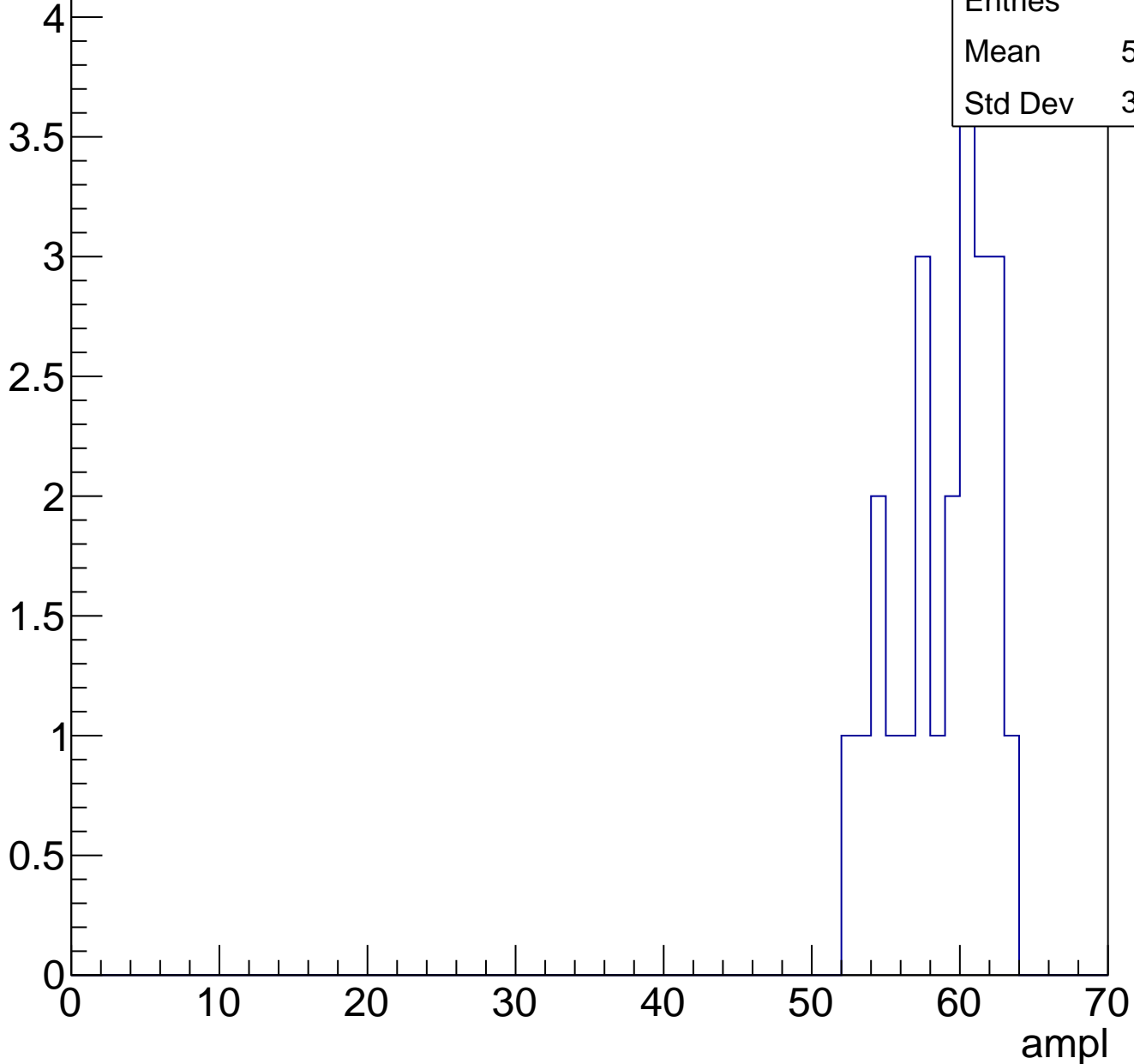
Entries	31
Mean	53.97
Std Dev	3.095



B1L103S, U15-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

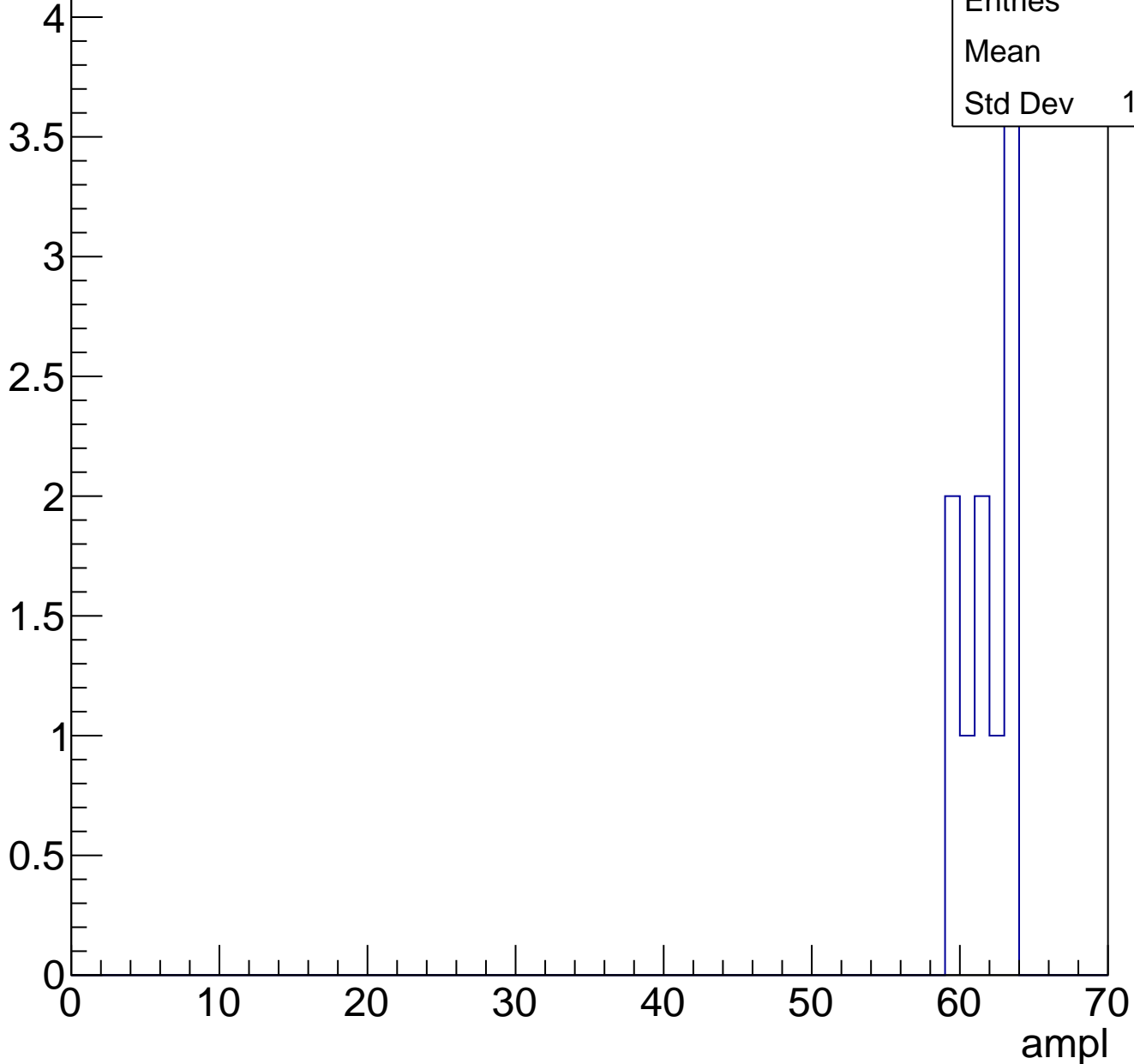
Entry



B1L103S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	61.4
Std Dev	1.562

B1L103S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch17, adc0

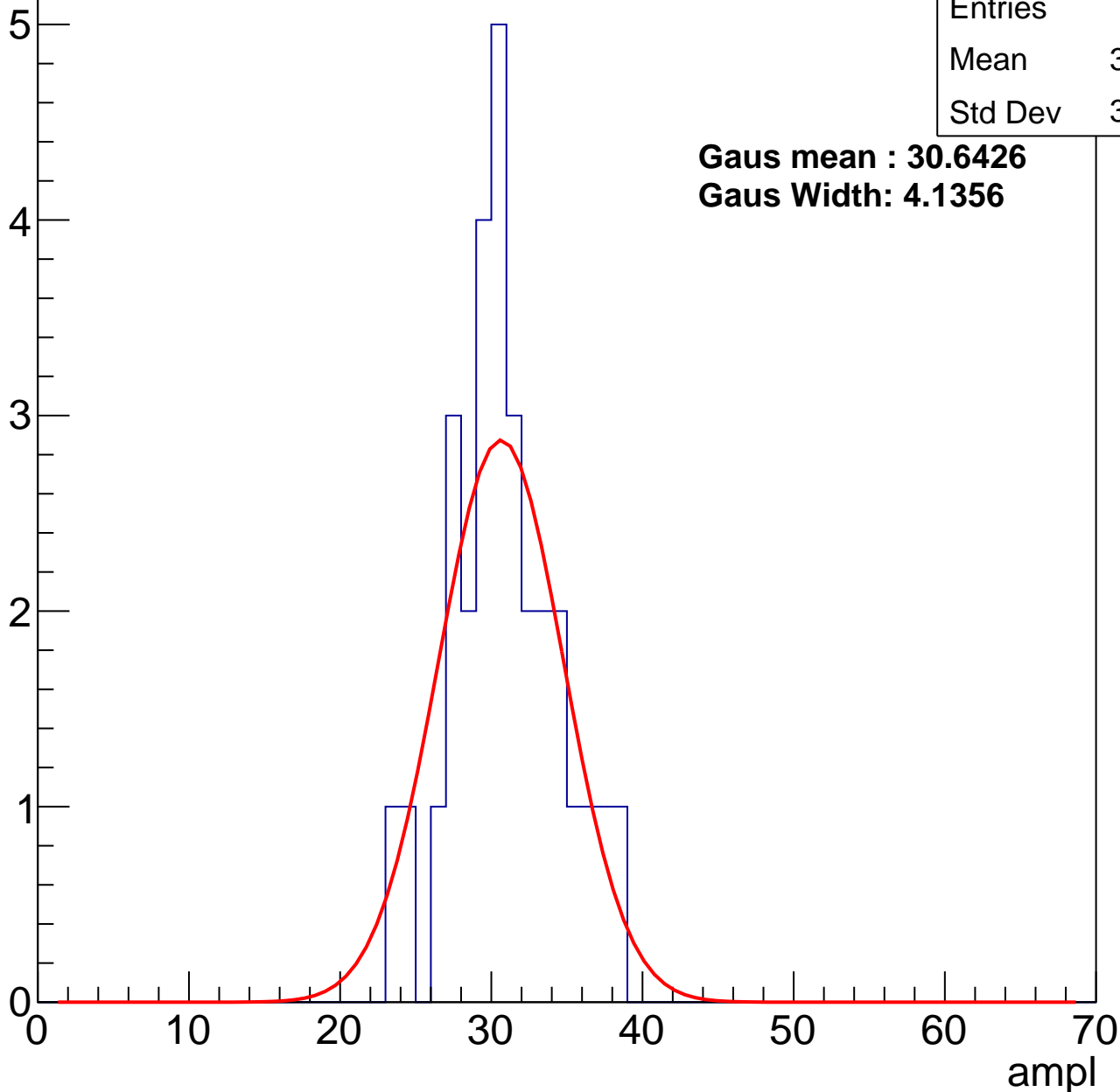
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	30.43
Std Dev	3.509

Gaus mean : 30.6426

Gaus Width: 4.1356



B1L103S, U15-ch17, adc1

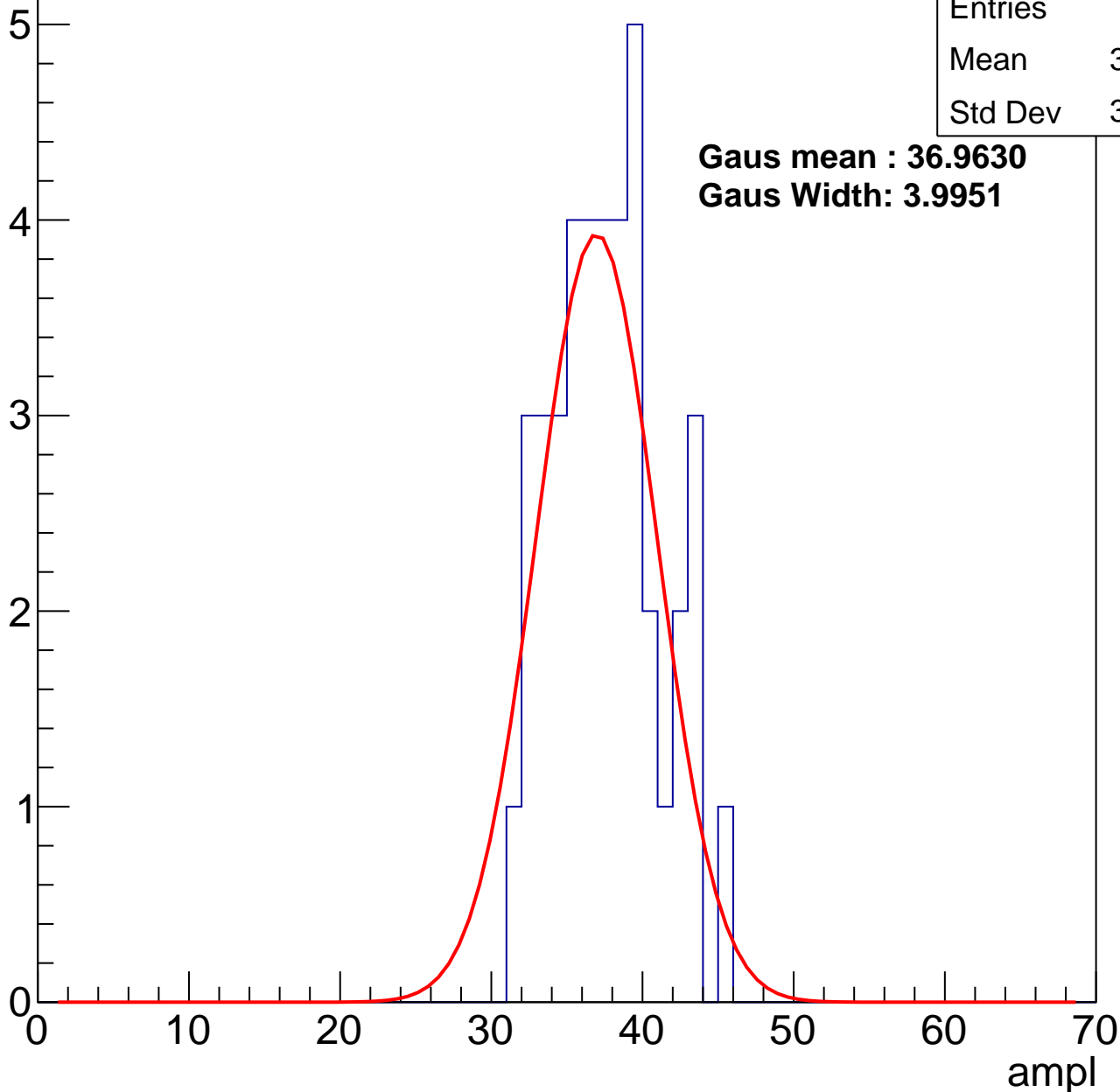
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	37.15
Std Dev	3.482

Gaus mean : 36.9630

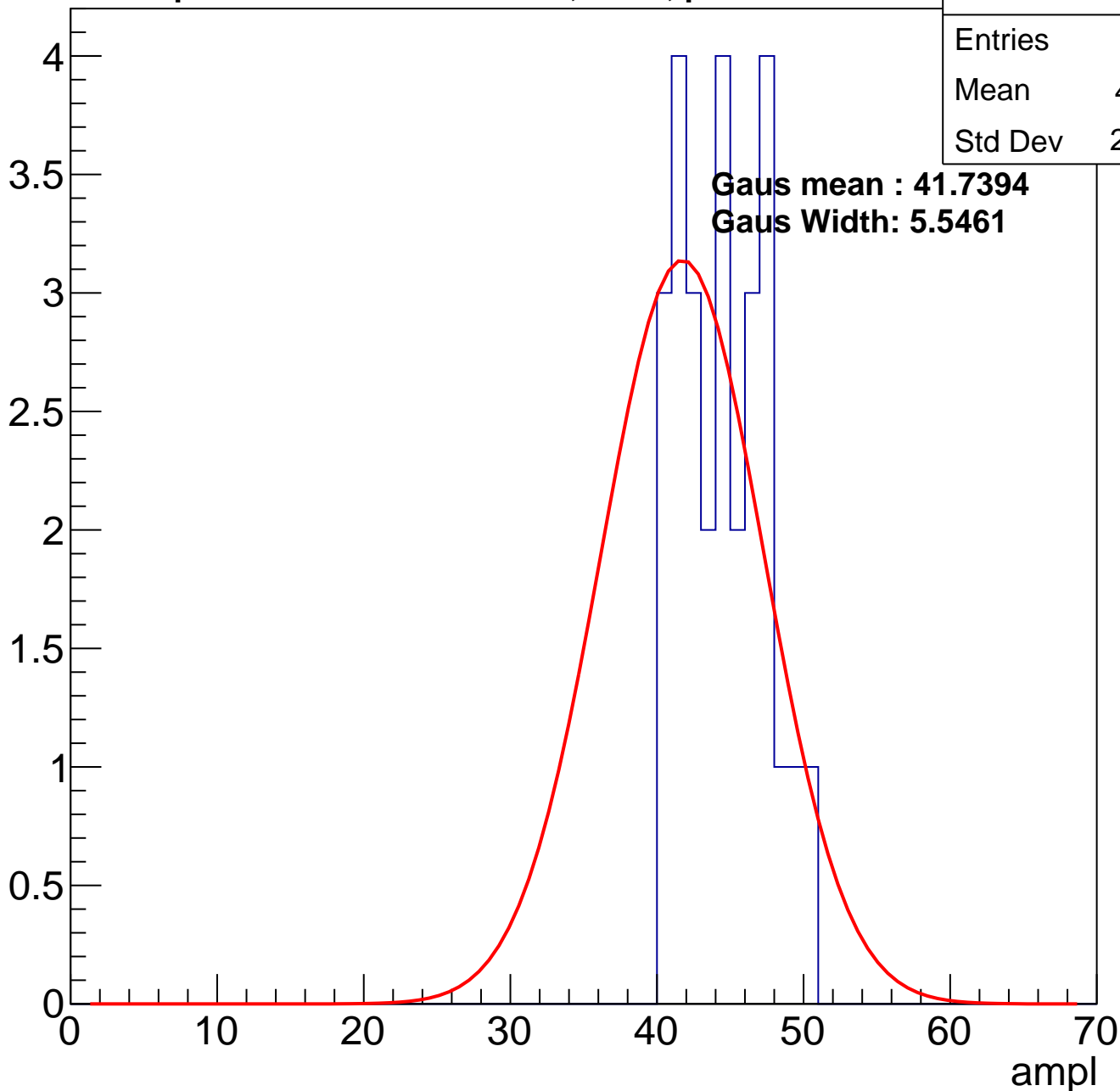
Gaus Width: 3.9951



B1L103S, U15-ch17, adc2

calib_packv5_042523_0143.root, FC#7, port C2

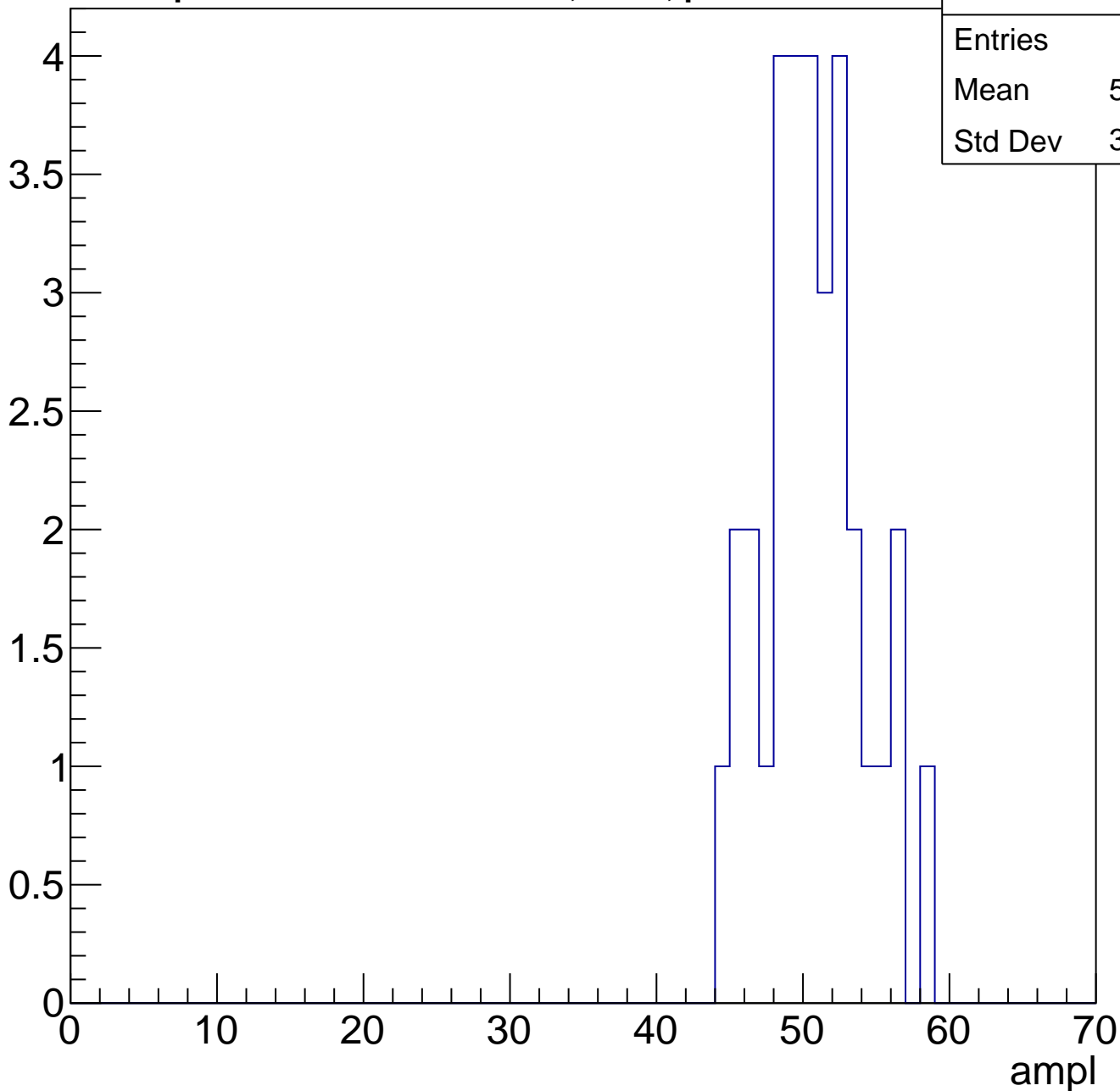
Entry



B1L103S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

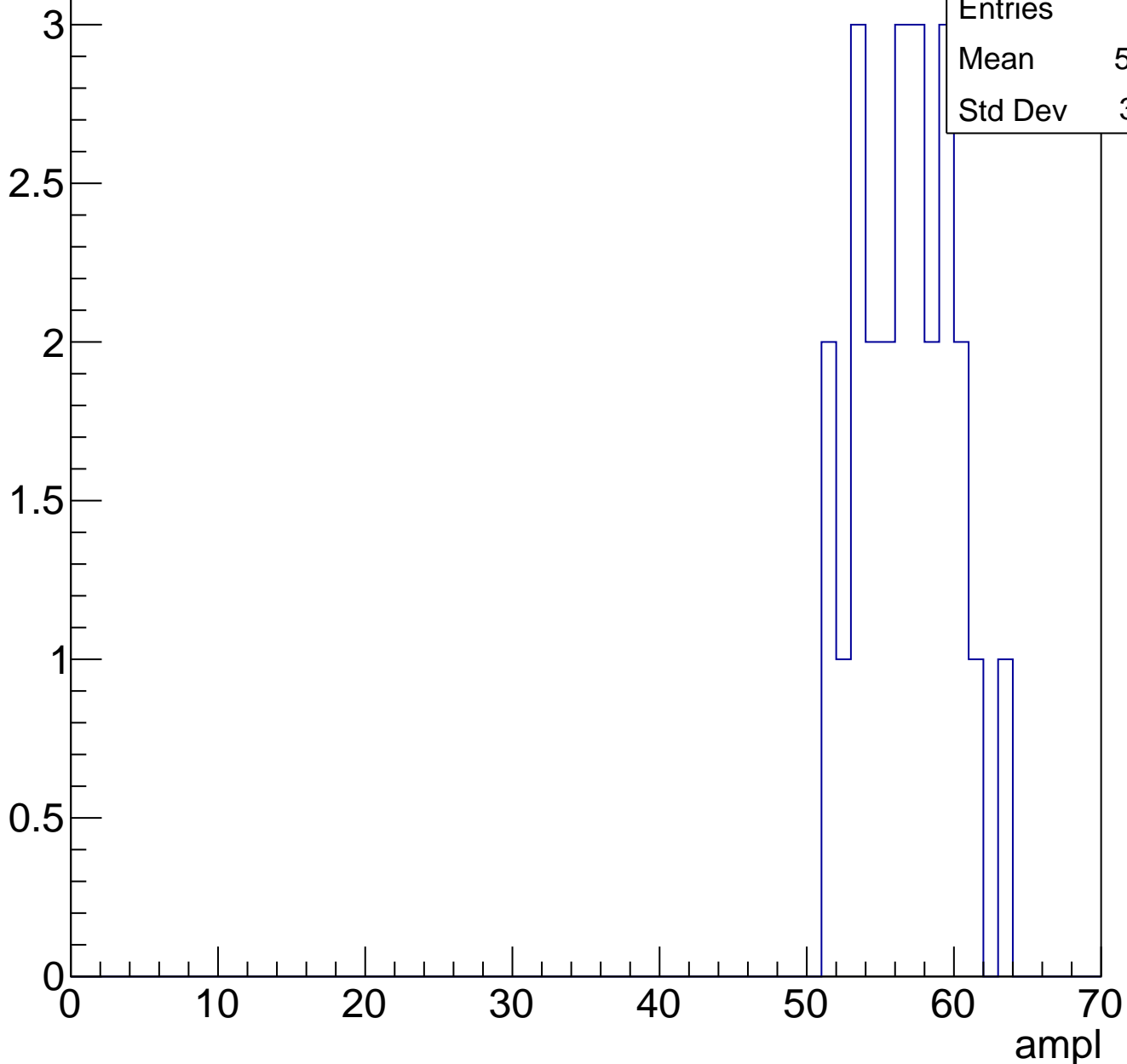
Entry



B1L103S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

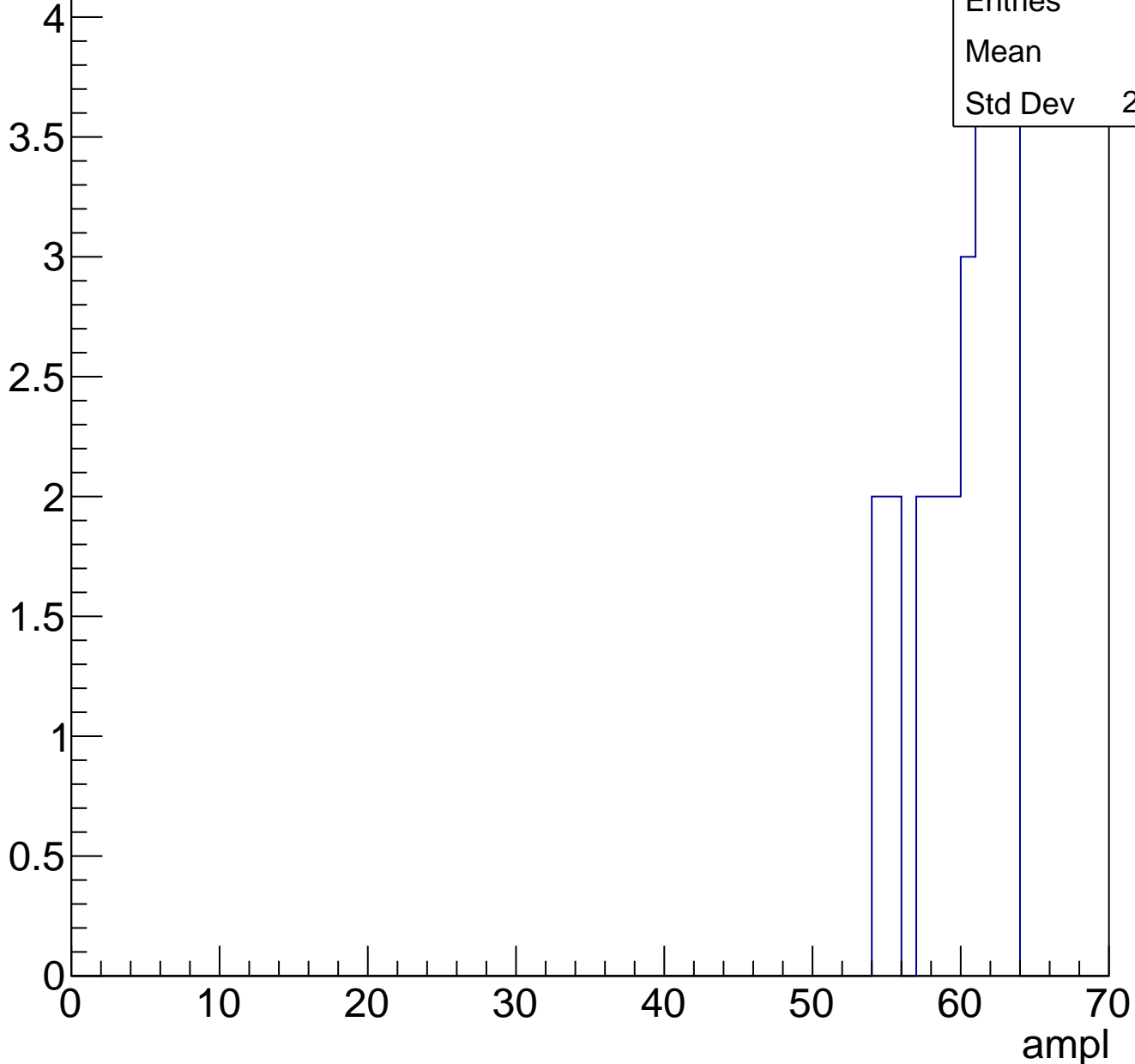
Entry



B1L103S, U15-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	25
Mean	59.6
Std Dev	2.843

B1L103S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch18, adc0

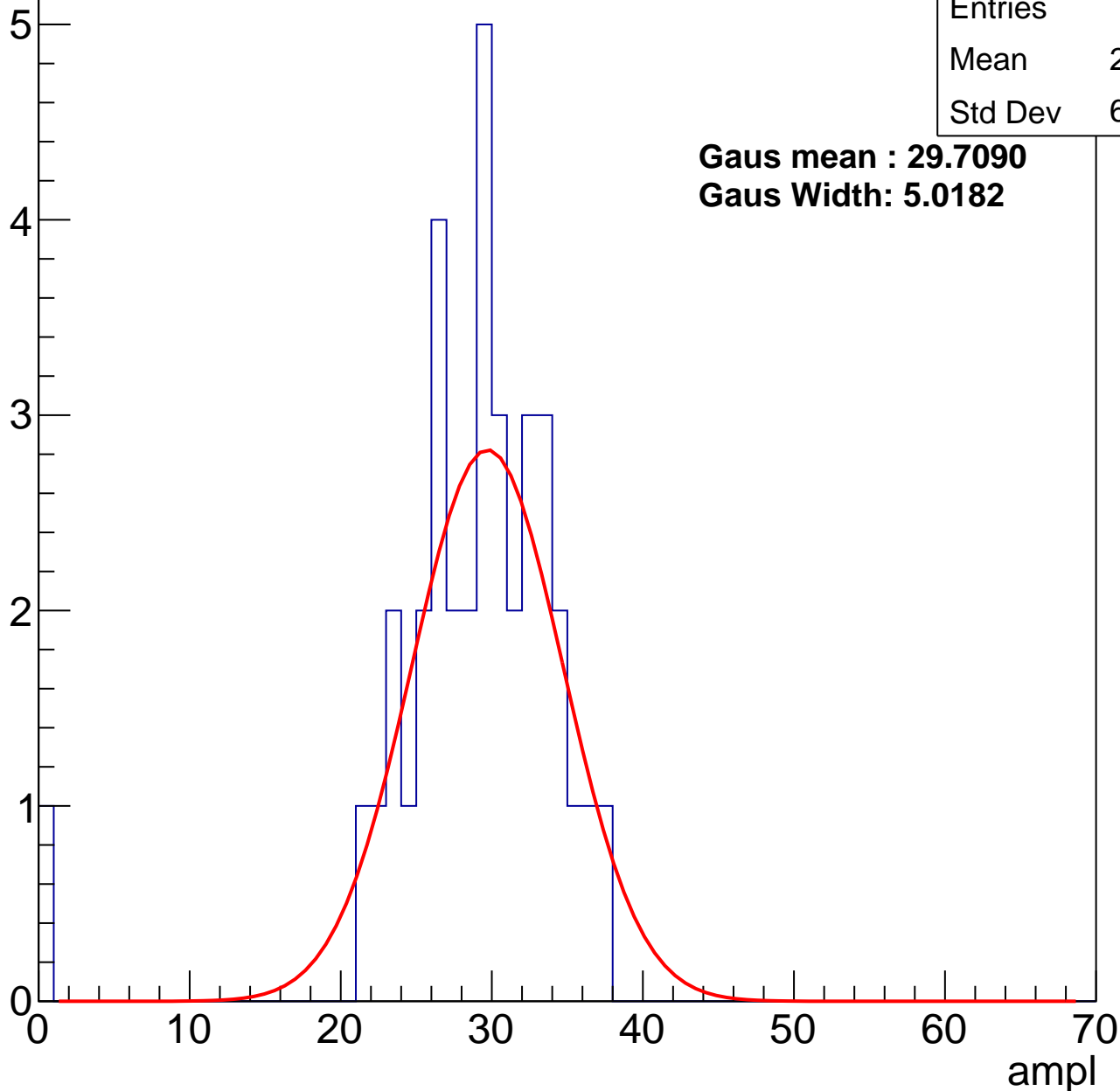
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	28.24
Std Dev	6.135

Gaus mean : 29.7090

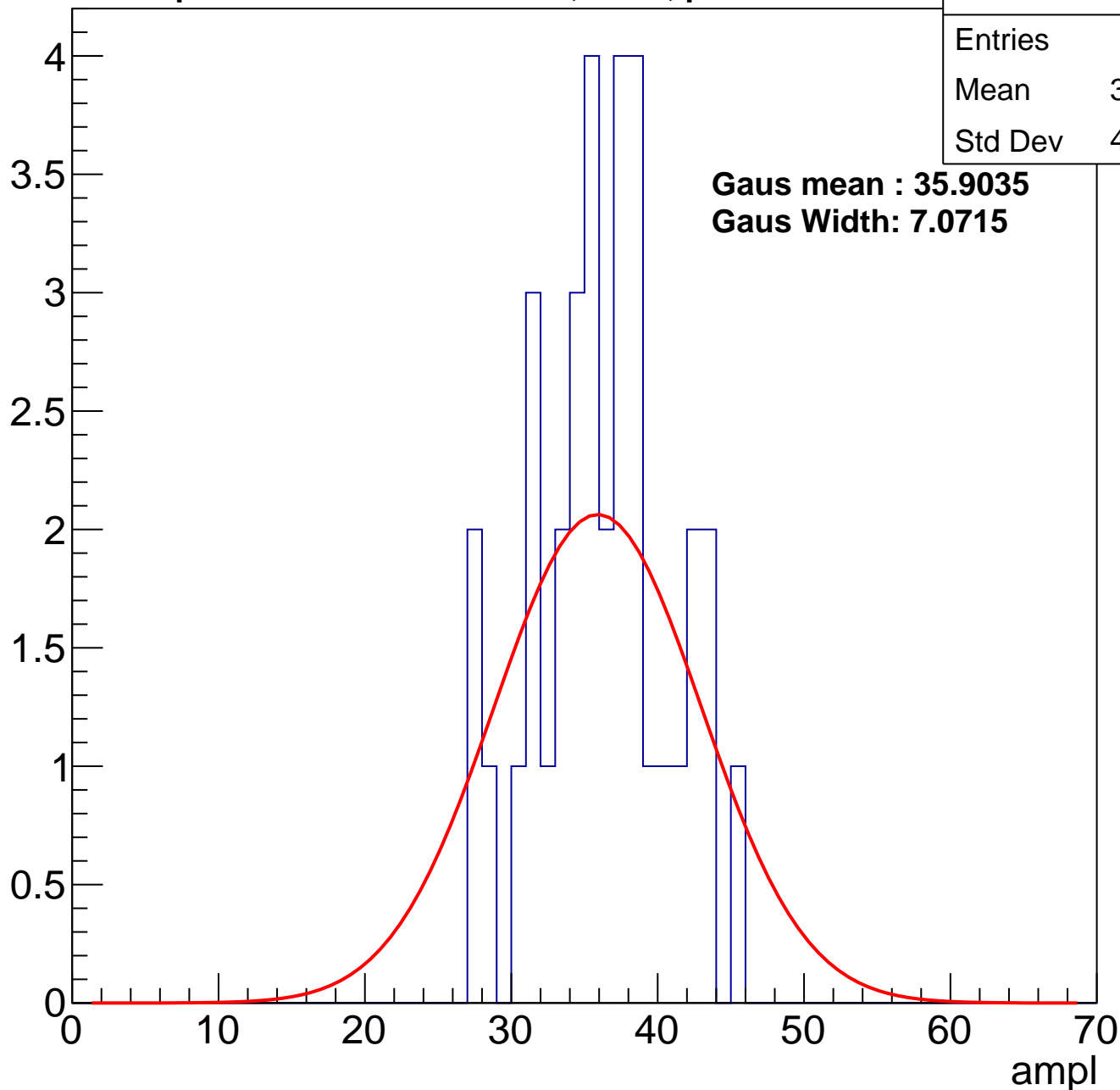
Gaus Width: 5.0182



B1L103S, U15-ch18, adc1

calib_packv5_042523_0143.root, FC#7, port C2

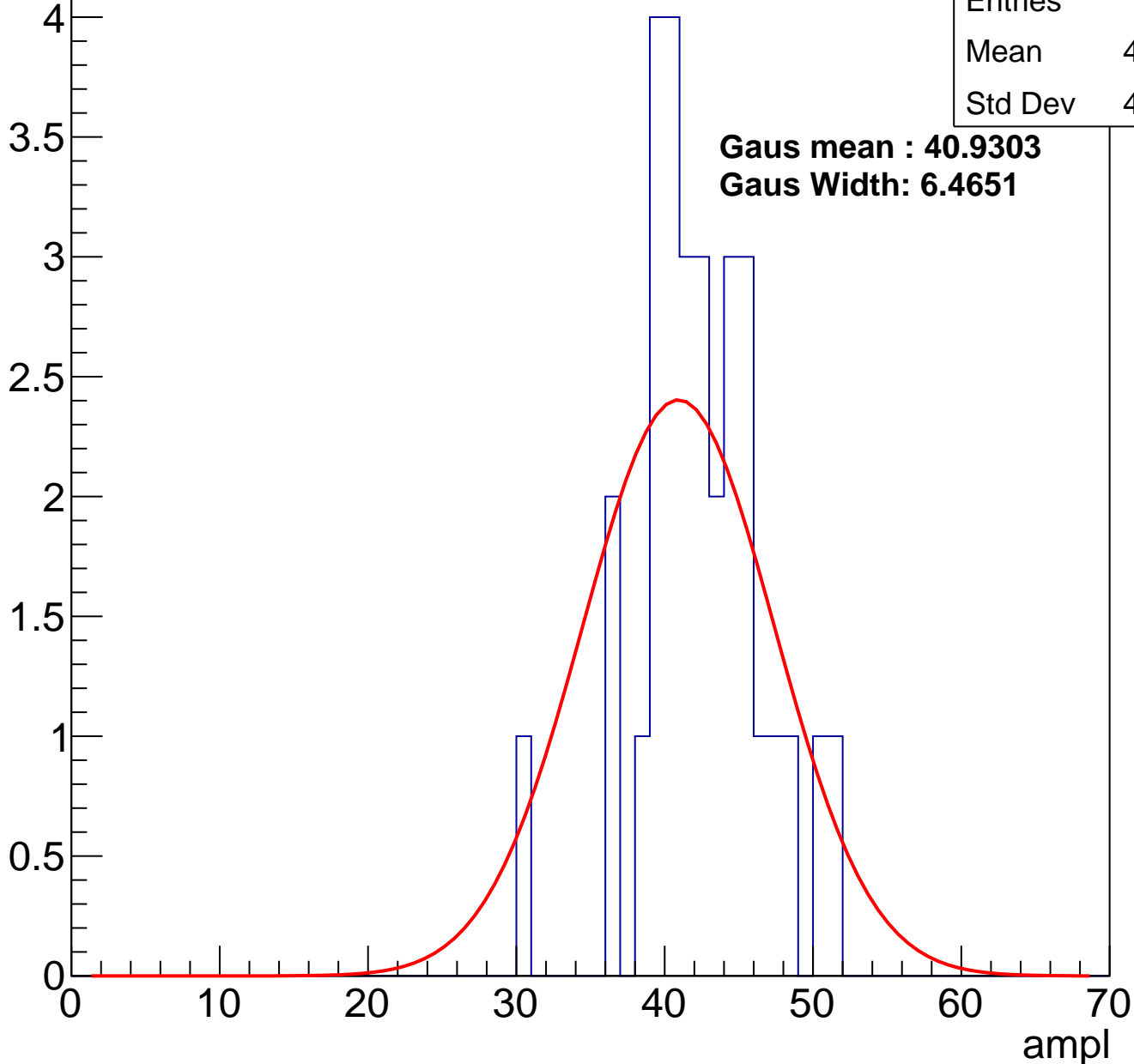
Entry



B1L103S, U15-ch18, adc2

calib_packv5_042523_0143.root, FC#7, port C2

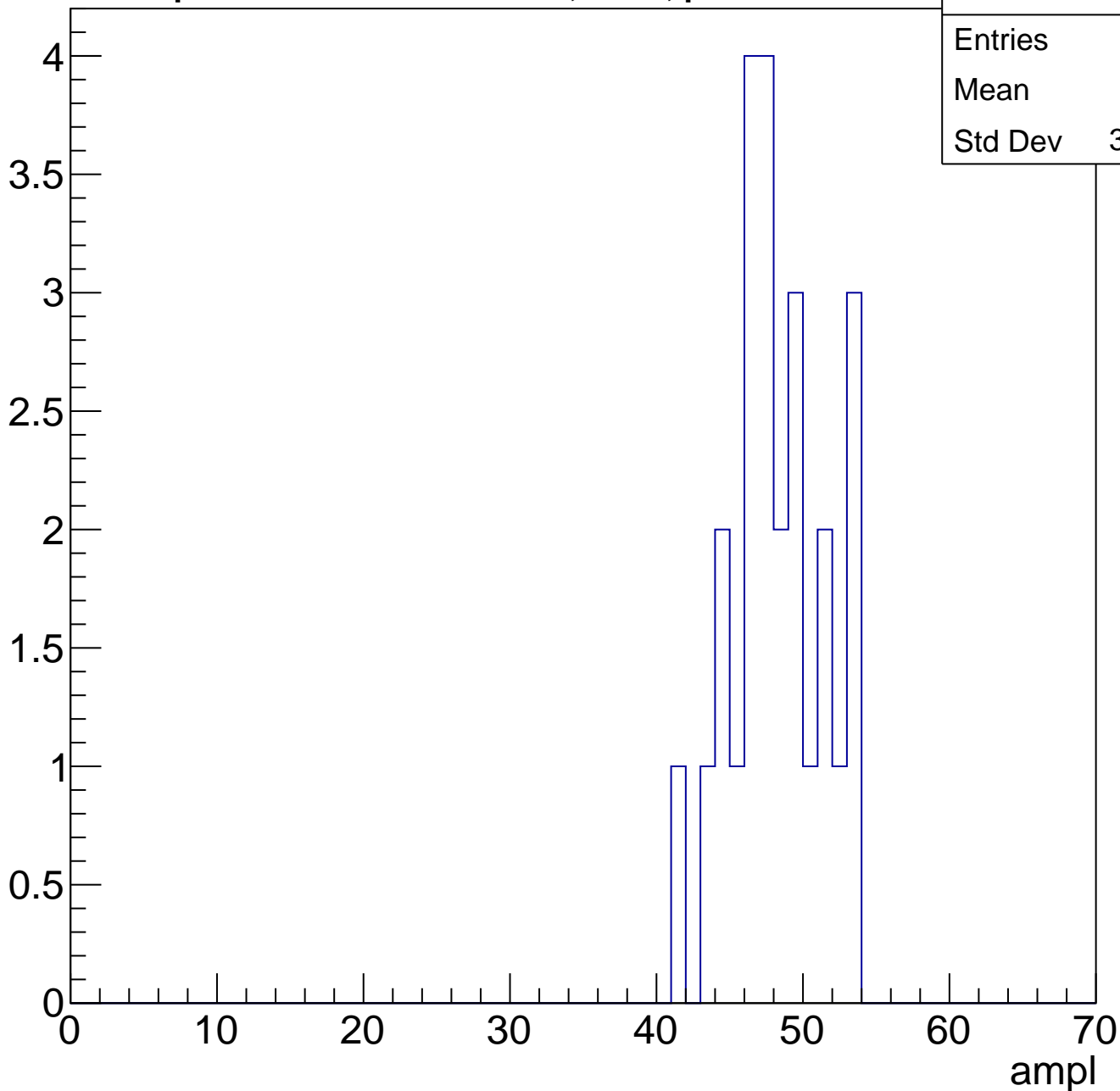
Entry



B1L103S, U15-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



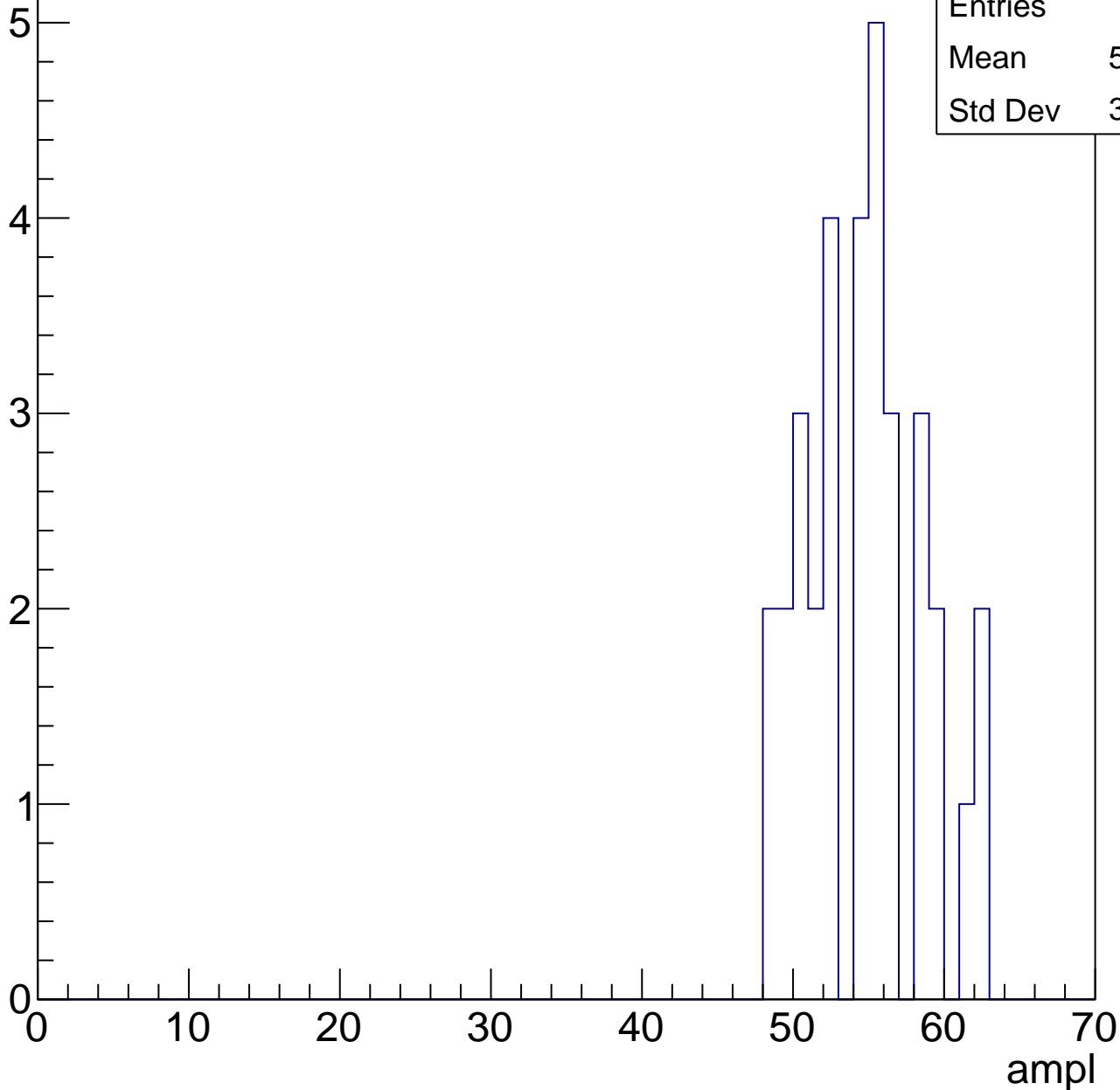
Entries	25
Mean	47.8
Std Dev	3.162

B1L103S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	54.24
Std Dev	3.869

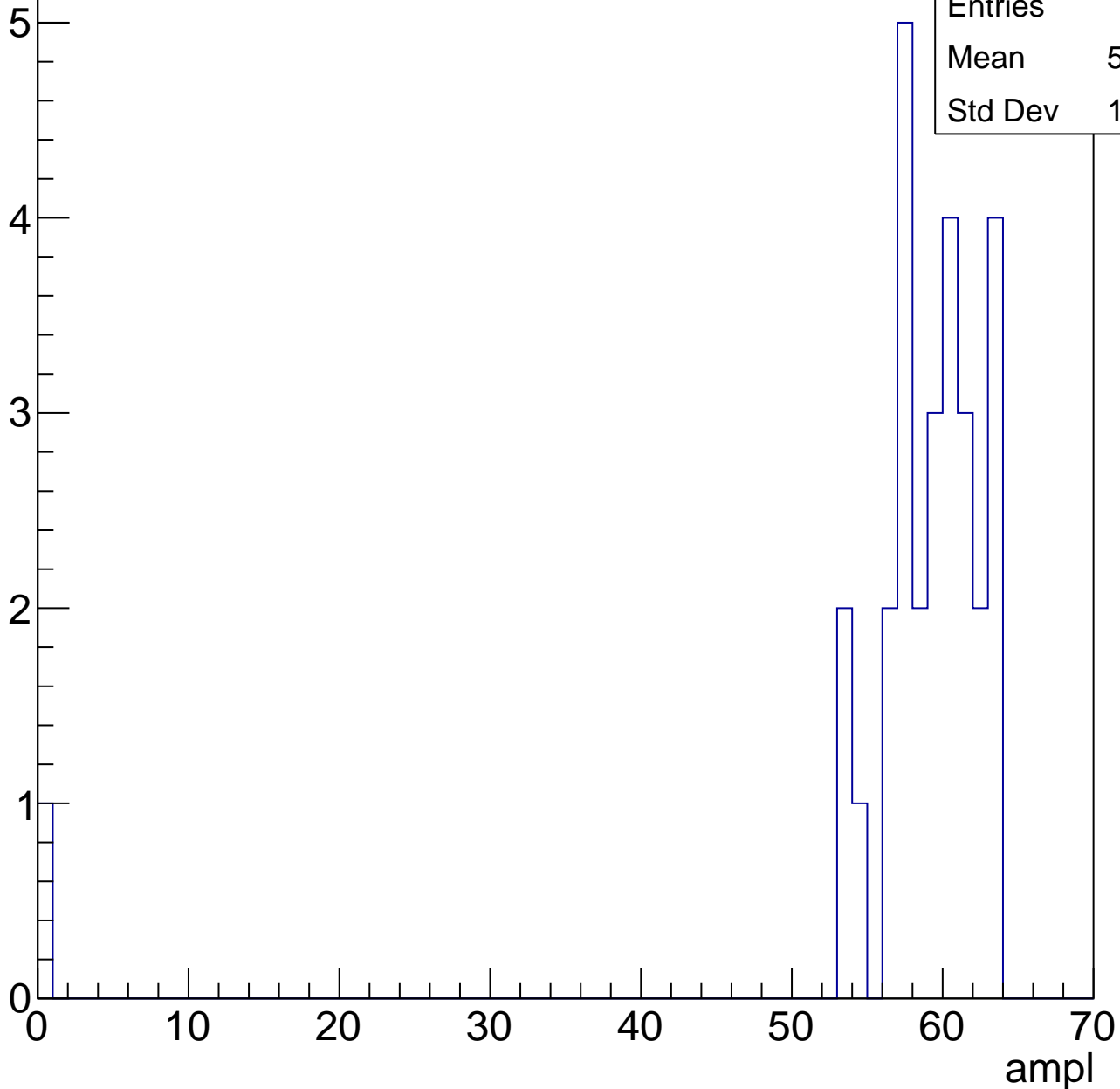


B1L103S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	56.86
Std Dev	11.12



B1L103S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

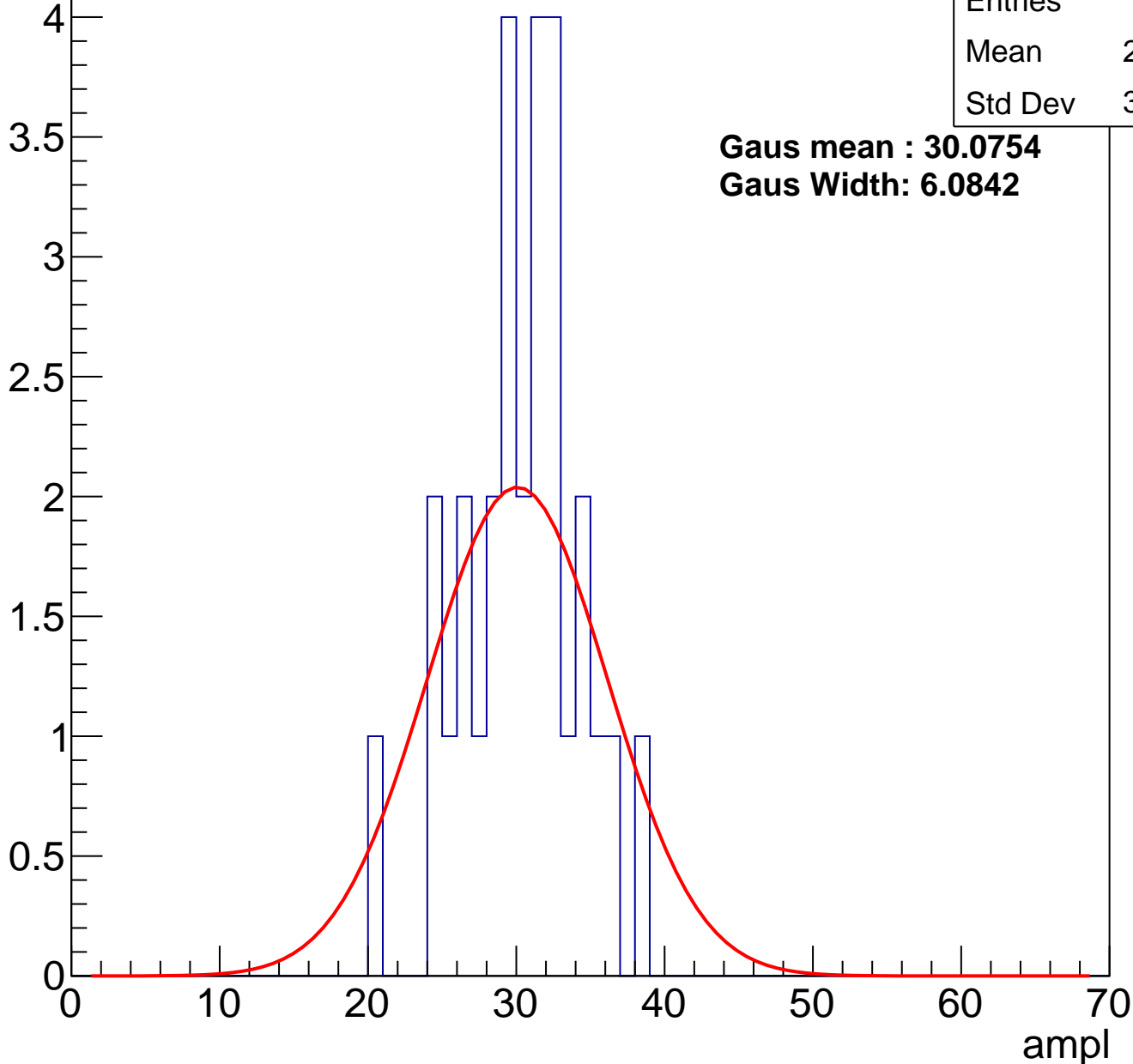
Entry



B1L103S, U15-ch19, adc0

calib_packv5_042523_0143.root, FC#7, port C2

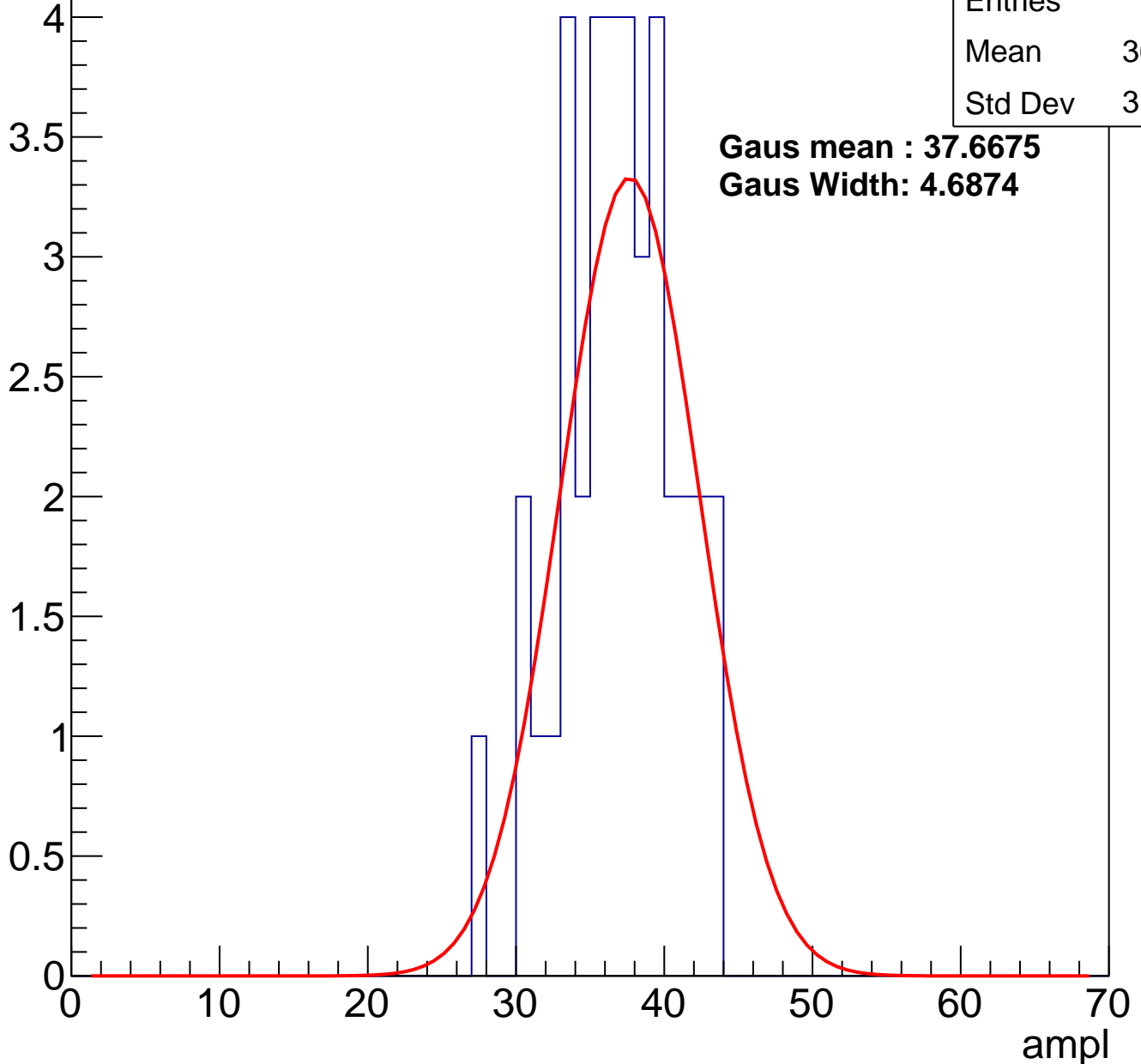
Entry



B1L103S, U15-ch19, adc1

calib_packv5_042523_0143.root, FC#7, port C2

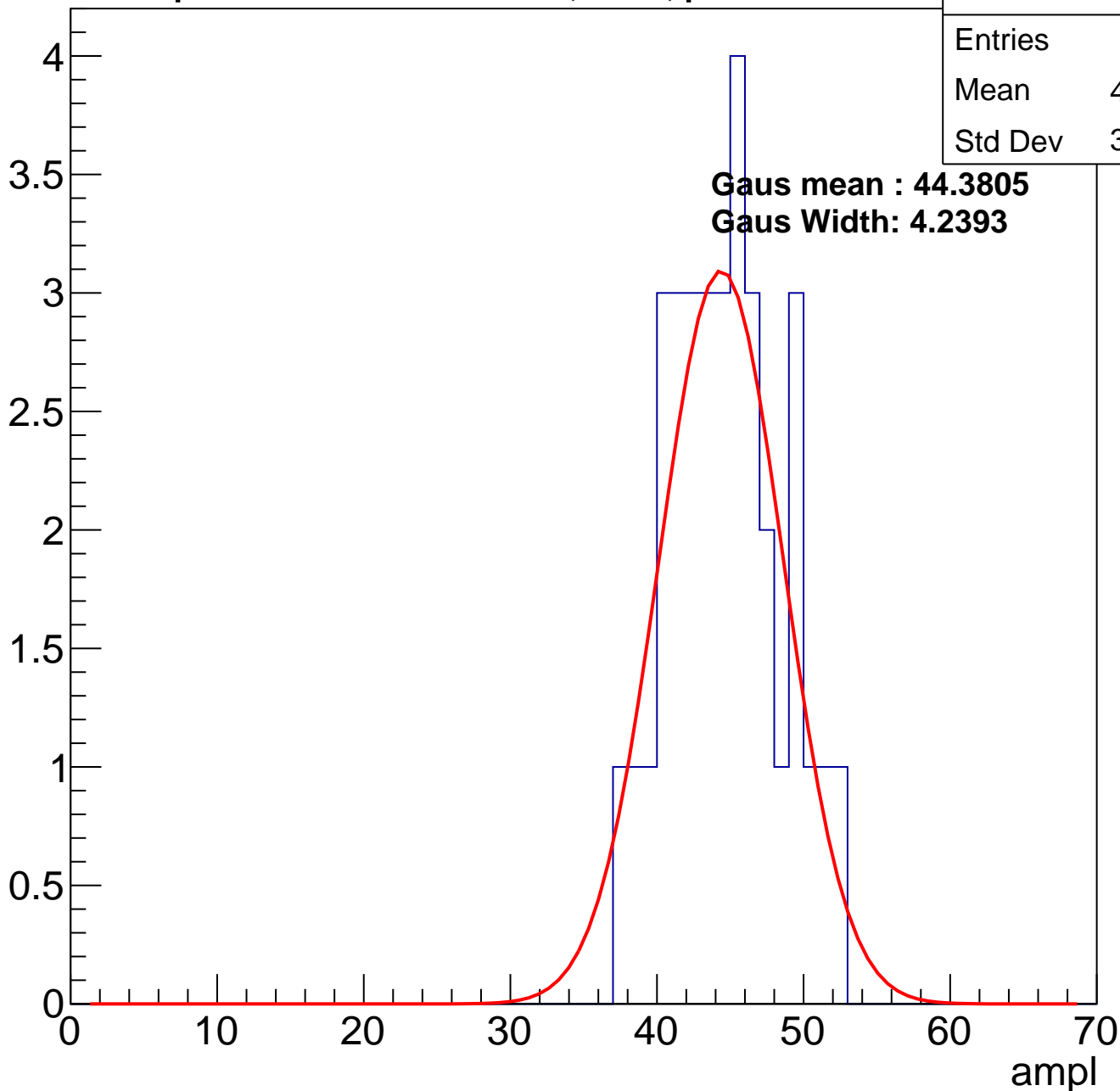
Entry



B1L103S, U15-ch19, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

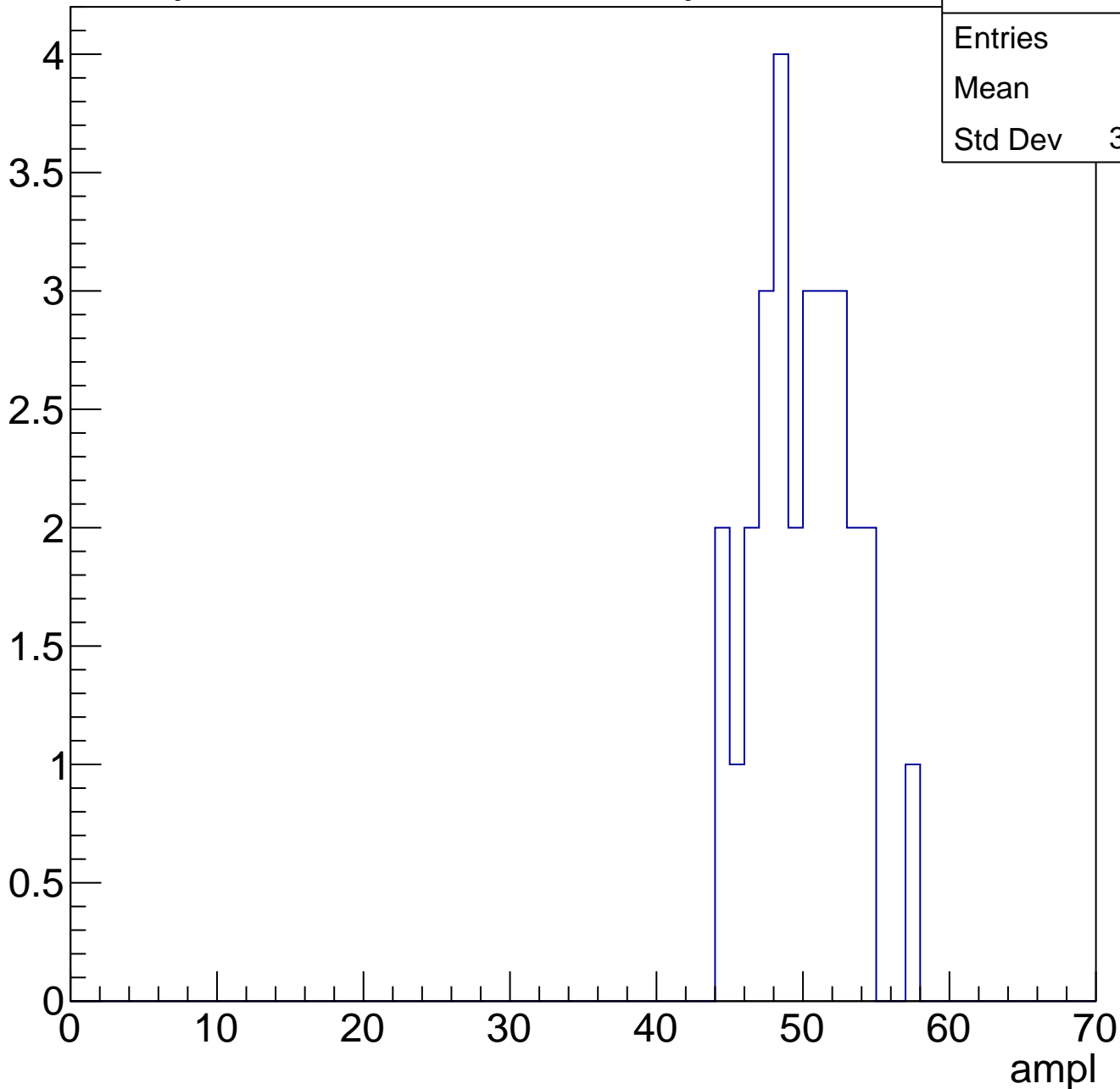


Entries	34
Mean	44.24
Std Dev	3.726

B1L103S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



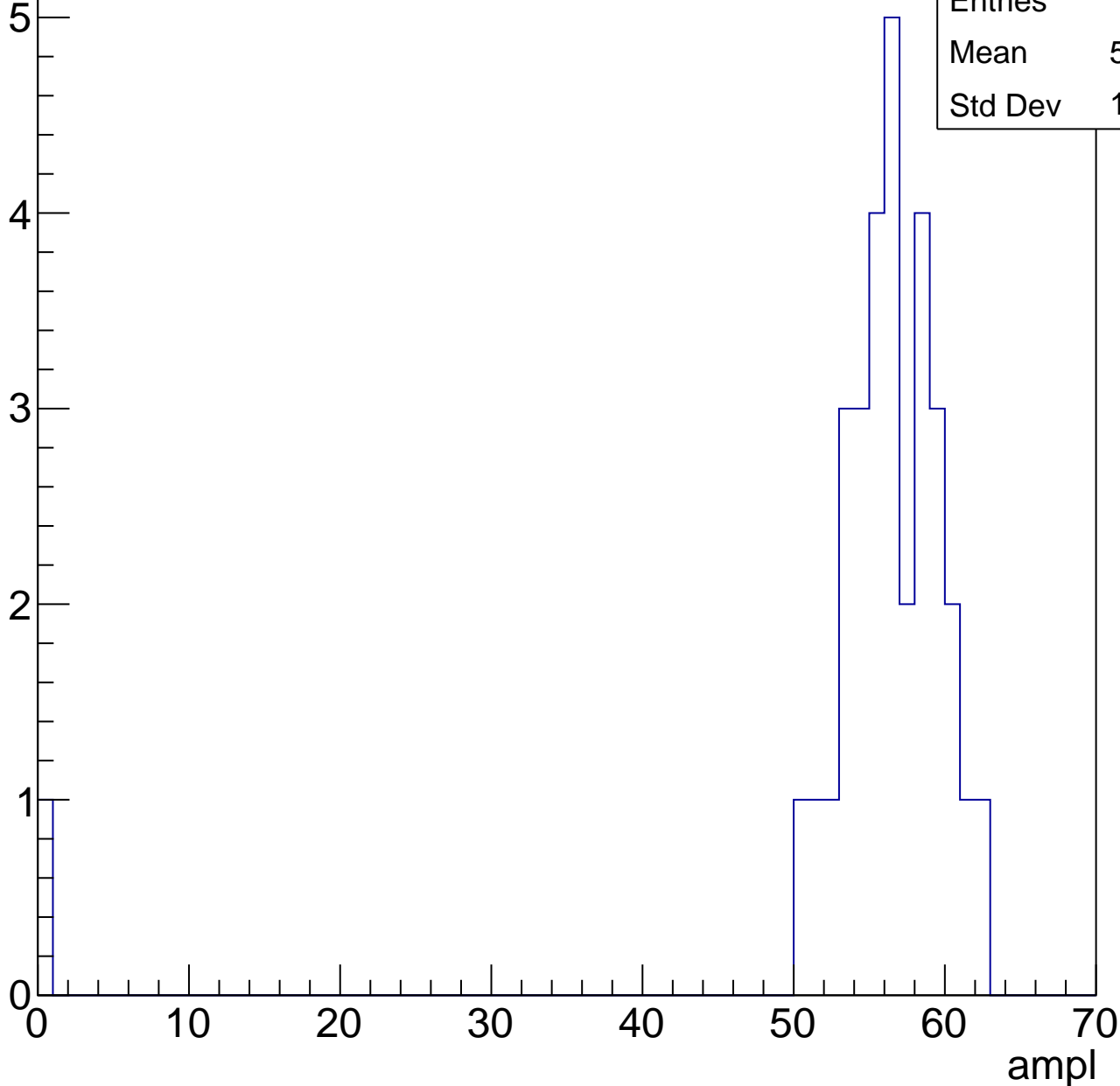
Entries	28
Mean	49.5
Std Dev	3.168

B1L103S, U15-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

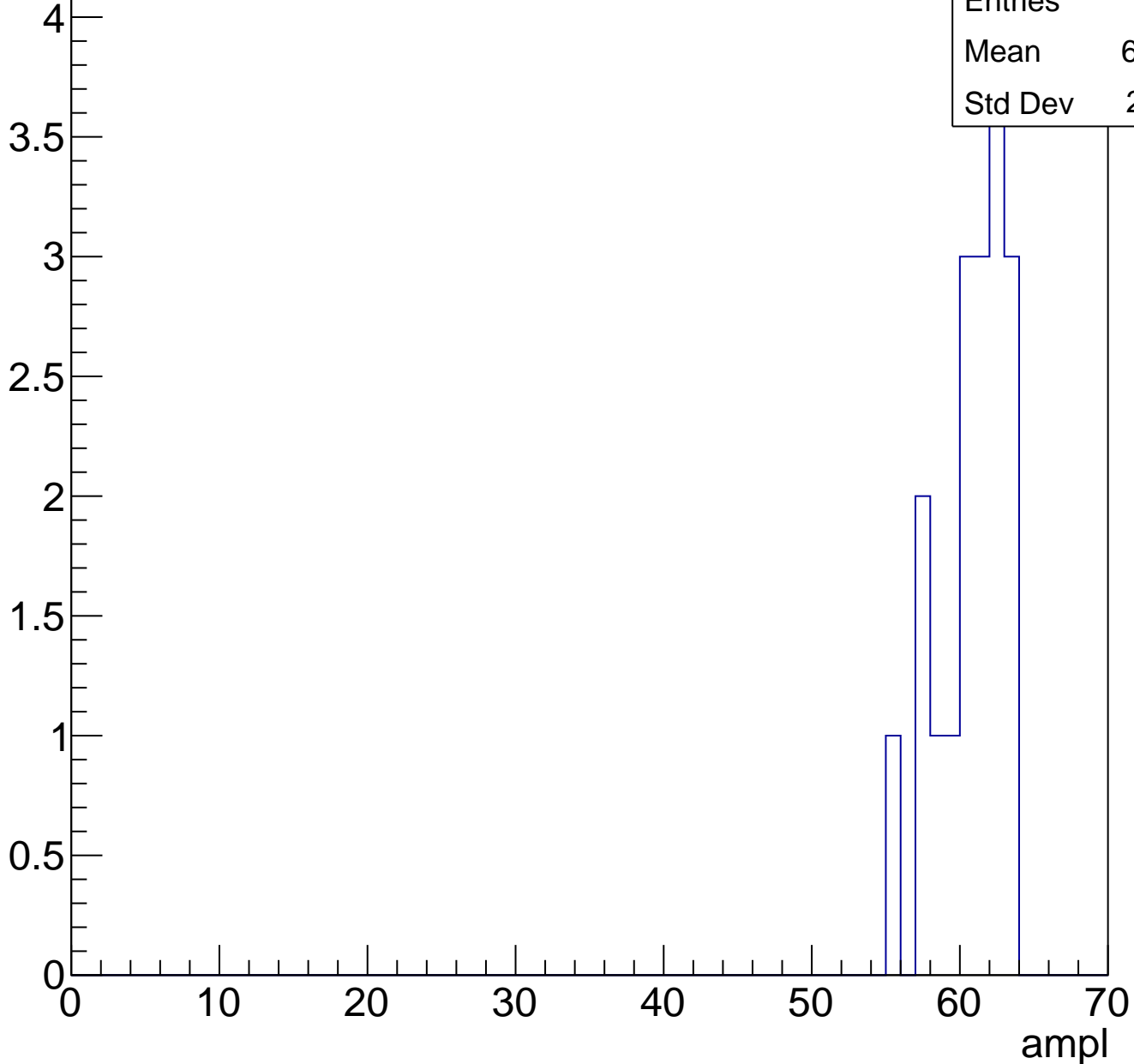
Entries	32
Mean	54.38
Std Dev	10.17



B1L103S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

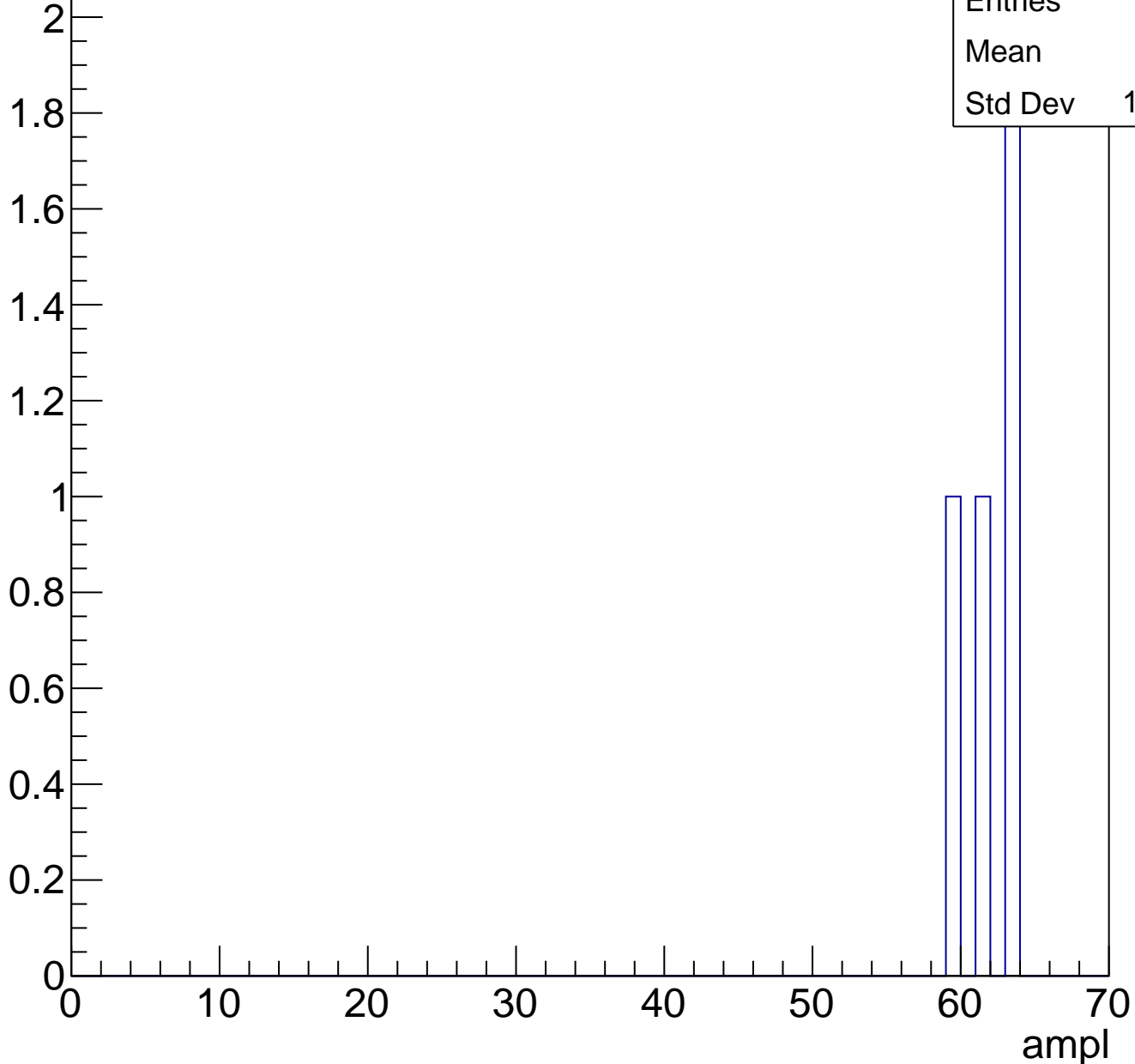


Entries	18
Mean	60.33
Std Dev	2.261

B1L103S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	61.5
Std Dev	1.658

B1L103S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch20, adc0

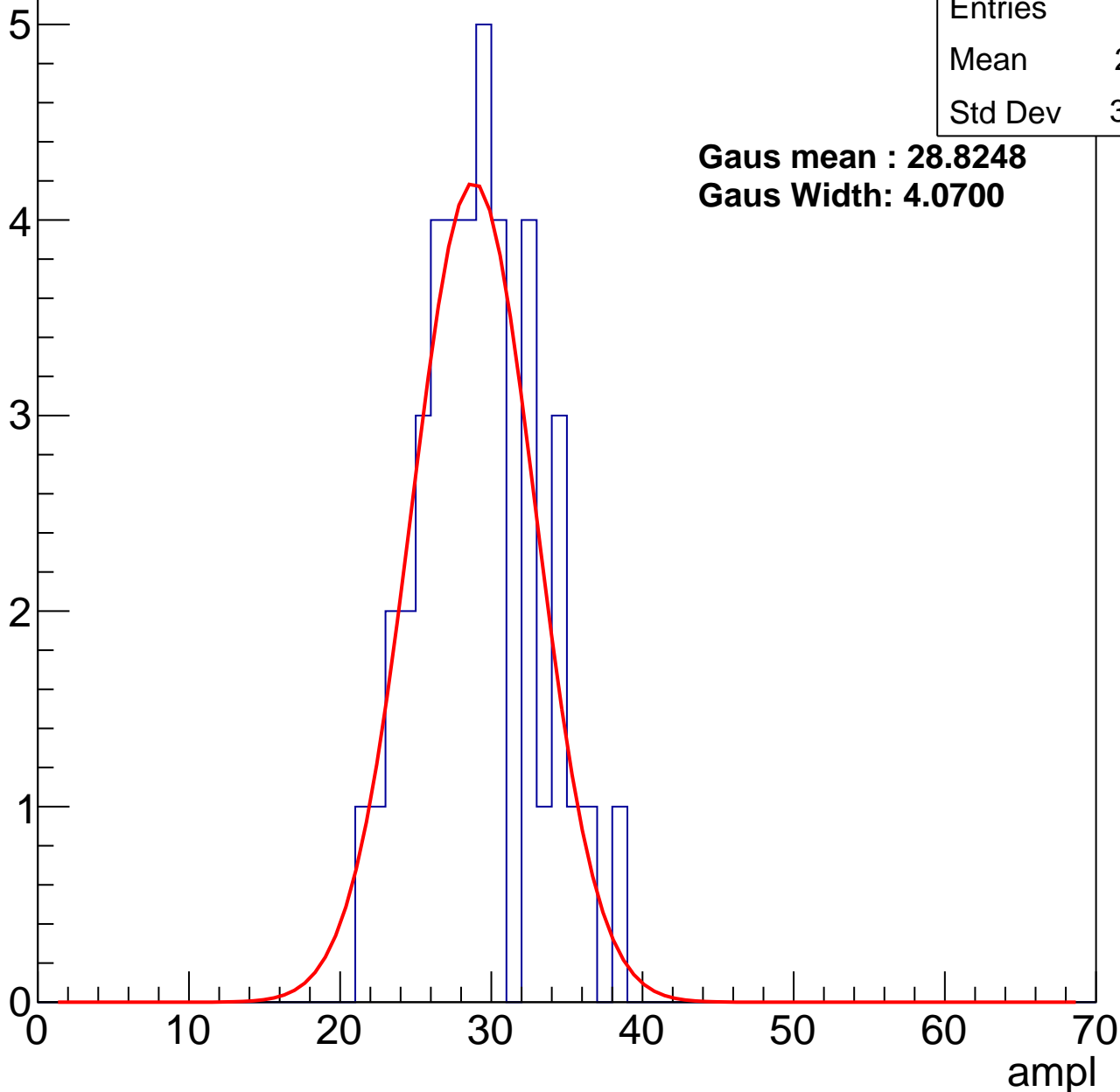
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	28.61
Std Dev	3.919

Gaus mean : 28.8248

Gaus Width: 4.0700



B1L103S, U15-ch20, adc1

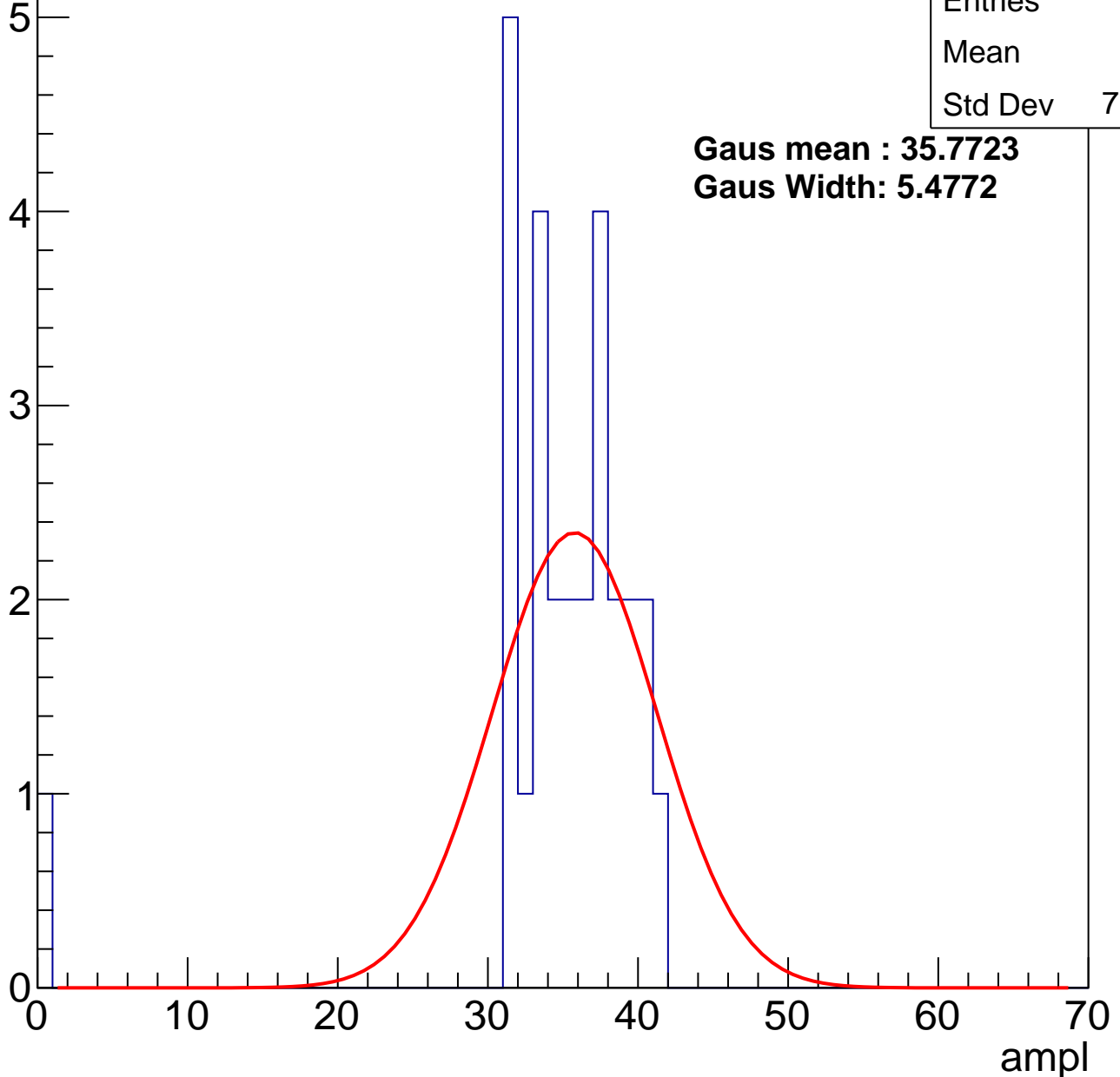
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	34
Std Dev	7.216

Gaus mean : 35.7723

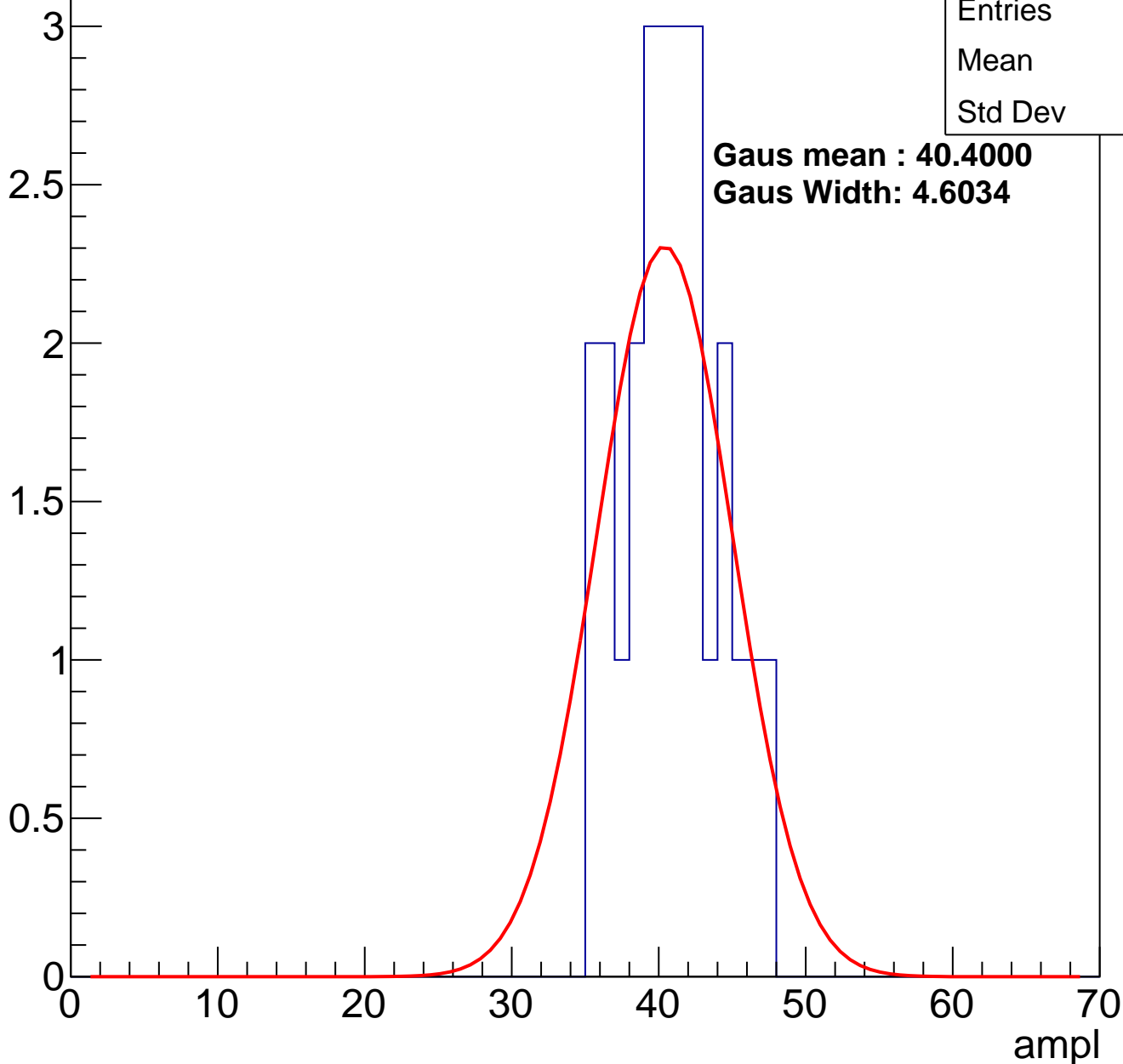
Gaus Width: 5.4772



B1L103S, U15-ch20, adc2

calib_packv5_042523_0143.root, FC#7, port C2

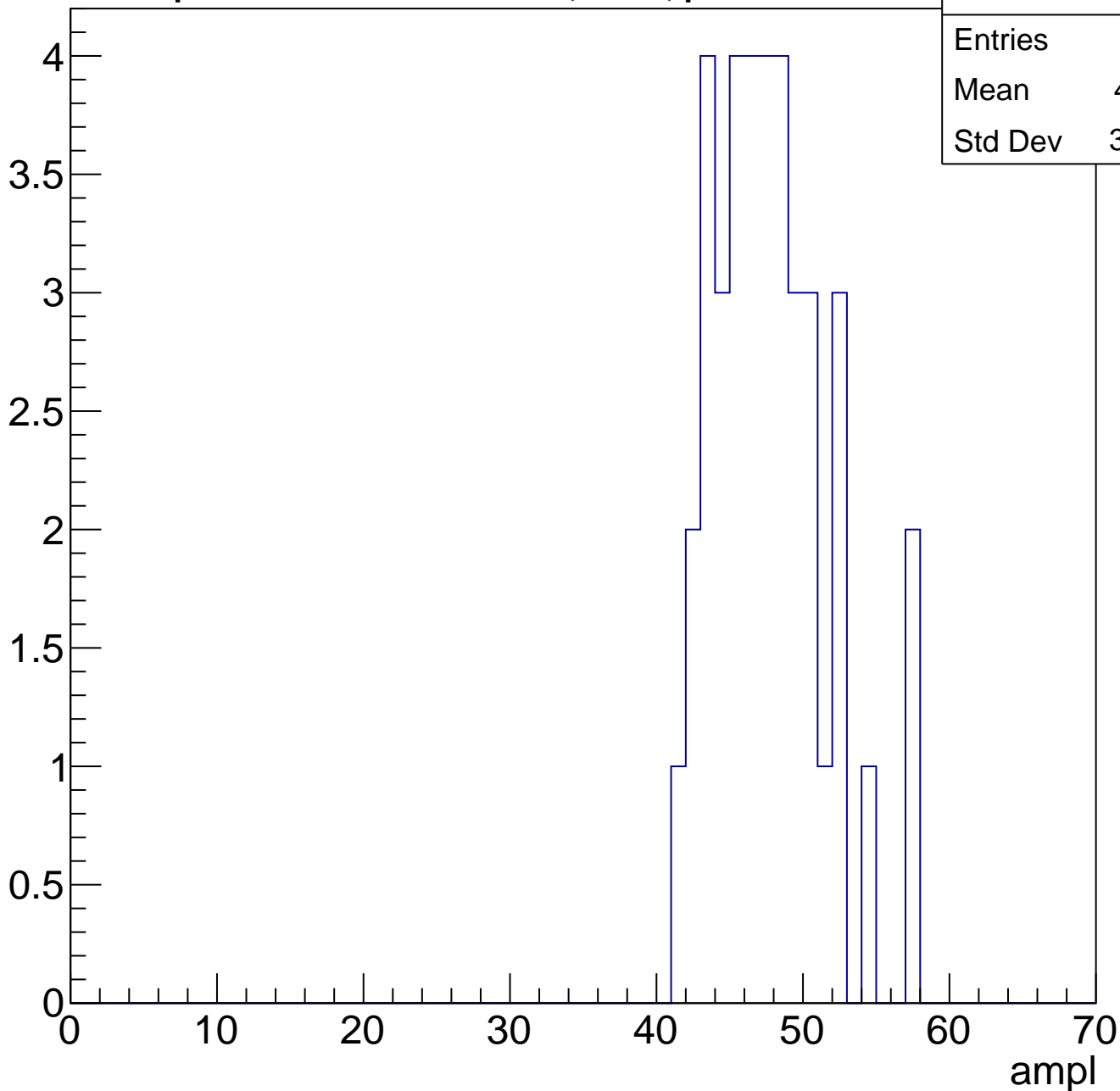
Entry



B1L103S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

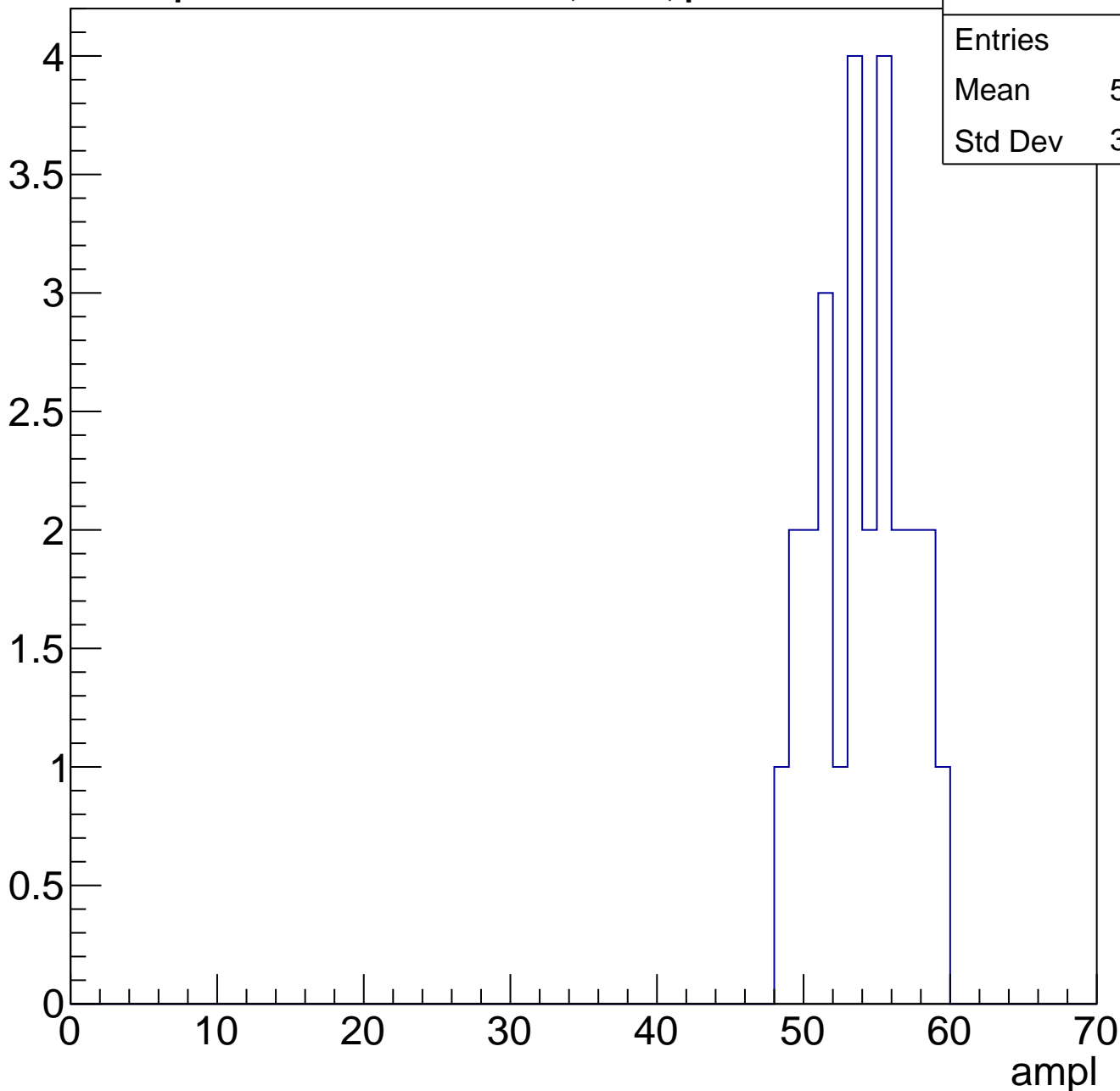
Entry



B1L103S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

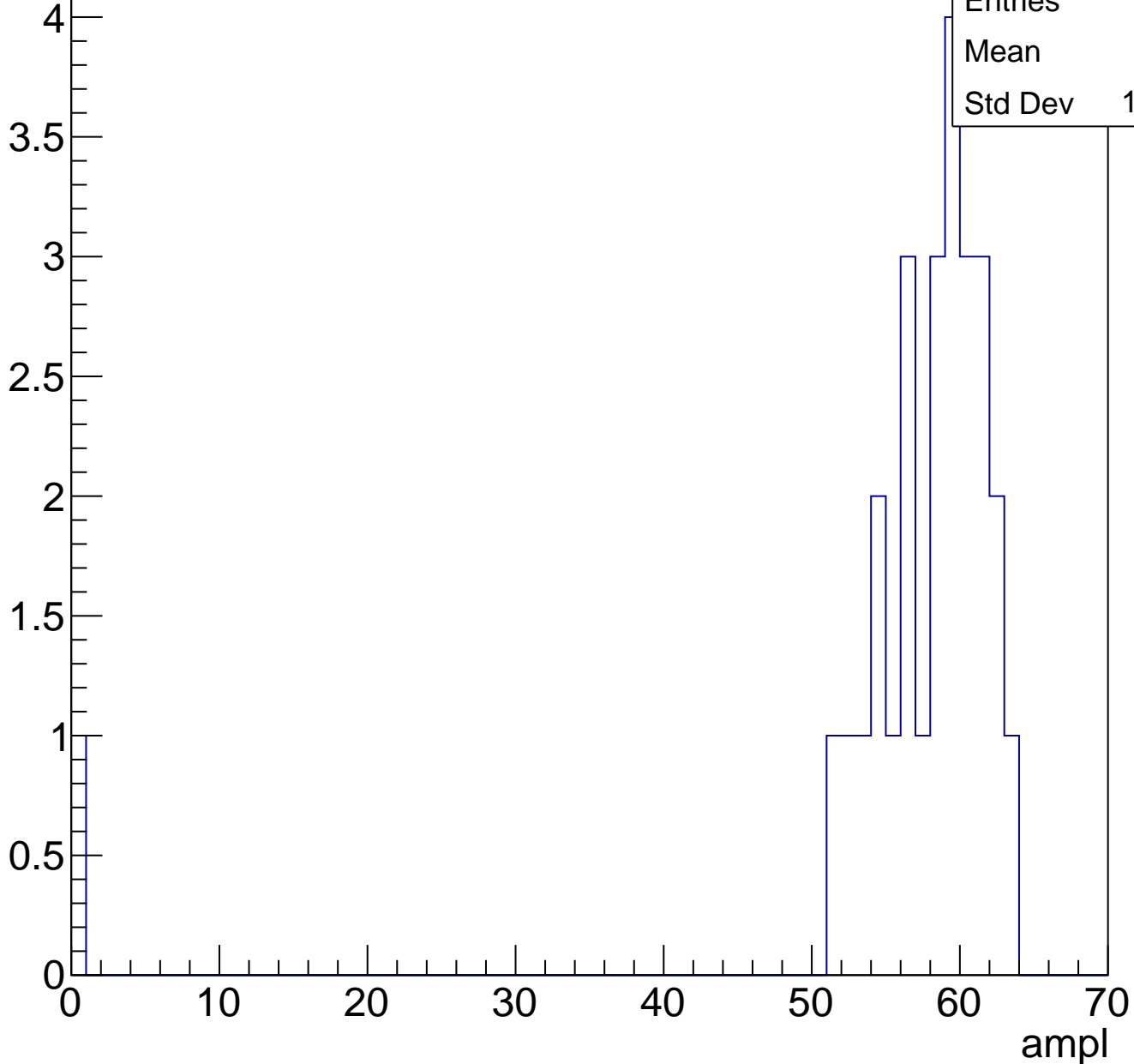


Entries	26
Mean	53.54
Std Dev	3.003

B1L103S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

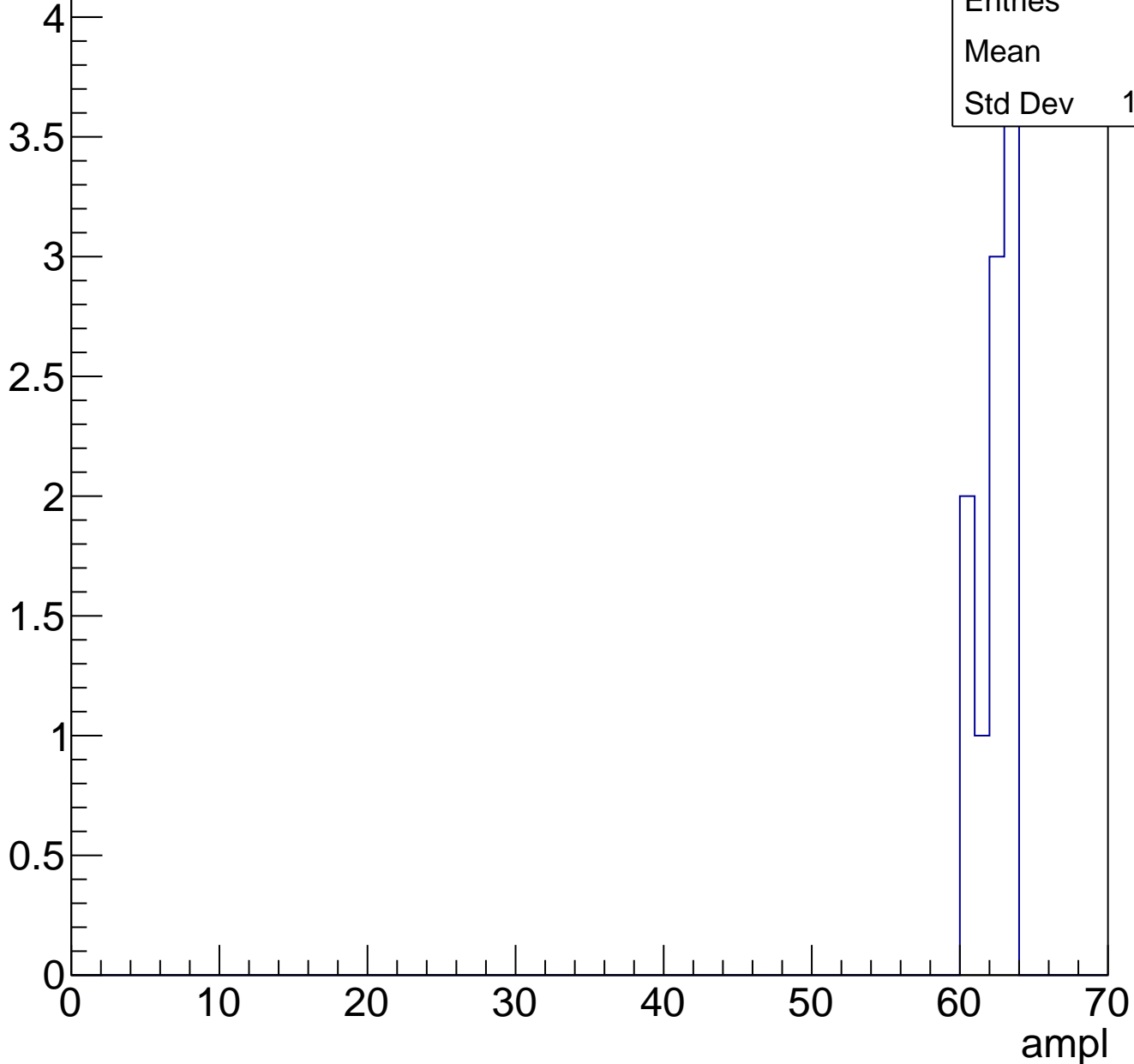
Entry



B1L103S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

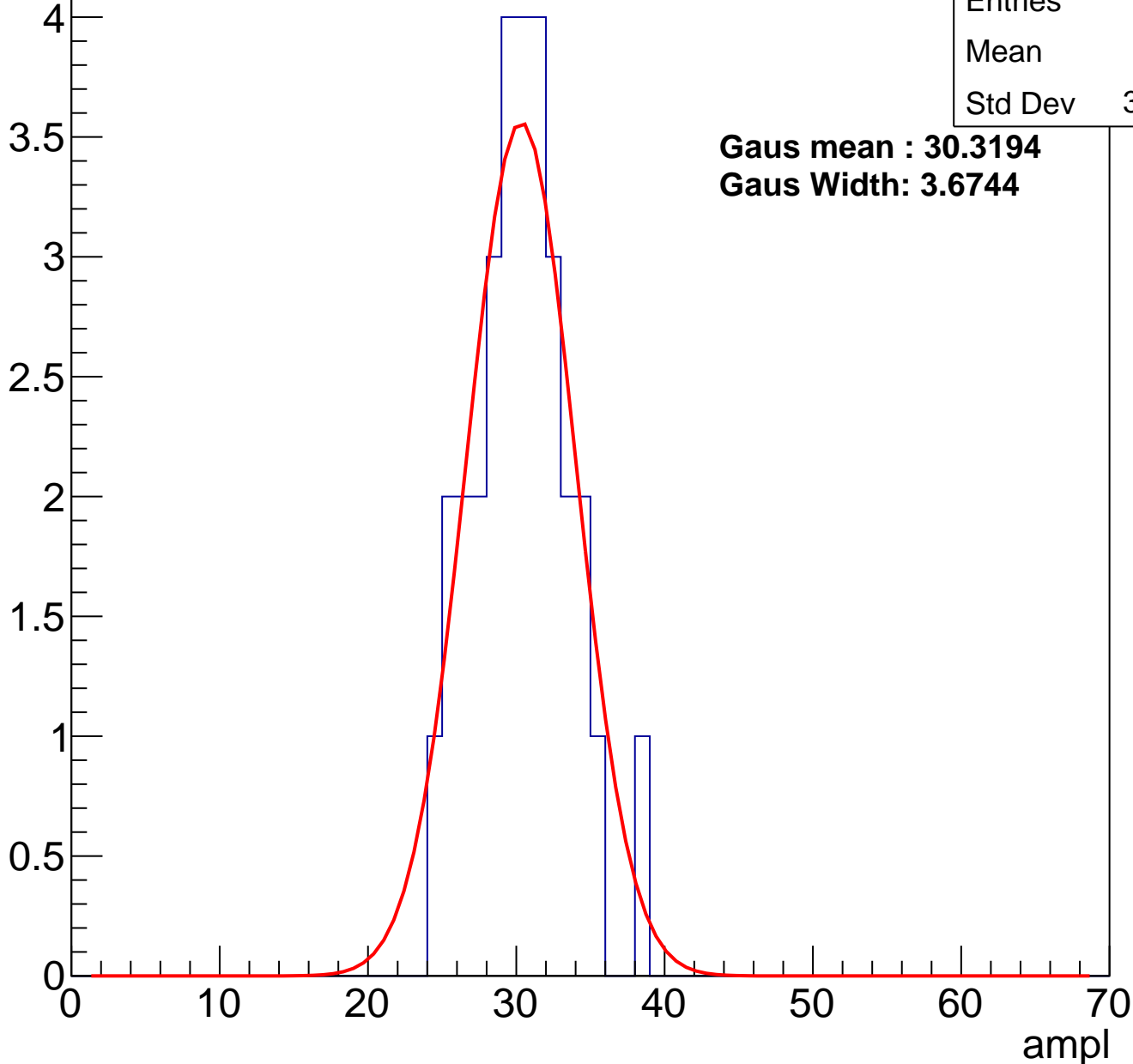
Entry



B1L103S, U15-ch21, adc0

calib_packv5_042523_0143.root, FC#7, port C2

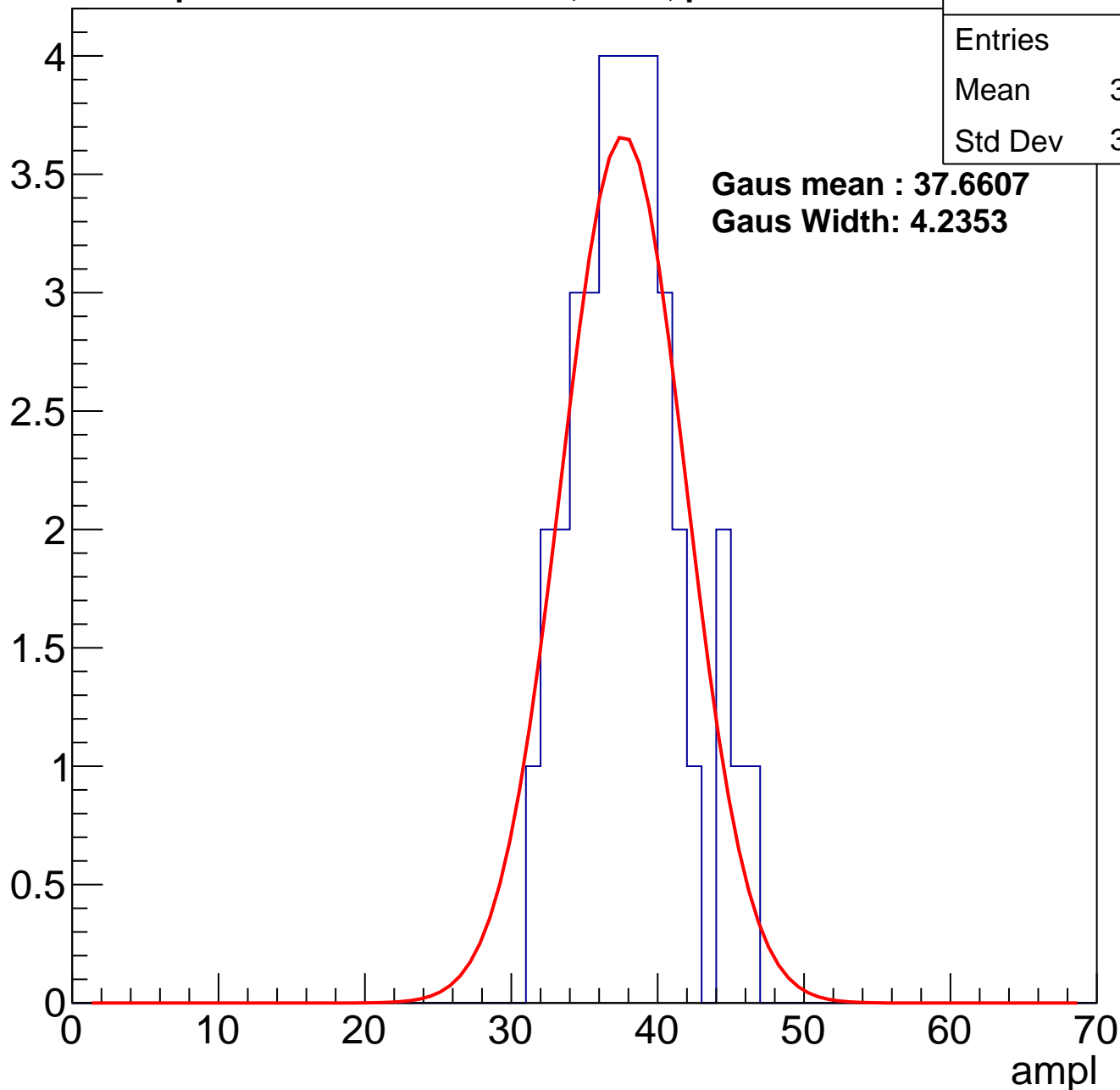
Entry



B1L103S, U15-ch21, adc1

calib_packv5_042523_0143.root, FC#7, port C2

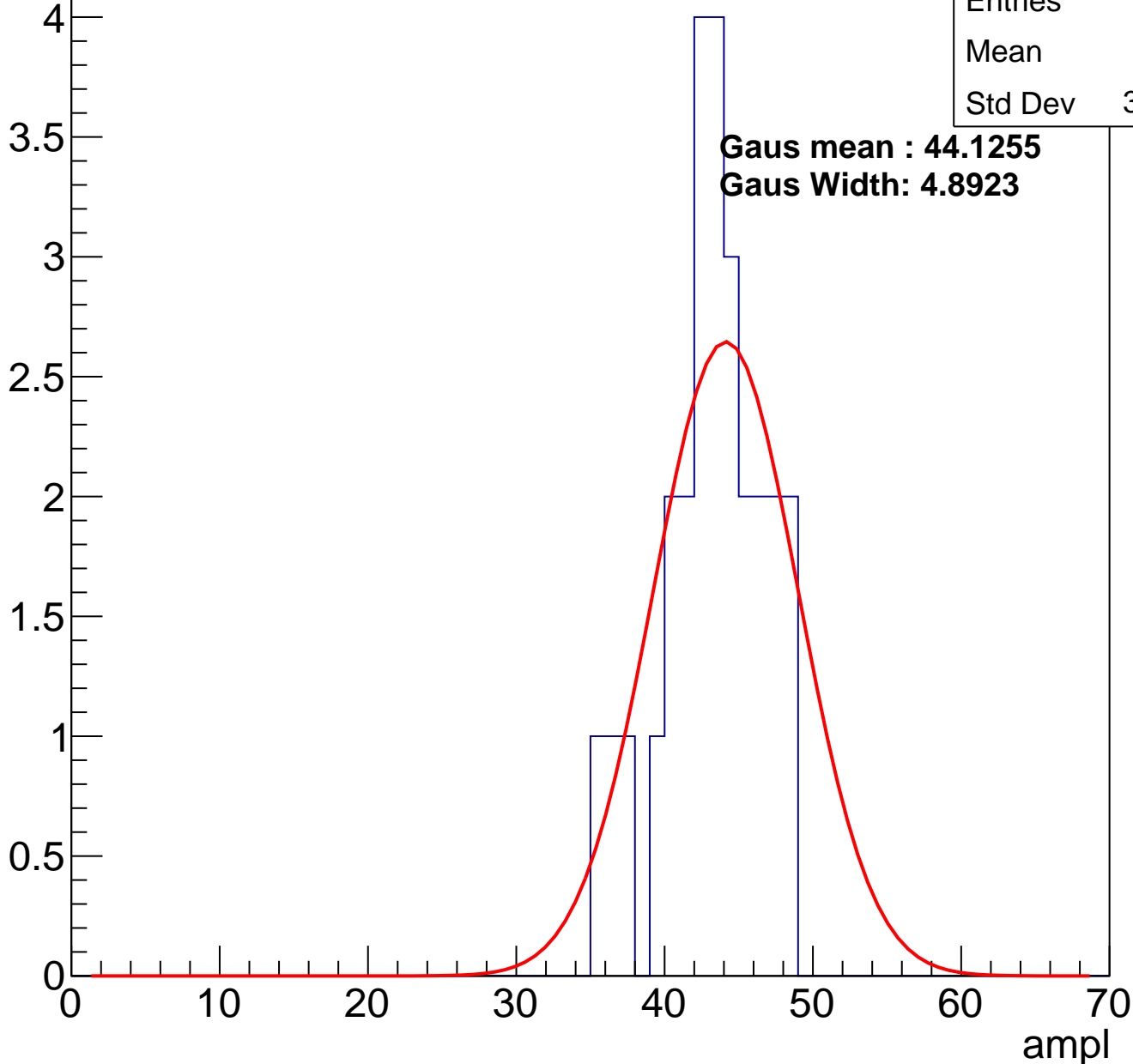
Entry



B1L103S, U15-ch21, adc2

calib_packv5_042523_0143.root, FC#7, port C2

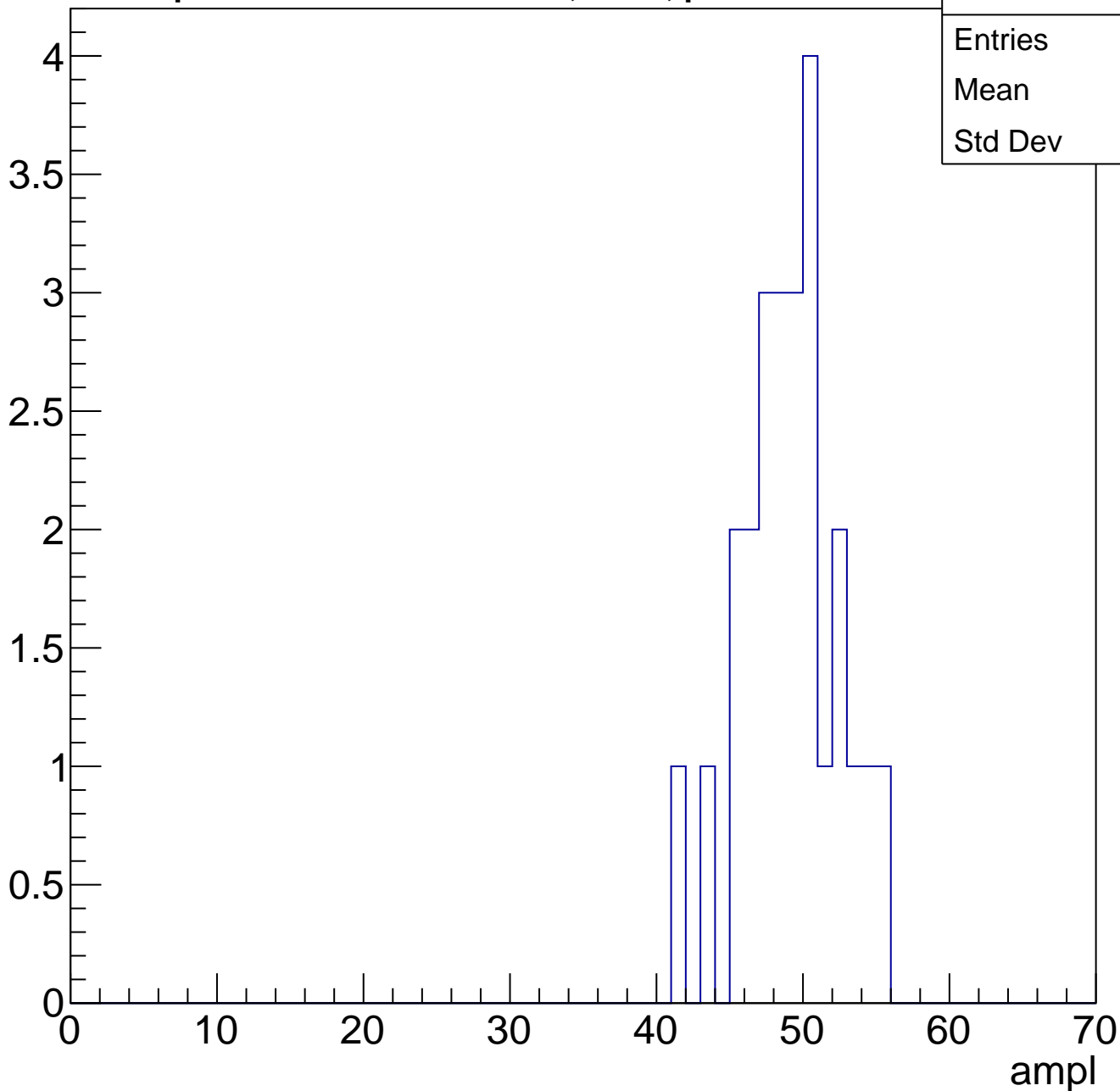
Entry



B1L103S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

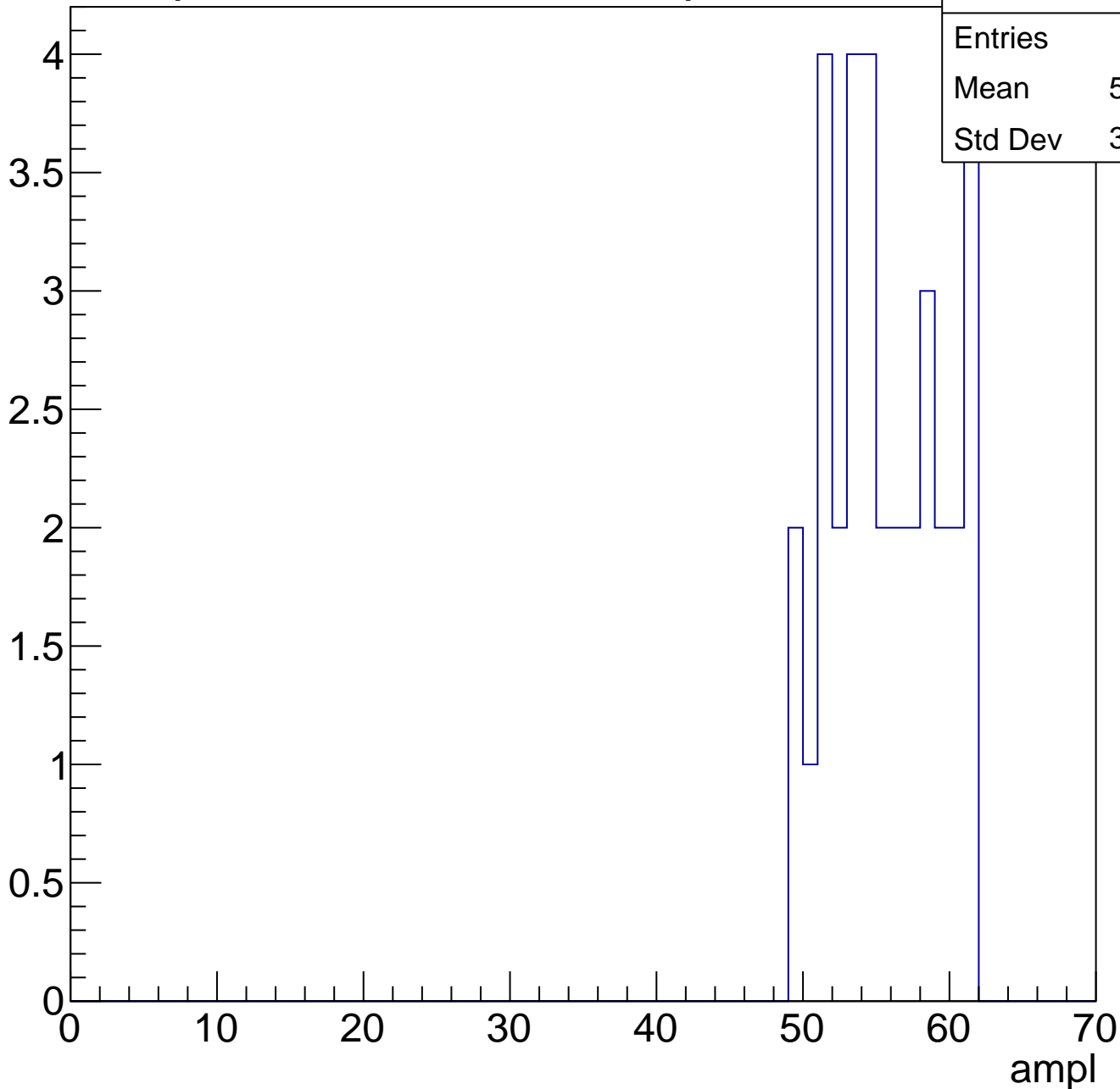


Entries	25
Mean	48.6
Std Dev	3.25

B1L103S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

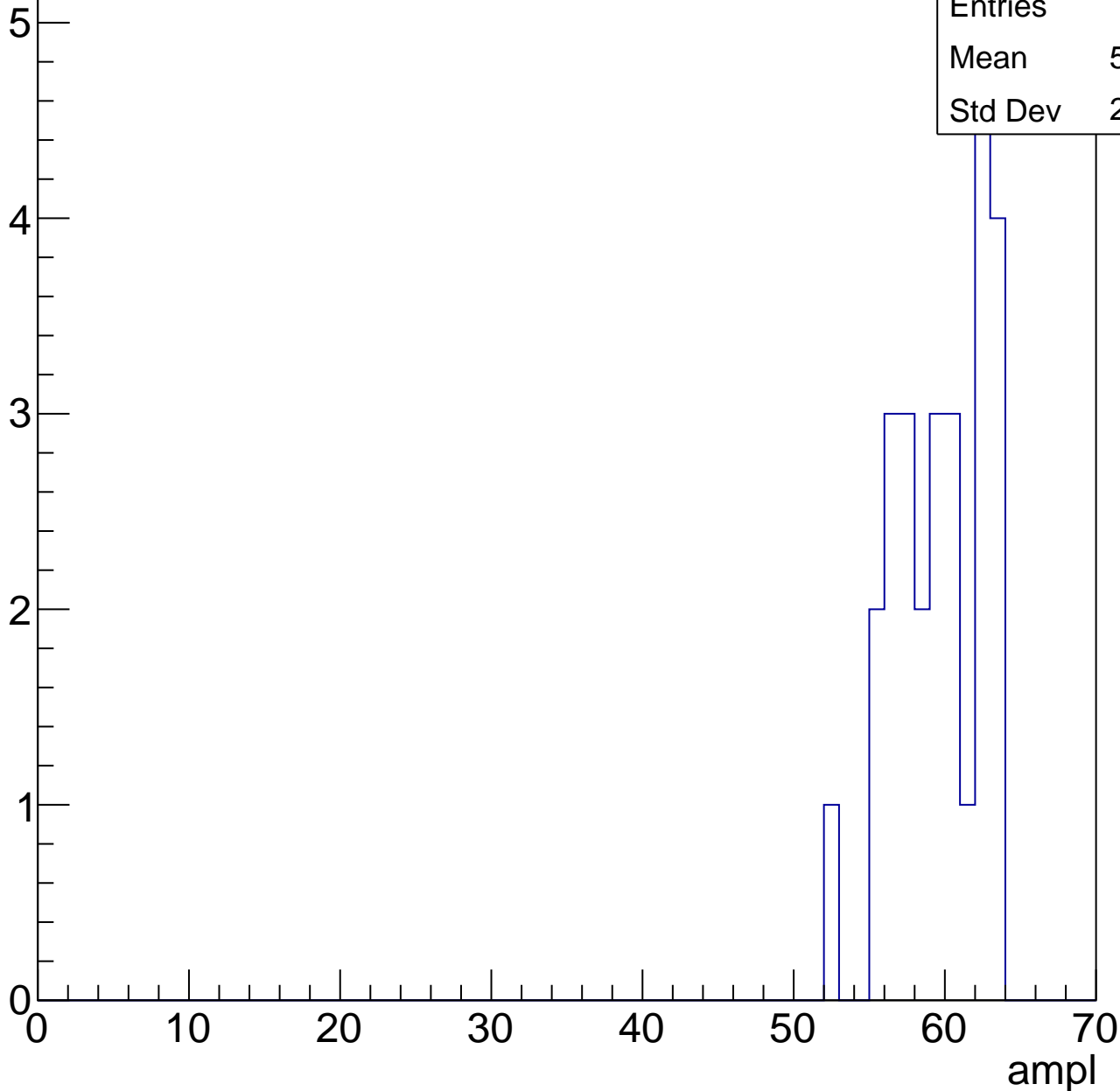


B1L103S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	59.15
Std Dev	2.965



B1L103S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

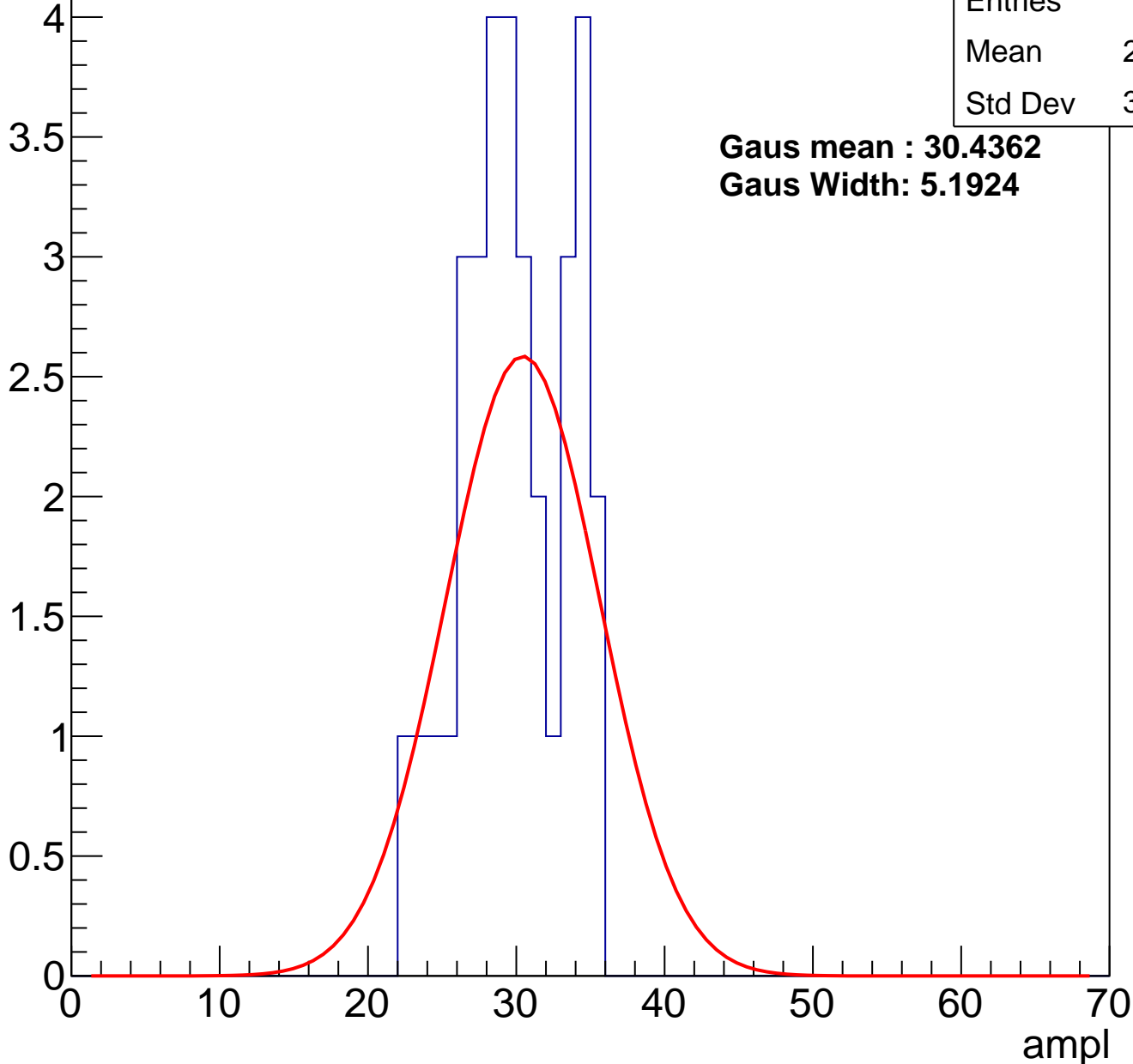


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch22, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch22, adc1

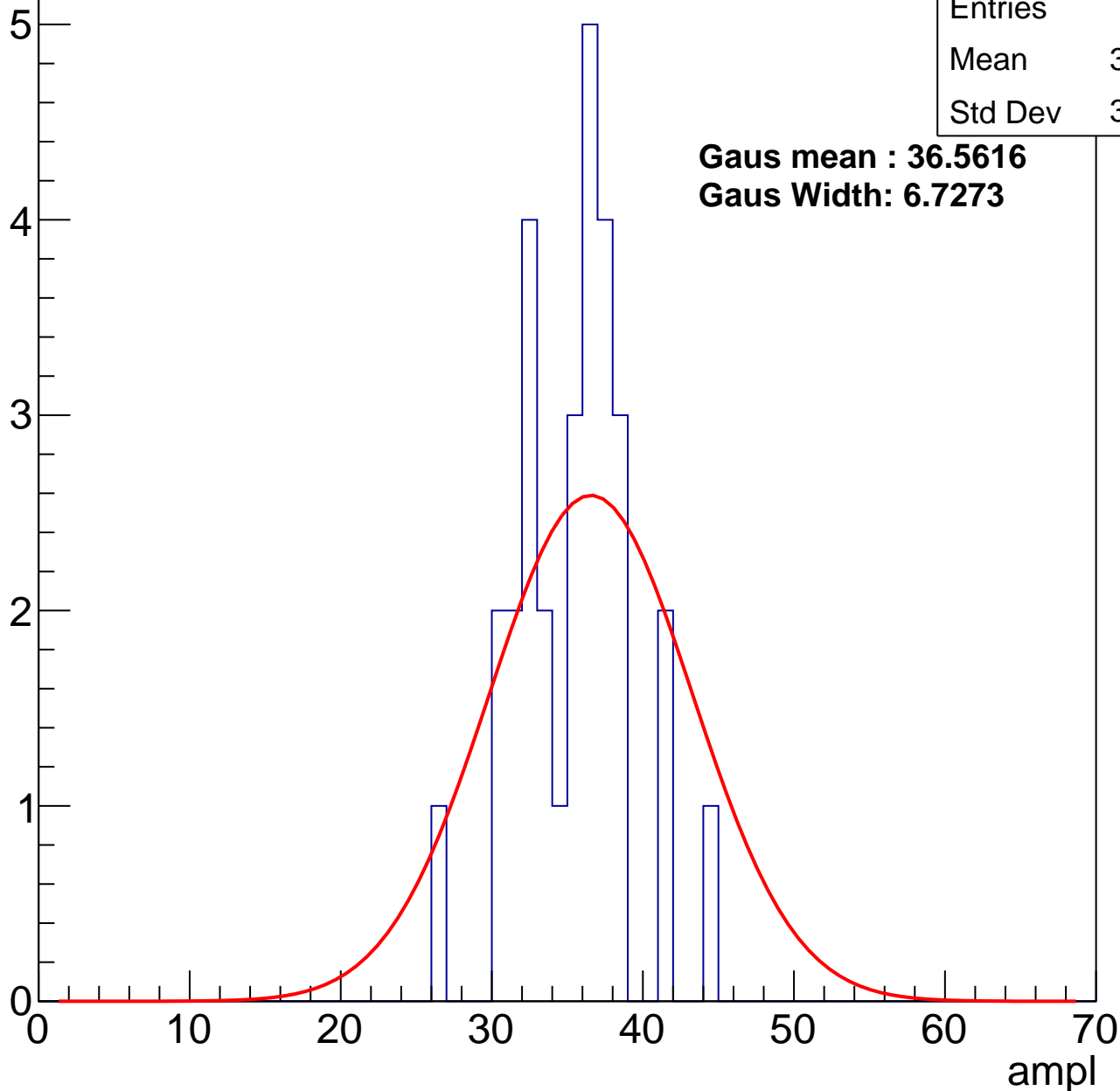
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	34.97
Std Dev	3.692

Gaus mean : 36.5616

Gaus Width: 6.7273



B1L103S, U15-ch22, adc2

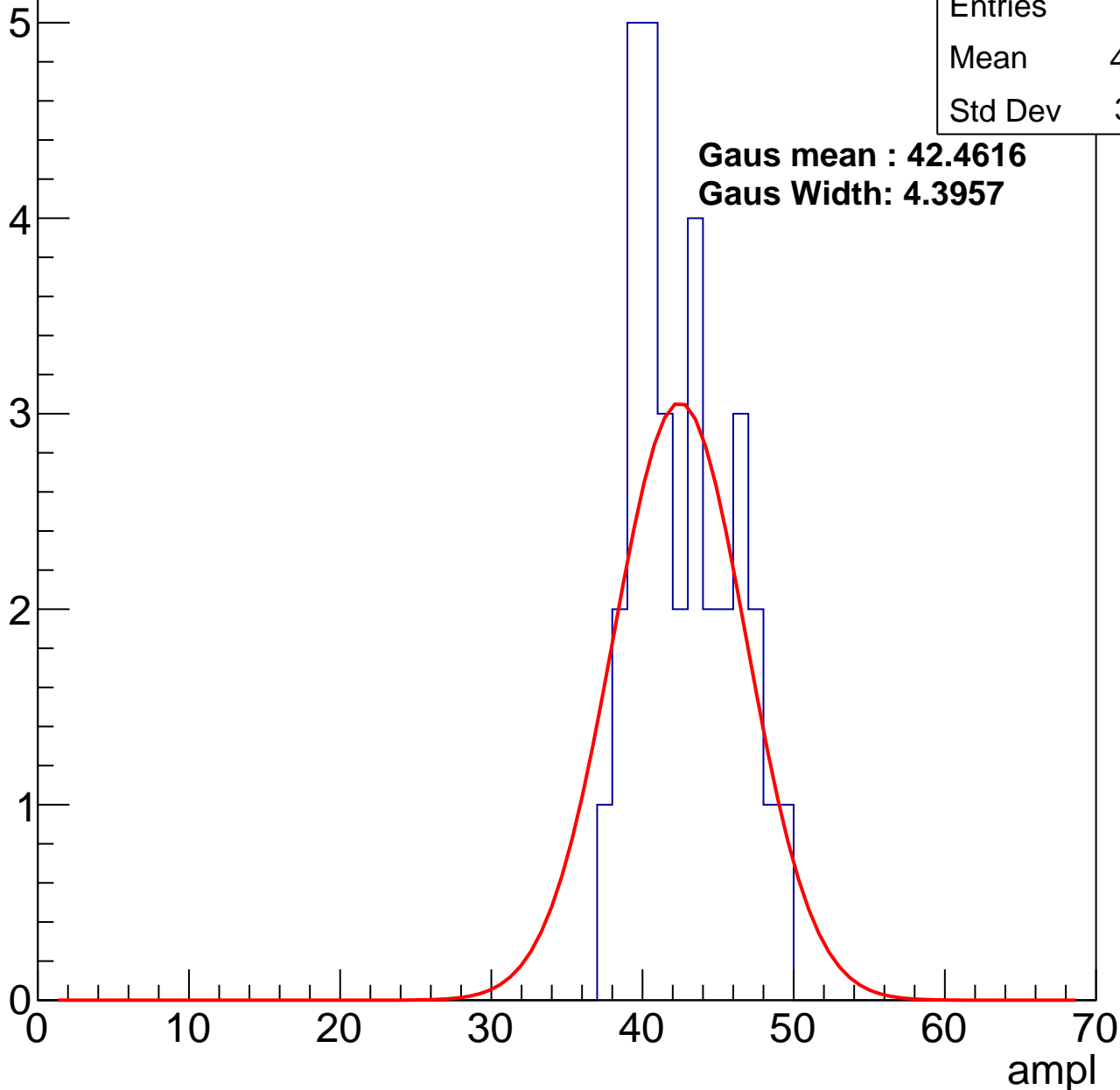
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	42.24
Std Dev	3.191

Gaus mean : 42.4616

Gaus Width: 4.3957



B1L103S, U15-ch22, adc3

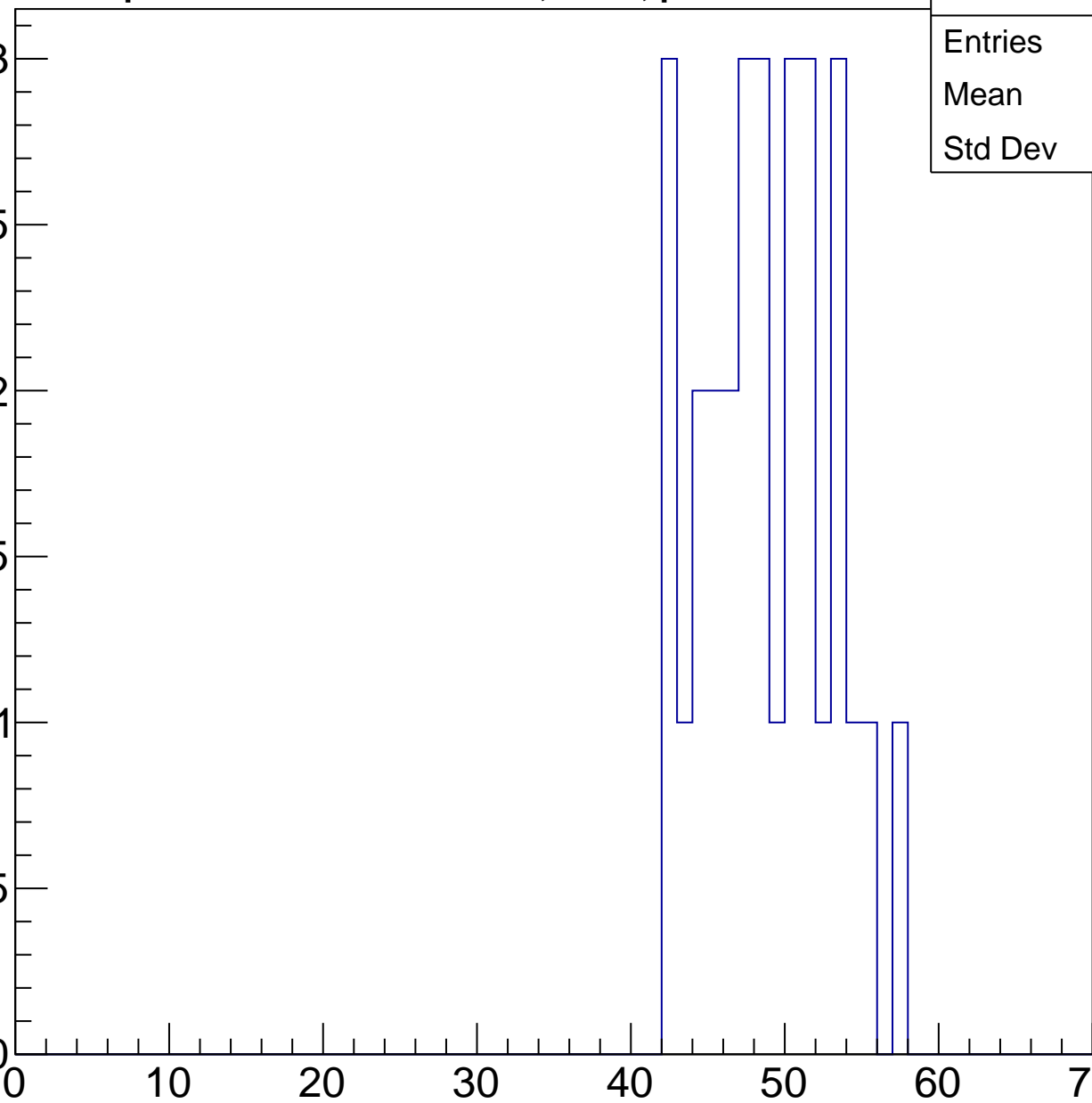
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	30
Mean	48.43
Std Dev	4.022

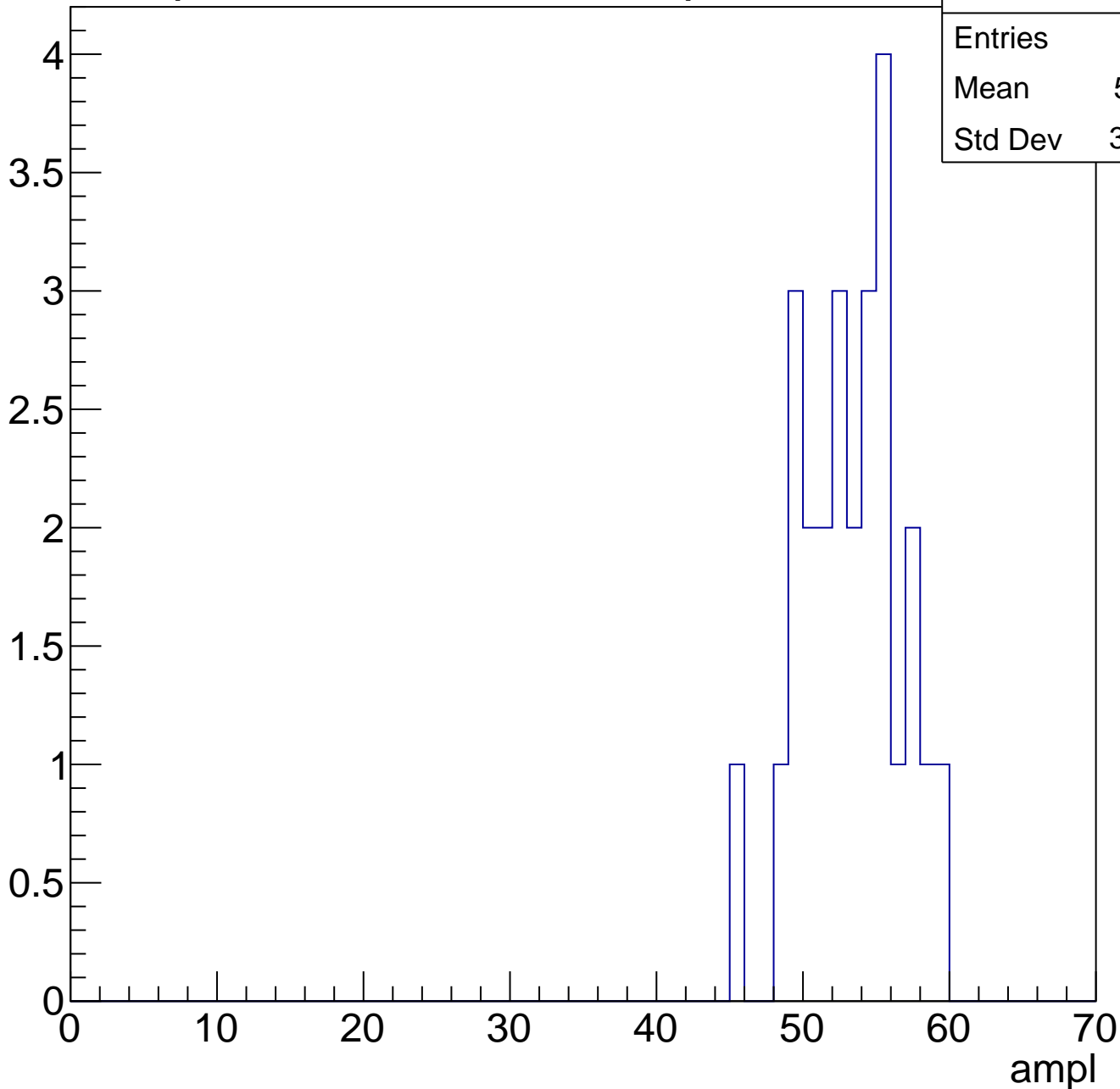
ampl



B1L103S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

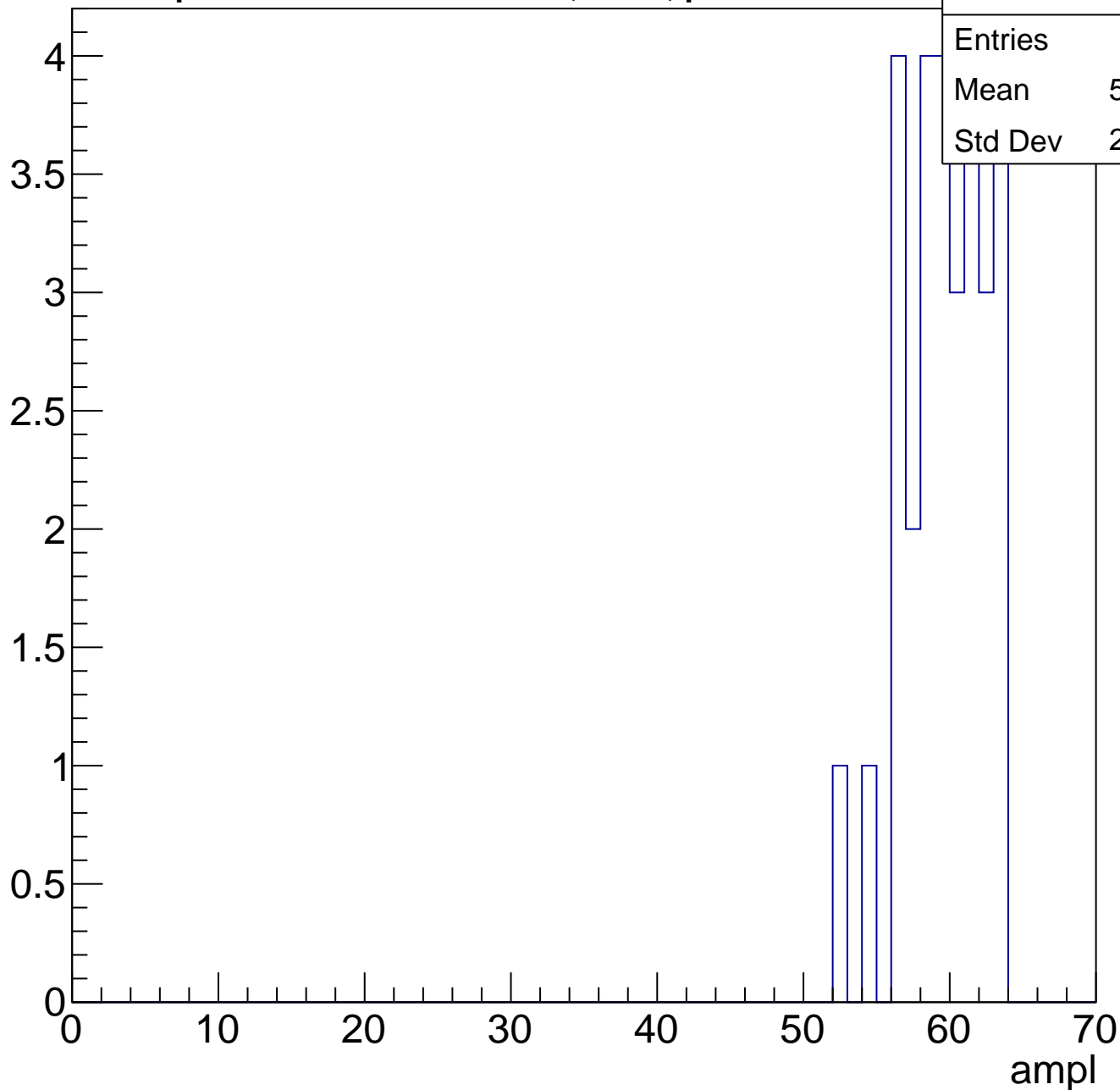


Entries	26
Mean	52.81
Std Dev	3.317

B1L103S, U15-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

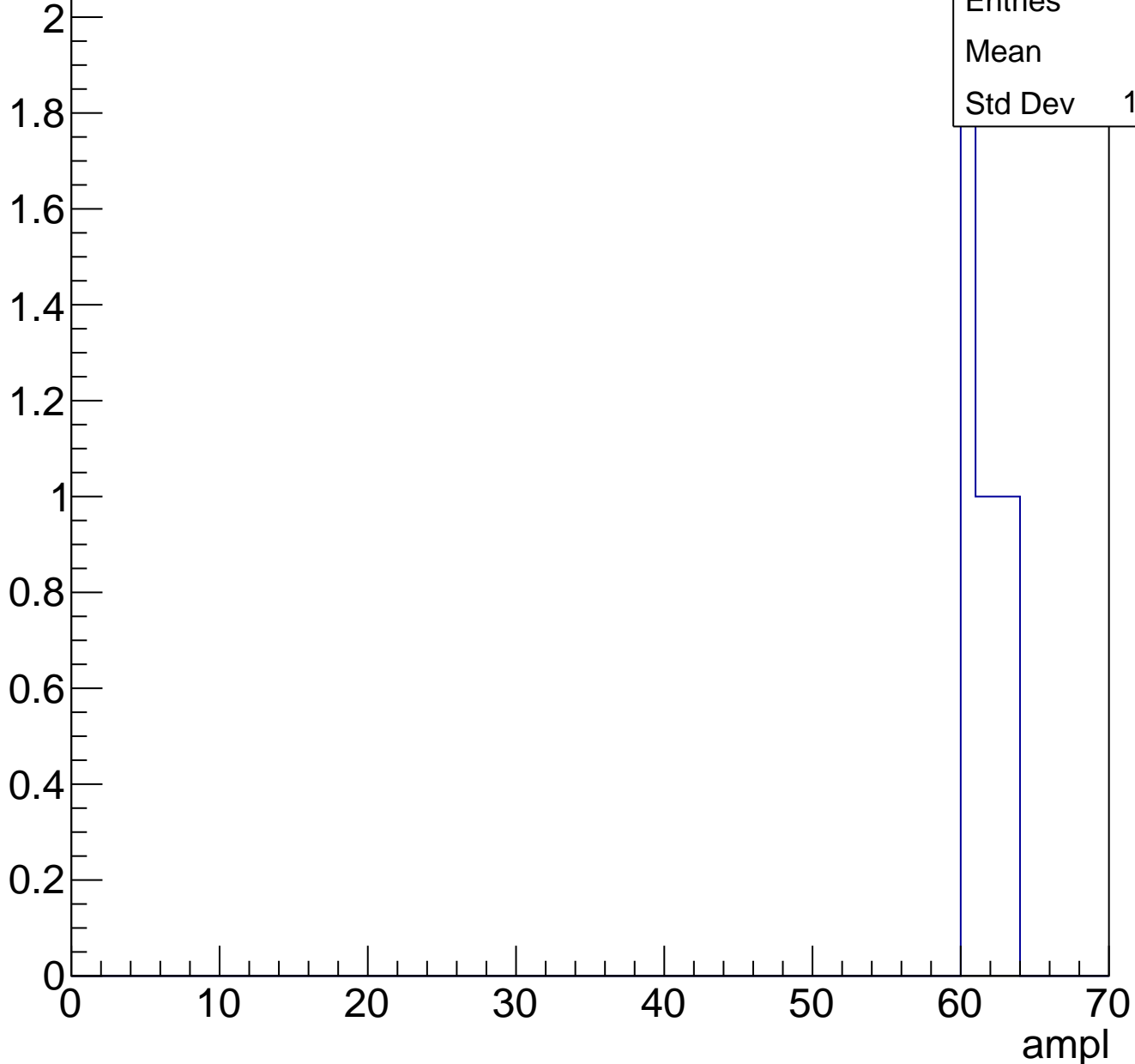
Entry



B1L103S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

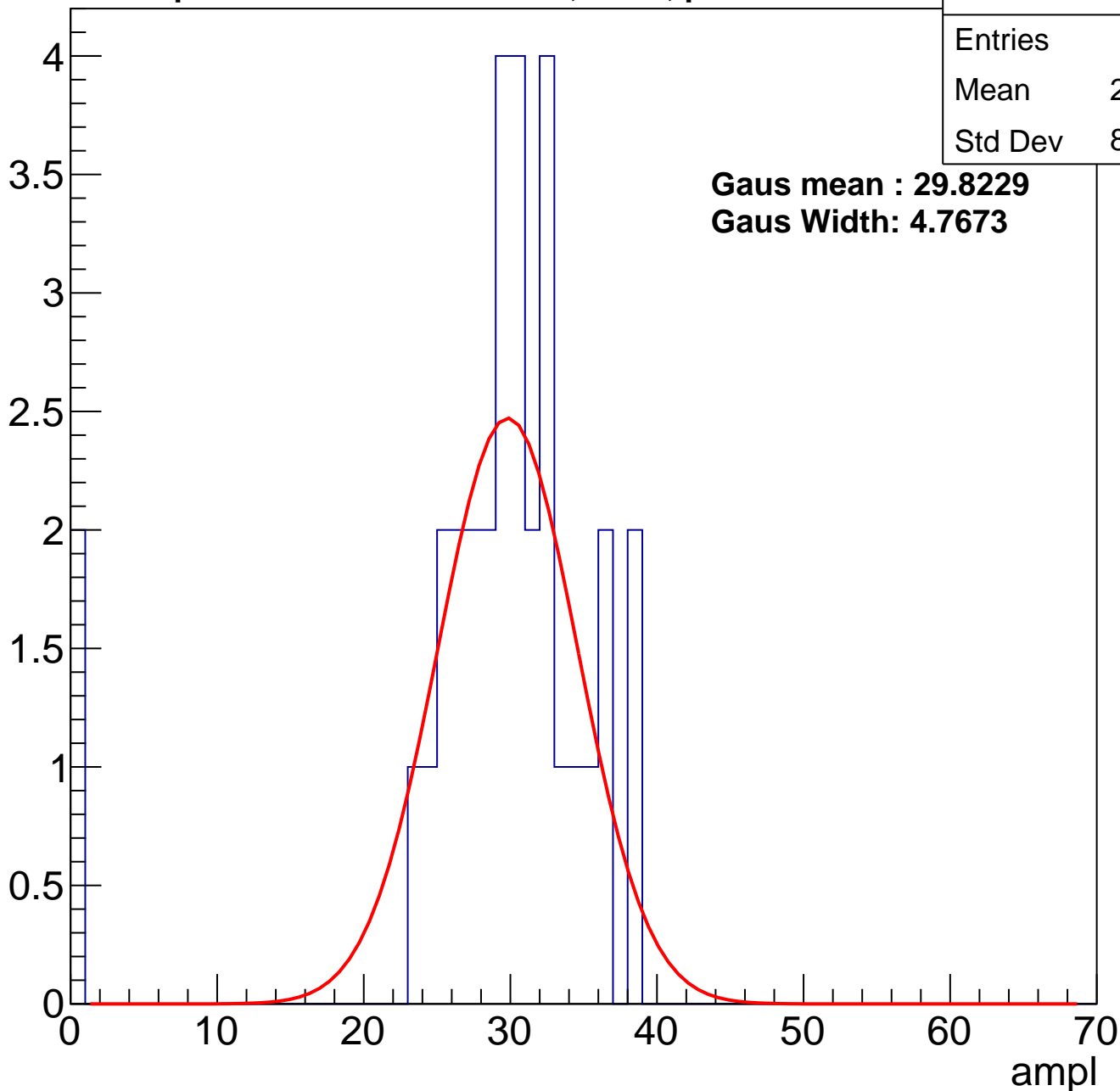
Entry



B1L103S, U15-ch23, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

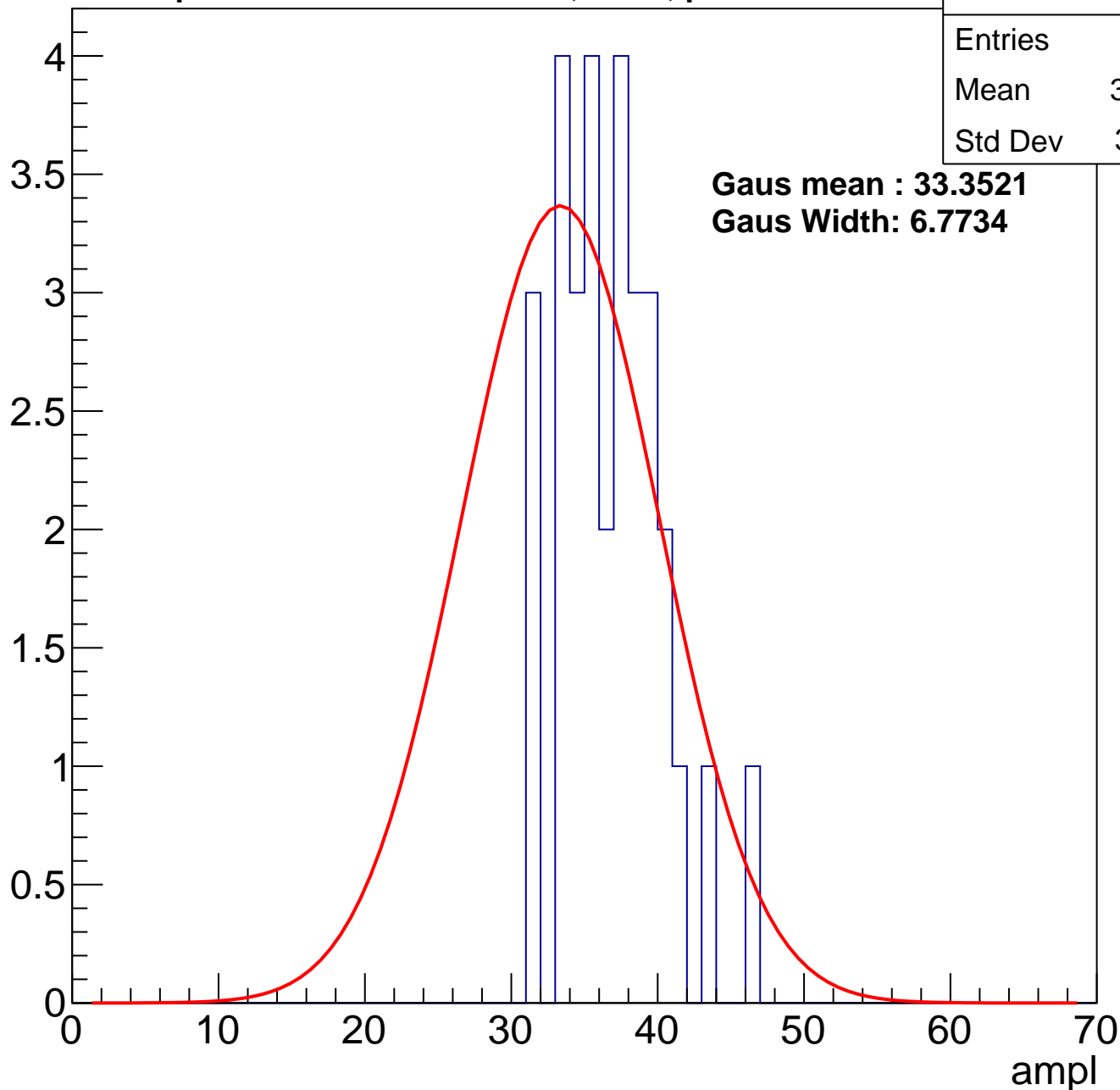


Entries	33
Mean	28.33
Std Dev	8.115

B1L103S, U15-ch23, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	31
Mean	36.39
Std Dev	3.461

B1L103S, U15-ch23, adc2

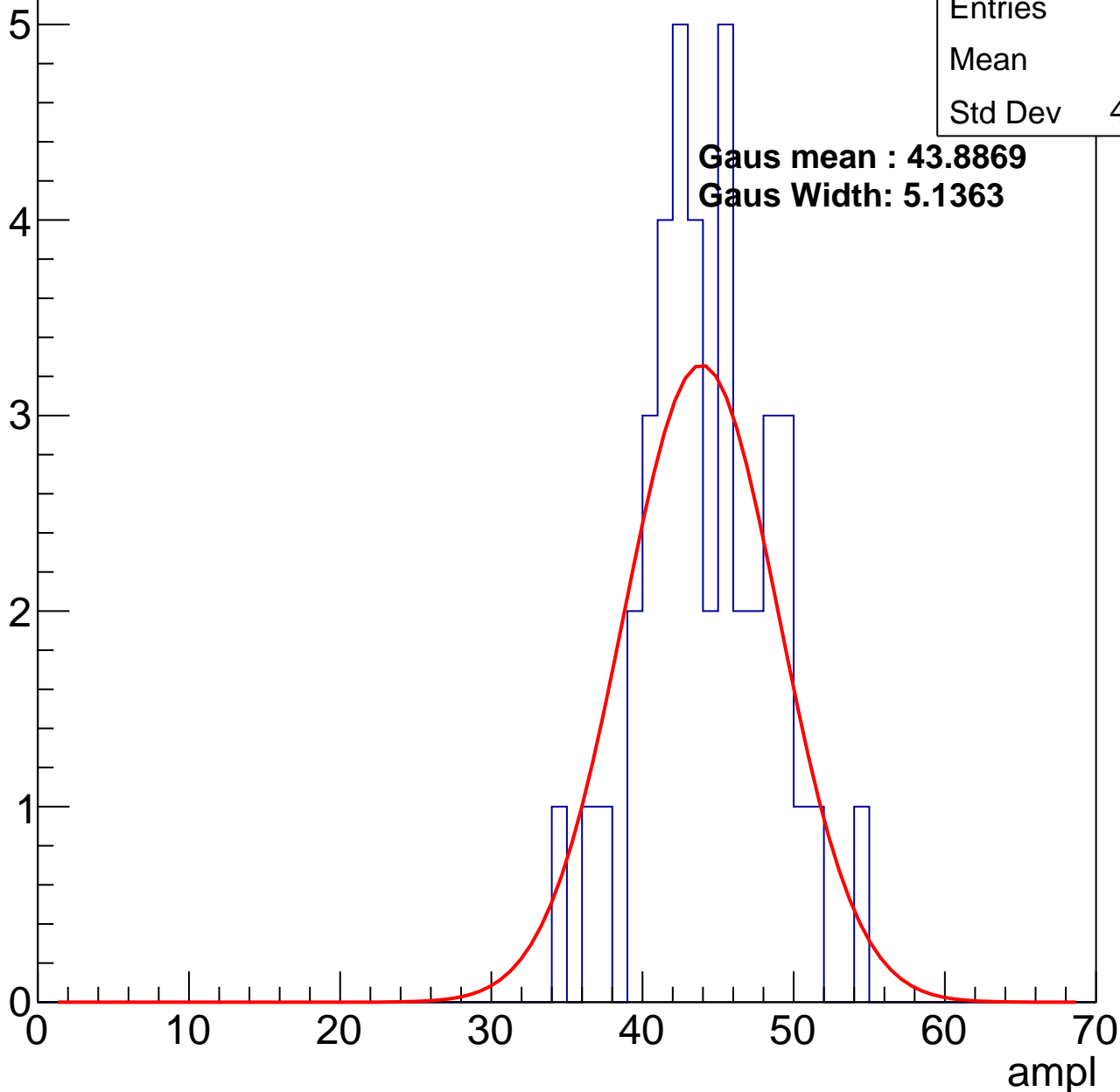
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	43.8
Std Dev	4.157

Gaus mean : 43.8869

Gaus Width: 5.1363

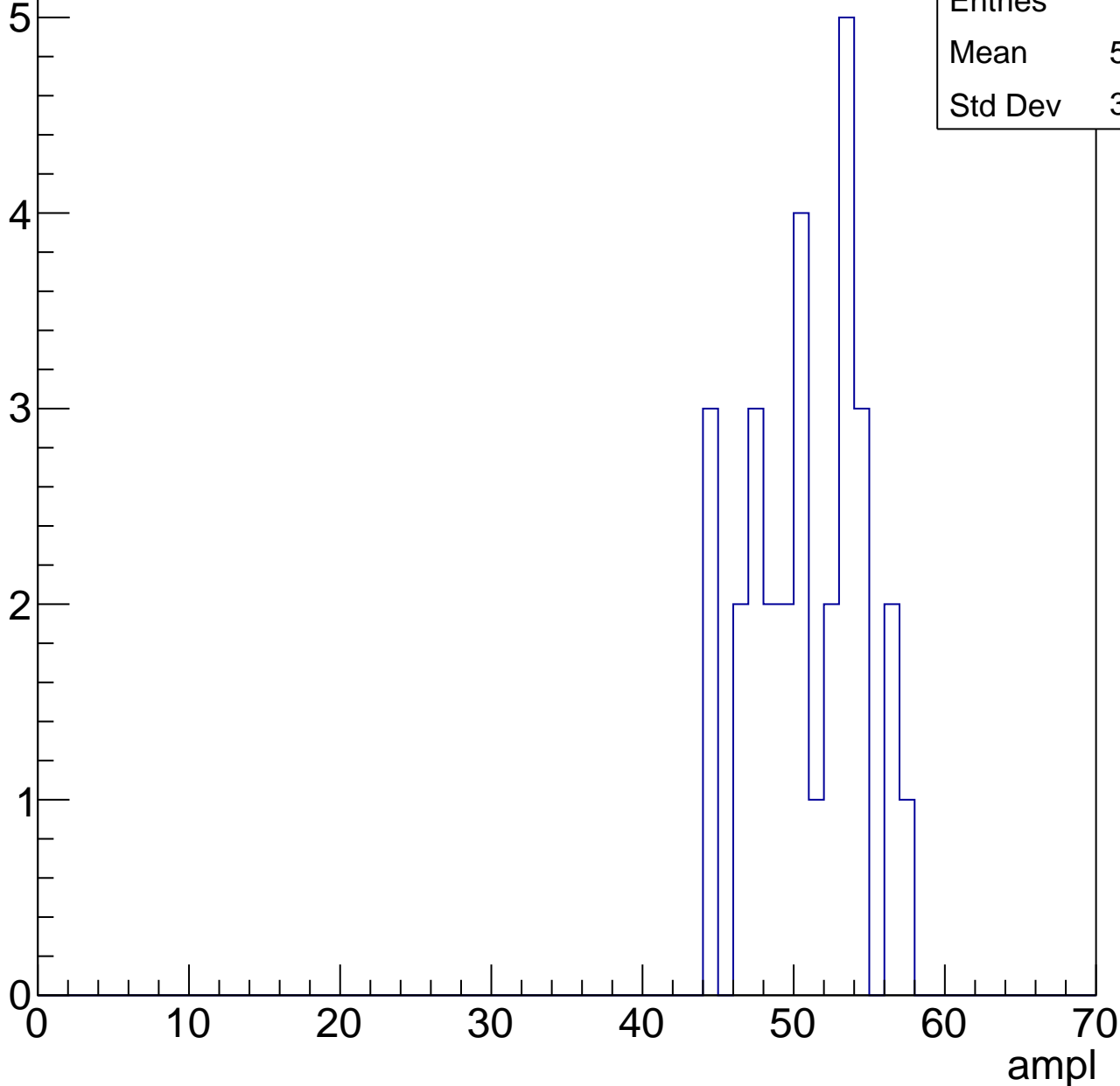


B1L103S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

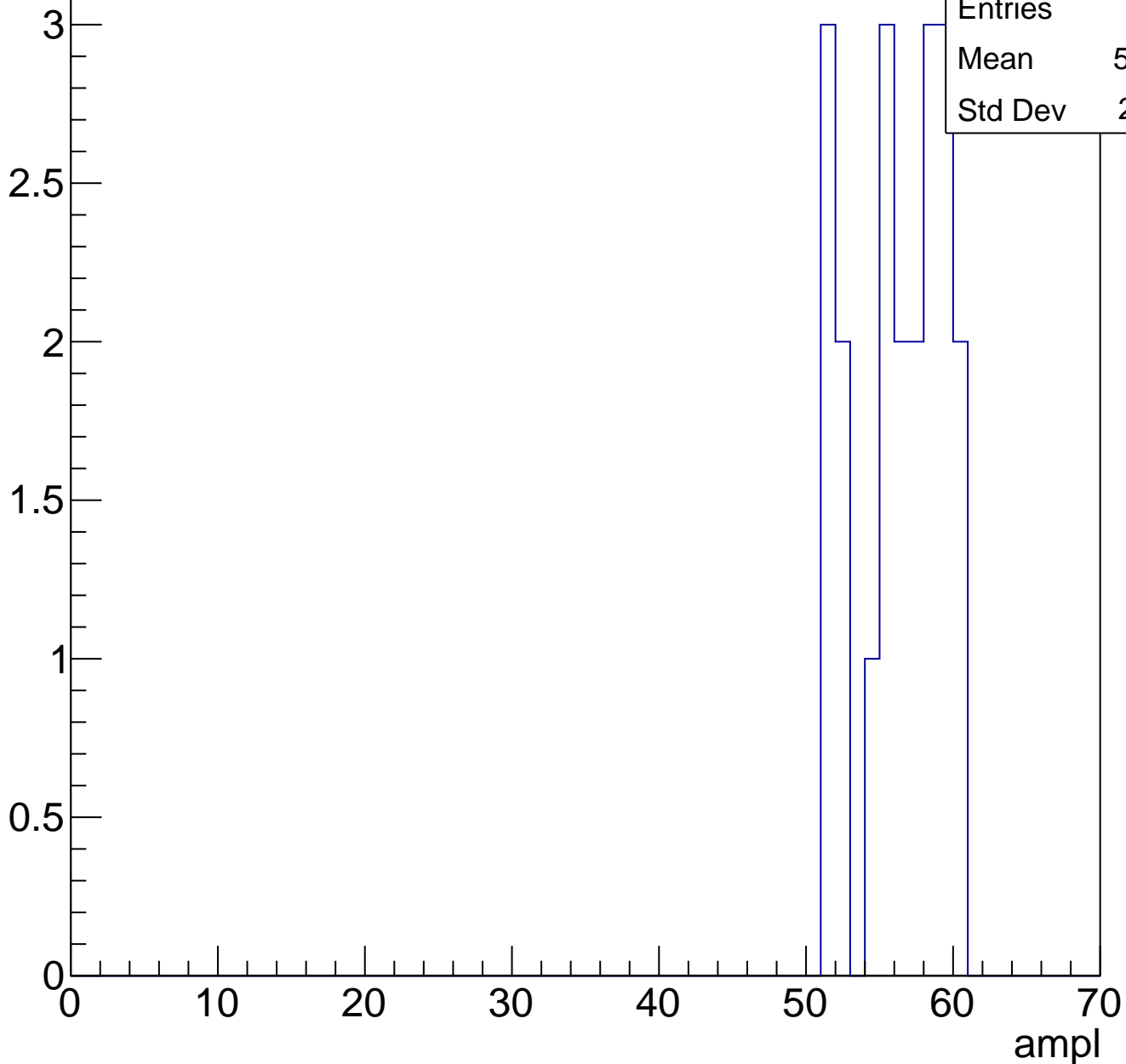
Entries	30
Mean	50.33
Std Dev	3.636



B1L103S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

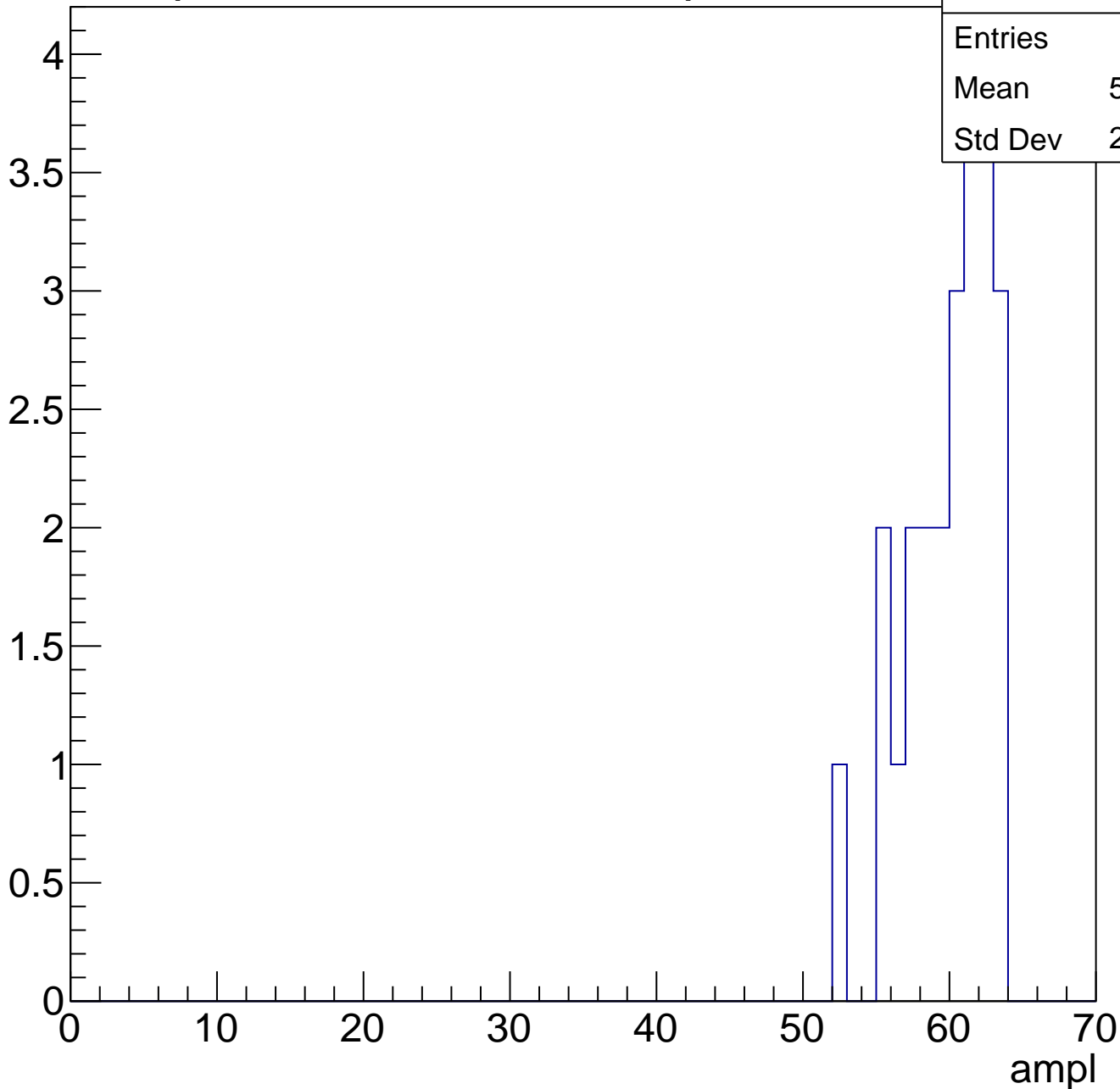


Entries	21
Mean	55.86
Std Dev	2.981

B1L103S, U15-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	24
Mean	59.46
Std Dev	2.872

B1L103S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B1L103S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U15-ch24, adc0

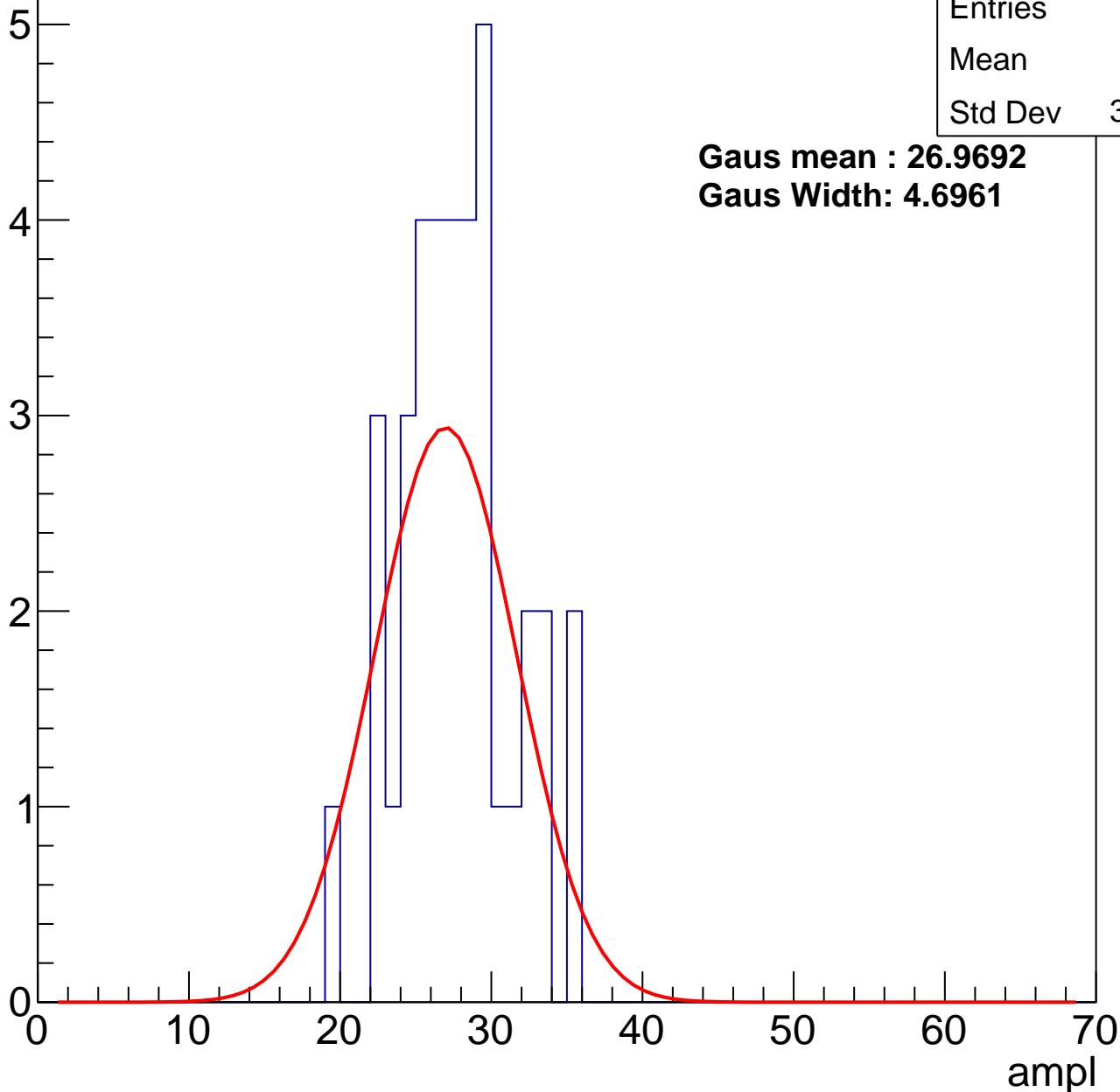
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	27.3
Std Dev	3.668

Gaus mean : 26.9692

Gaus Width: 4.6961



B1L103S, U15-ch24, adc1

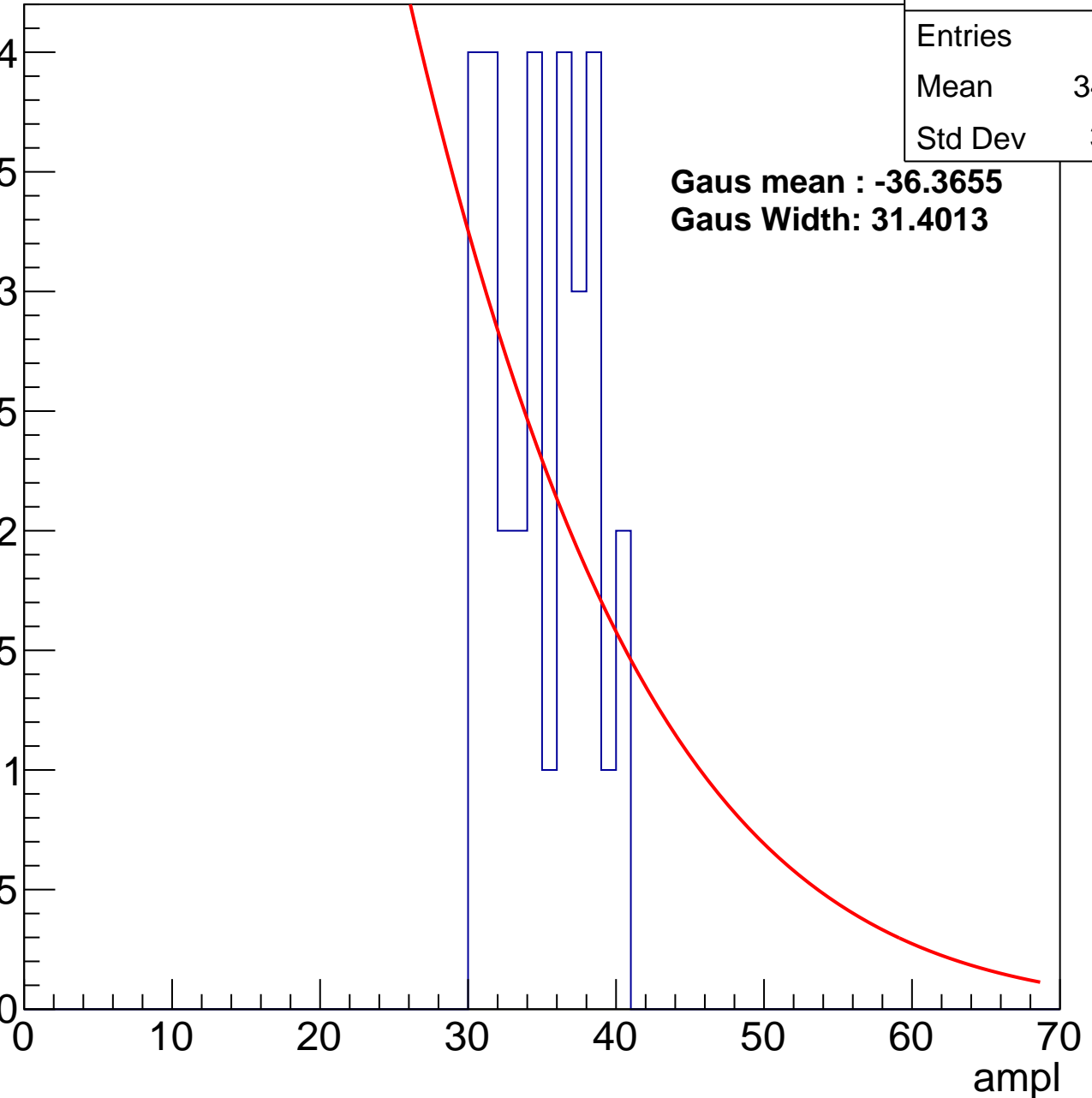
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	31
Mean	34.55
Std Dev	3.14

Gaus mean : -36.3655
Gaus Width: 31.4013



B1L103S, U15-ch24, adc2

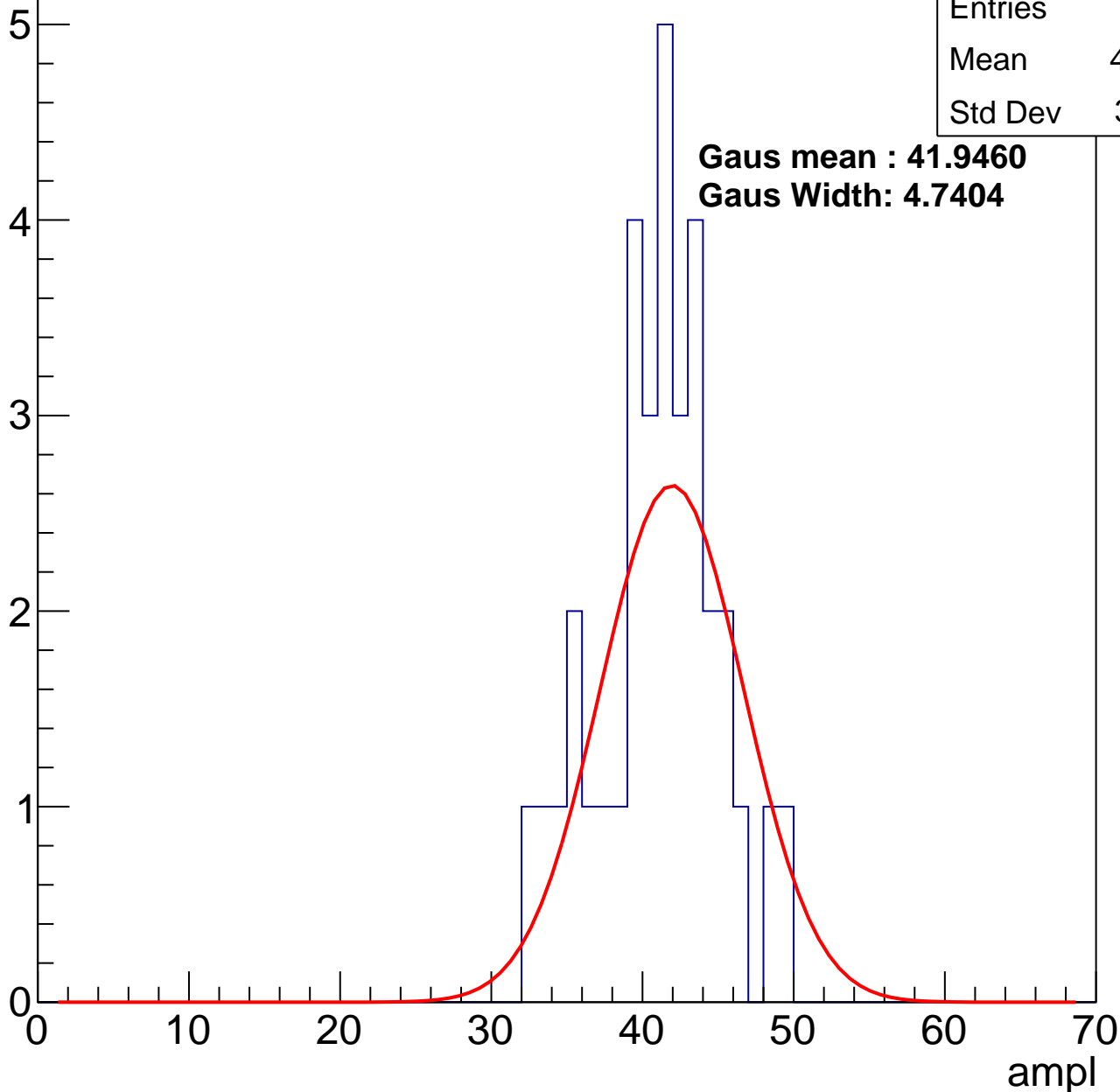
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	40.59
Std Dev	3.971

Gaus mean : 41.9460

Gaus Width: 4.7404

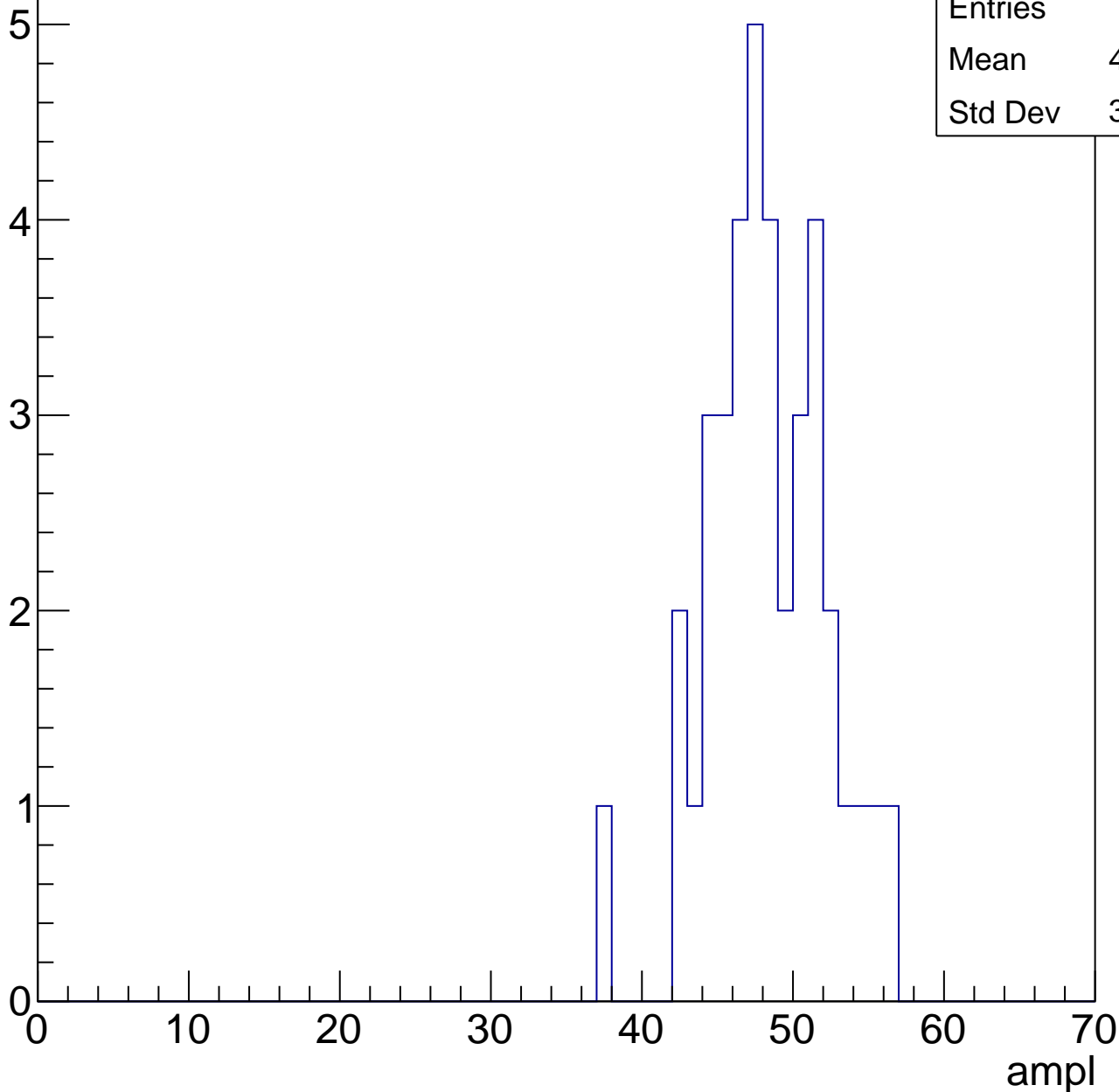


B1L103S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

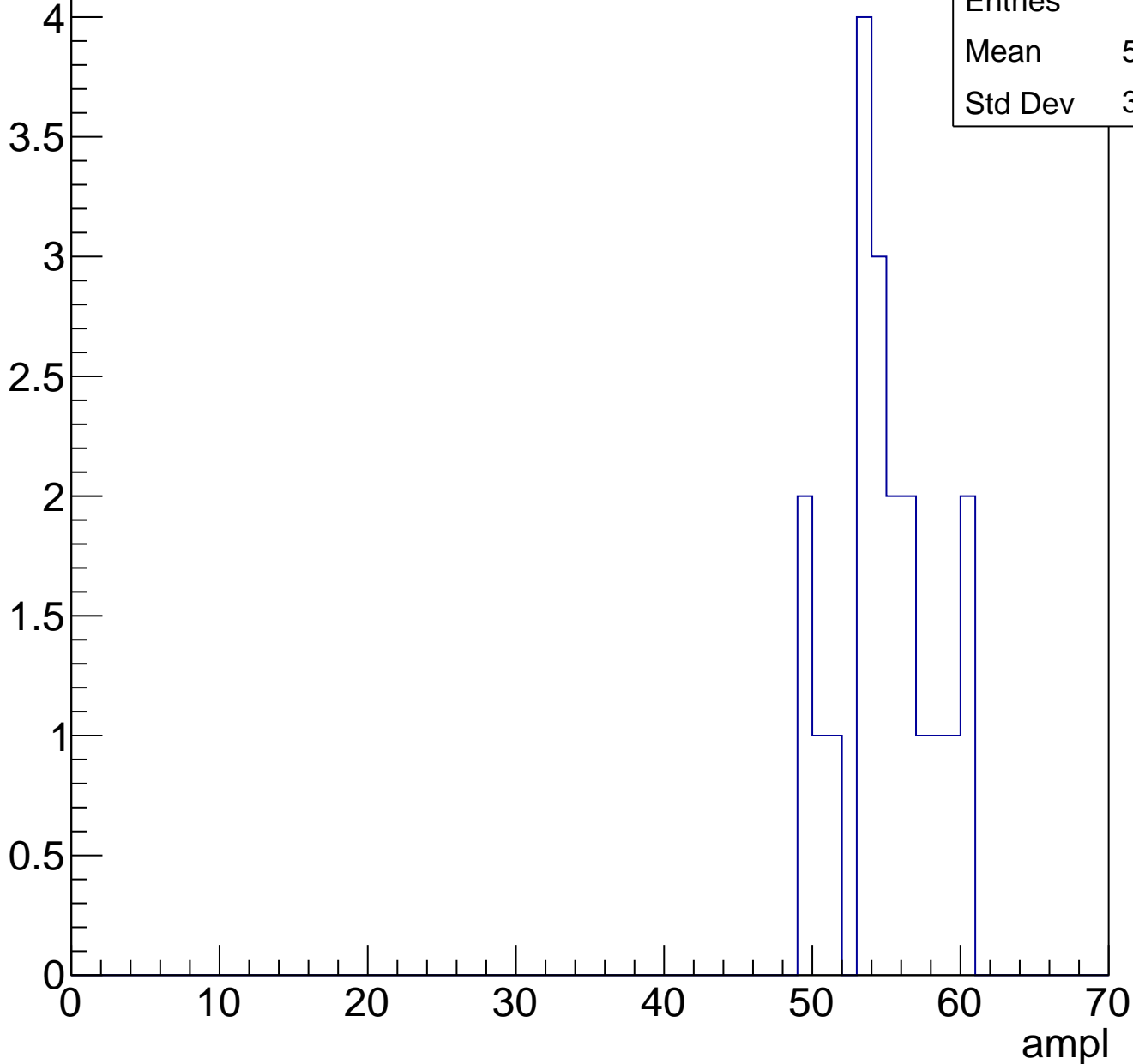
Entries	38
Mean	47.79
Std Dev	3.867



B1L103S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

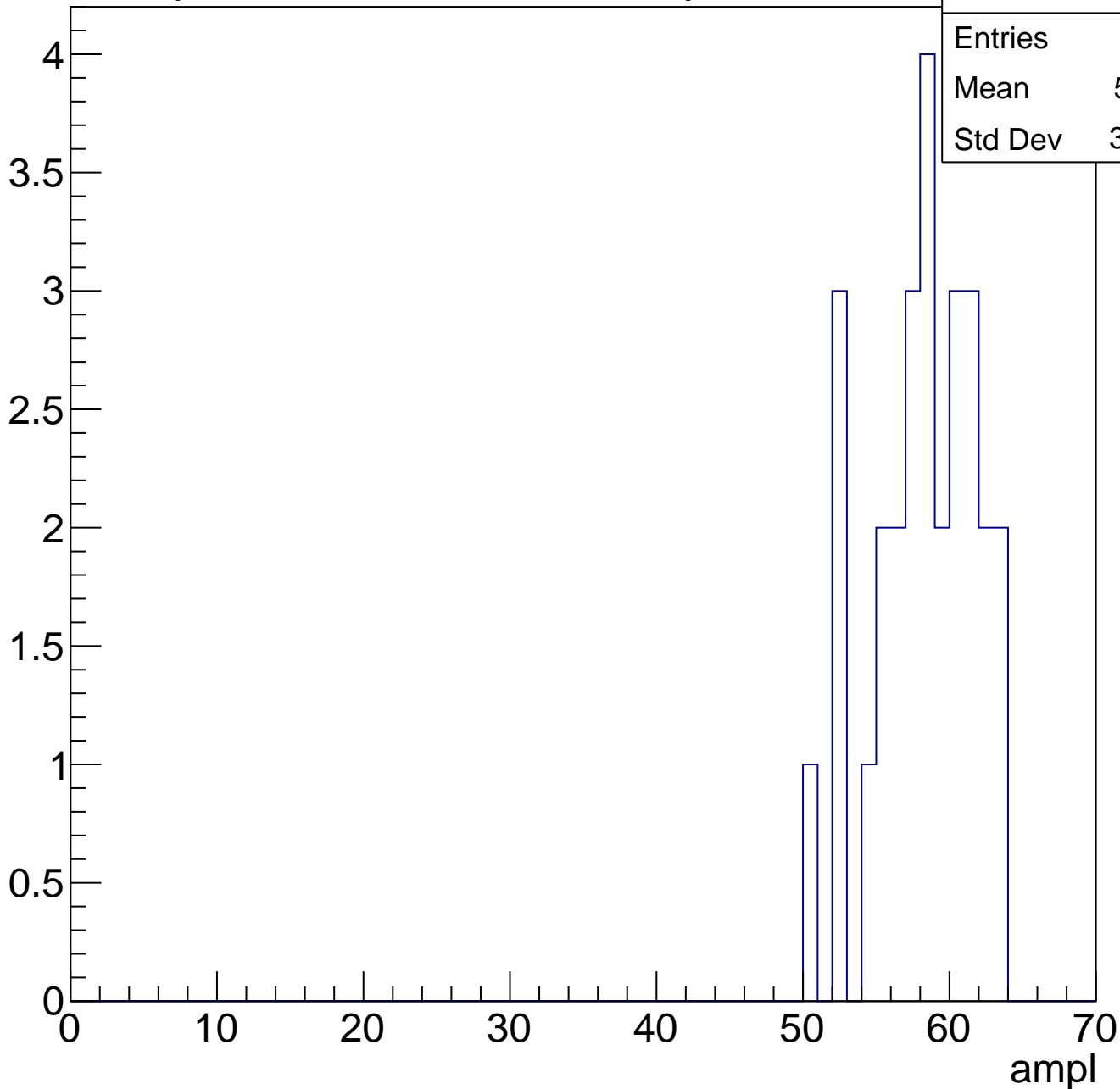
Entry



B1L103S, U15-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

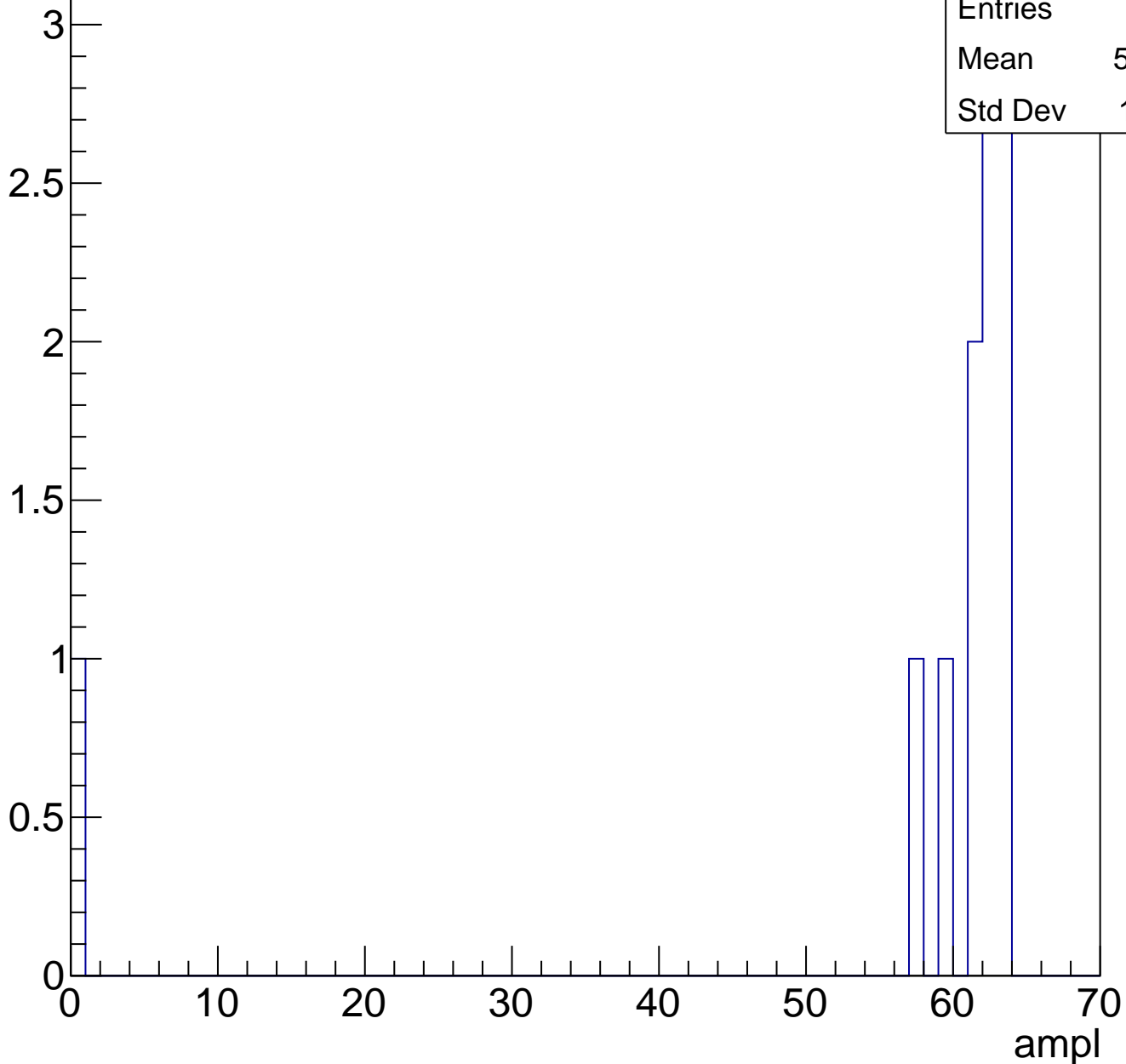


Entries	28
Mean	57.71
Std Dev	3.473

B1L103S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

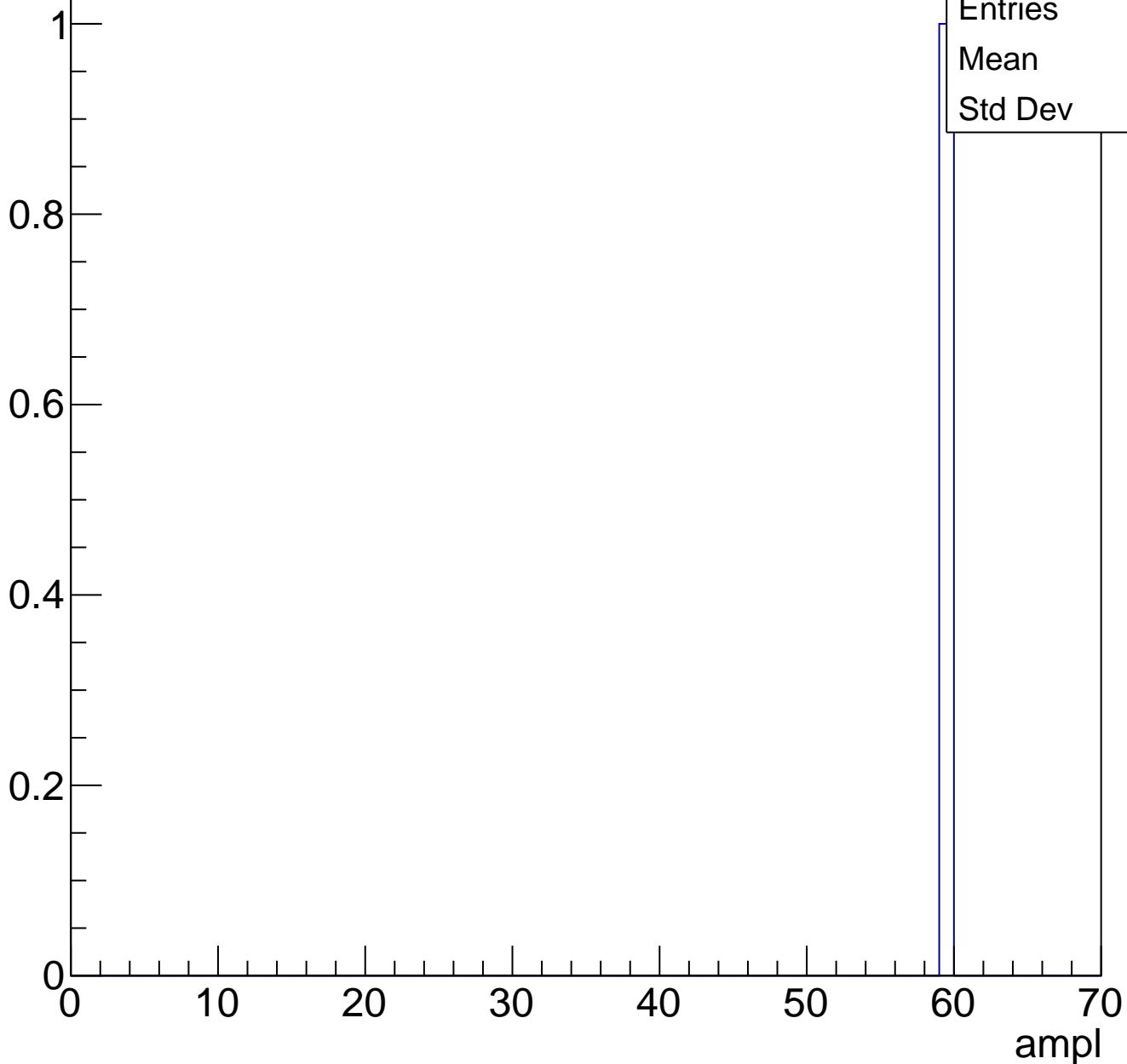
Entry



B1L103S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

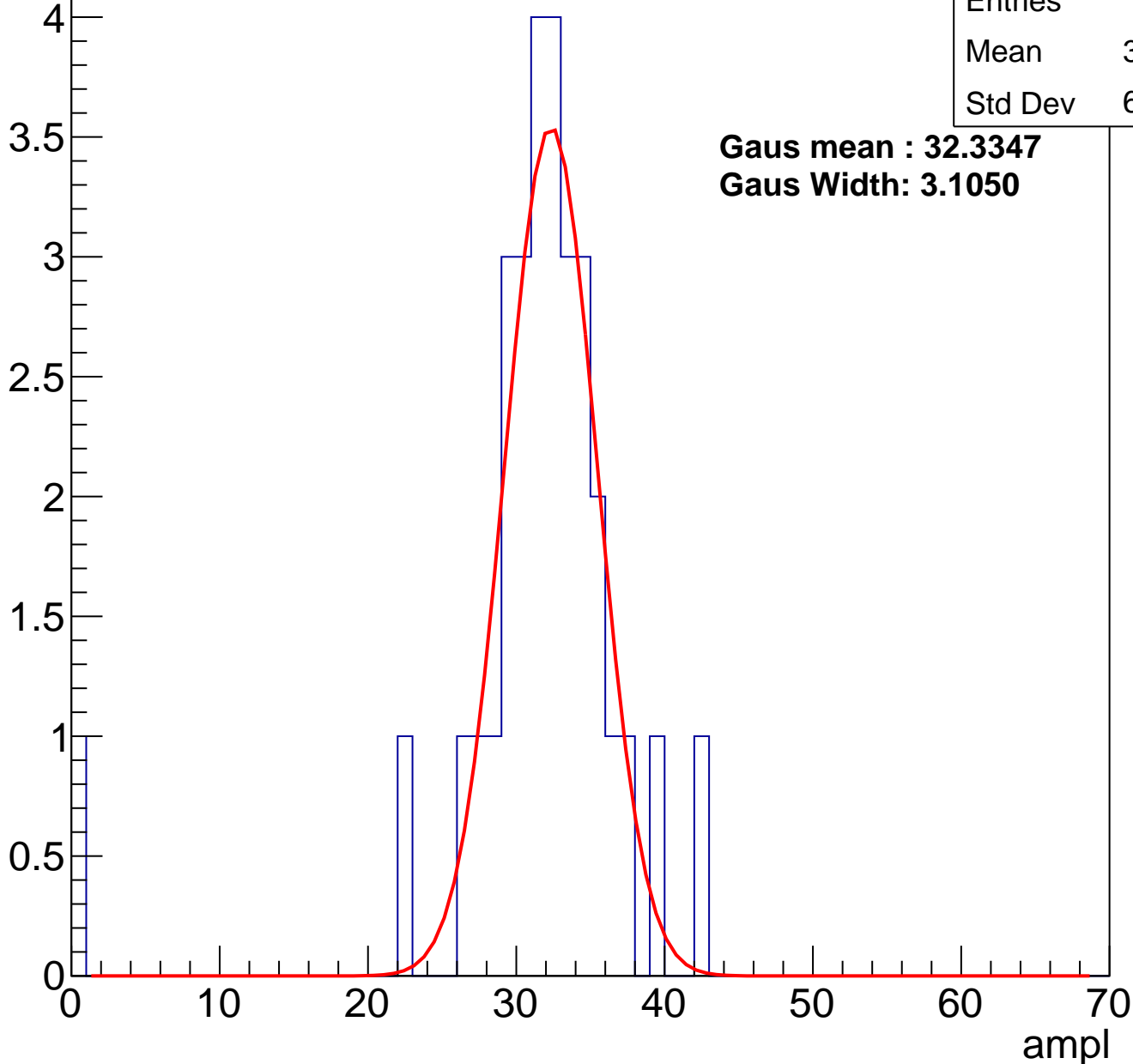
Entry



B1L103S, U15-ch25, adc0

calib_packv5_042523_0143.root, FC#7, port C2

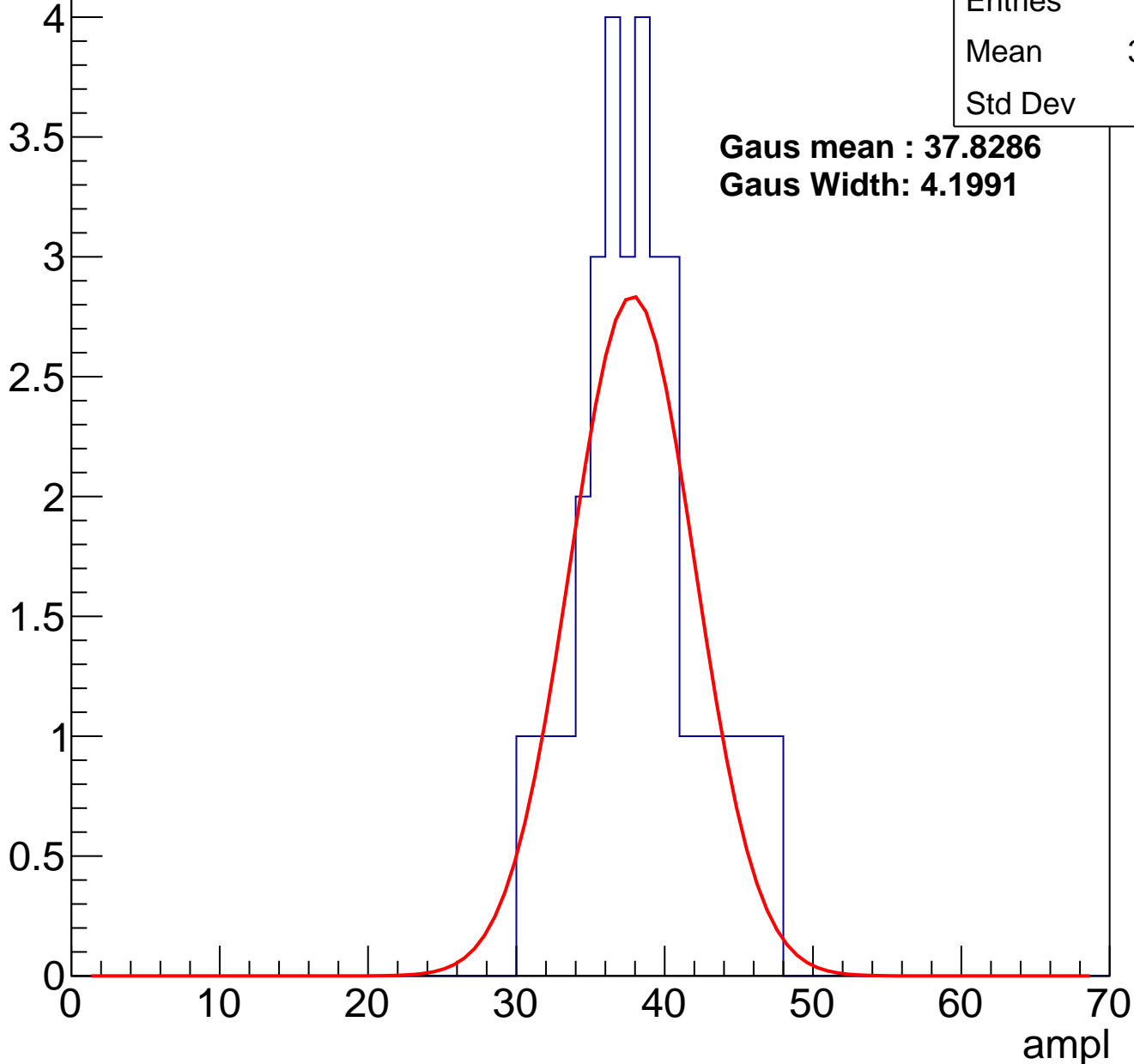
Entry



B1L103S, U15-ch25, adc1

calib_packv5_042523_0143.root, FC#7, port C2

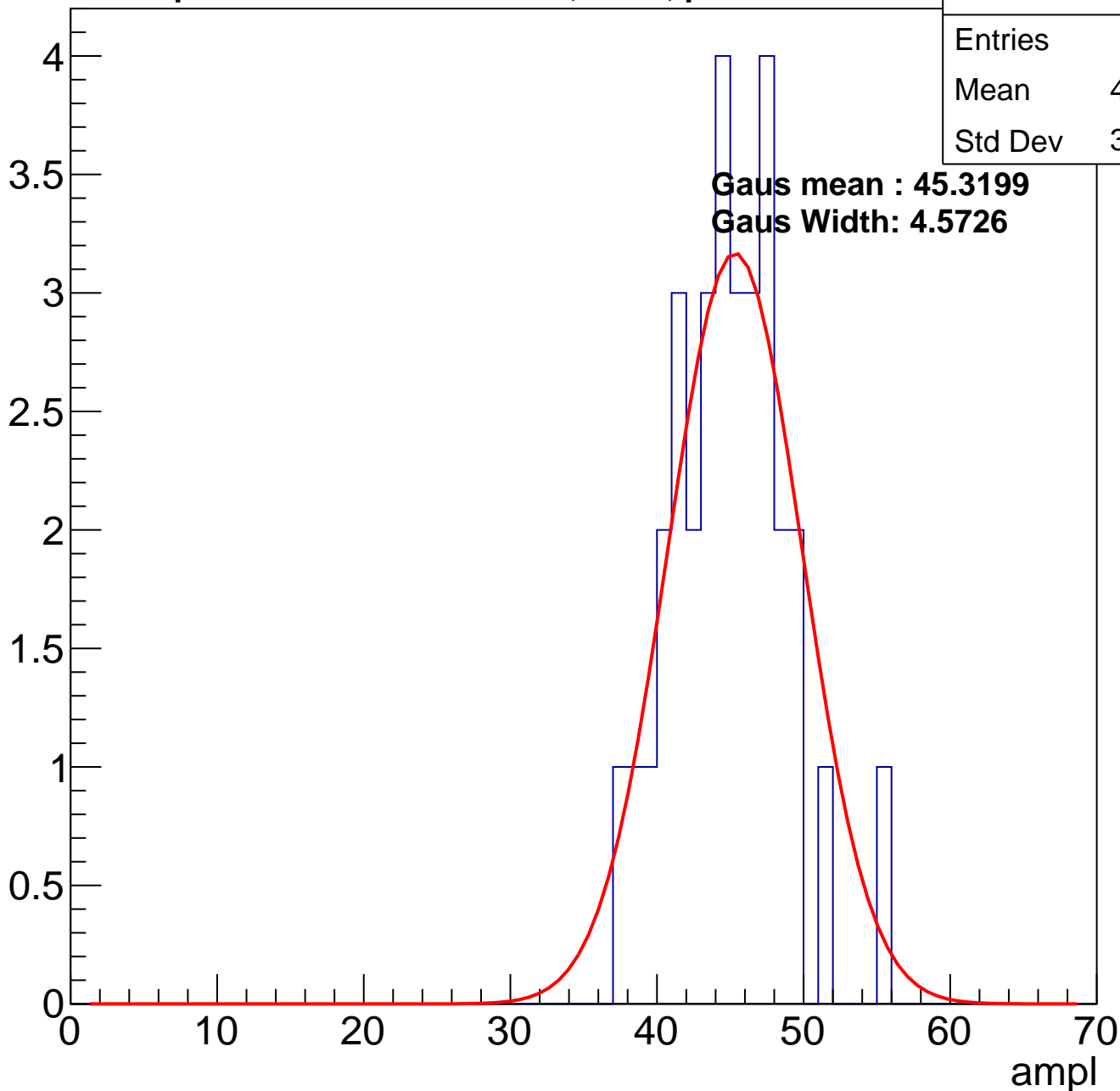
Entry



B1L103S, U15-ch25, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

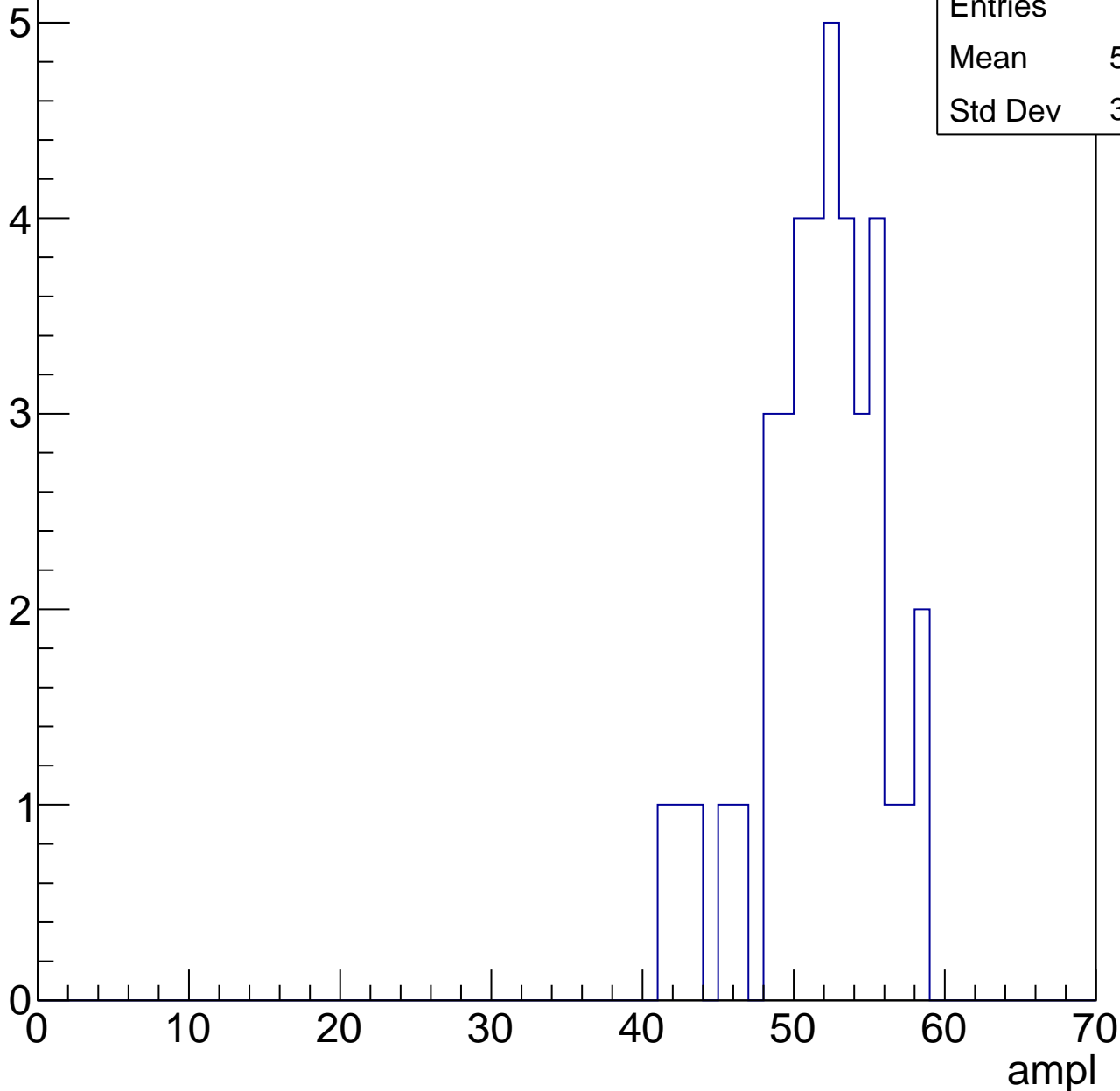


B1L103S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

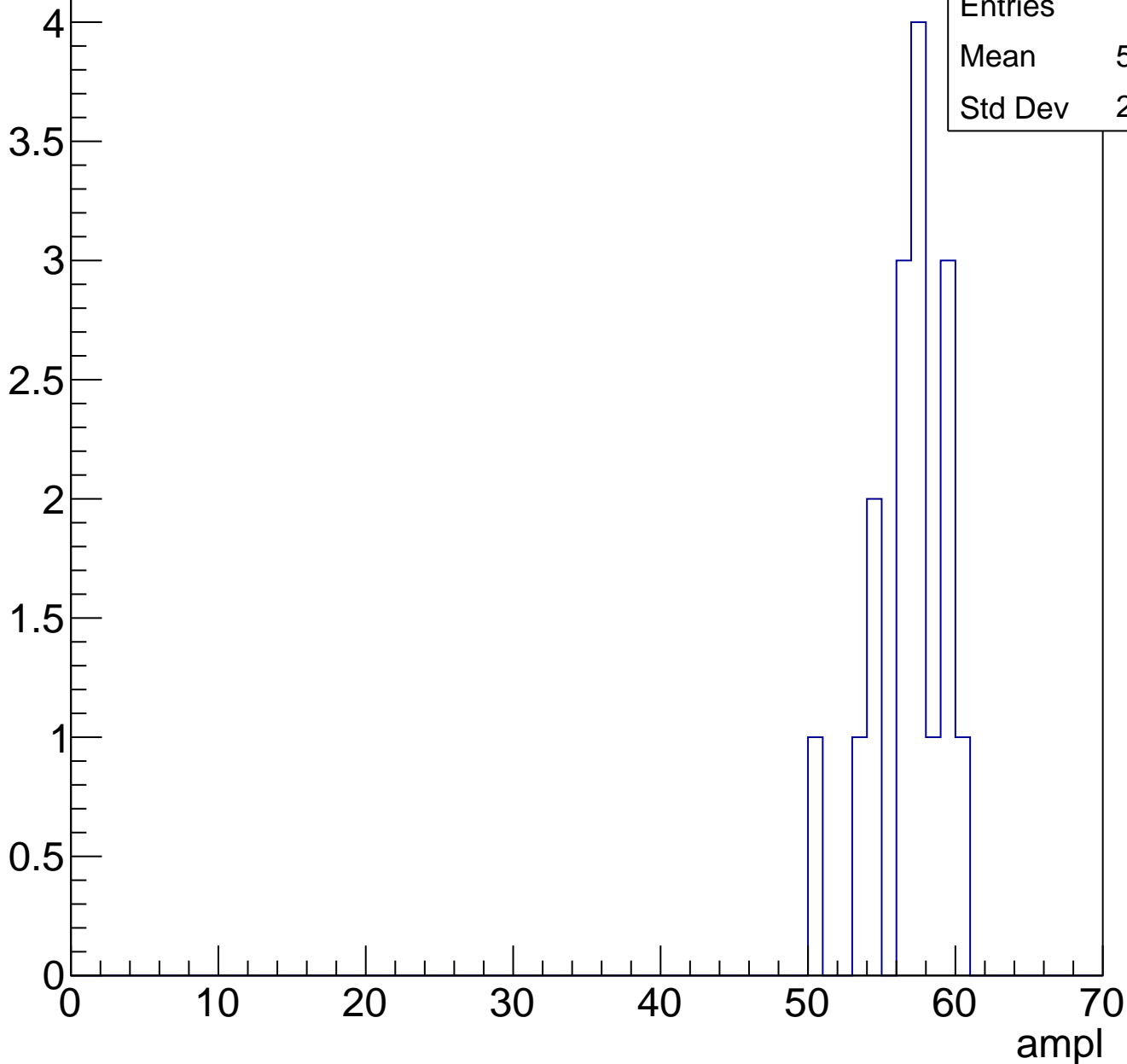
Entries	39
Mean	51.15
Std Dev	3.984



B1L103S, U15-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

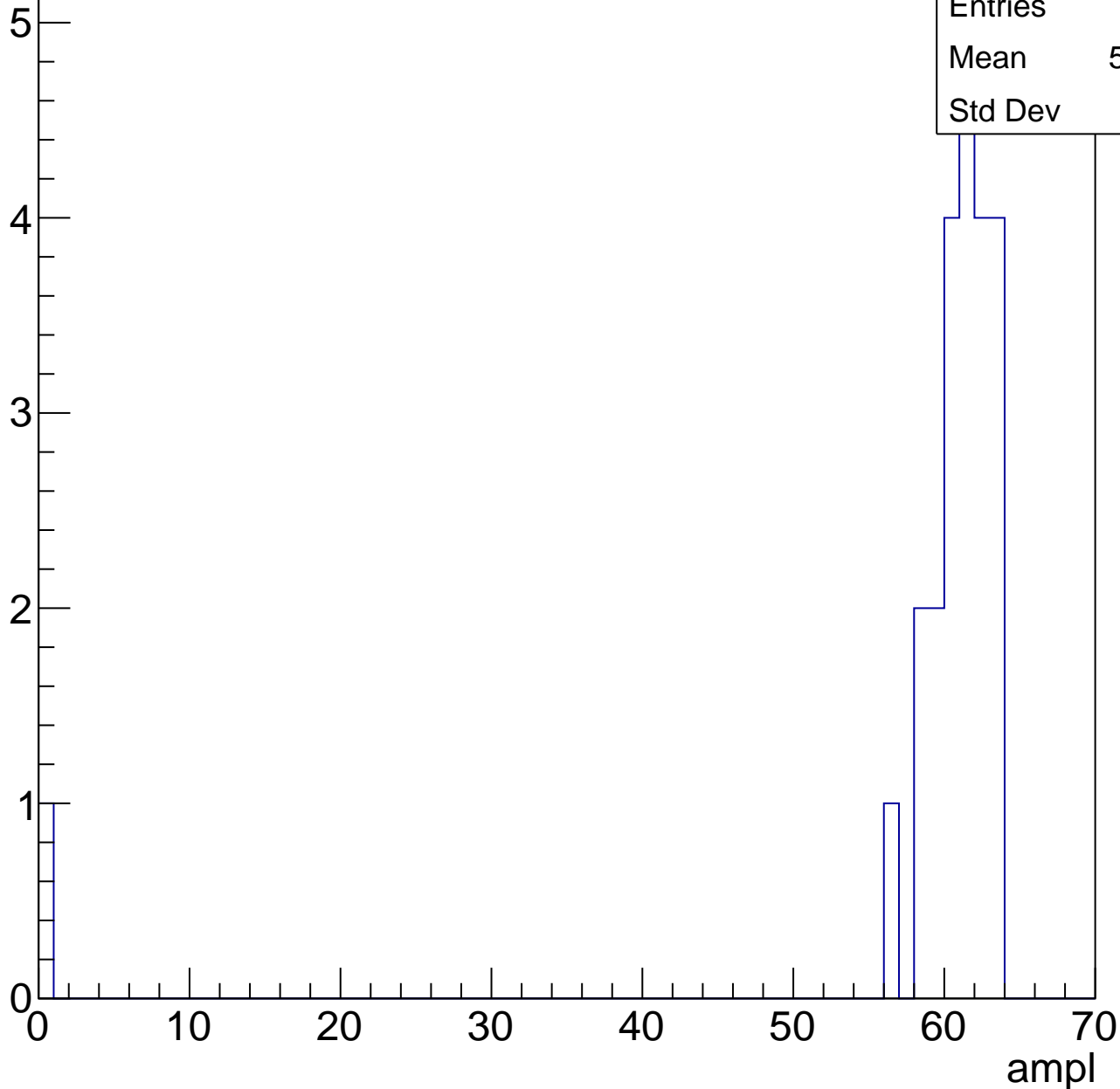


B1L103S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	58.04
Std Dev	12.5



B1L103S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch26, adc0

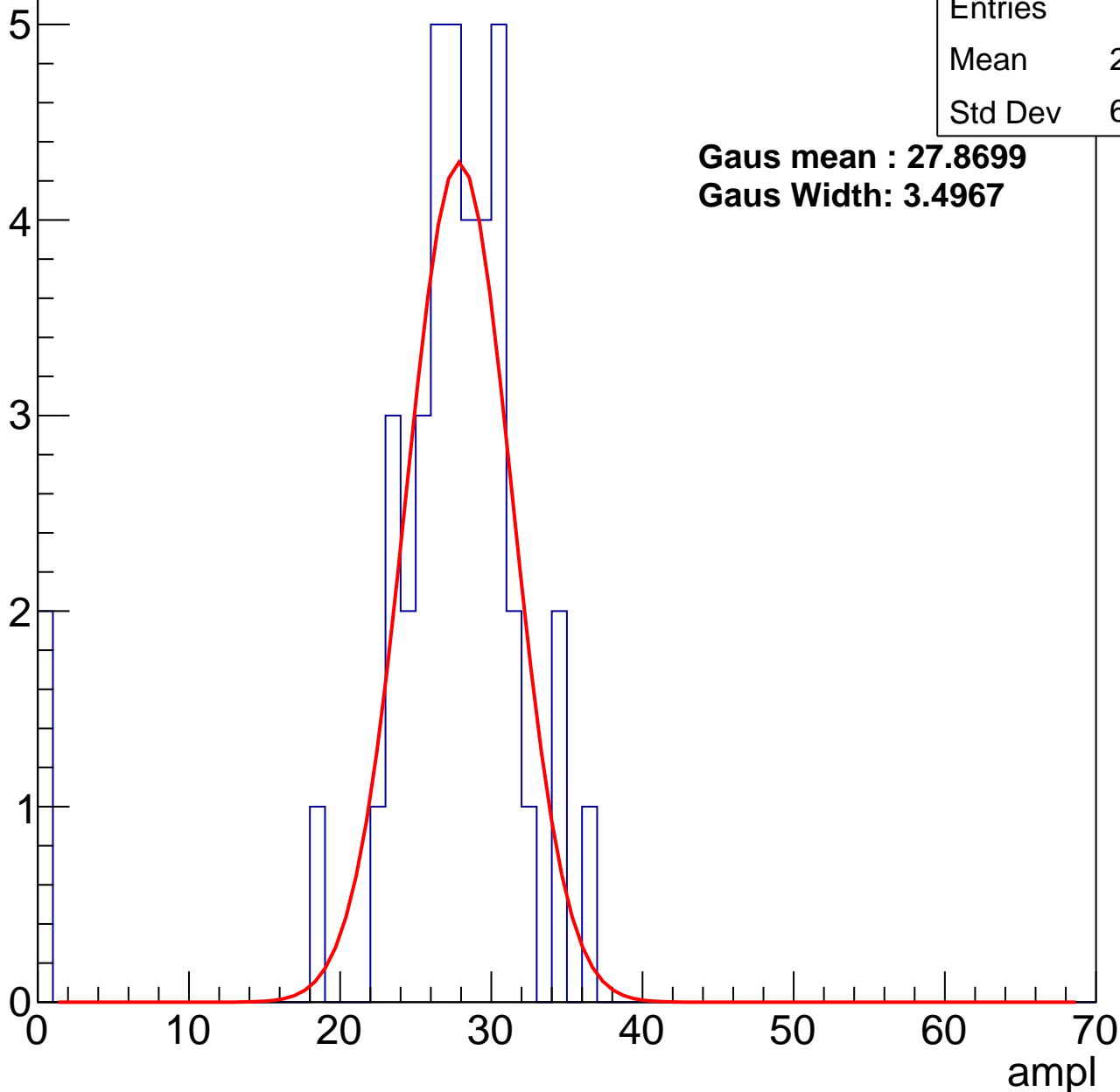
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	26.17
Std Dev	6.843

Gaus mean : 27.8699

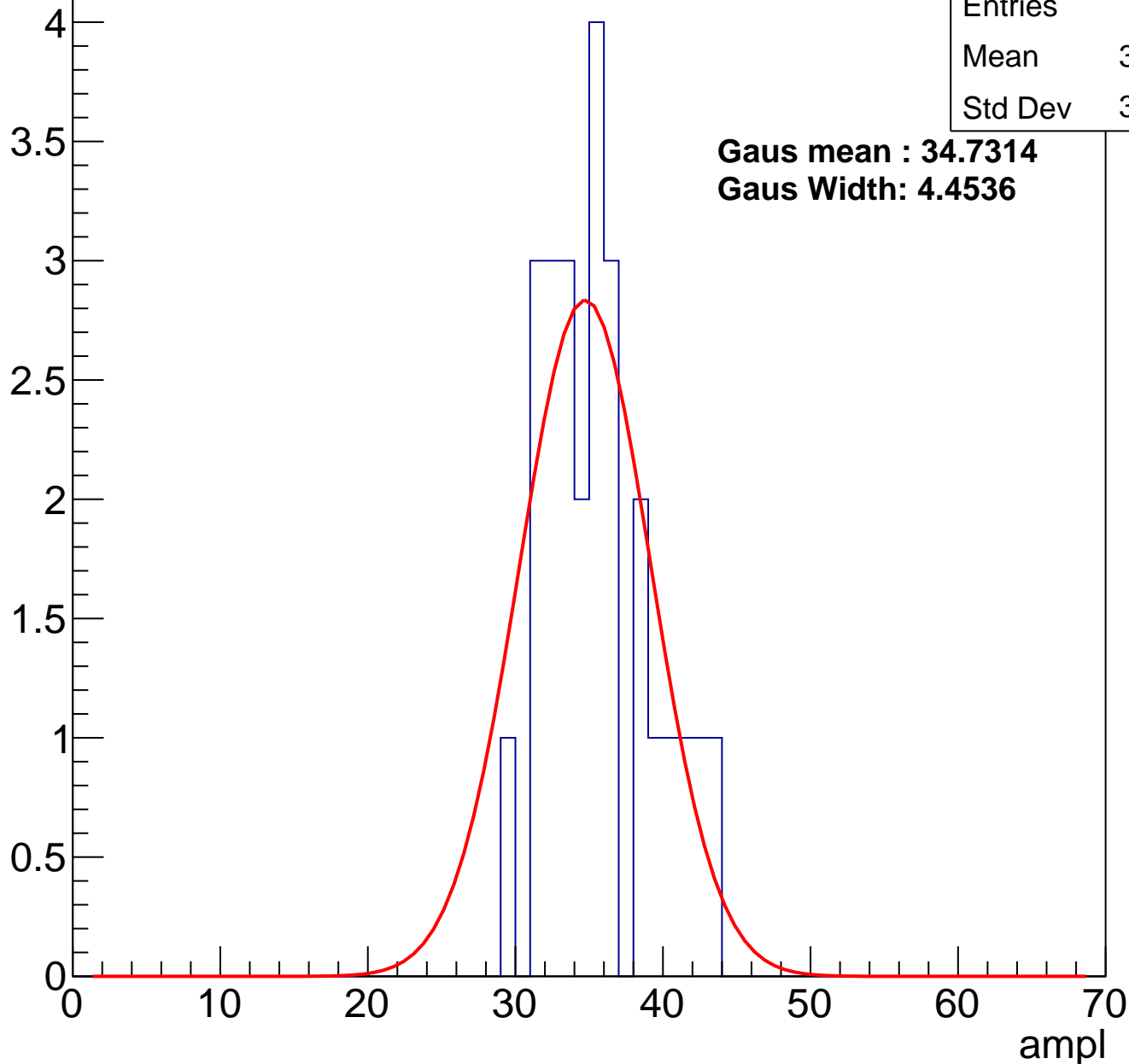
Gaus Width: 3.4967



B1L103S, U15-ch26, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch26, adc2

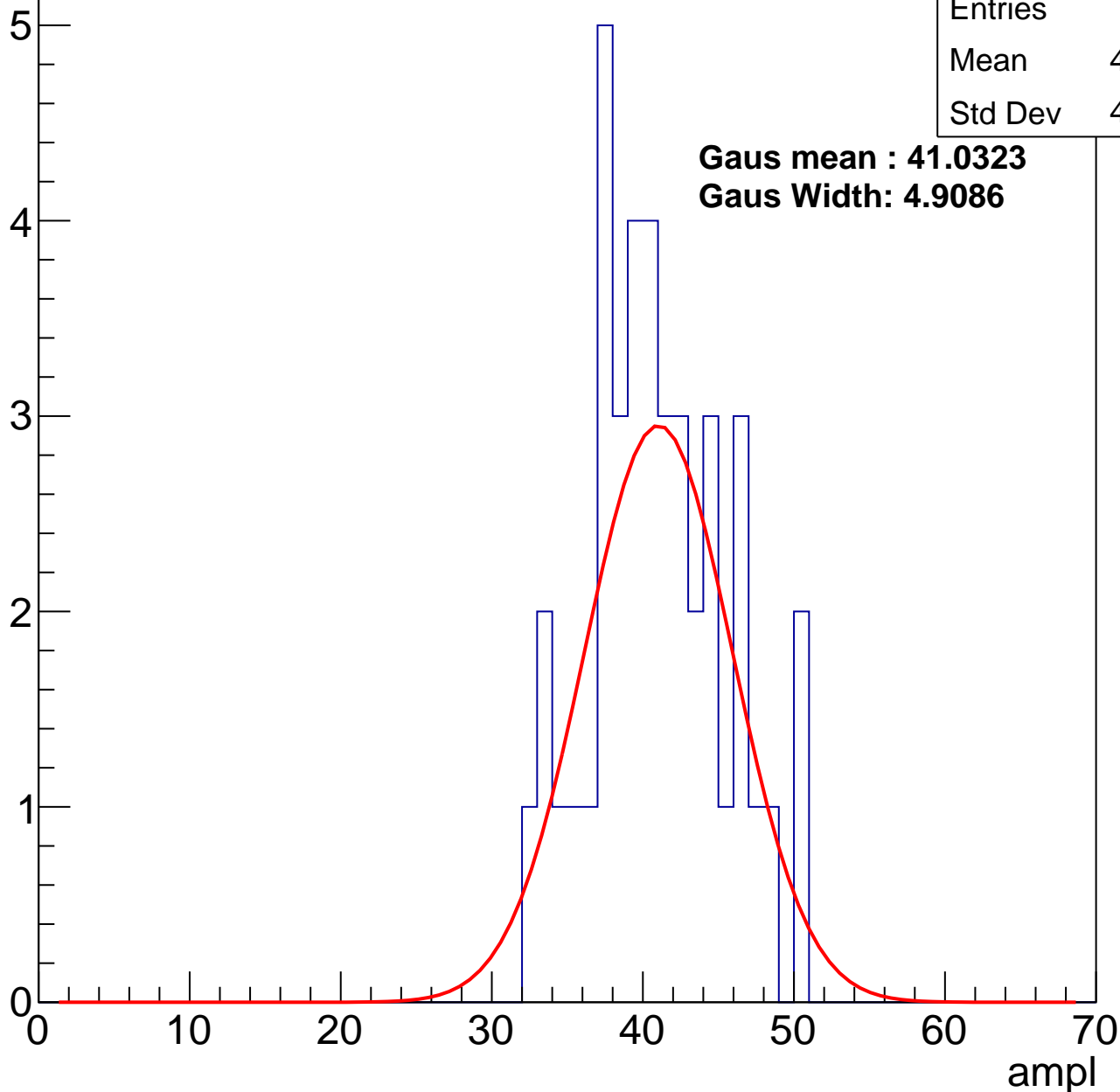
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	40.56
Std Dev	4.456

Gaus mean : 41.0323

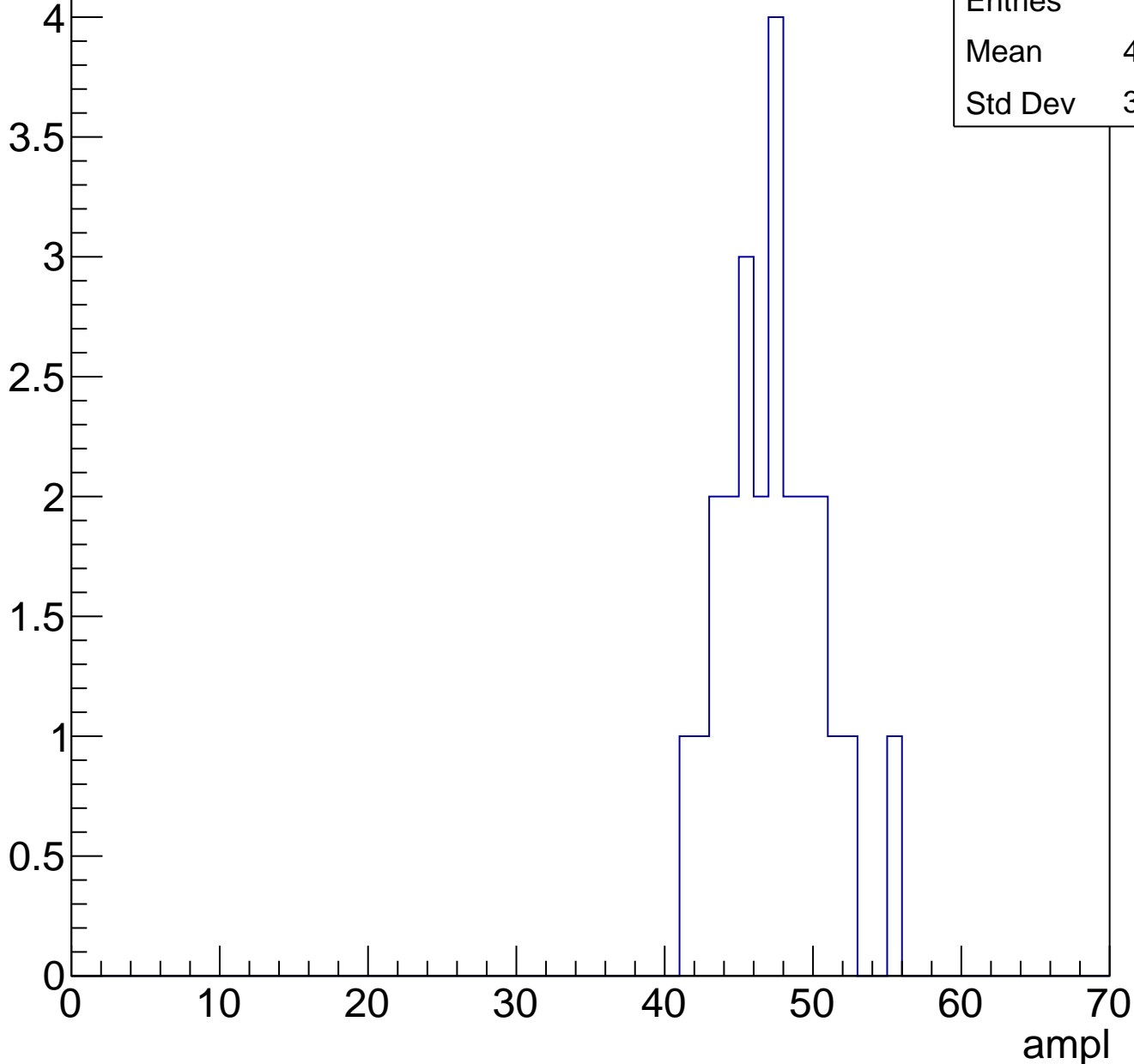
Gaus Width: 4.9086



B1L103S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



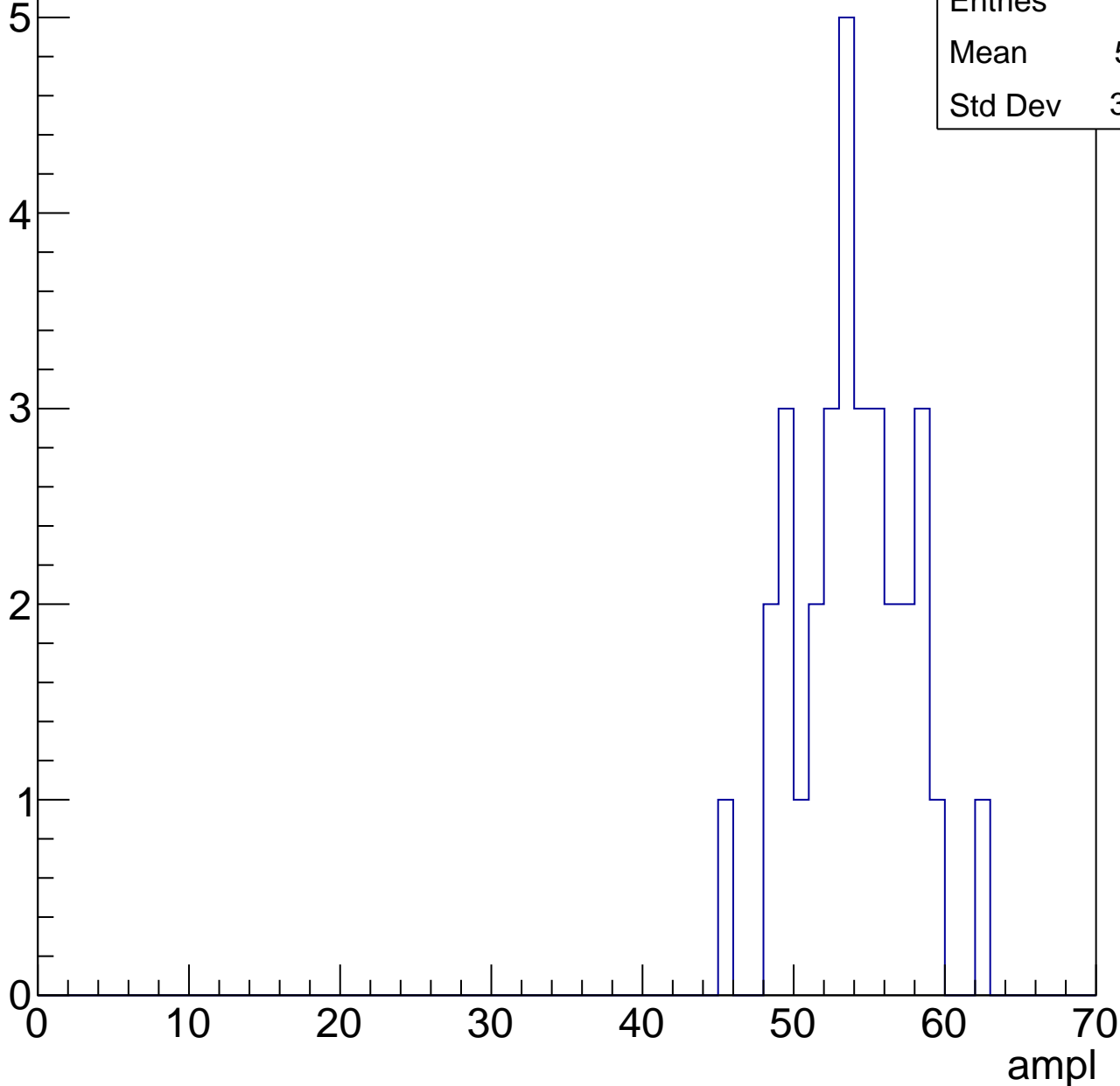
Entries	24
Mean	46.83
Std Dev	3.274

B1L103S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

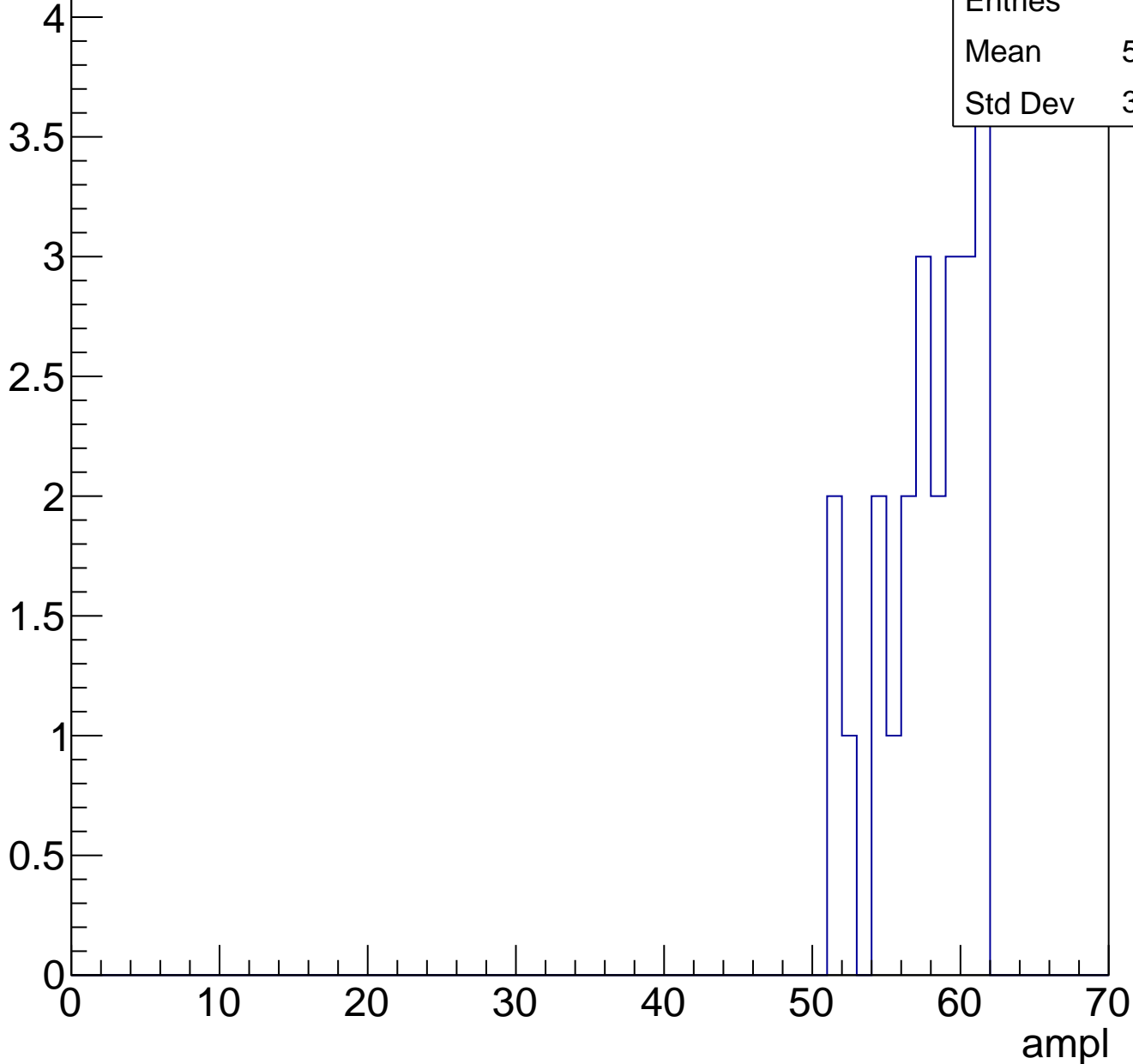
Entries	32
Mean	53.41
Std Dev	3.682



B1L103S, U15-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

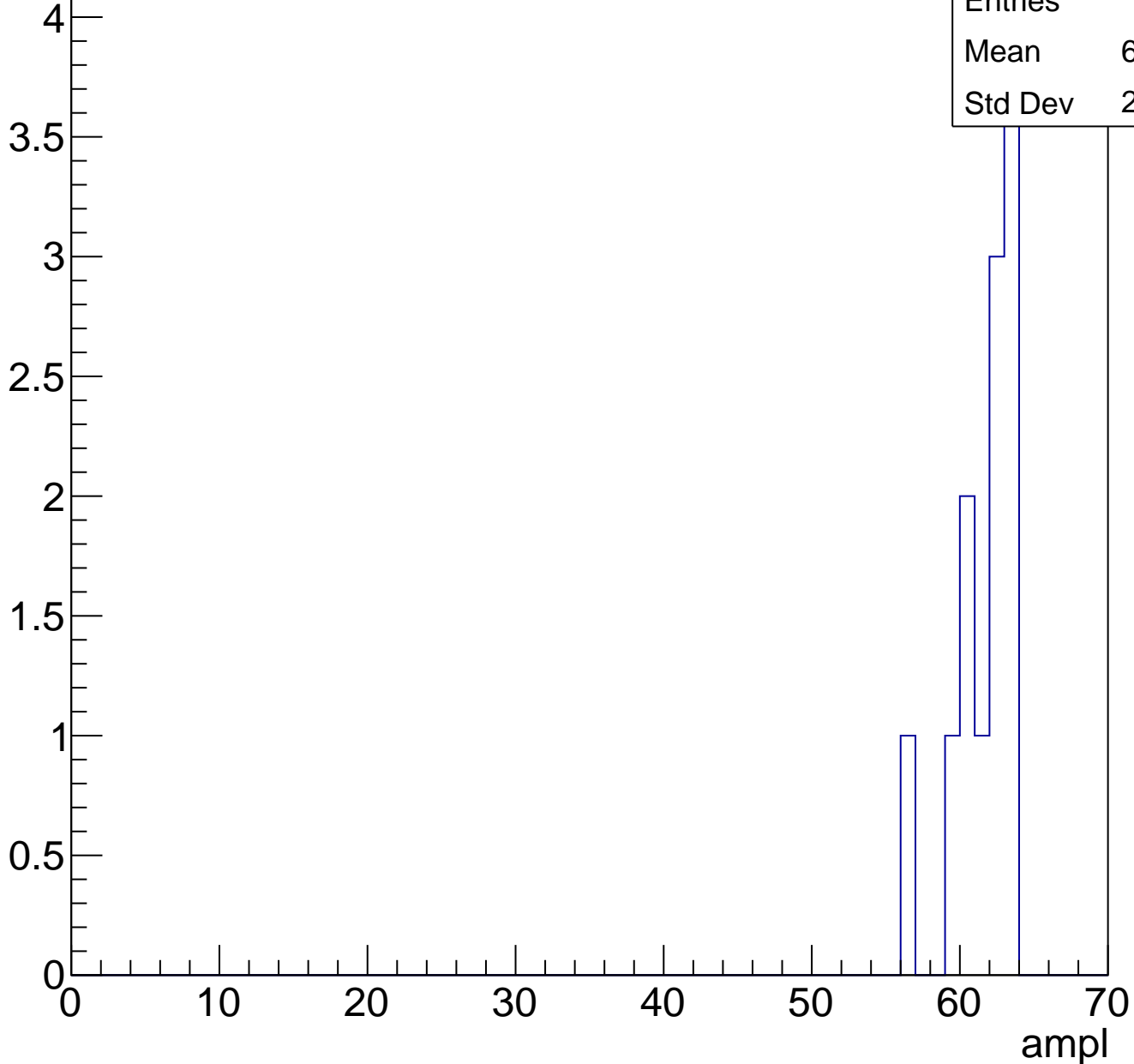
Entry



B1L103S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

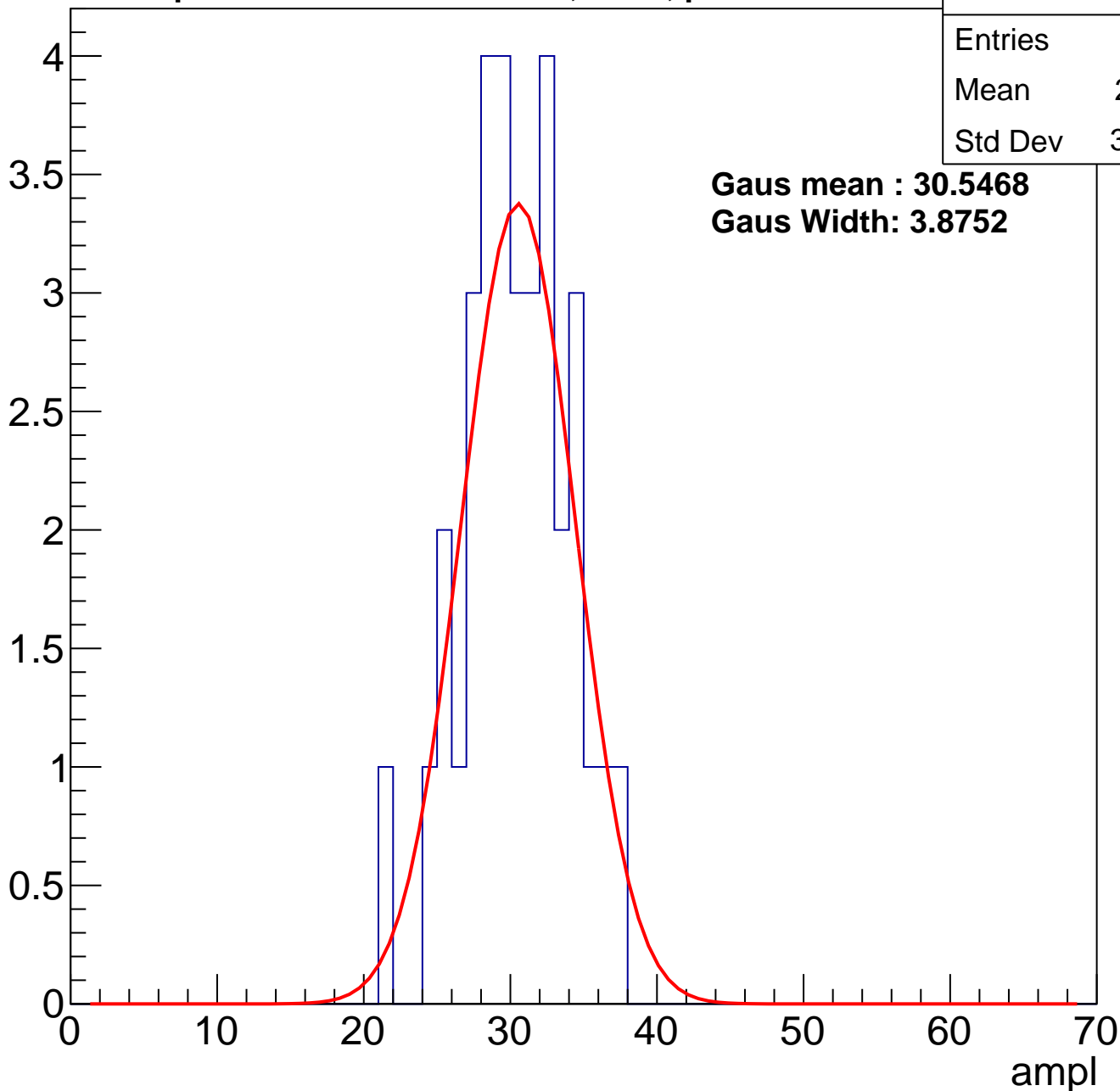
Entry



B1L103S, U15-ch27, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch27, adc1

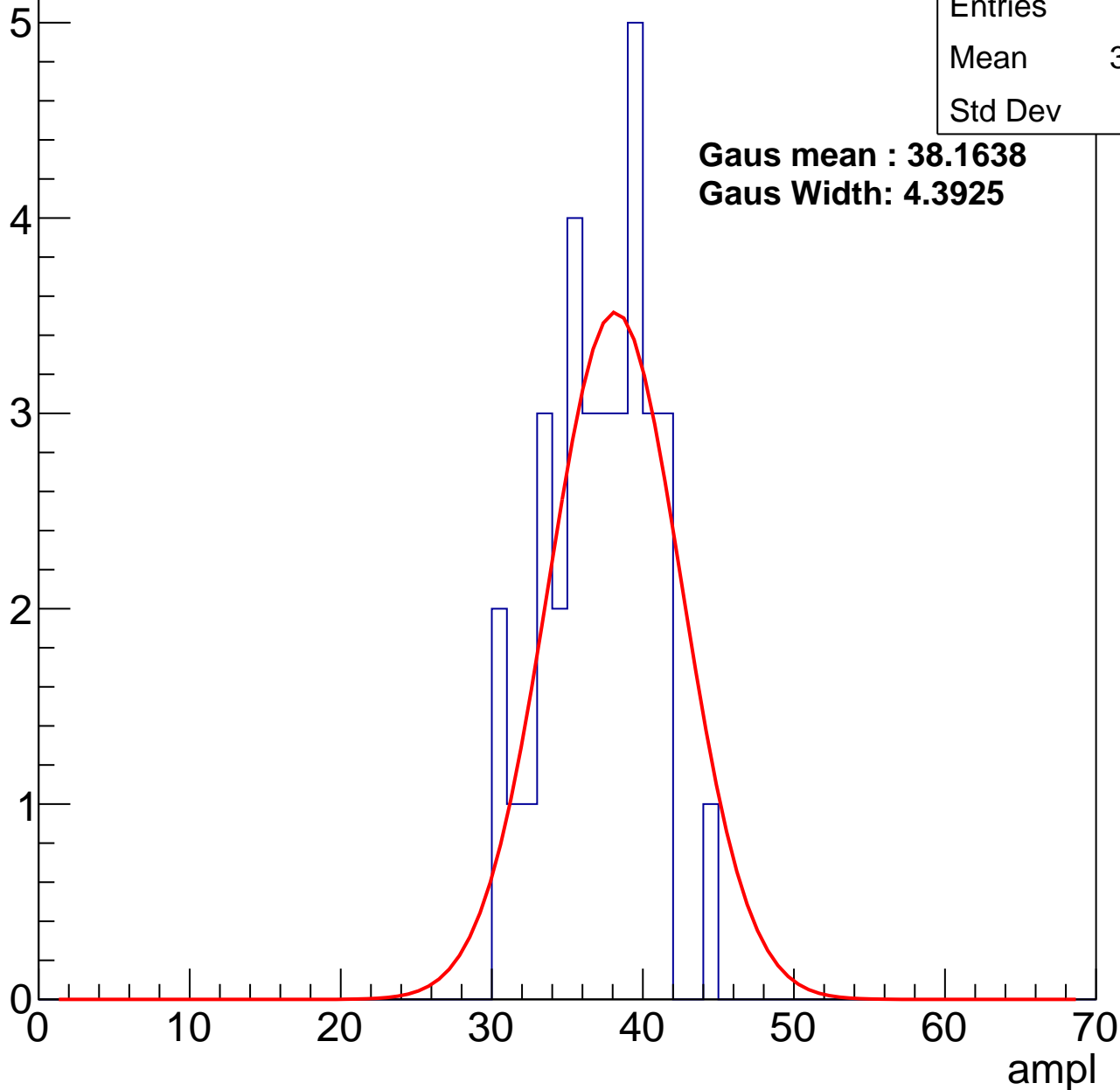
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	36.62
Std Dev	3.37

Gaus mean : 38.1638

Gaus Width: 4.3925



B1L103S, U15-ch27, adc2

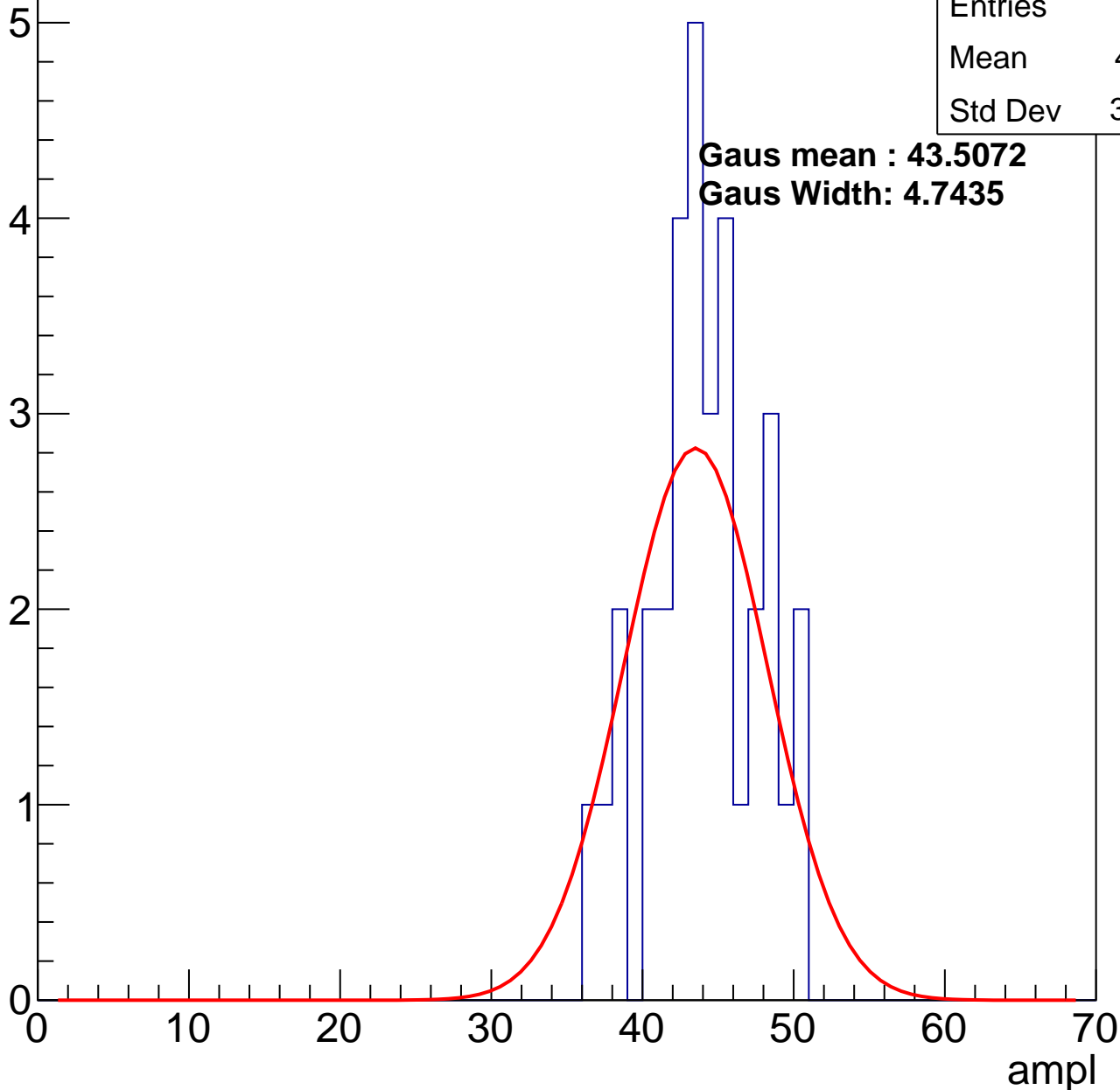
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	43.61
Std Dev	3.576

Gaus mean : 43.5072

Gaus Width: 4.7435

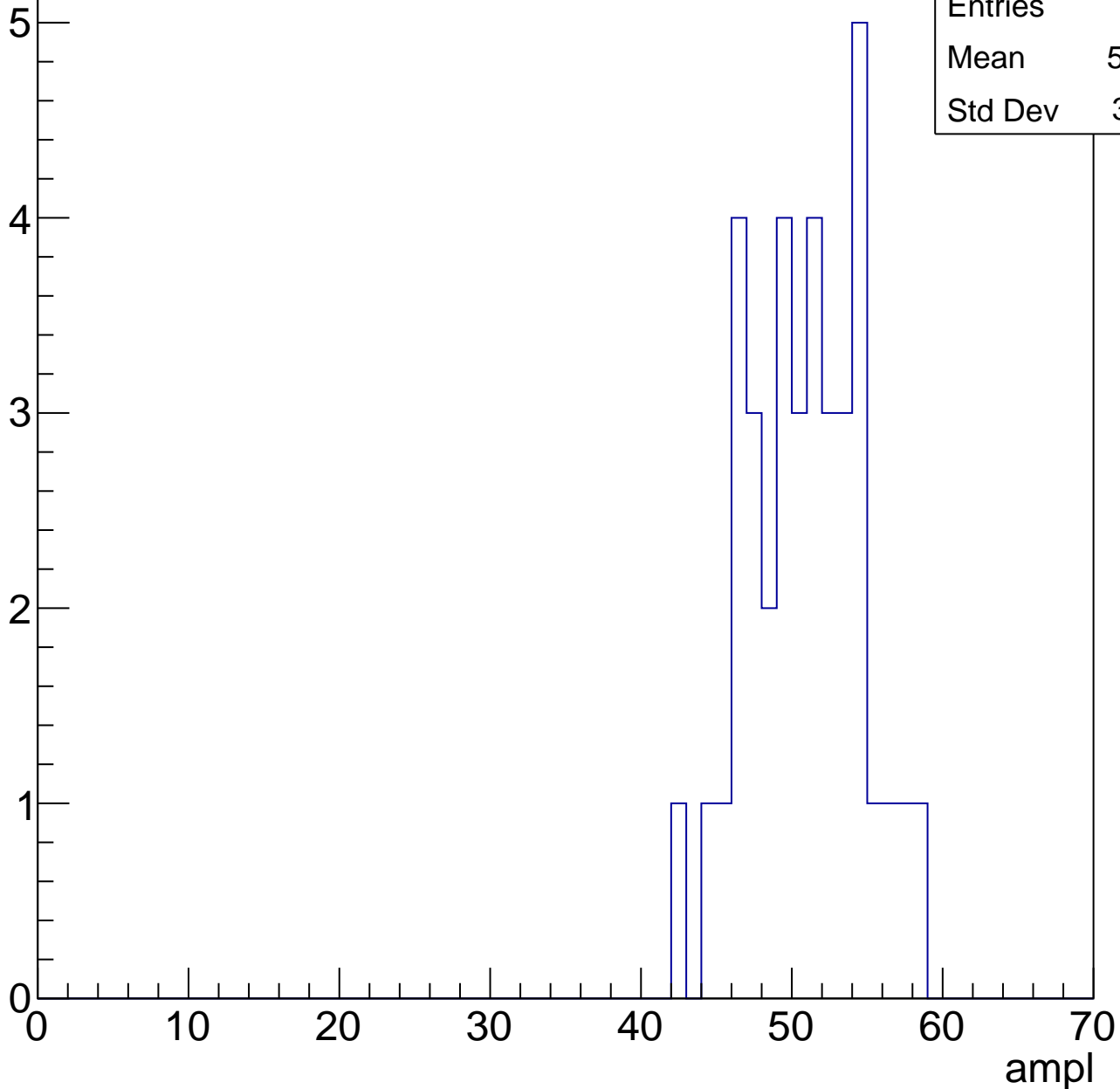


B1L103S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

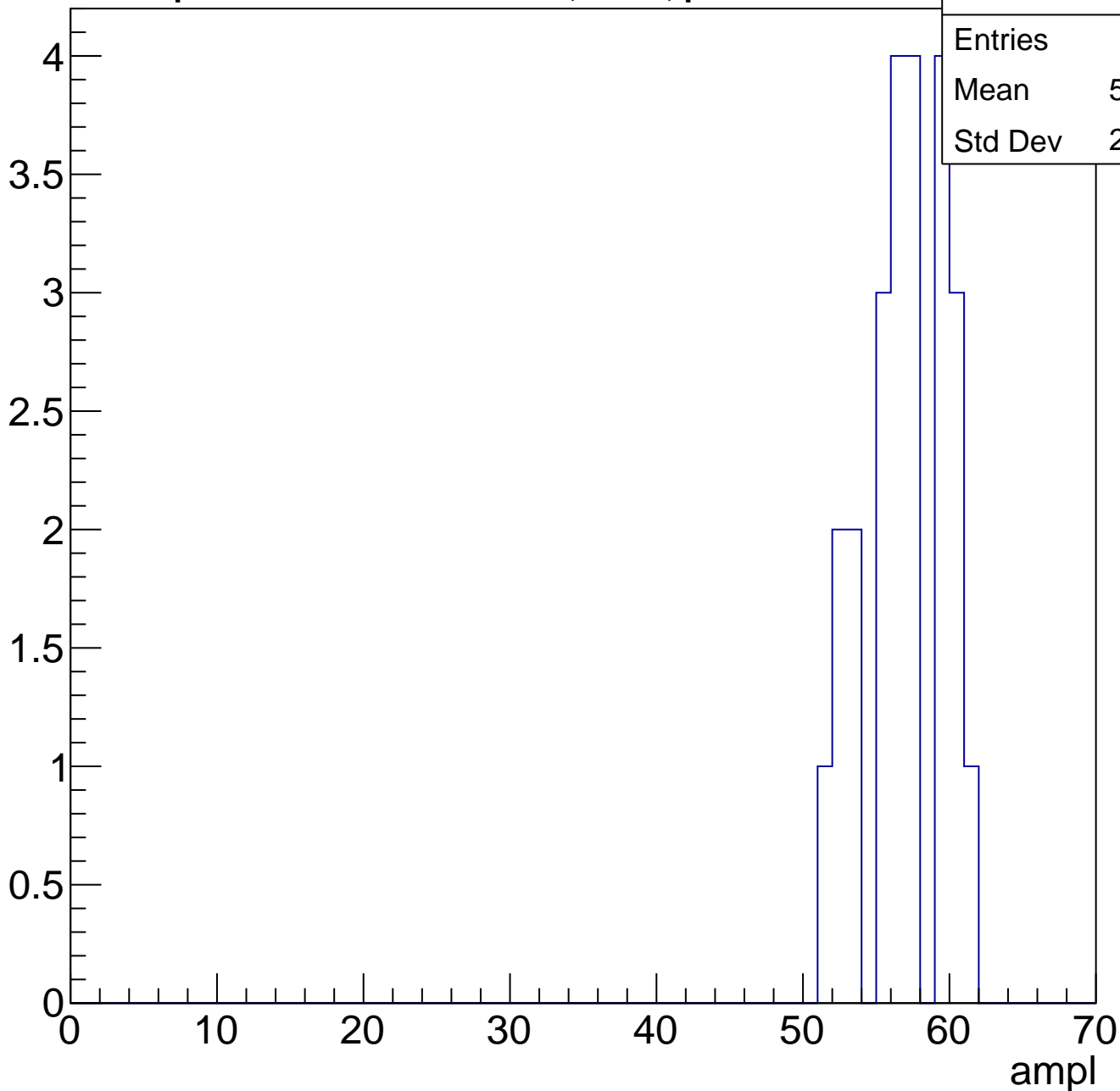
Entries	38
Mean	50.34
Std Dev	3.701



B1L103S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

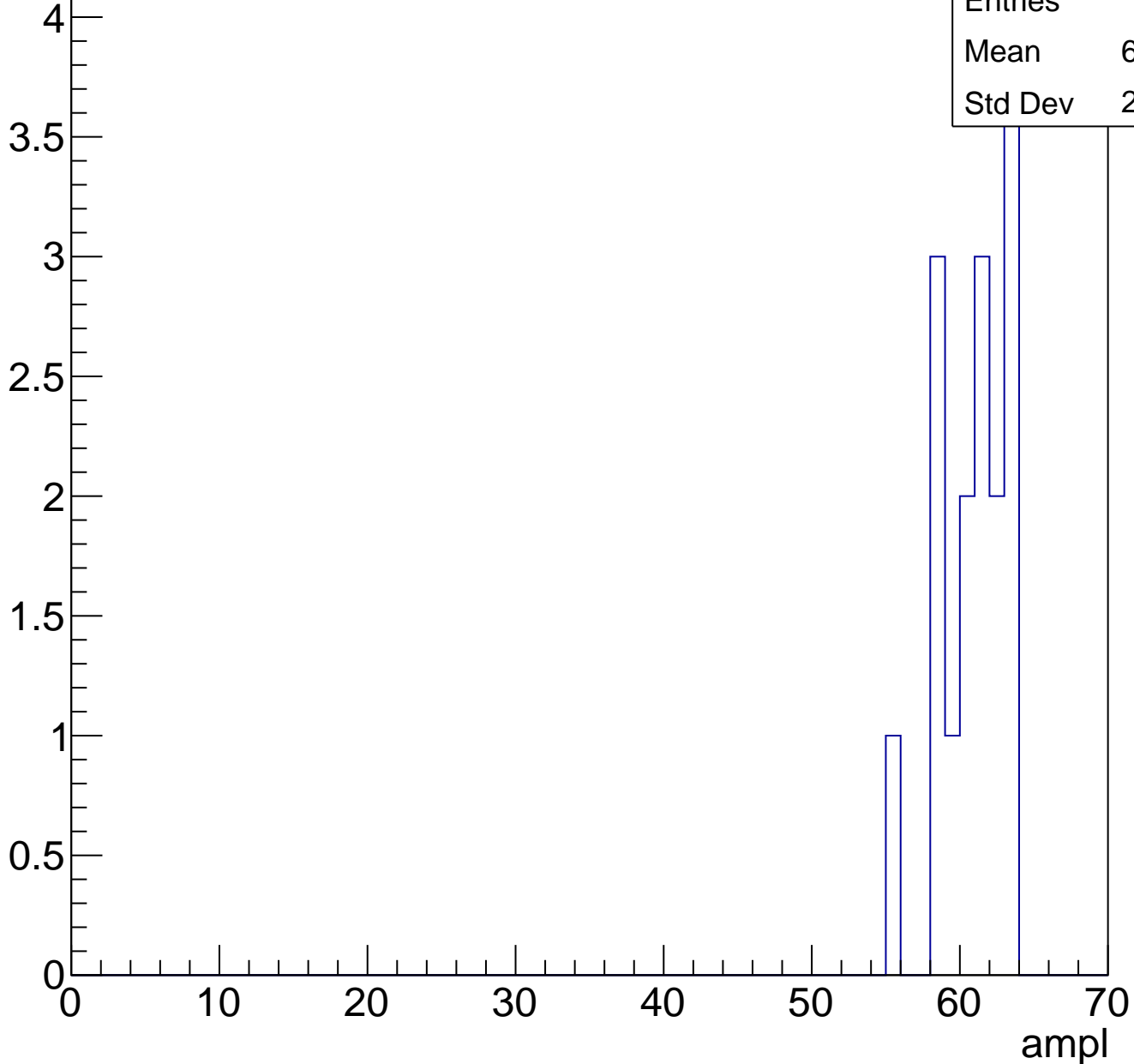
Entry



B1L103S, U15-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	16
Mean	60.44
Std Dev	2.263

B1L103S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

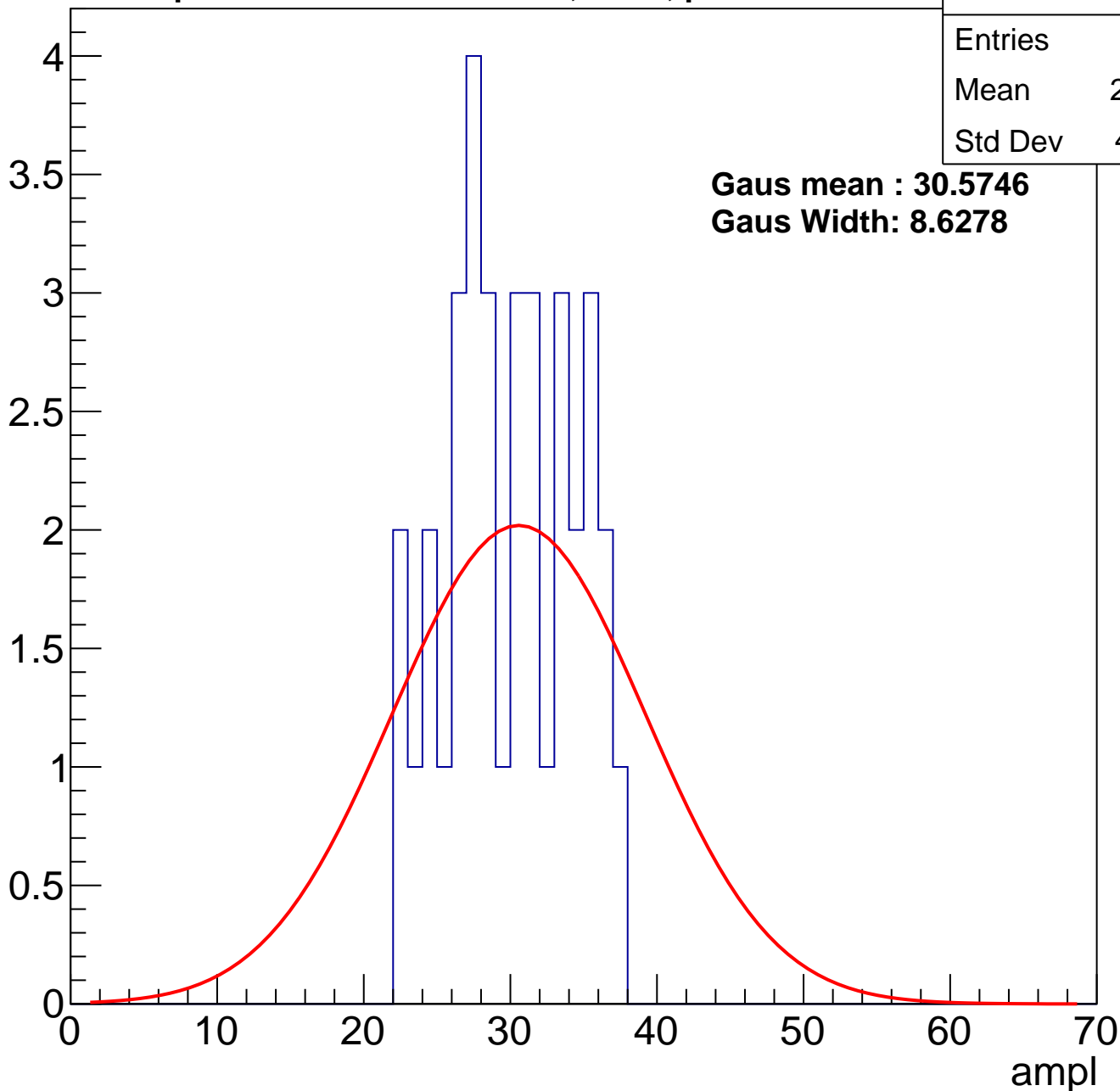
ampl

Entries	2
Mean	60.5
Std Dev	2.5

B1L103S, U15-ch28, adc0

calib_packv5_042523_0143.root, FC#7, port C2

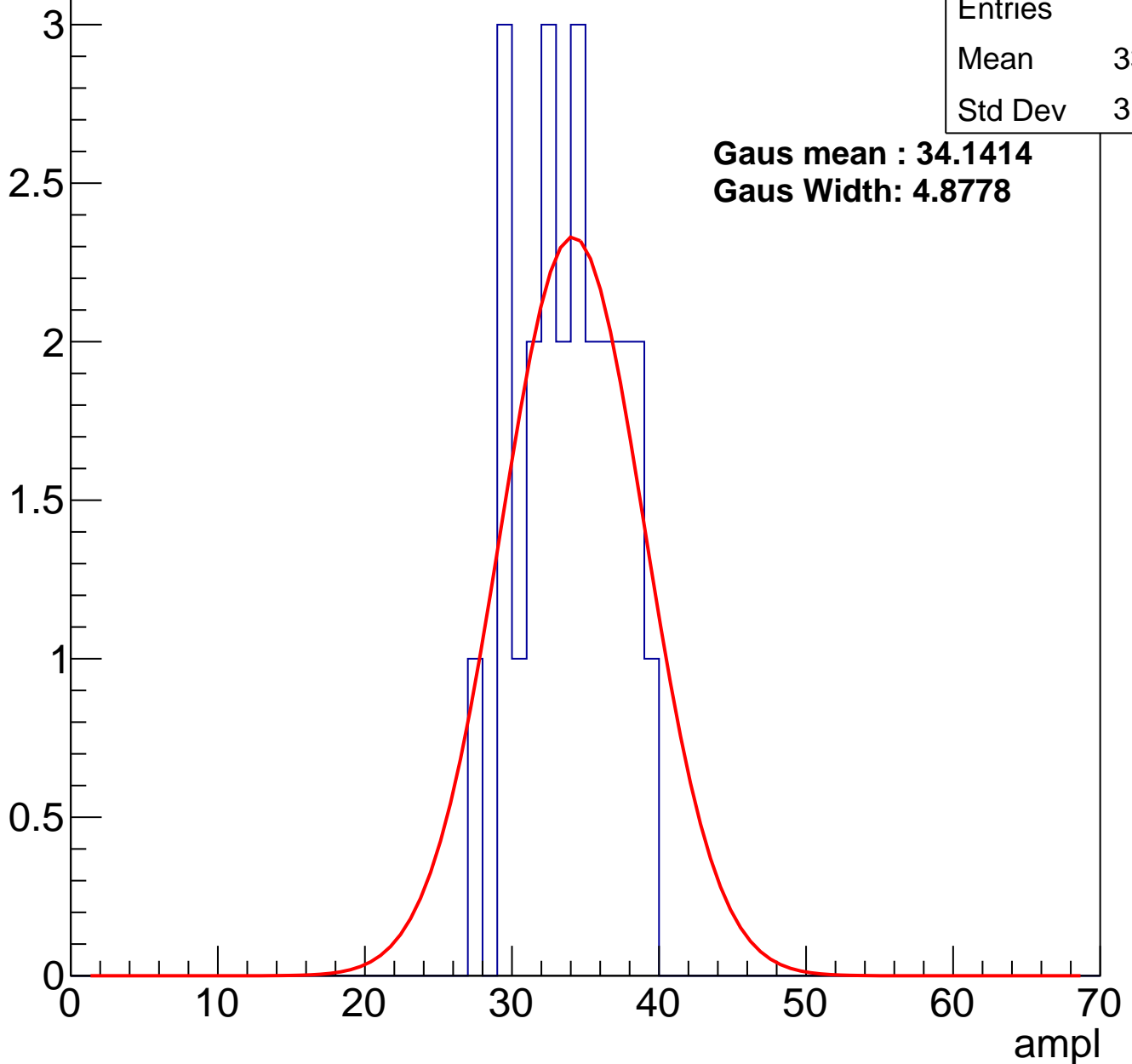
Entry



B1L103S, U15-ch28, adc1

calib_packv5_042523_0143.root, FC#7, port C2

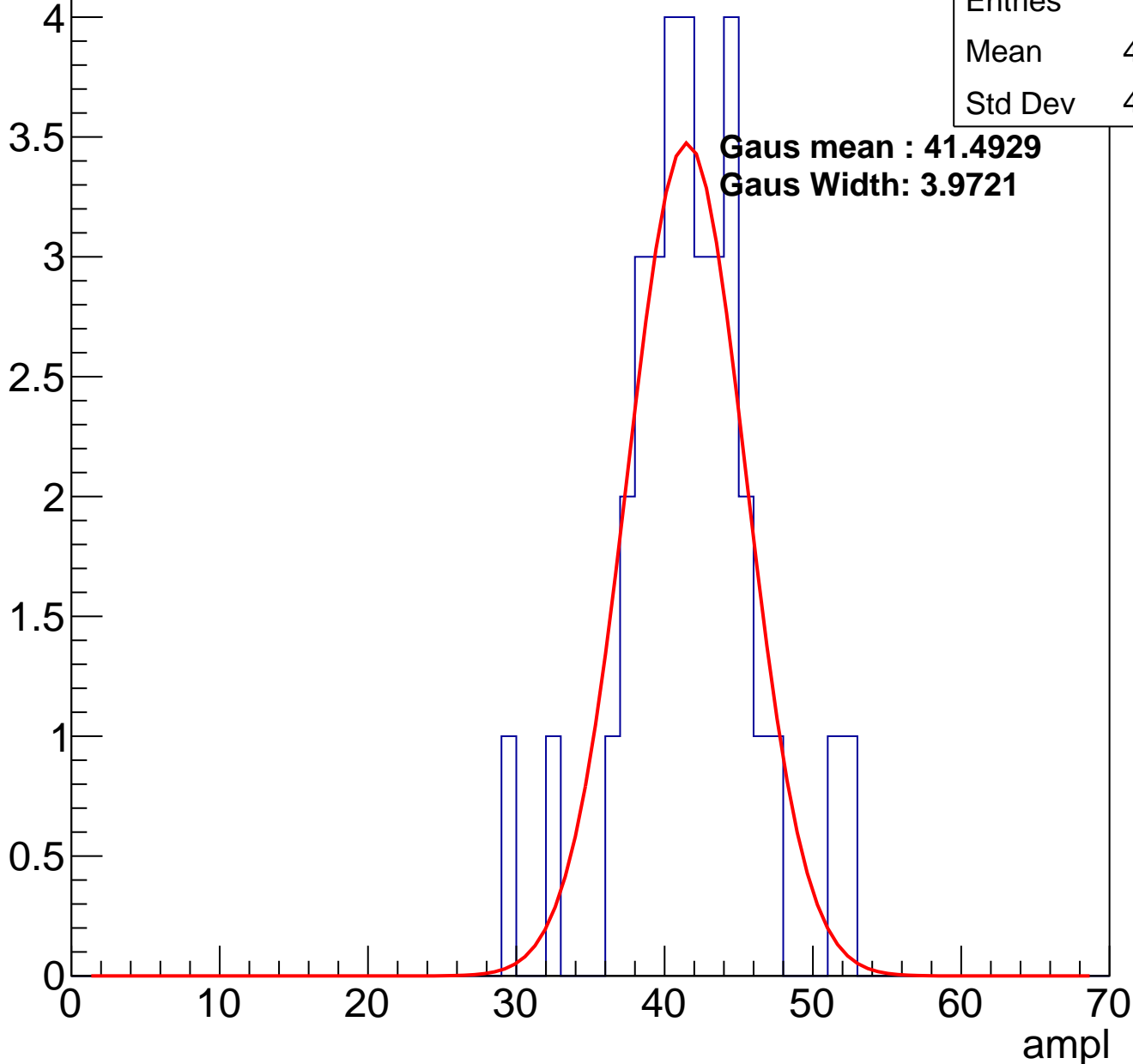
Entry



B1L103S, U15-ch28, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch28, adc3

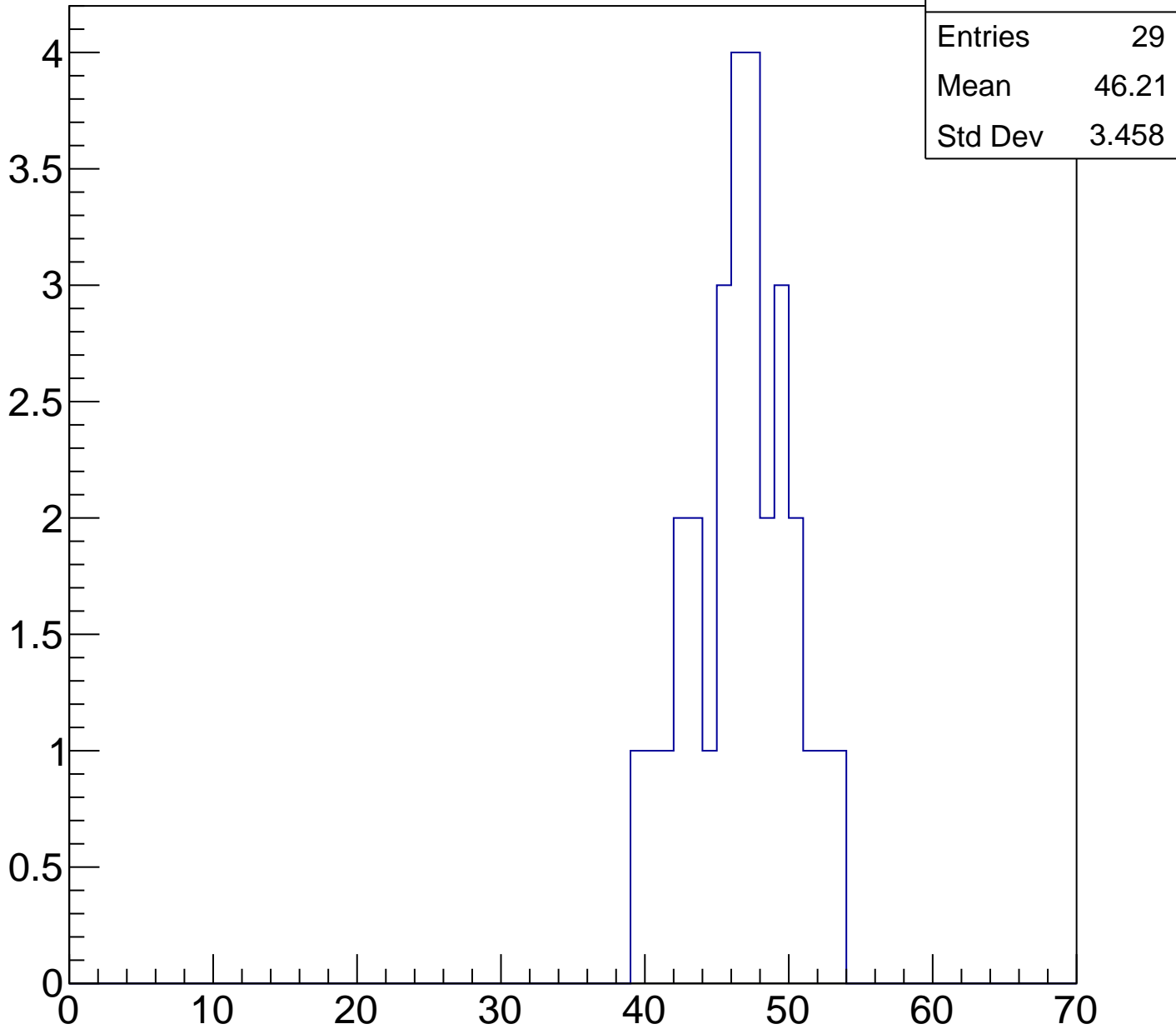
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	29
Mean	46.21
Std Dev	3.458

ampl

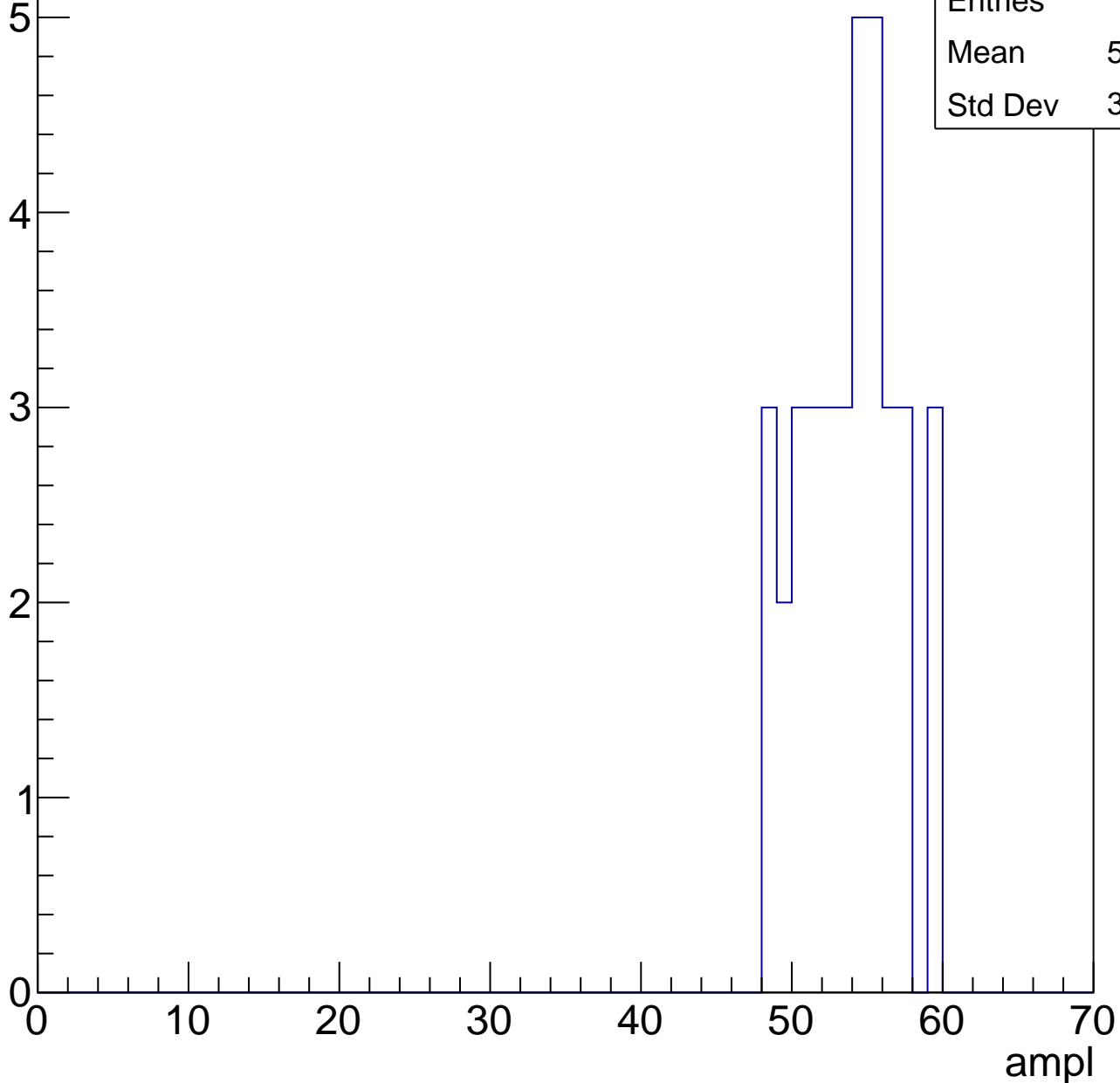


B1L103S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

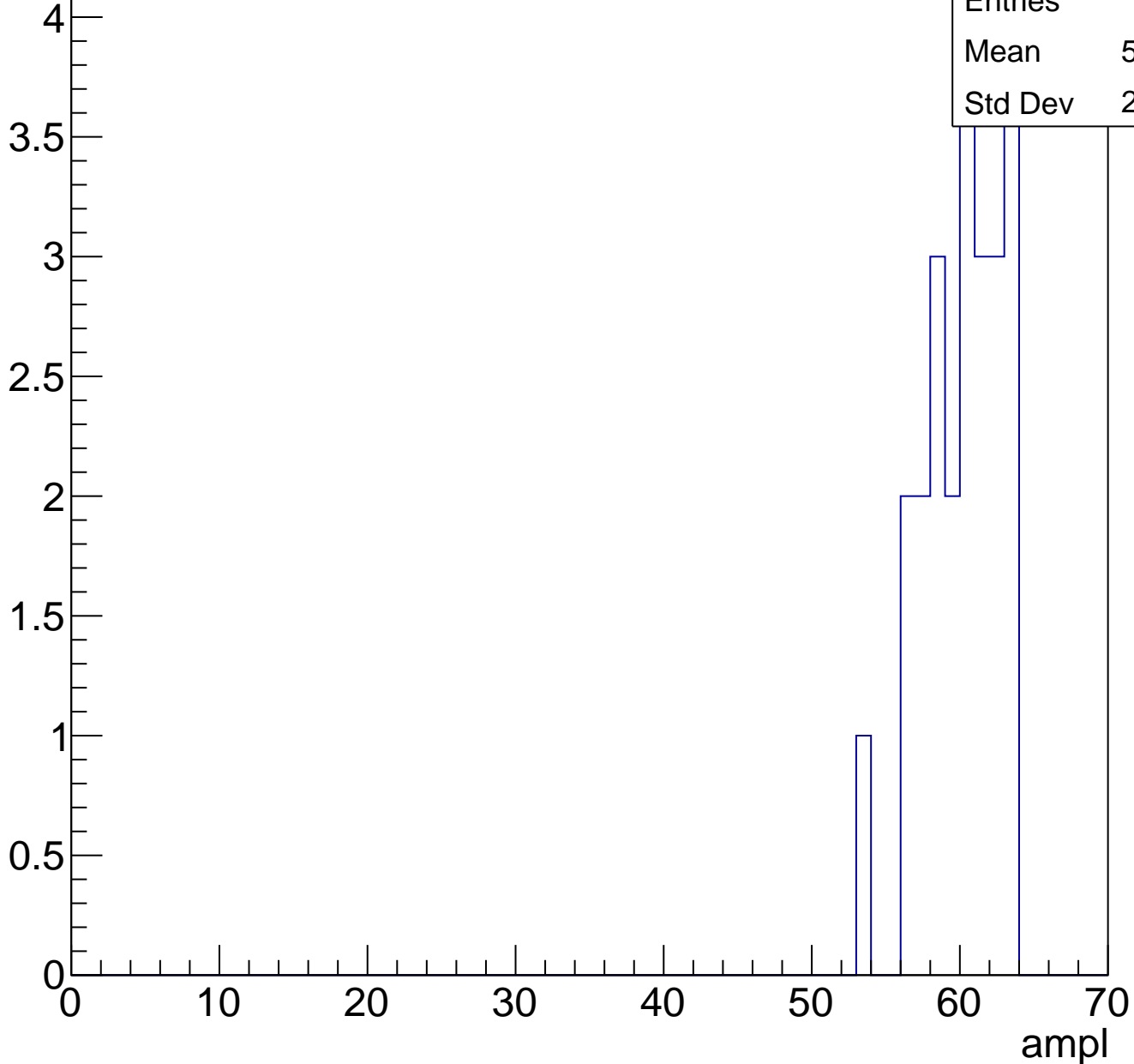
Entries	36
Mean	53.36
Std Dev	3.128



B1L103S, U15-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	24
Mean	59.67
Std Dev	2.593

B1L103S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

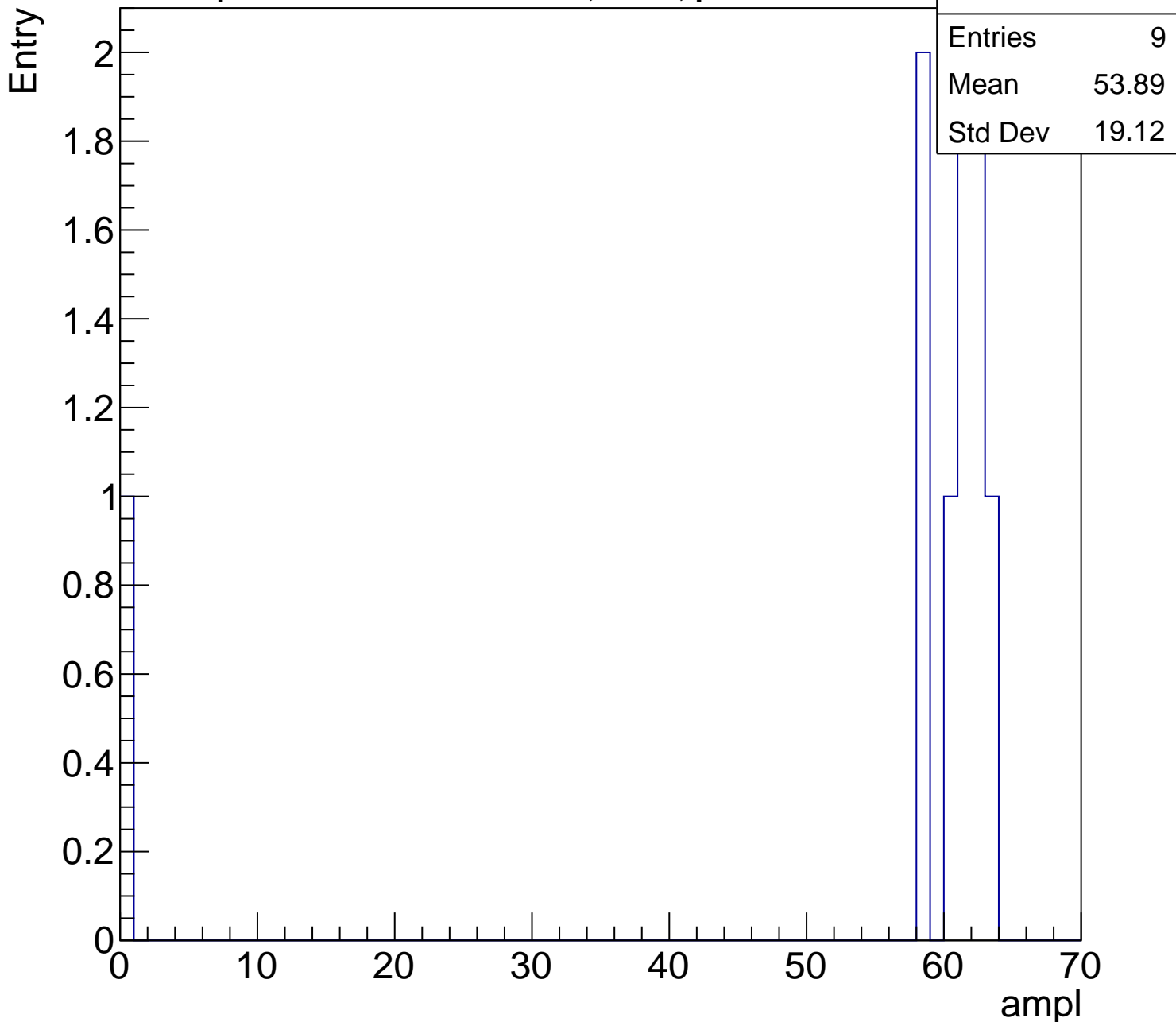
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.89
Std Dev	19.12

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch29, adc0

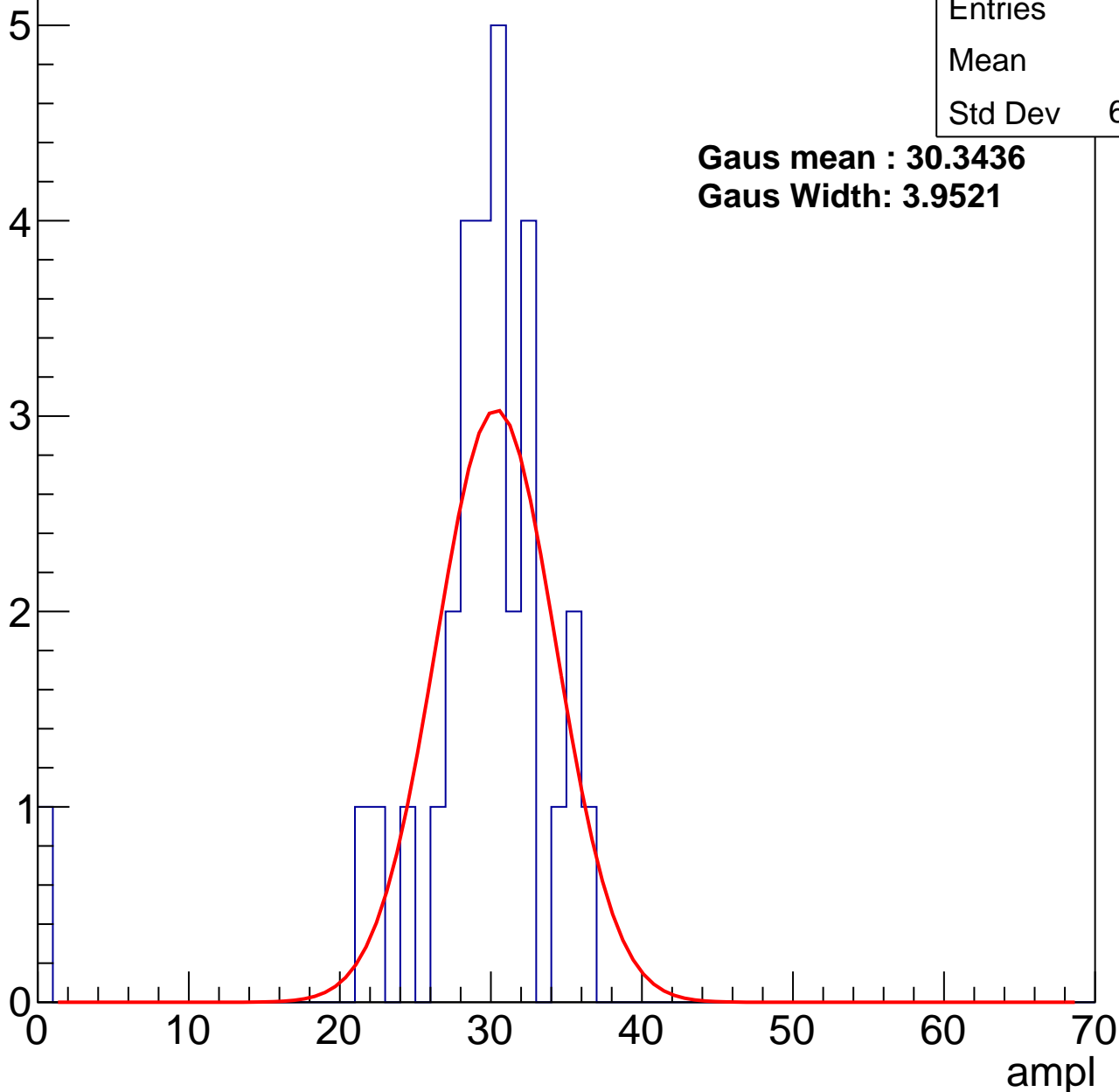
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	28.5
Std Dev	6.292

Gaus mean : 30.3436

Gaus Width: 3.9521



B1L103S, U15-ch29, adc1

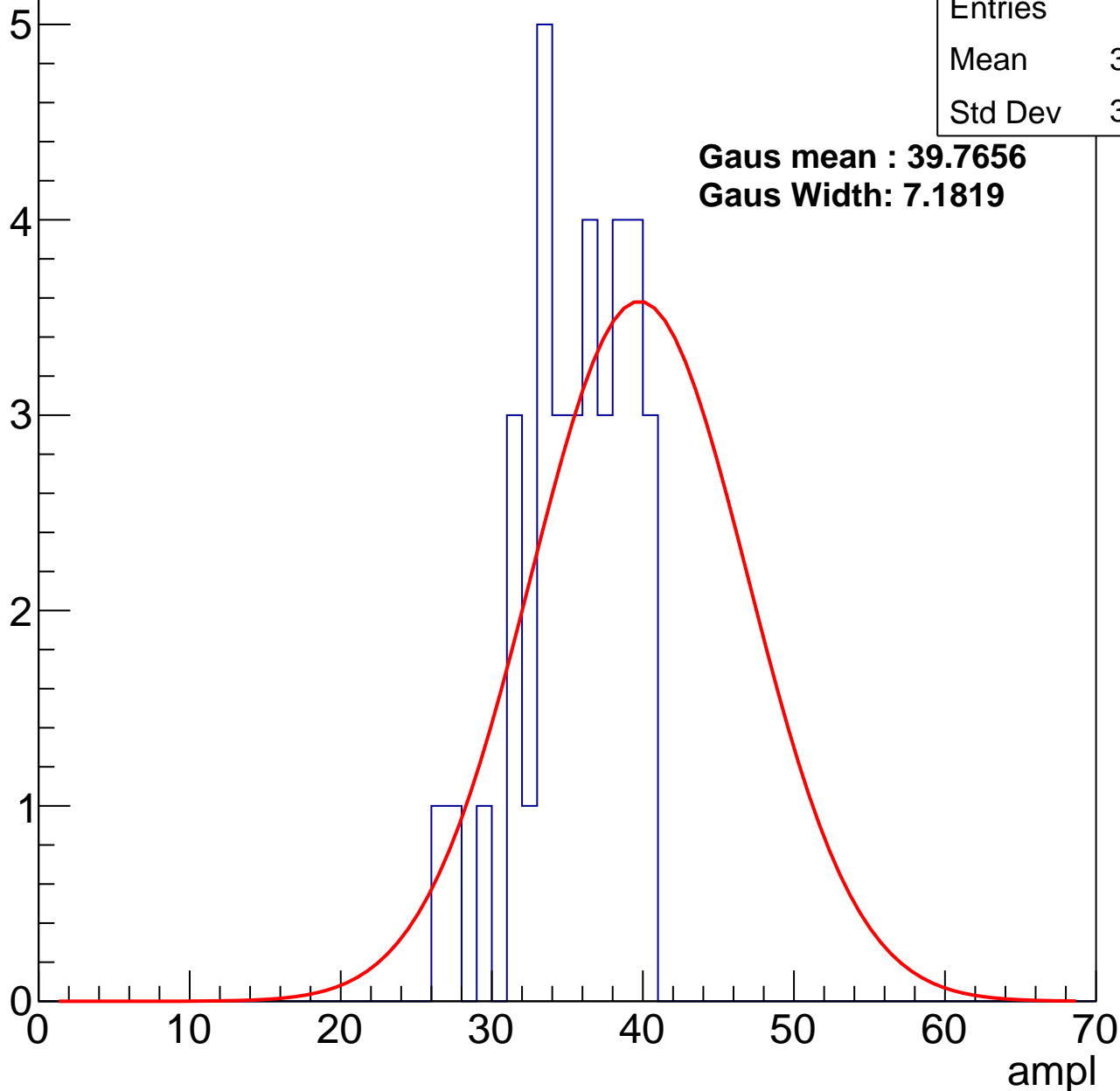
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	35.06
Std Dev	3.543

Gaus mean : 39.7656

Gaus Width: 7.1819



B1L103S, U15-ch29, adc2

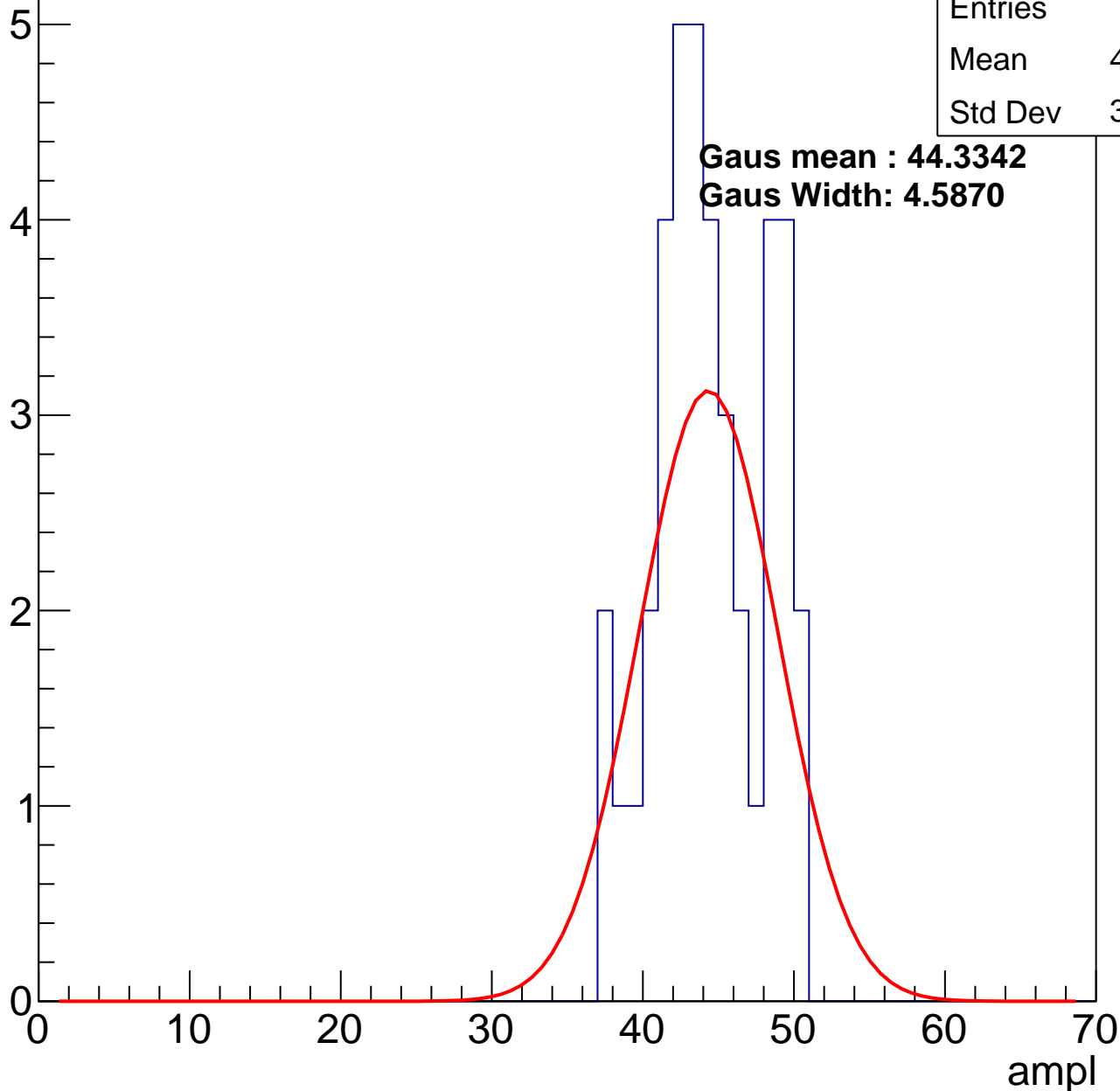
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	43.95
Std Dev	3.563

Gaus mean : 44.3342

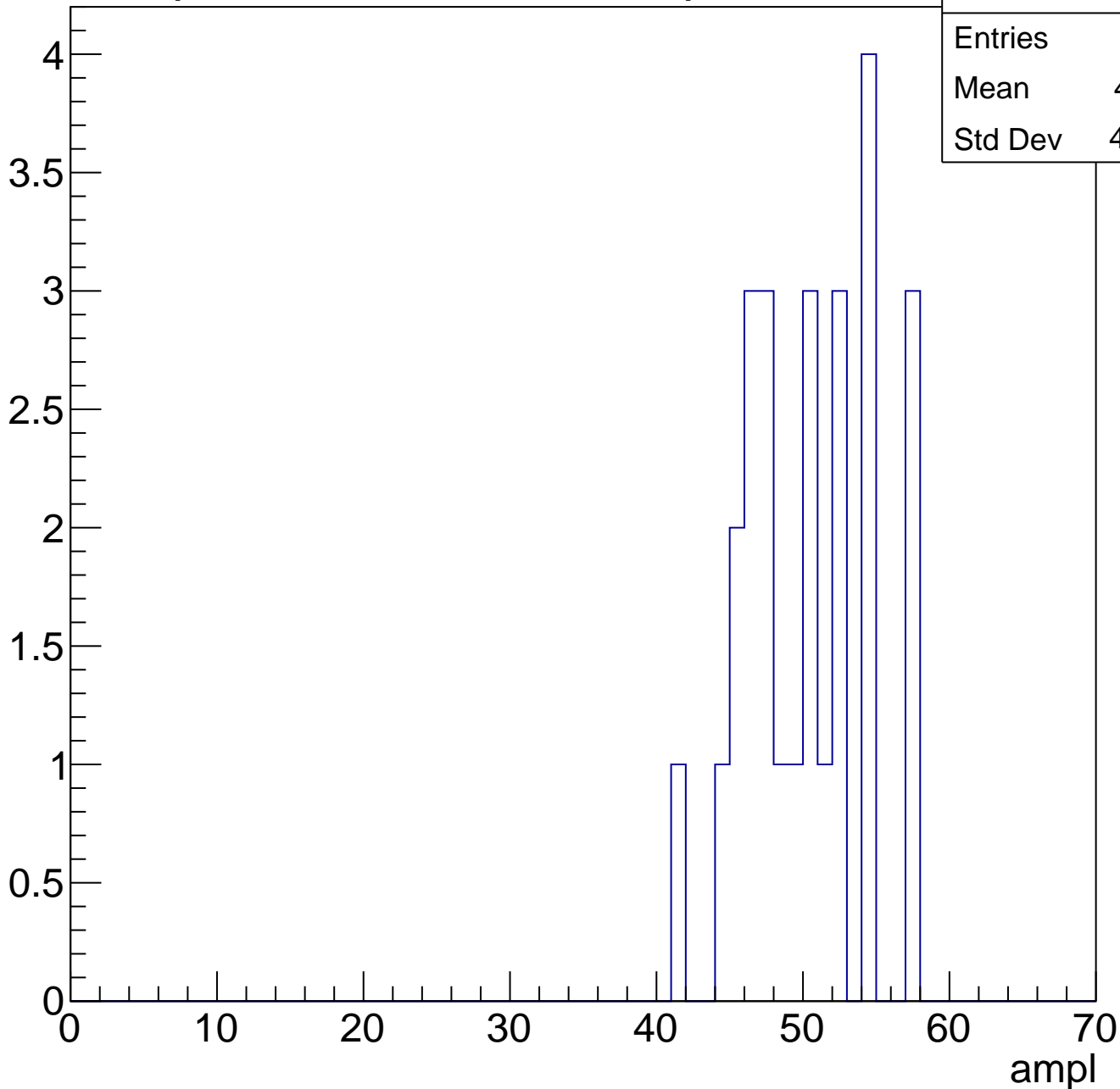
Gaus Width: 4.5870



B1L103S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

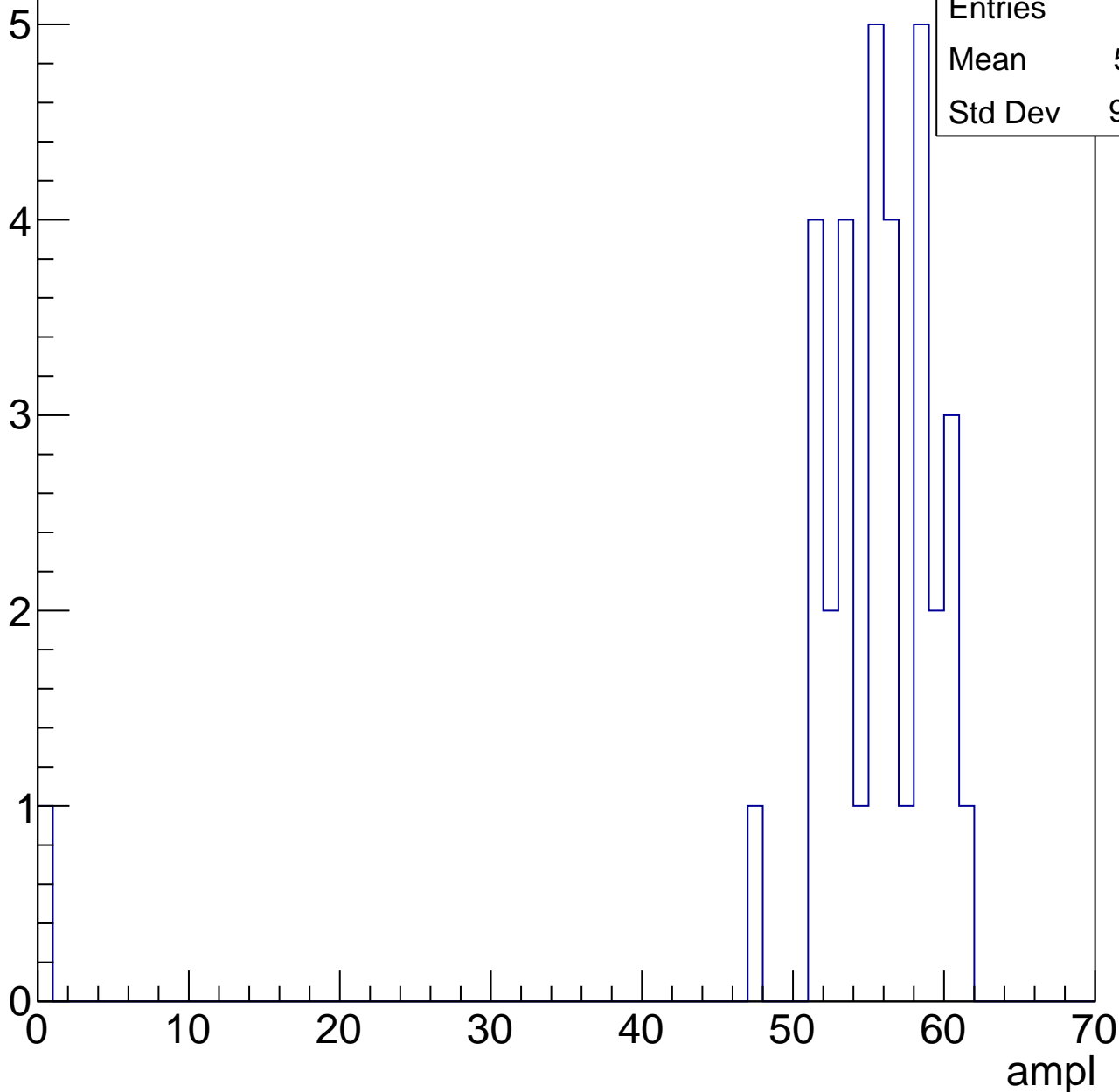


B1L103S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

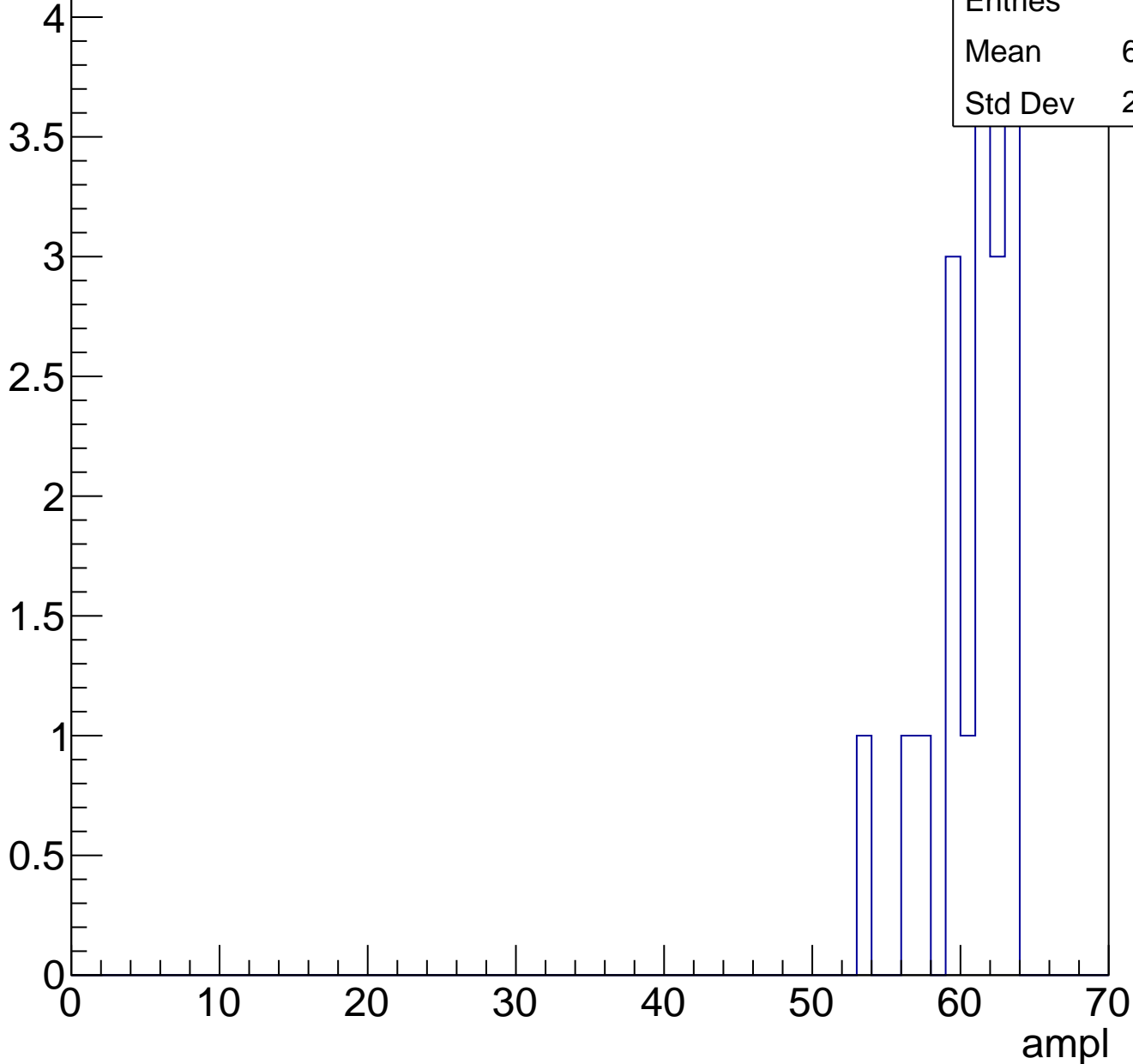
Entries	34
Mean	53.71
Std Dev	9.892



B1L103S, U15-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

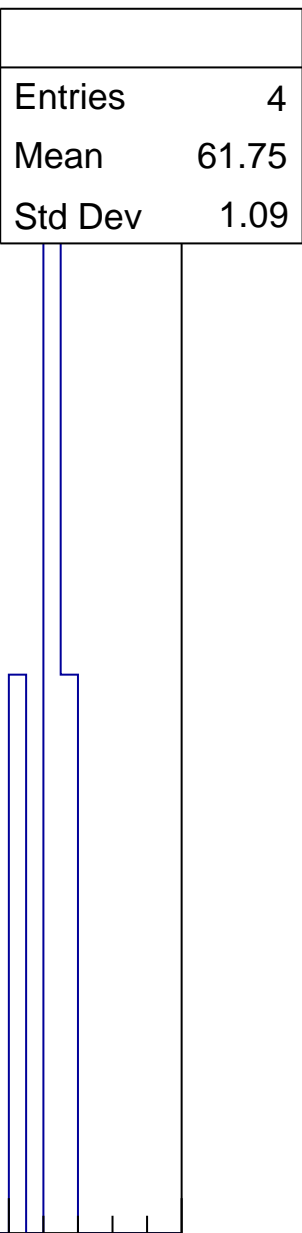
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch30, adc0

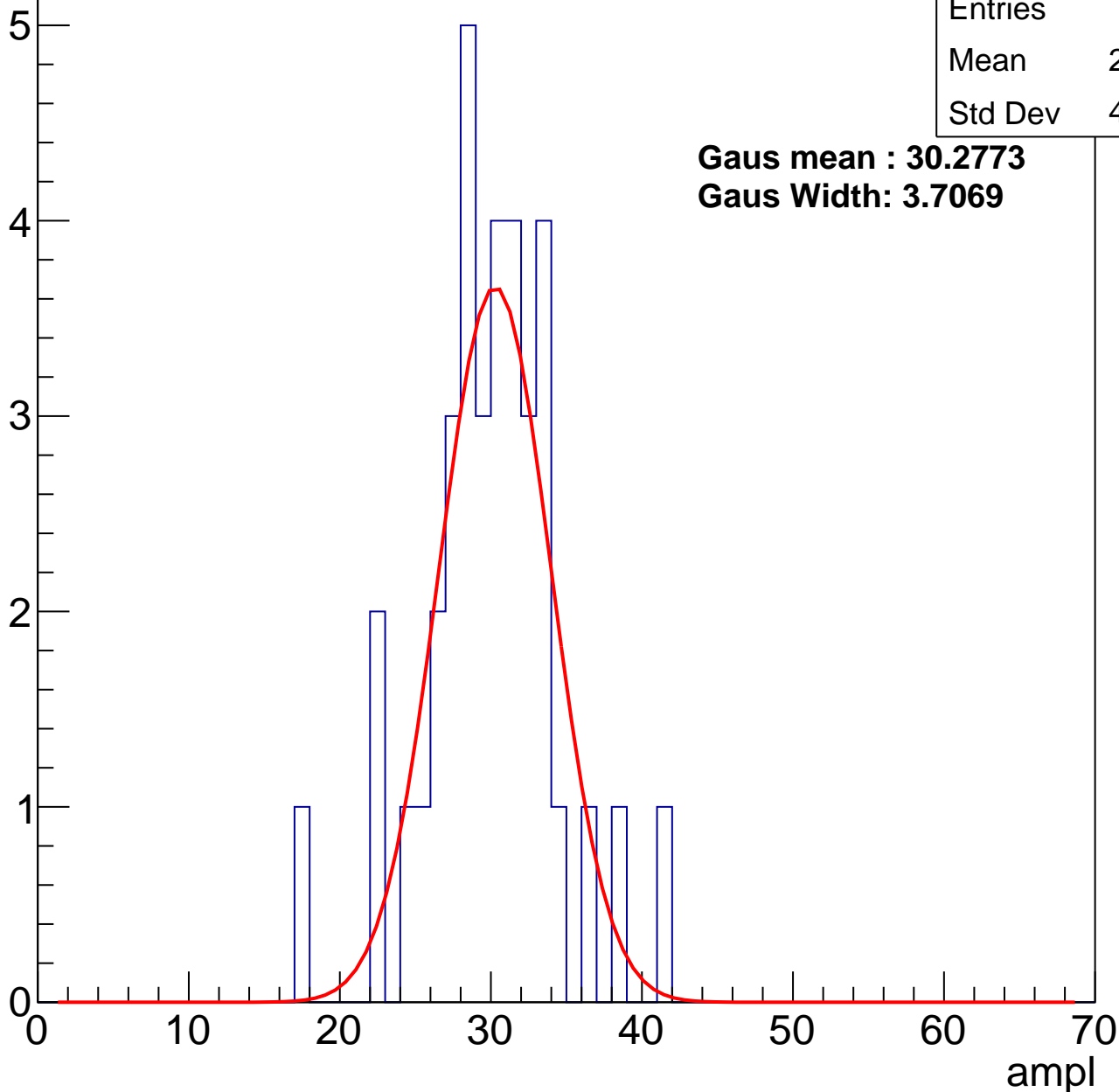
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	29.49
Std Dev	4.409

Gaus mean : 30.2773

Gaus Width: 3.7069



B1L103S, U15-ch30, adc1

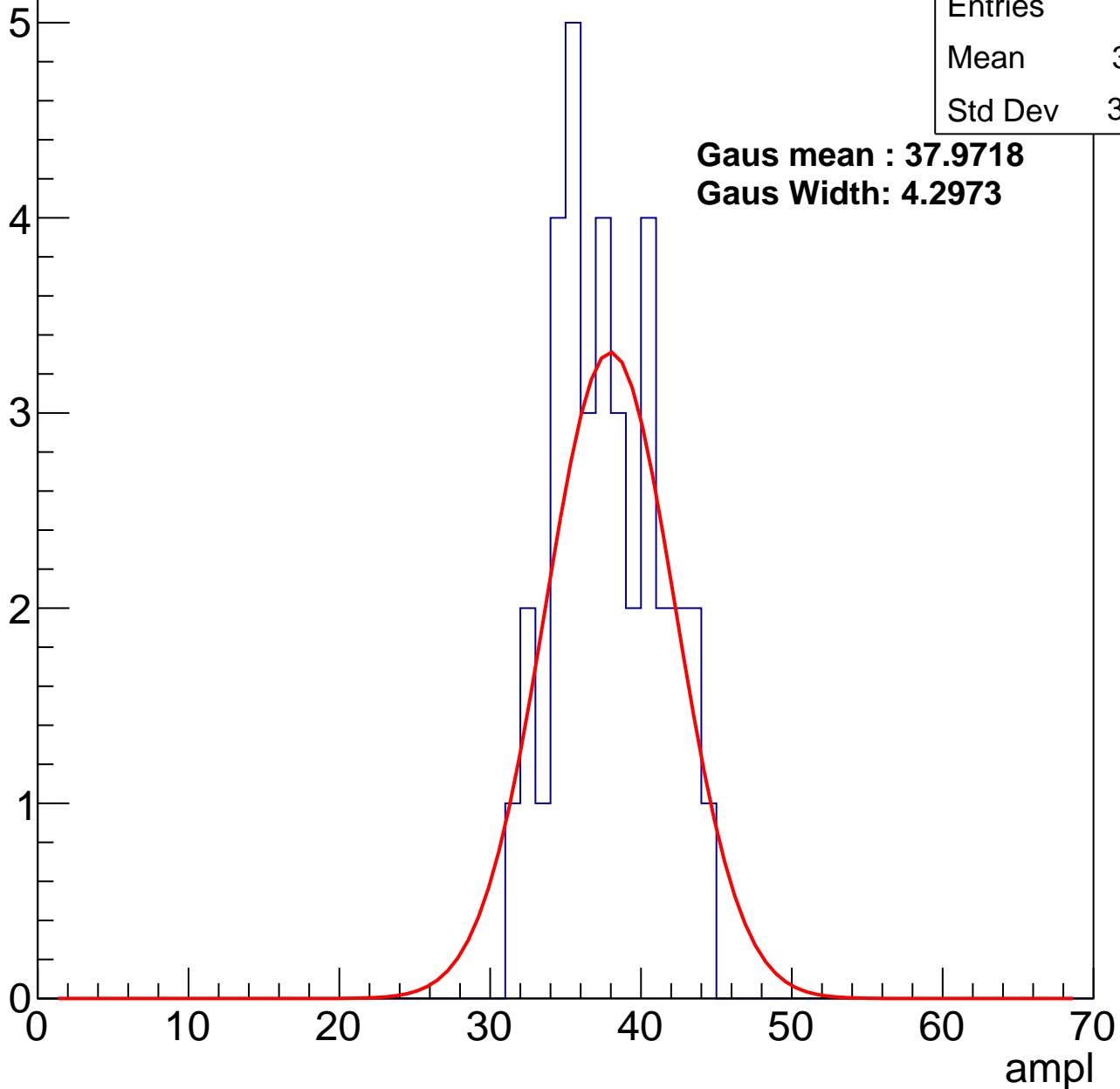
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	37.31
Std Dev	3.365

Gaus mean : 37.9718

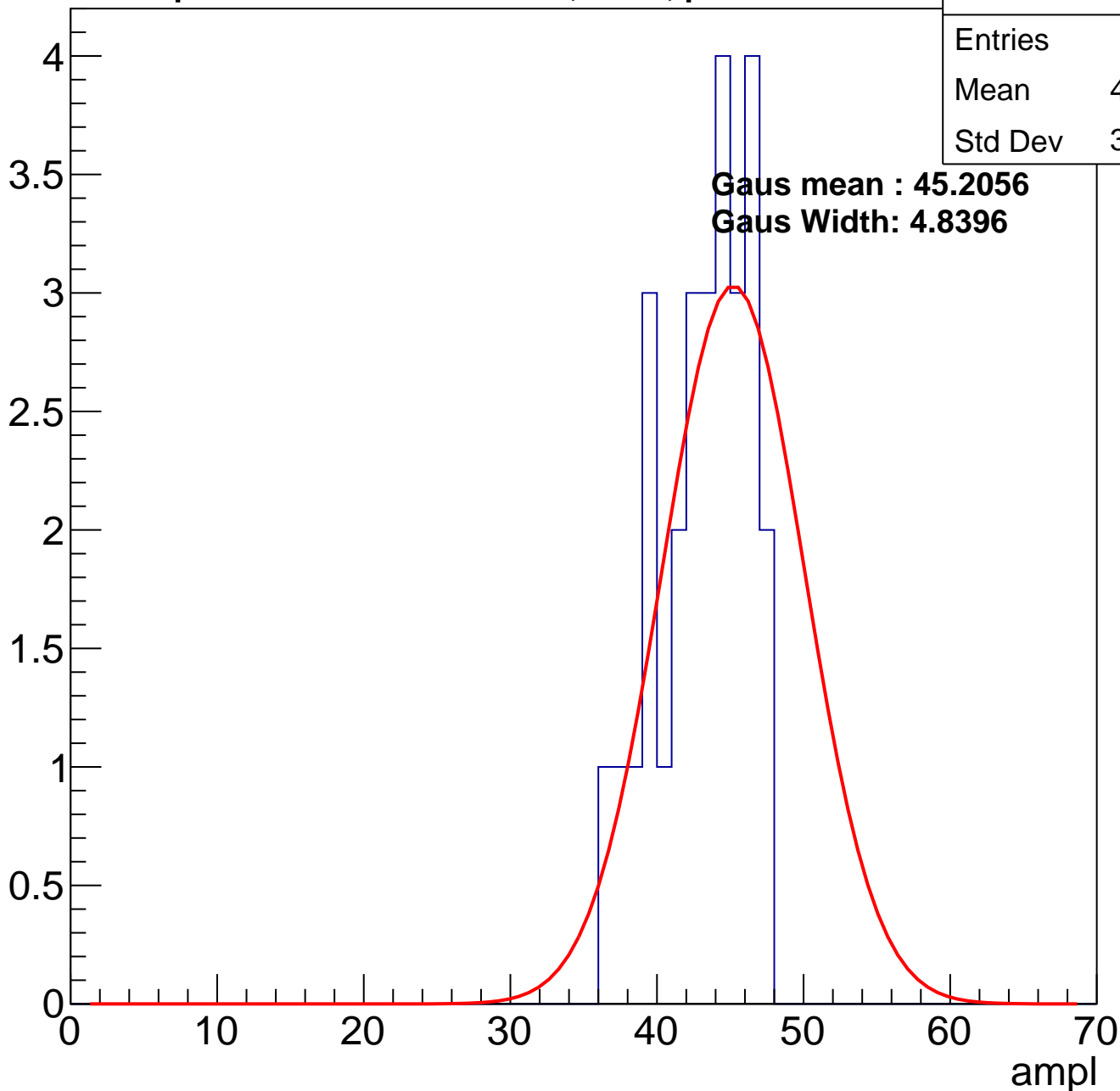
Gaus Width: 4.2973



B1L103S, U15-ch30, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch30, adc3

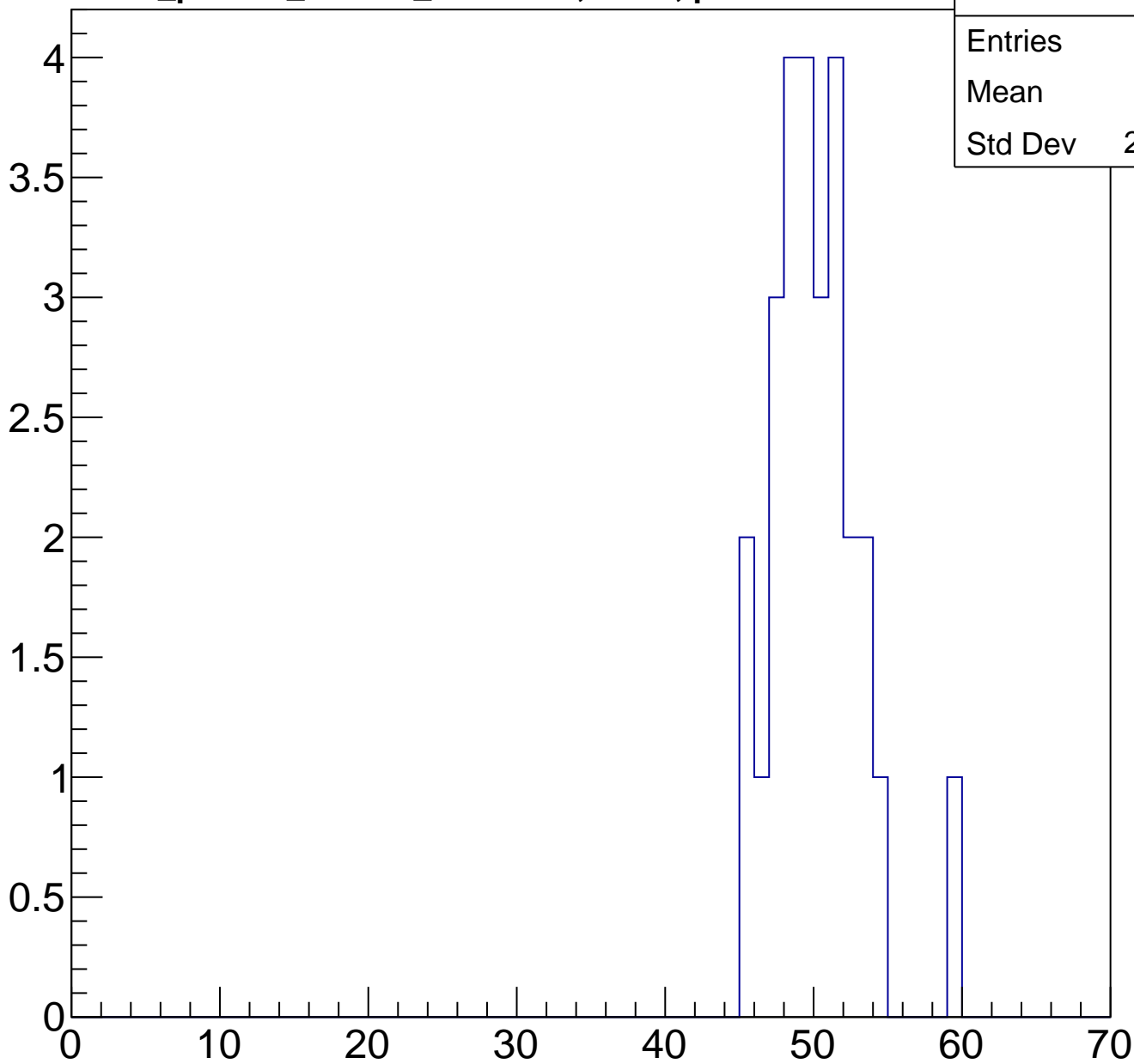
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	27
Mean	49.7
Std Dev	2.967

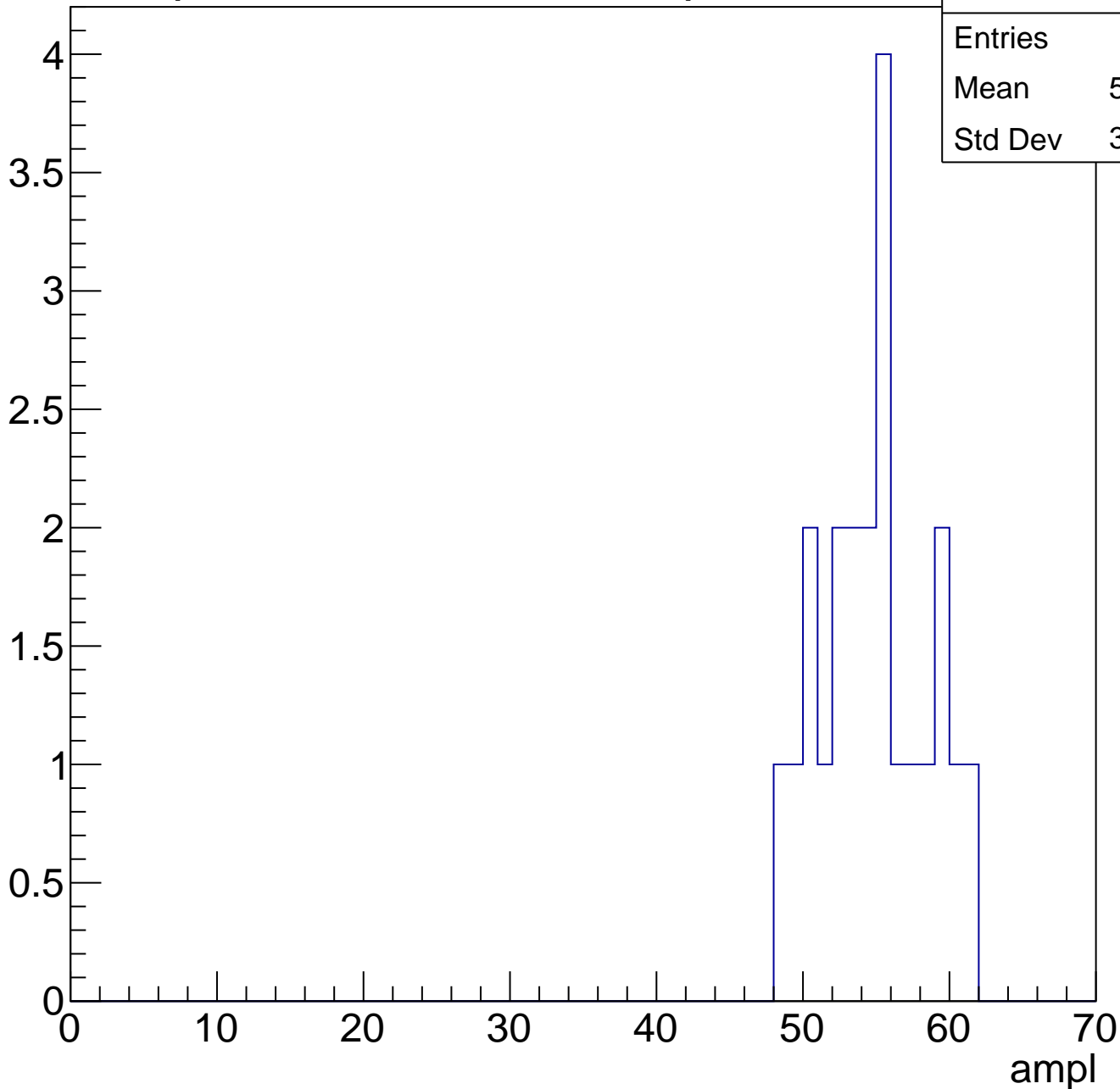
ampl



B1L103S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

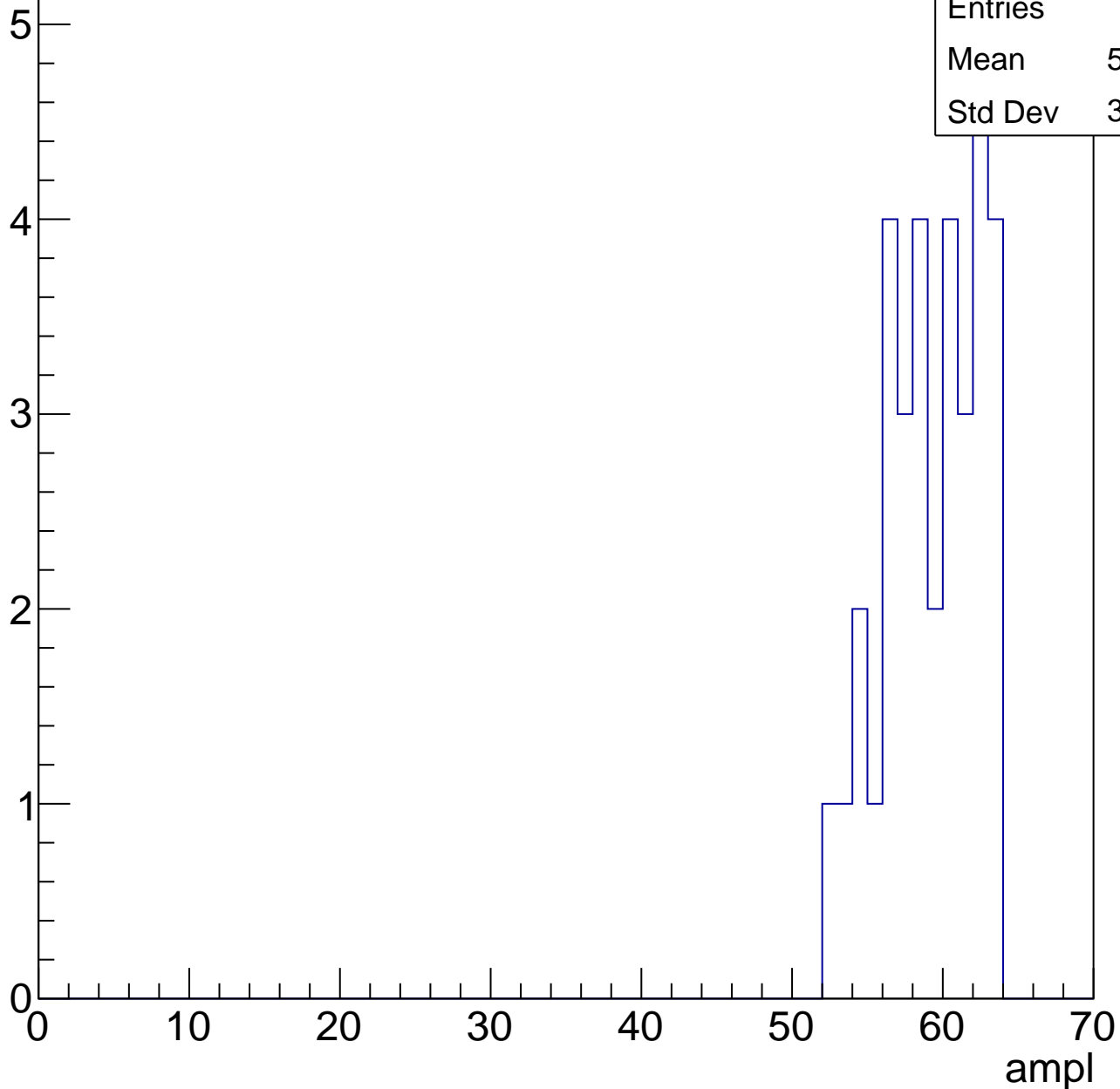


B1L103S, U15-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

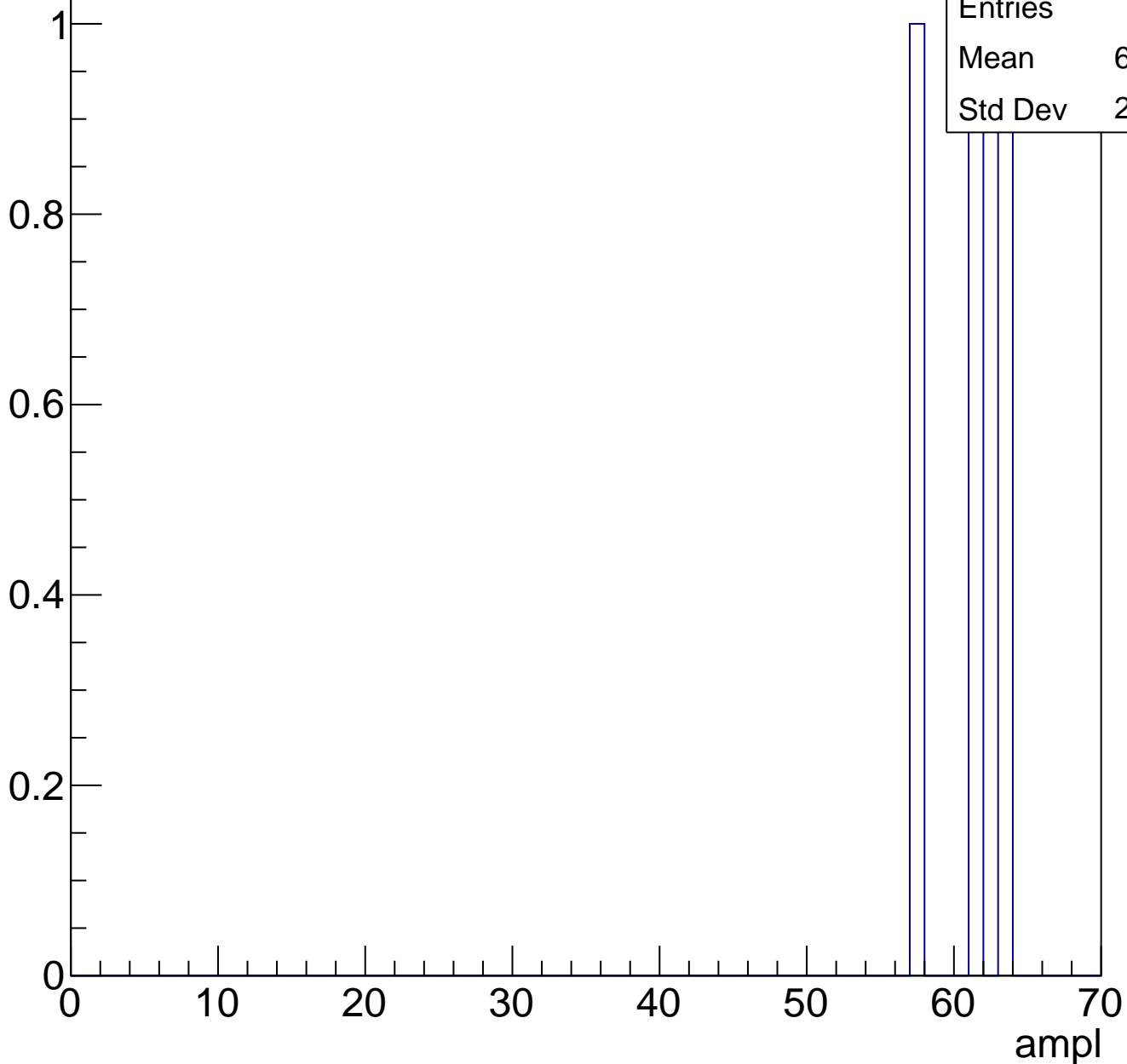
Entries	34
Mean	58.76
Std Dev	3.097



B1L103S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch31, adc0

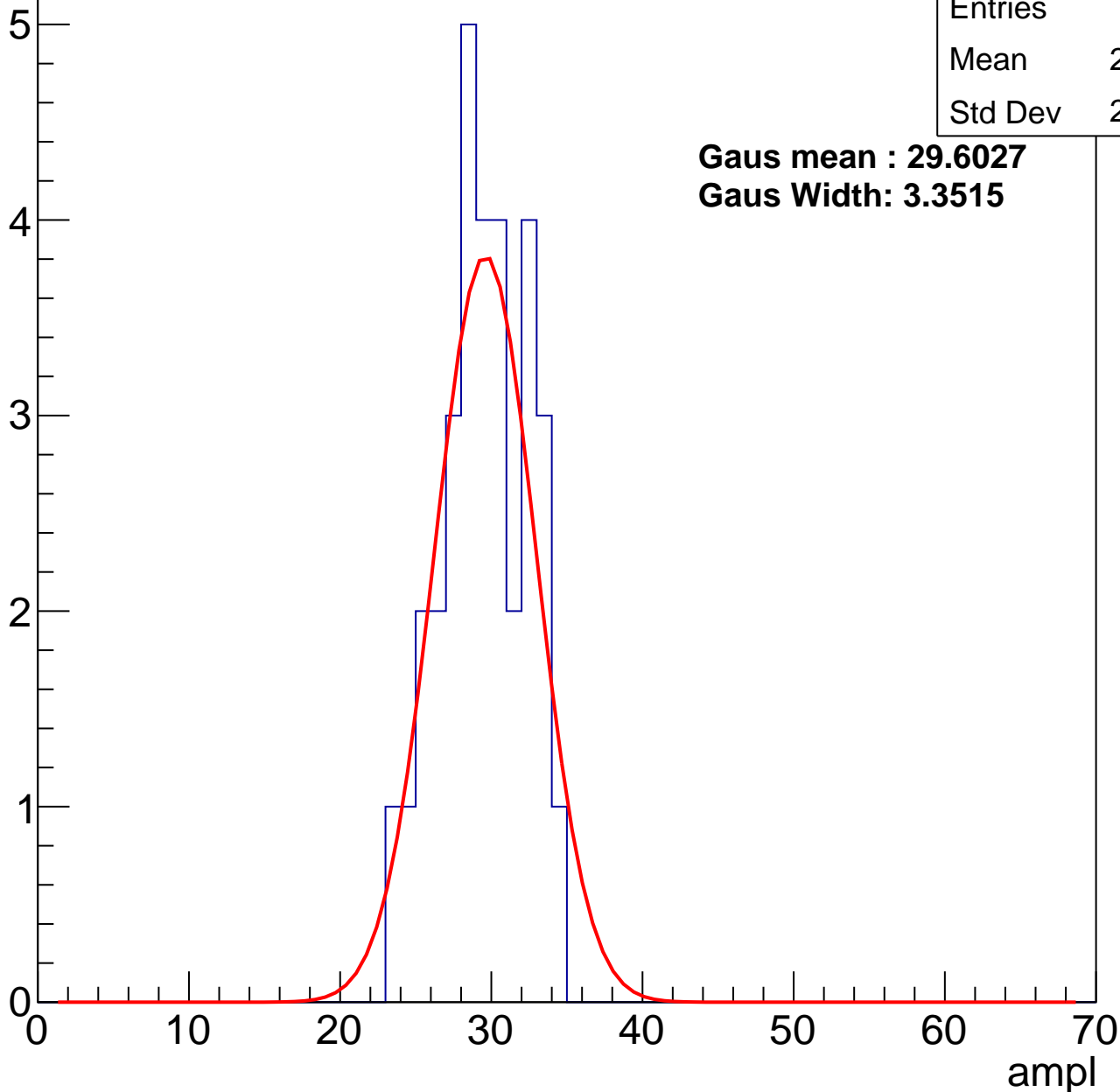
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	29.03
Std Dev	2.789

Gaus mean : 29.6027

Gaus Width: 3.3515



B1L103S, U15-ch31, adc1

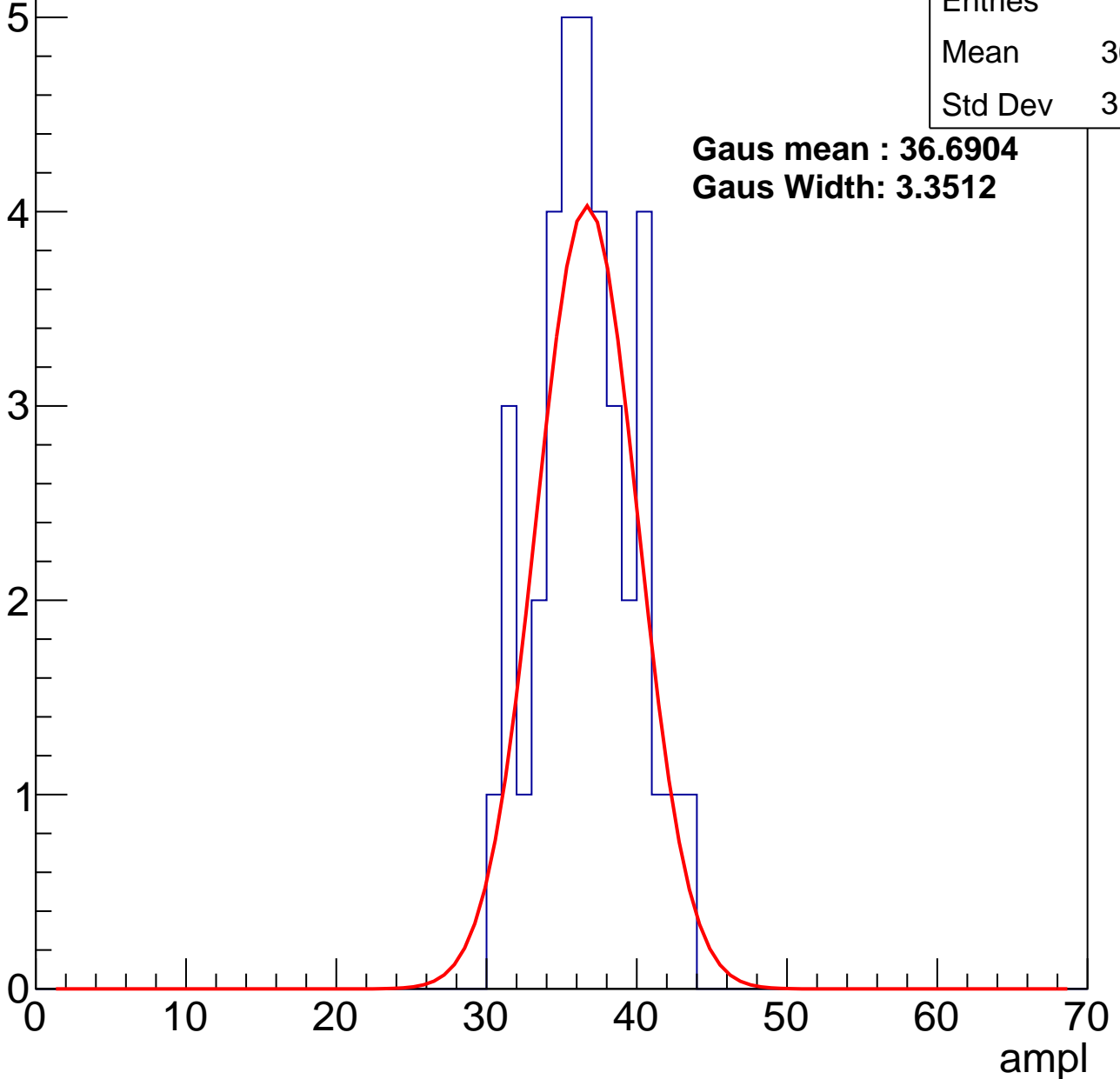
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	36.16
Std Dev	3.175

Gaus mean : 36.6904

Gaus Width: 3.3512



B1L103S, U15-ch31, adc2

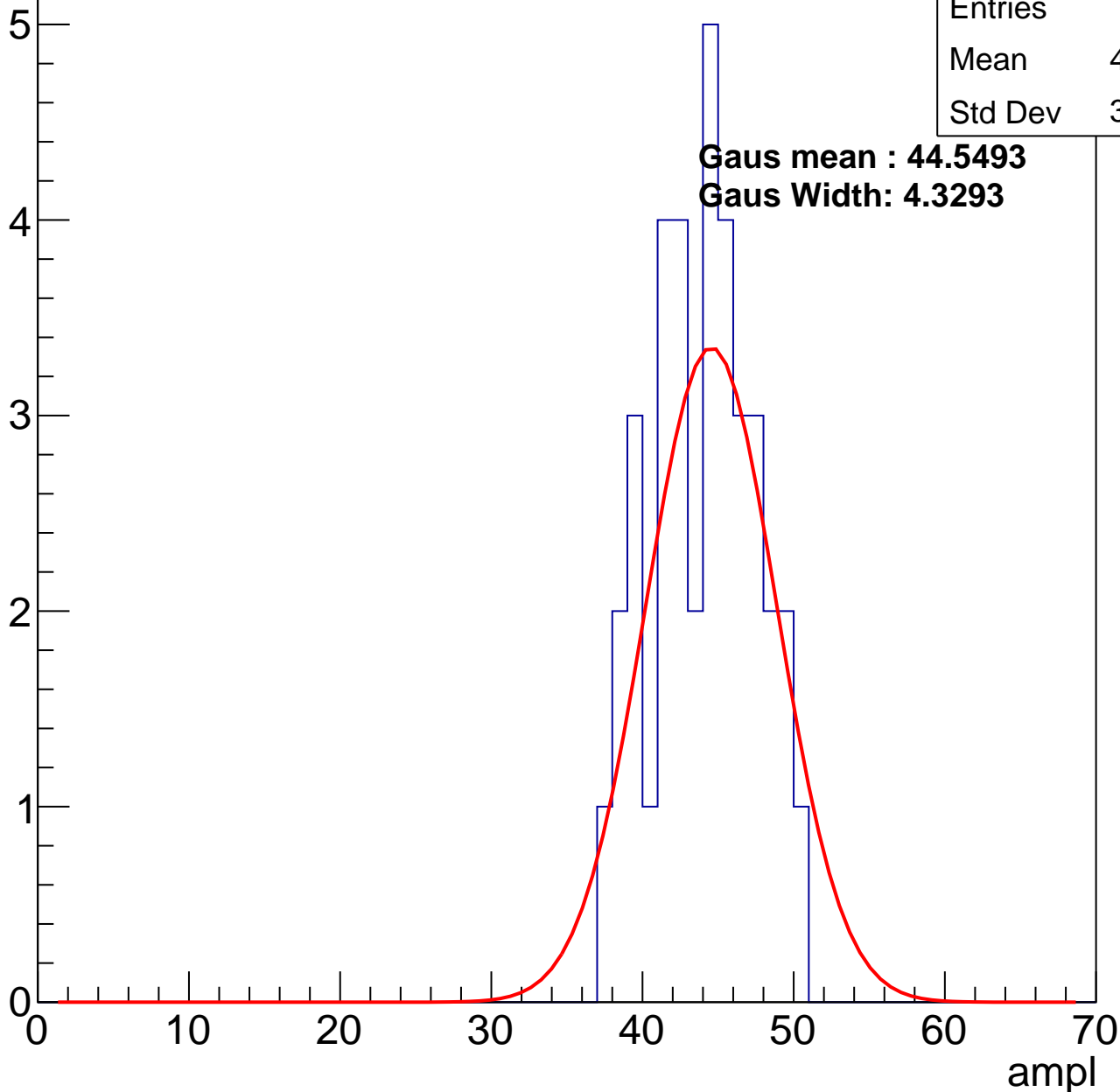
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	43.54
Std Dev	3.366

Gaus mean : 44.5493

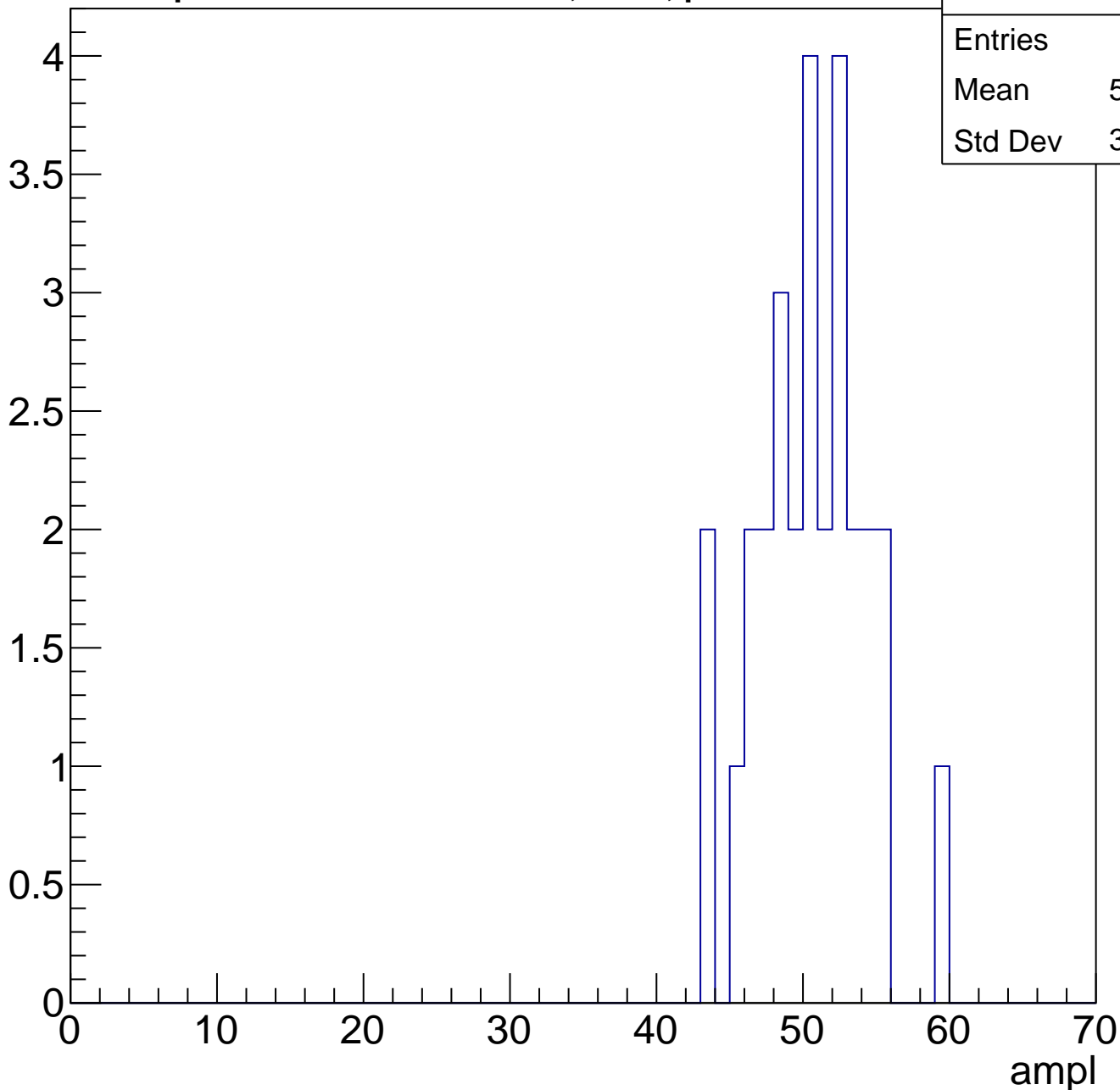
Gaus Width: 4.3293



B1L103S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

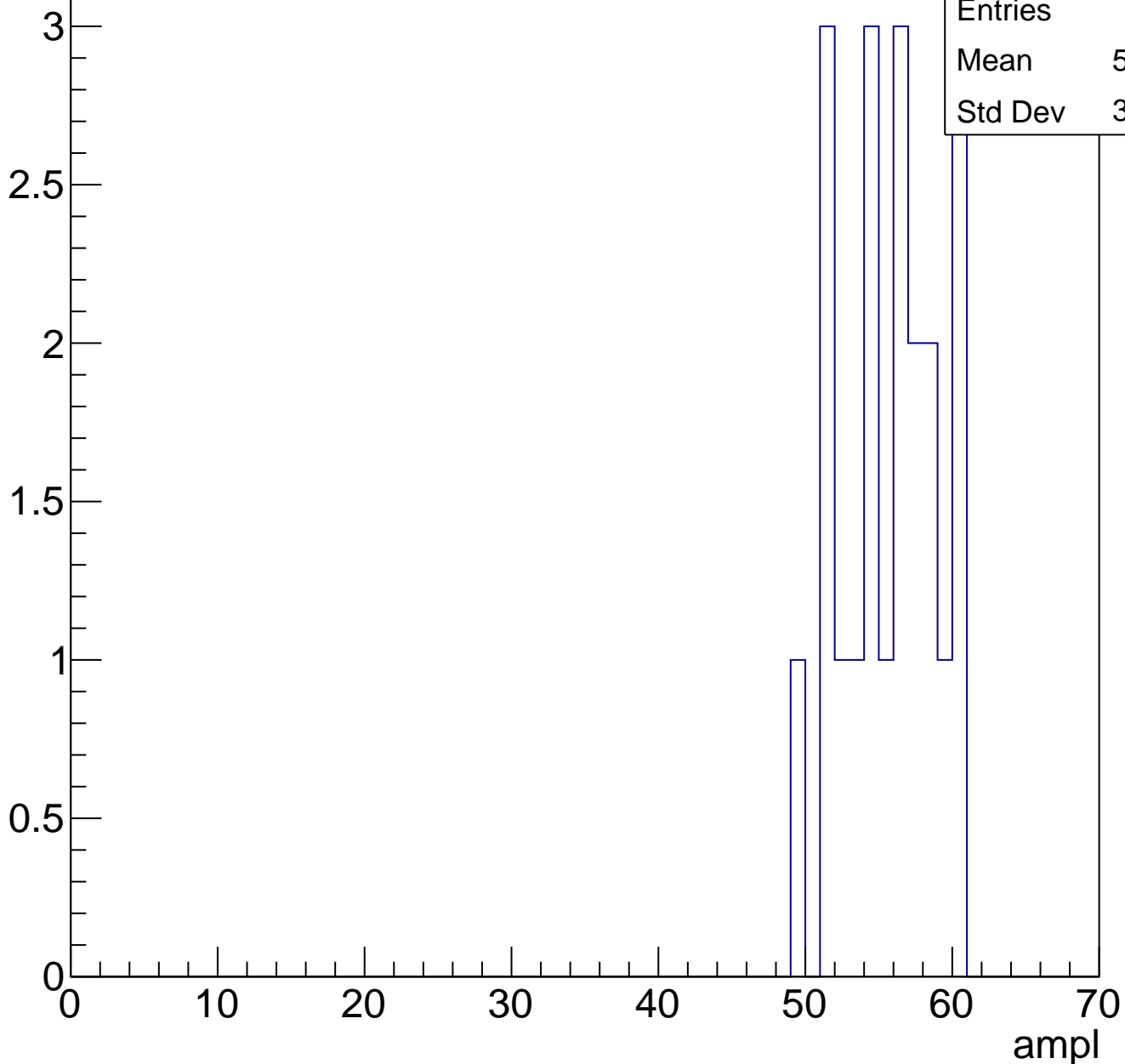
Entry



B1L103S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

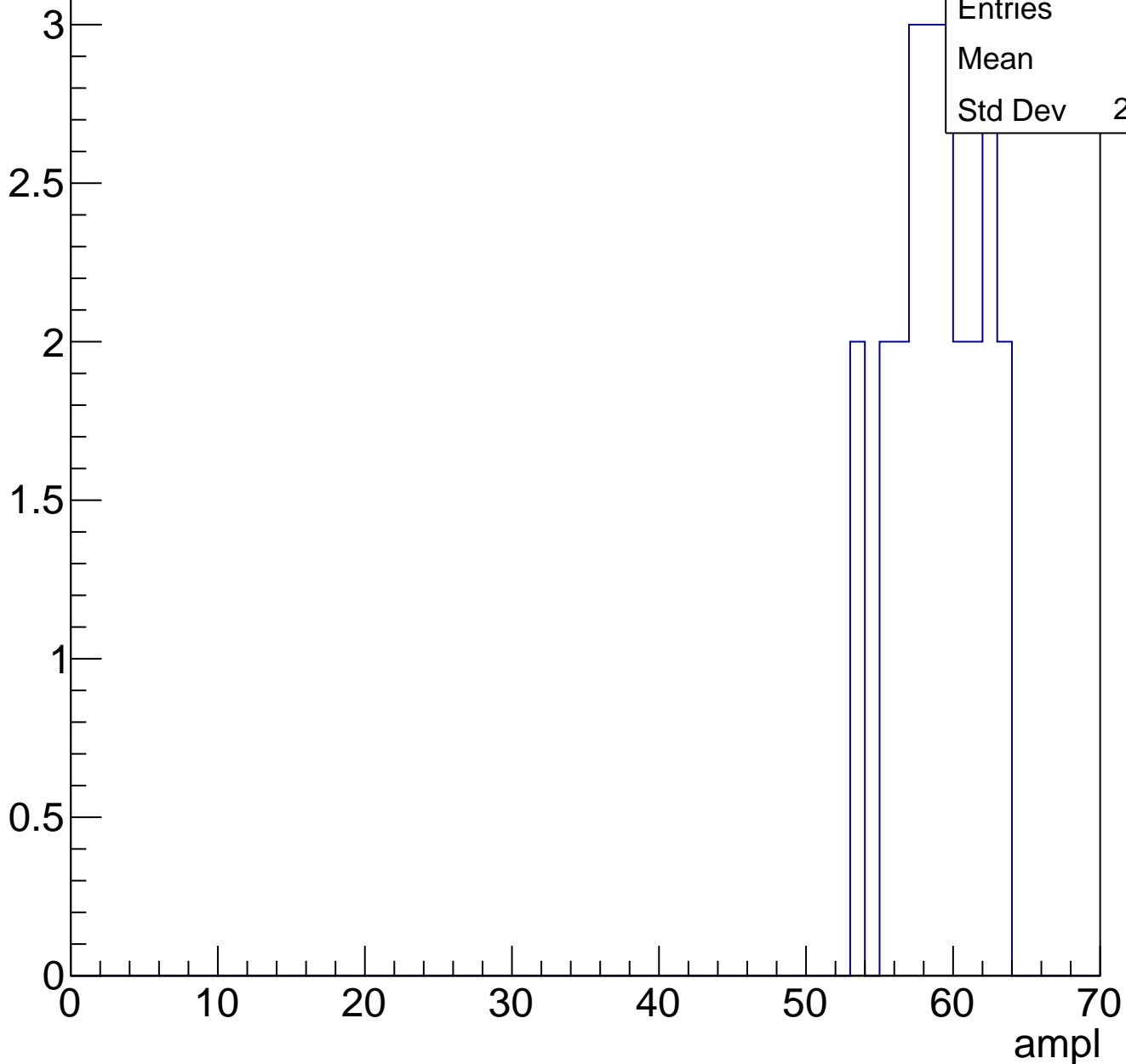


Entries	21
Mean	55.29
Std Dev	3.224

B1L103S, U15-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

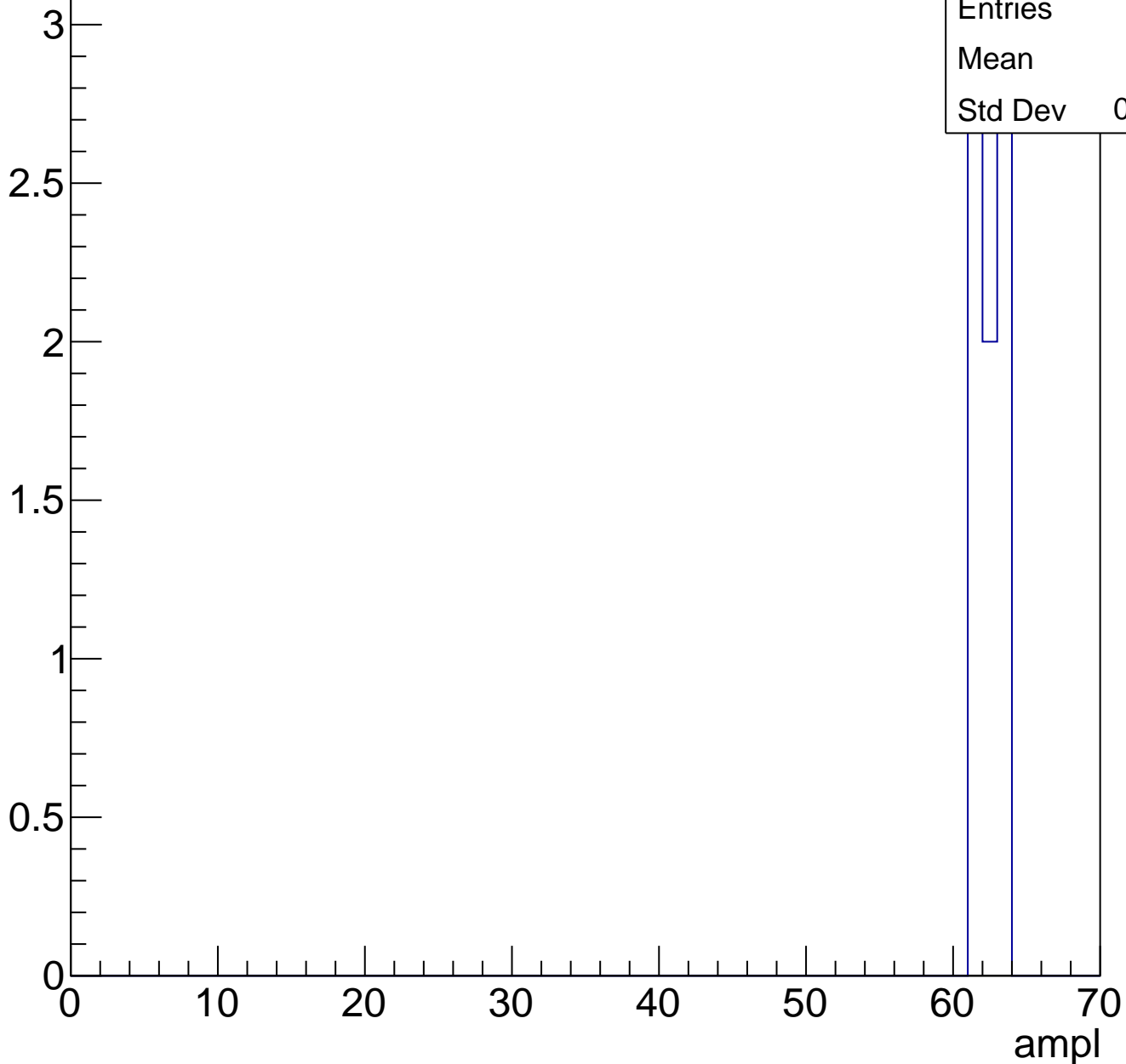
Entry



B1L103S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

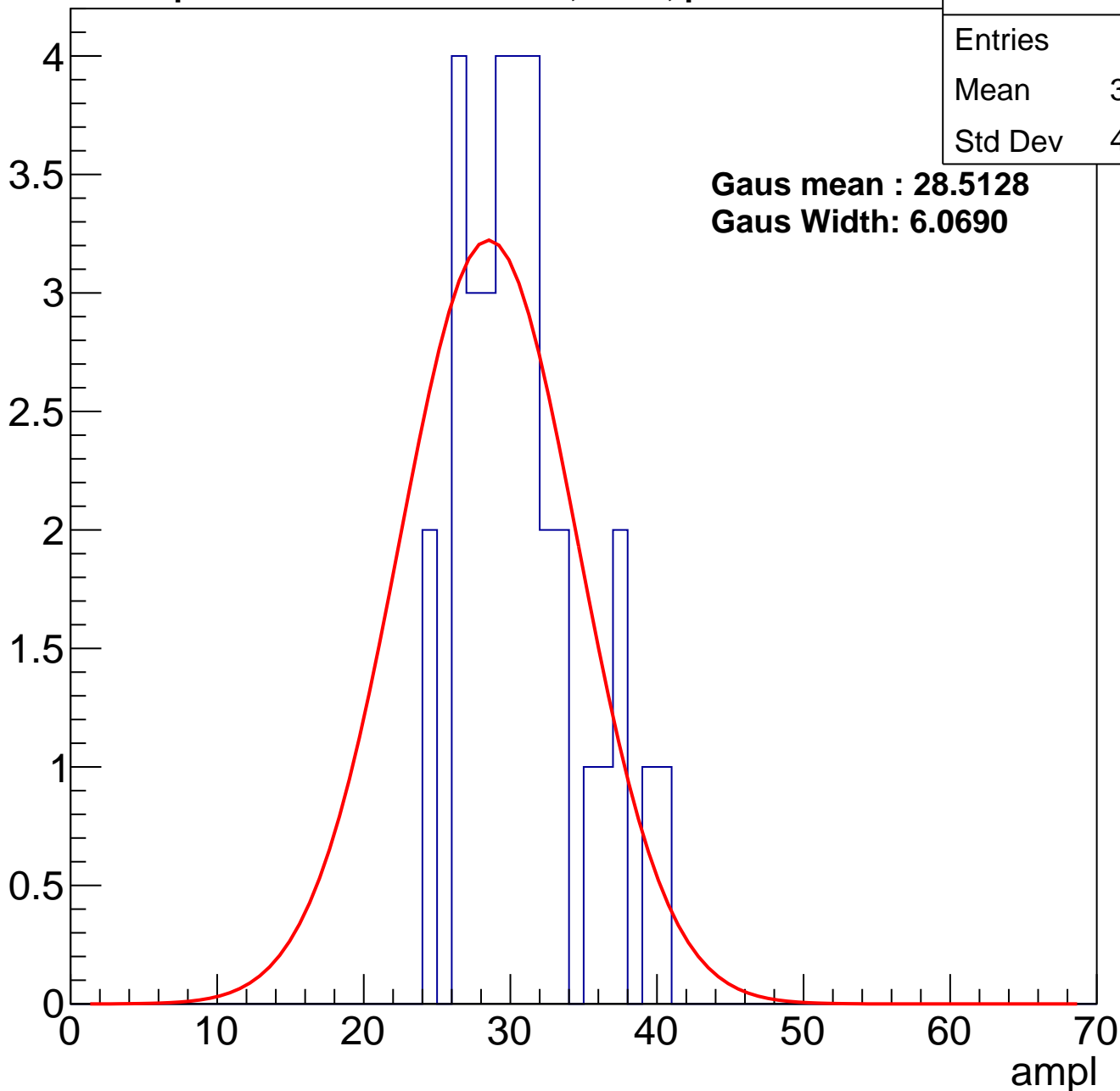


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch32, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	34
Mean	30.32
Std Dev	4.005

Gaus mean : 28.5128

Gaus Width: 6.0690

B1L103S, U15-ch32, adc1

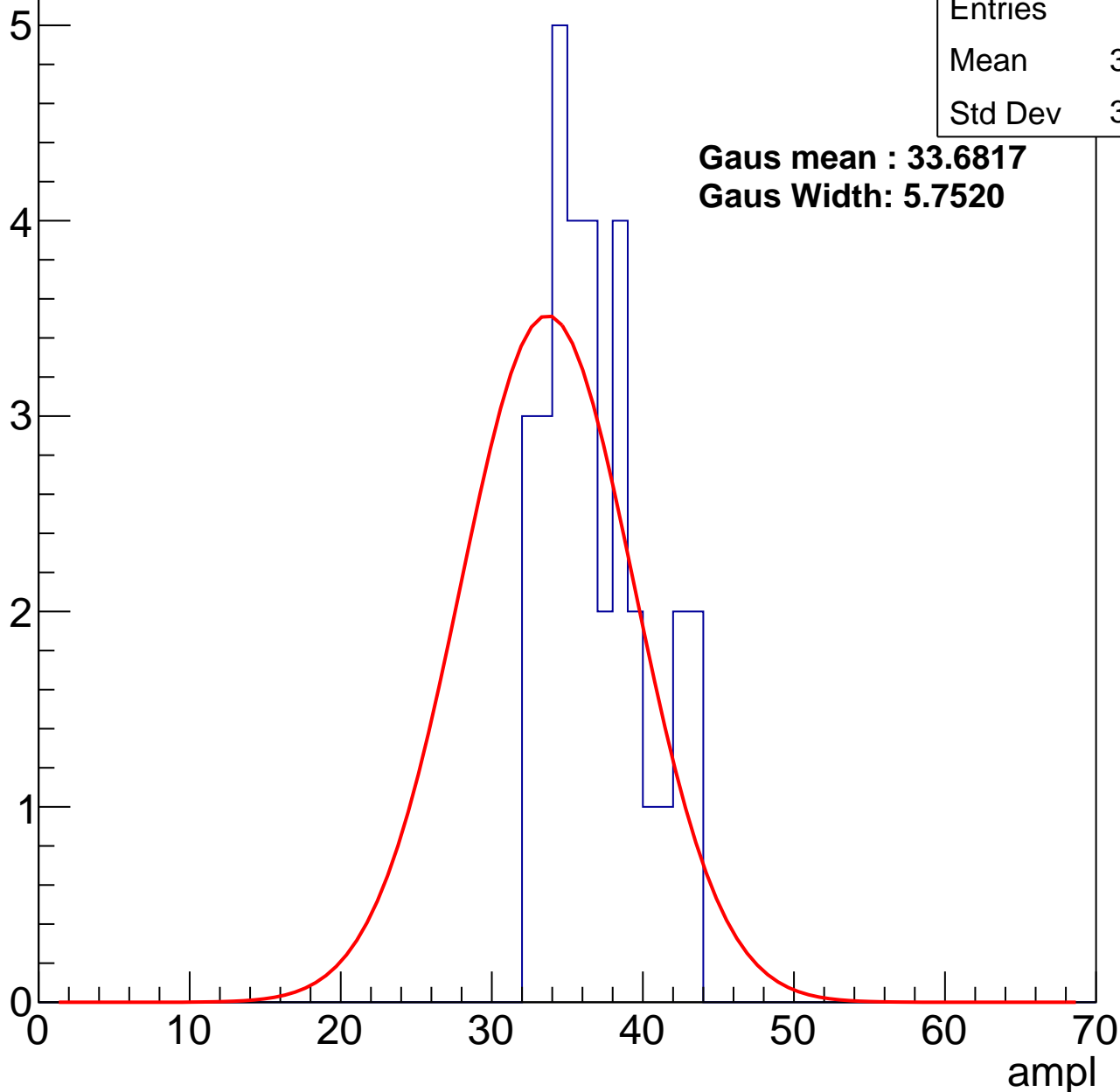
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	36.48
Std Dev	3.202

Gaus mean : 33.6817

Gaus Width: 5.7520



B1L103S, U15-ch32, adc2

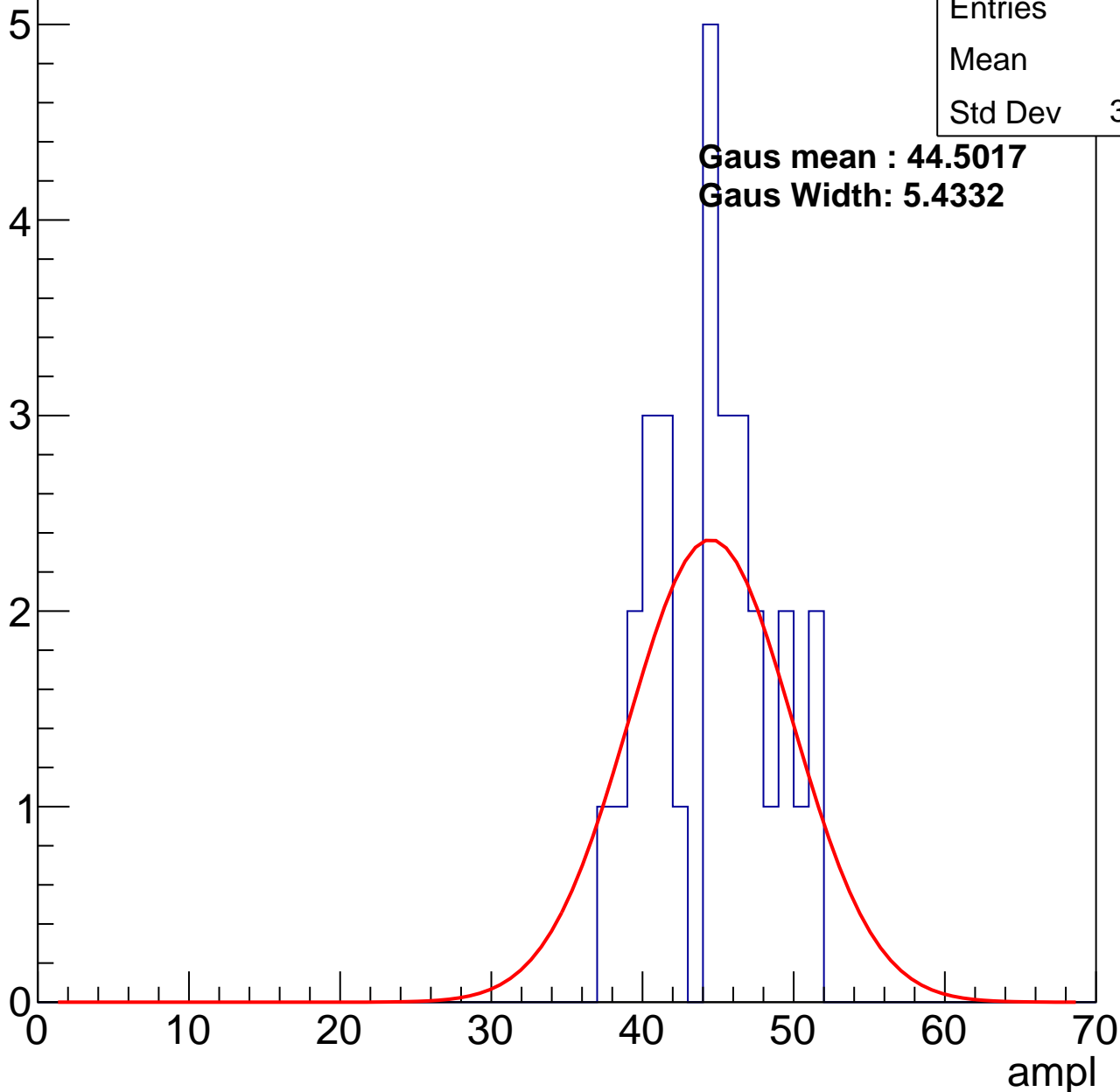
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	44.1
Std Dev	3.859

Gaus mean : 44.5017

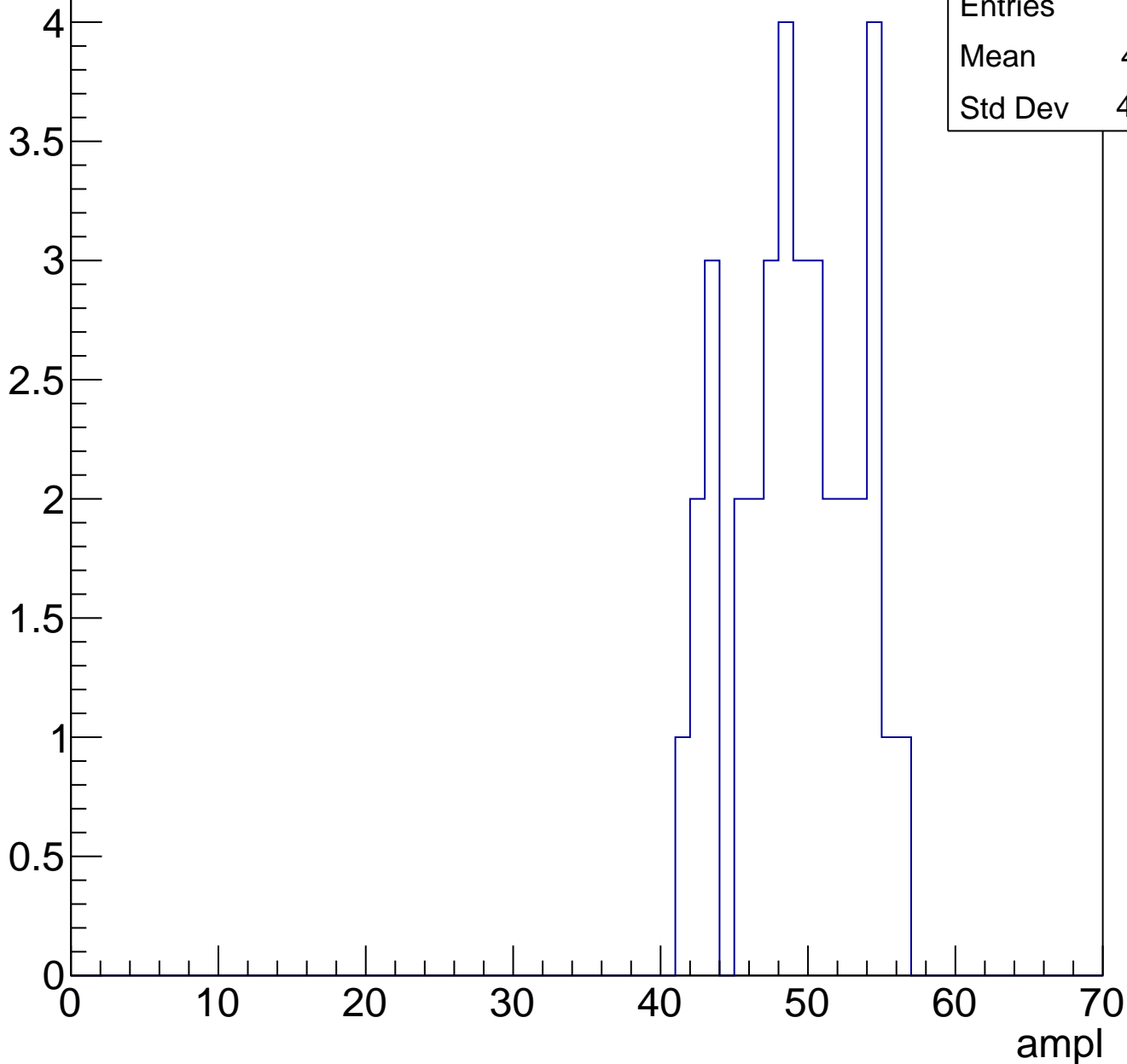
Gaus Width: 5.4332



B1L103S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

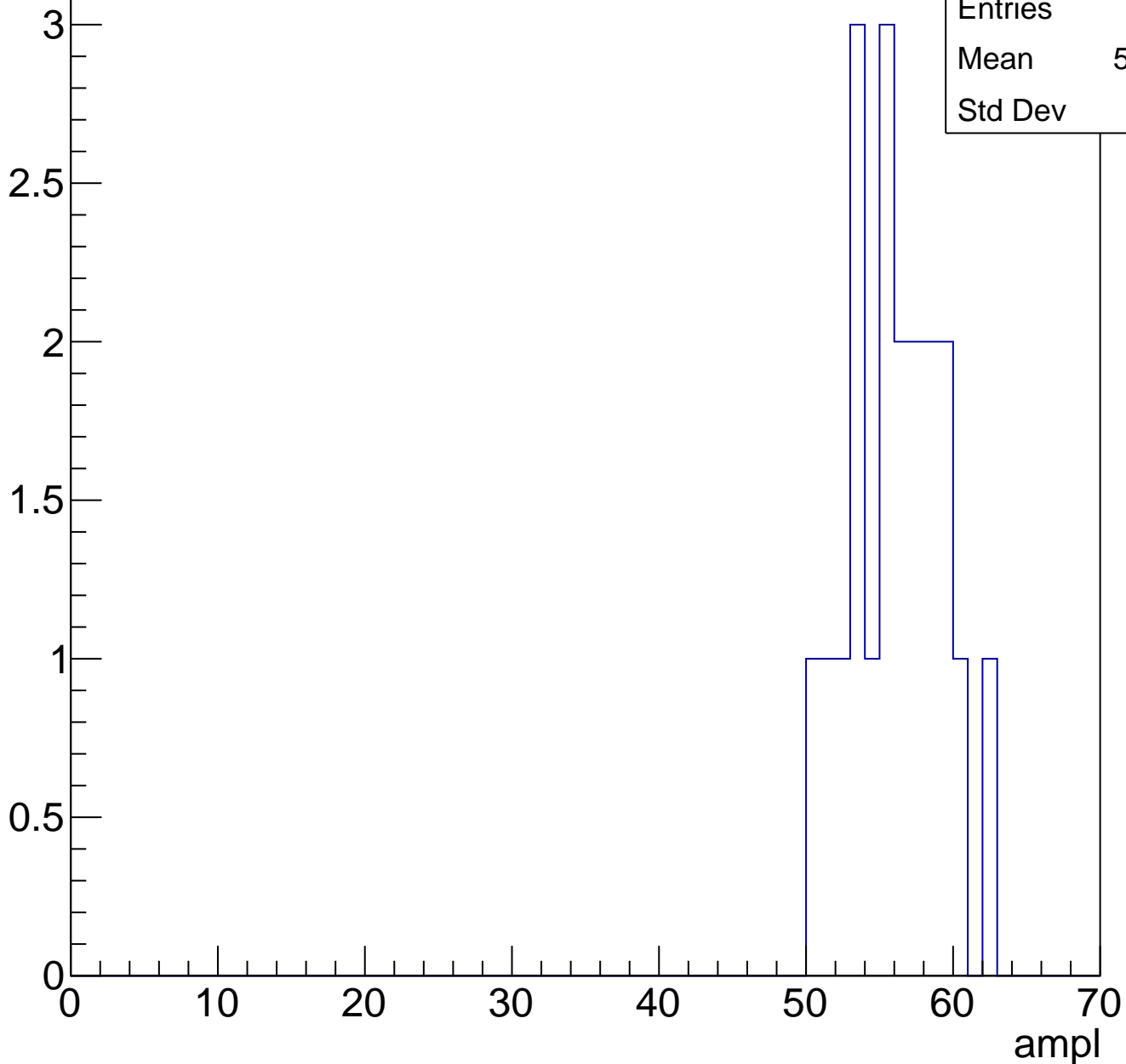
Entry



B1L103S, U15-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

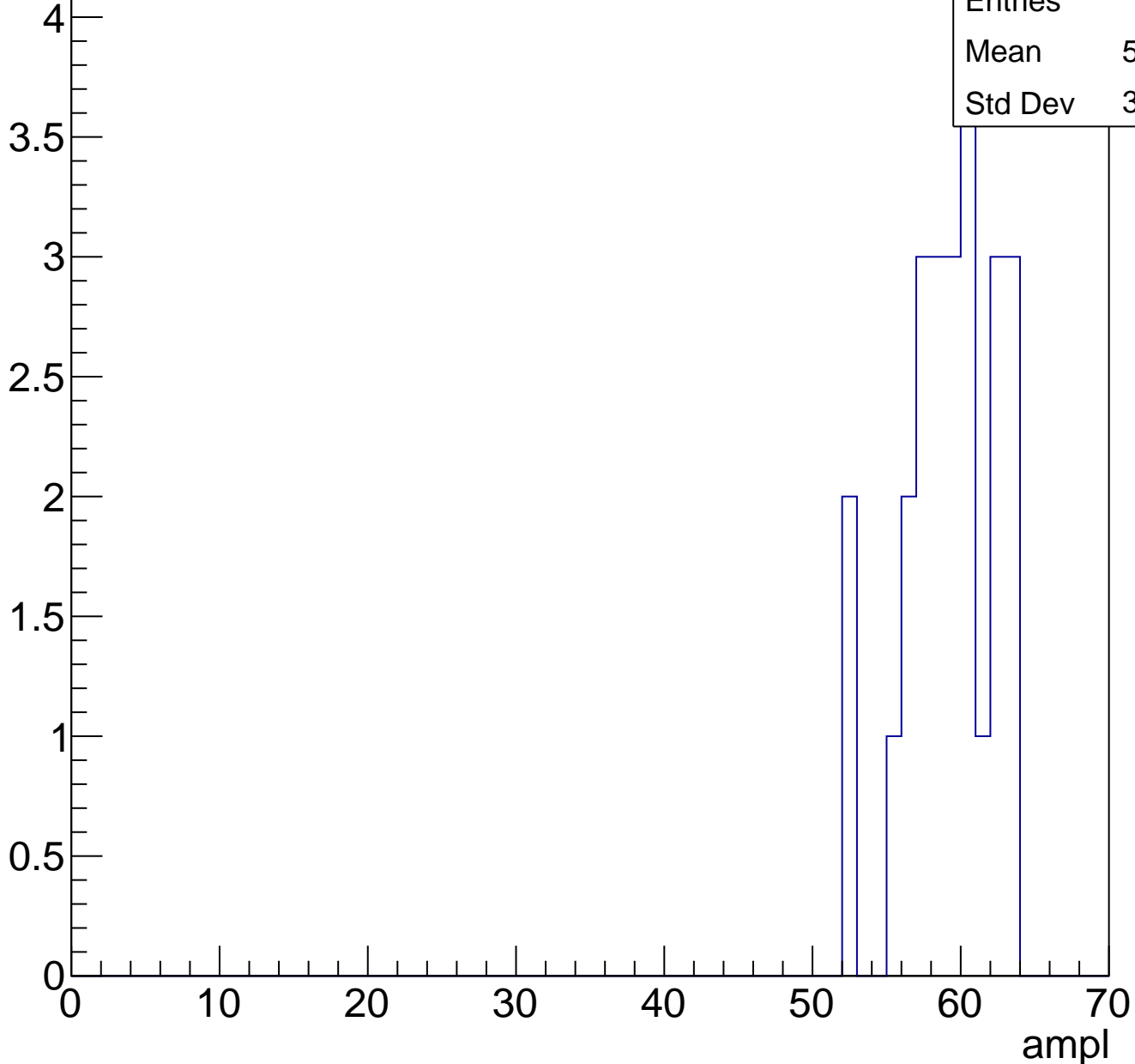
Entry



B1L103S, U15-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

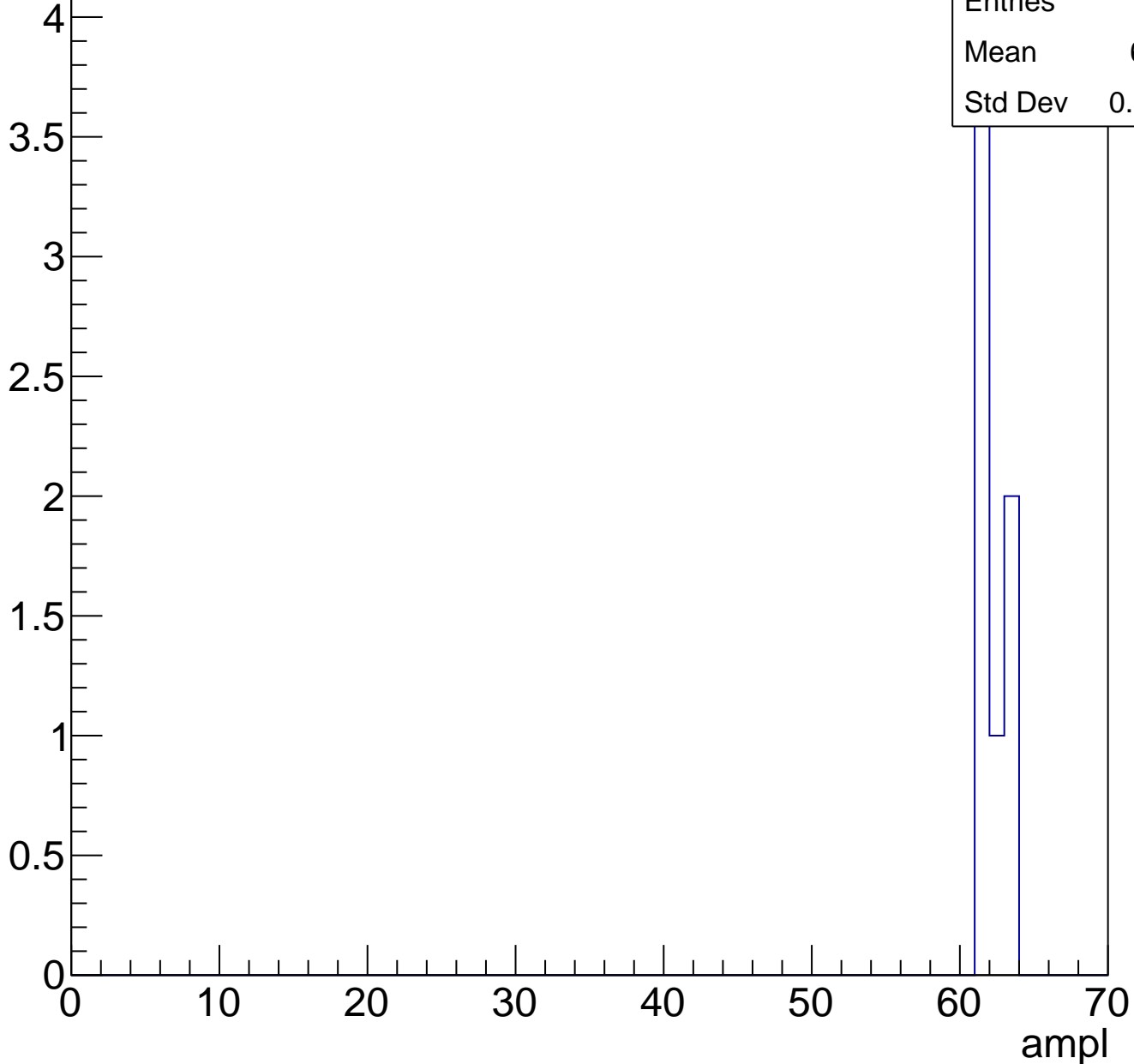
Entry



B1L103S, U15-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

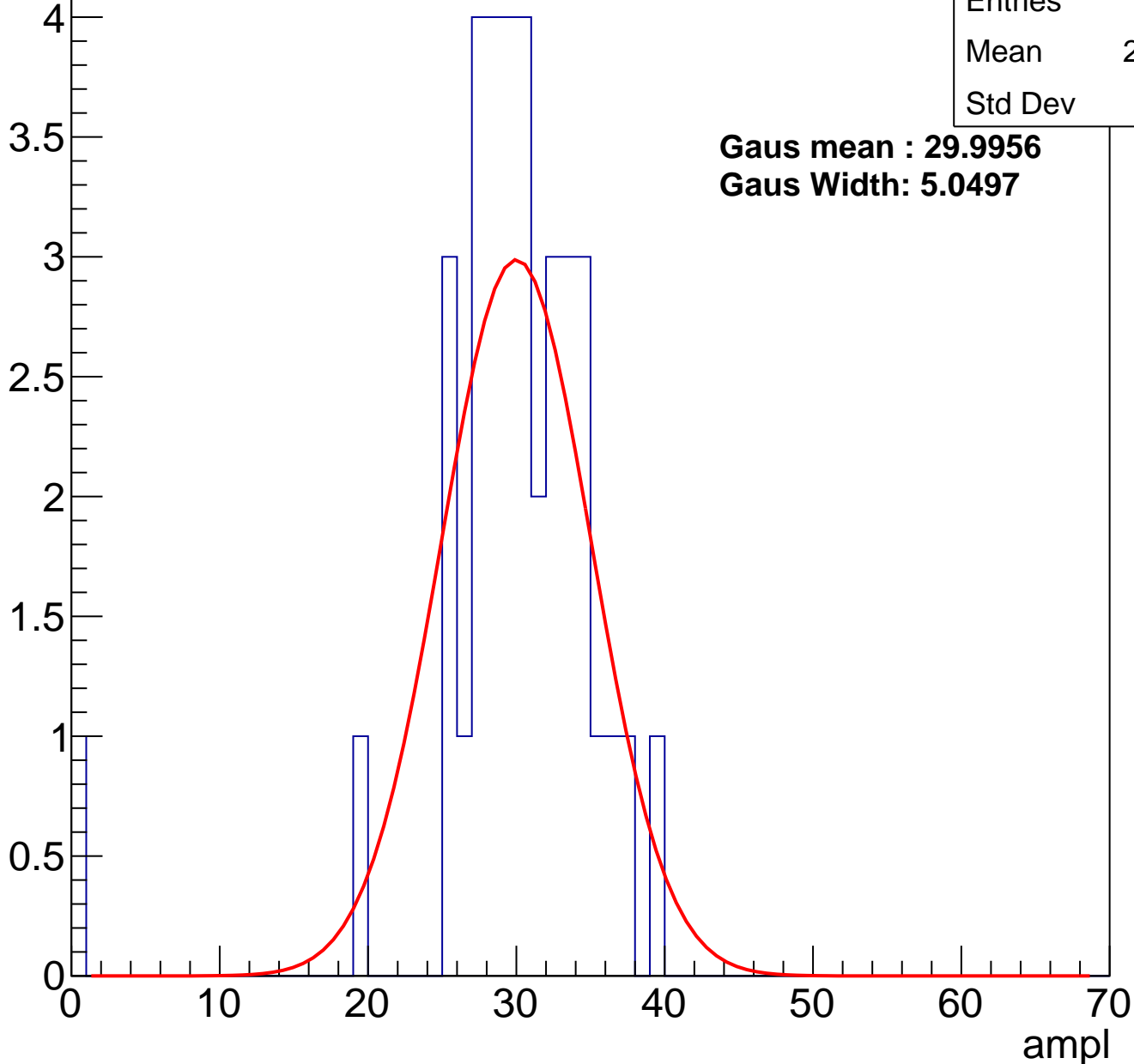


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch33, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch33, adc1

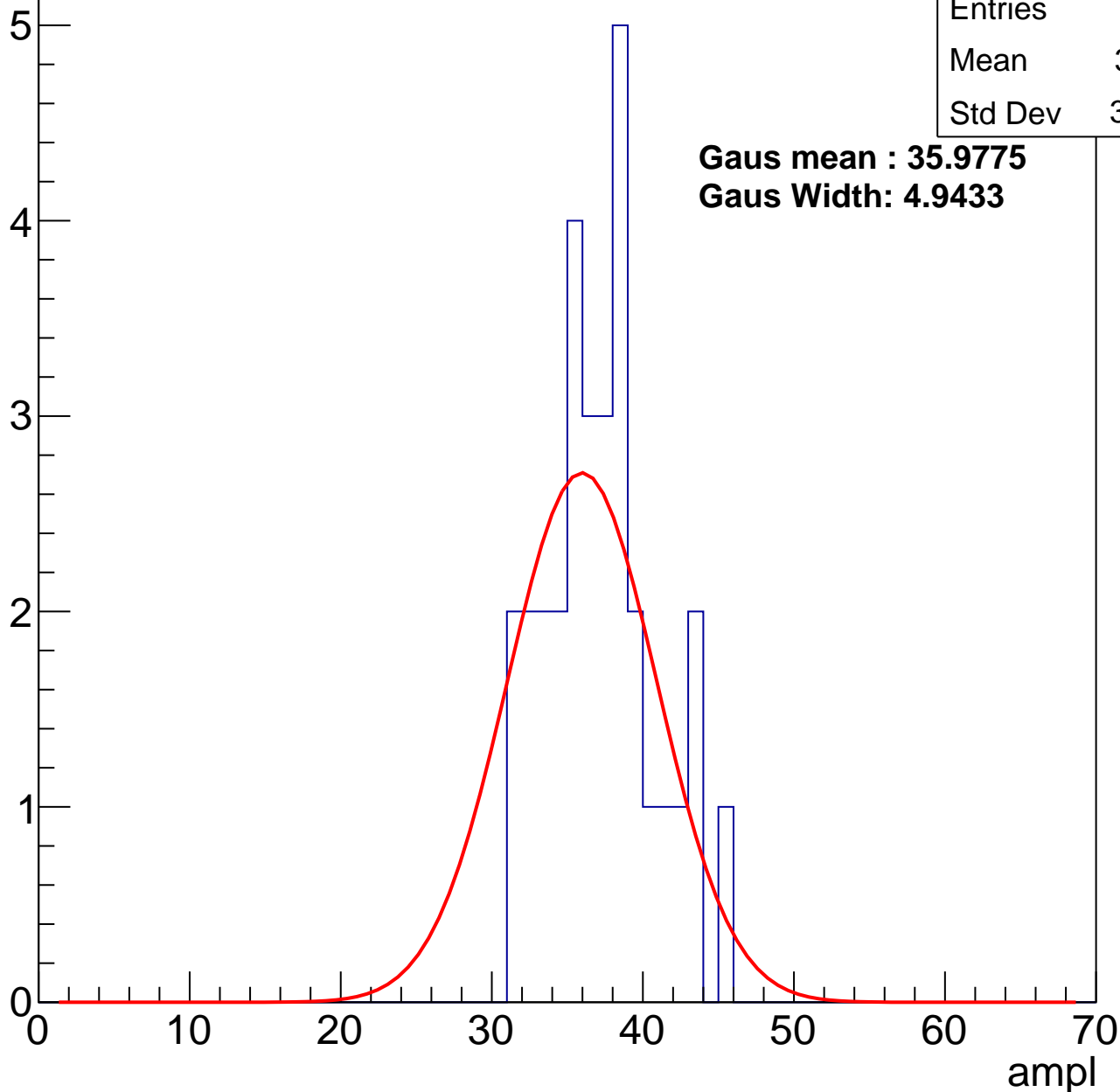
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	36.81
Std Dev	3.542

Gaus mean : 35.9775

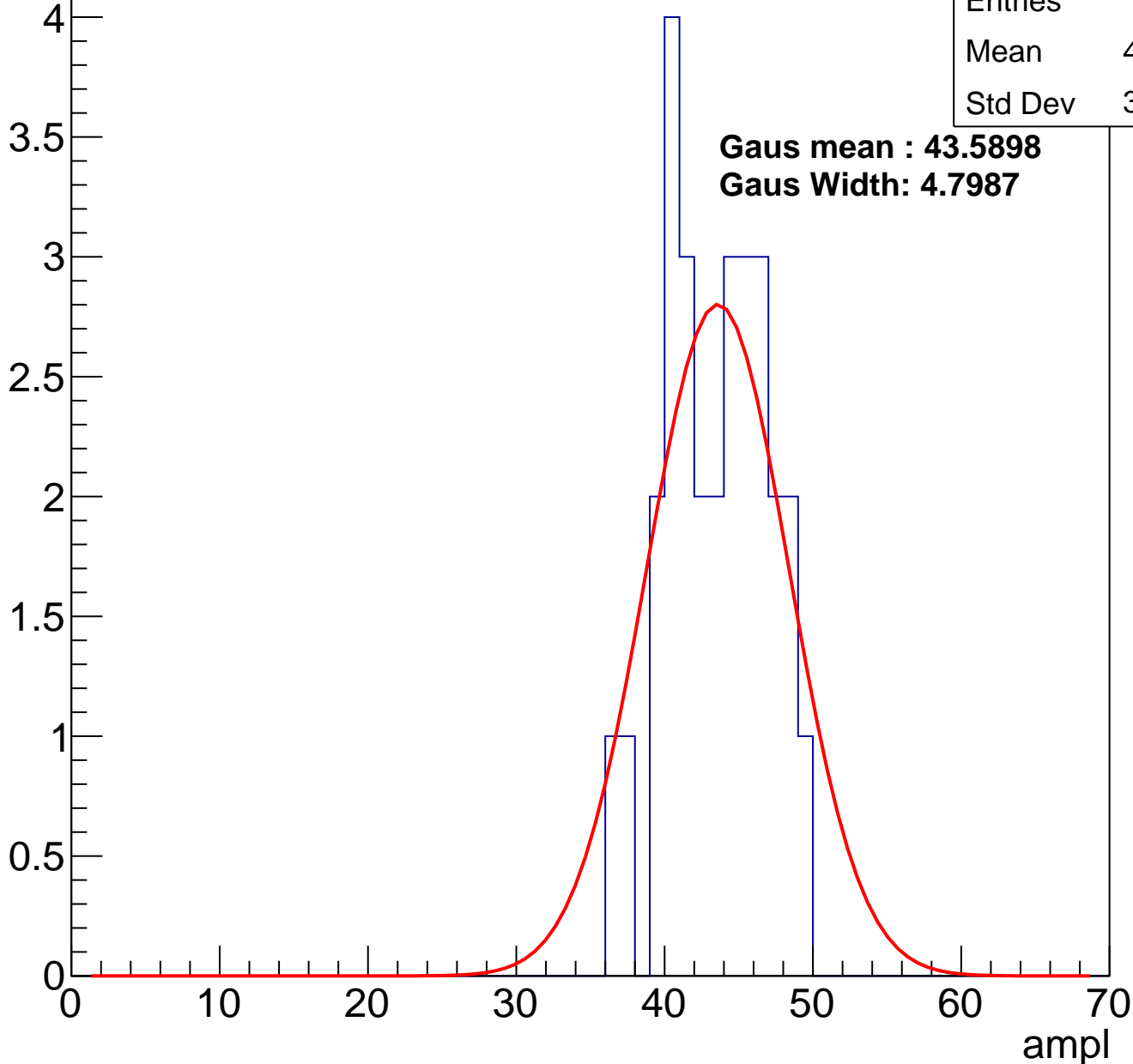
Gaus Width: 4.9433



B1L103S, U15-ch33, adc2

calib_packv5_042523_0143.root, FC#7, port C2

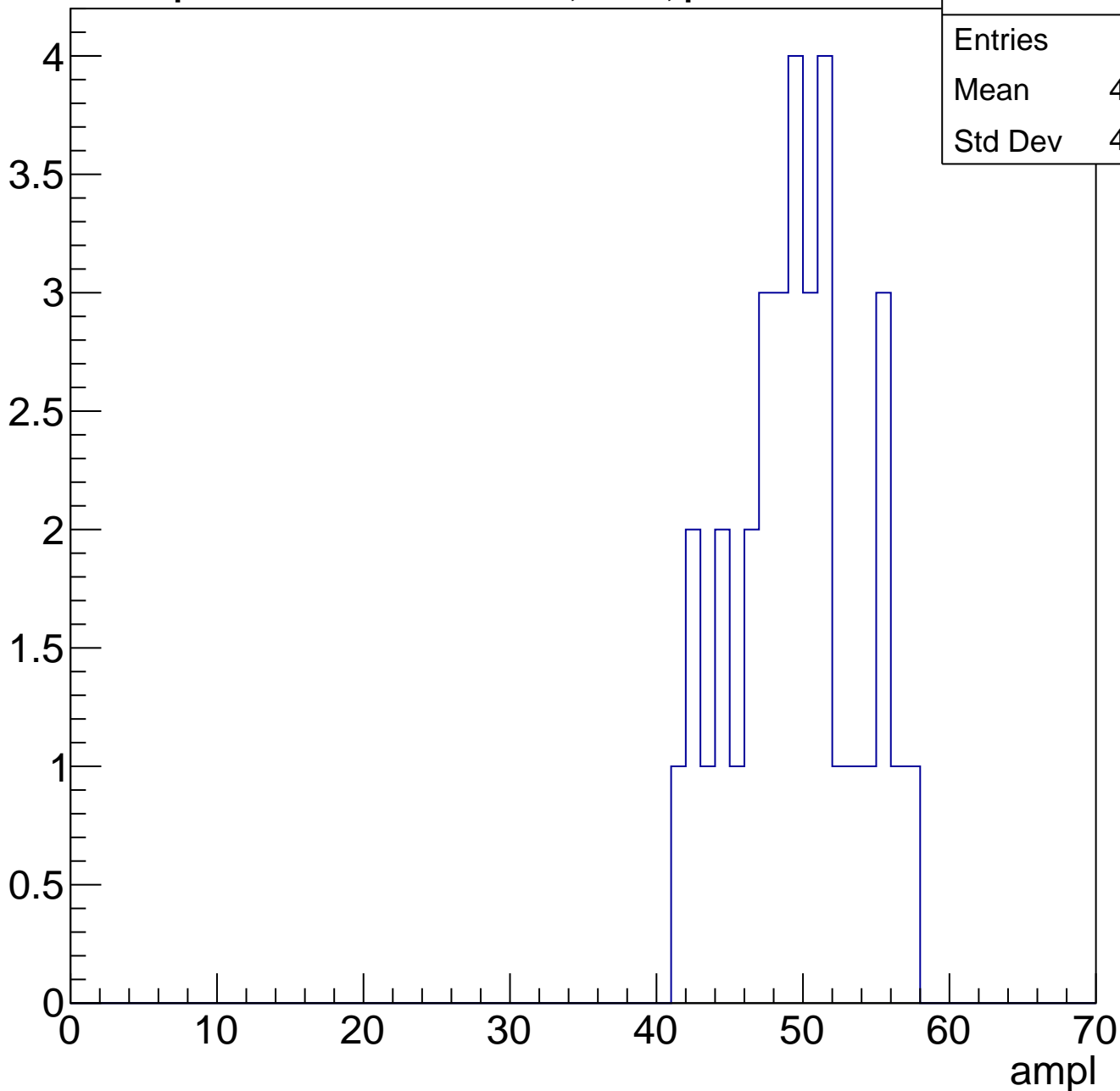
Entry



B1L103S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

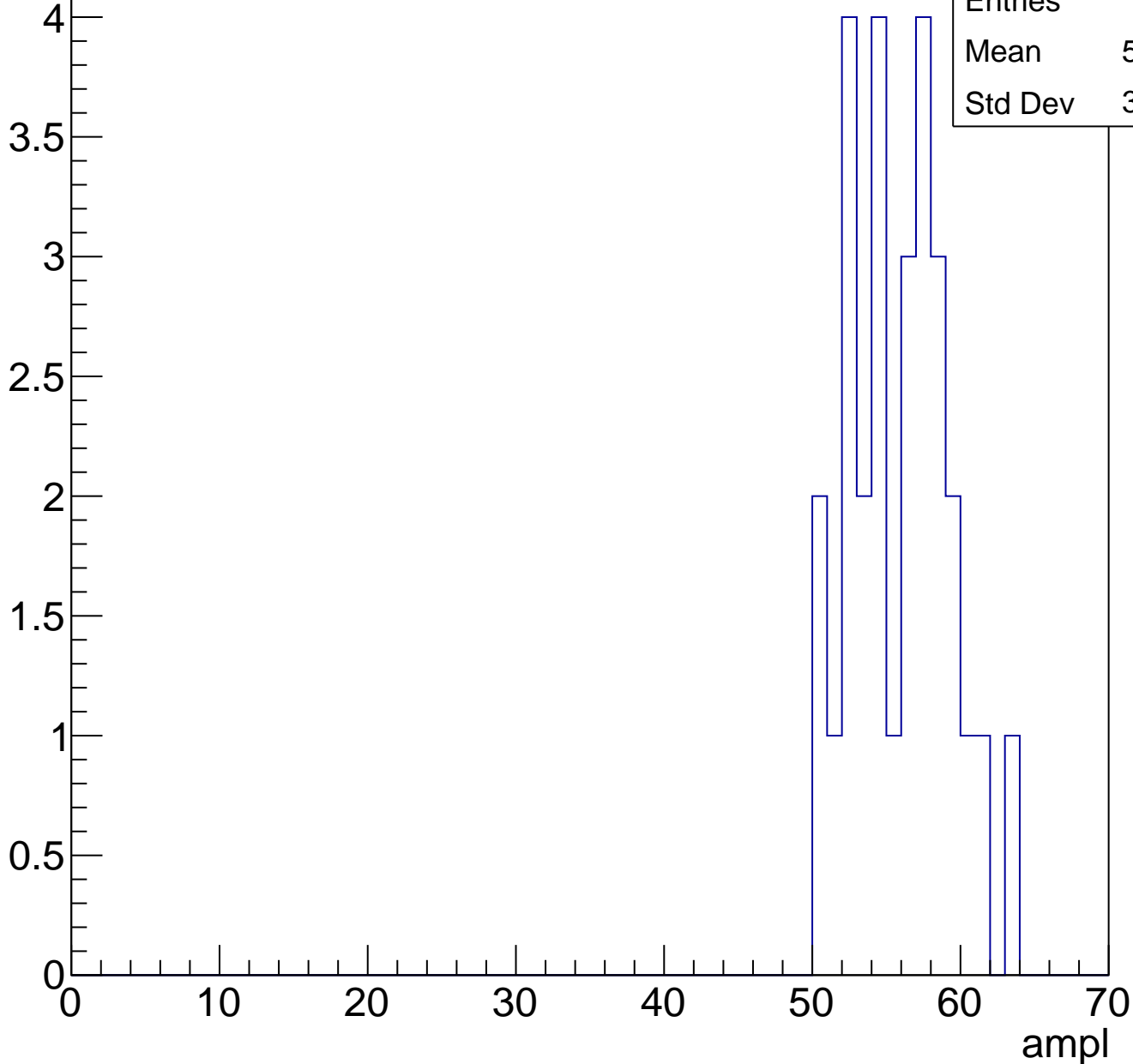
Entry



B1L103S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

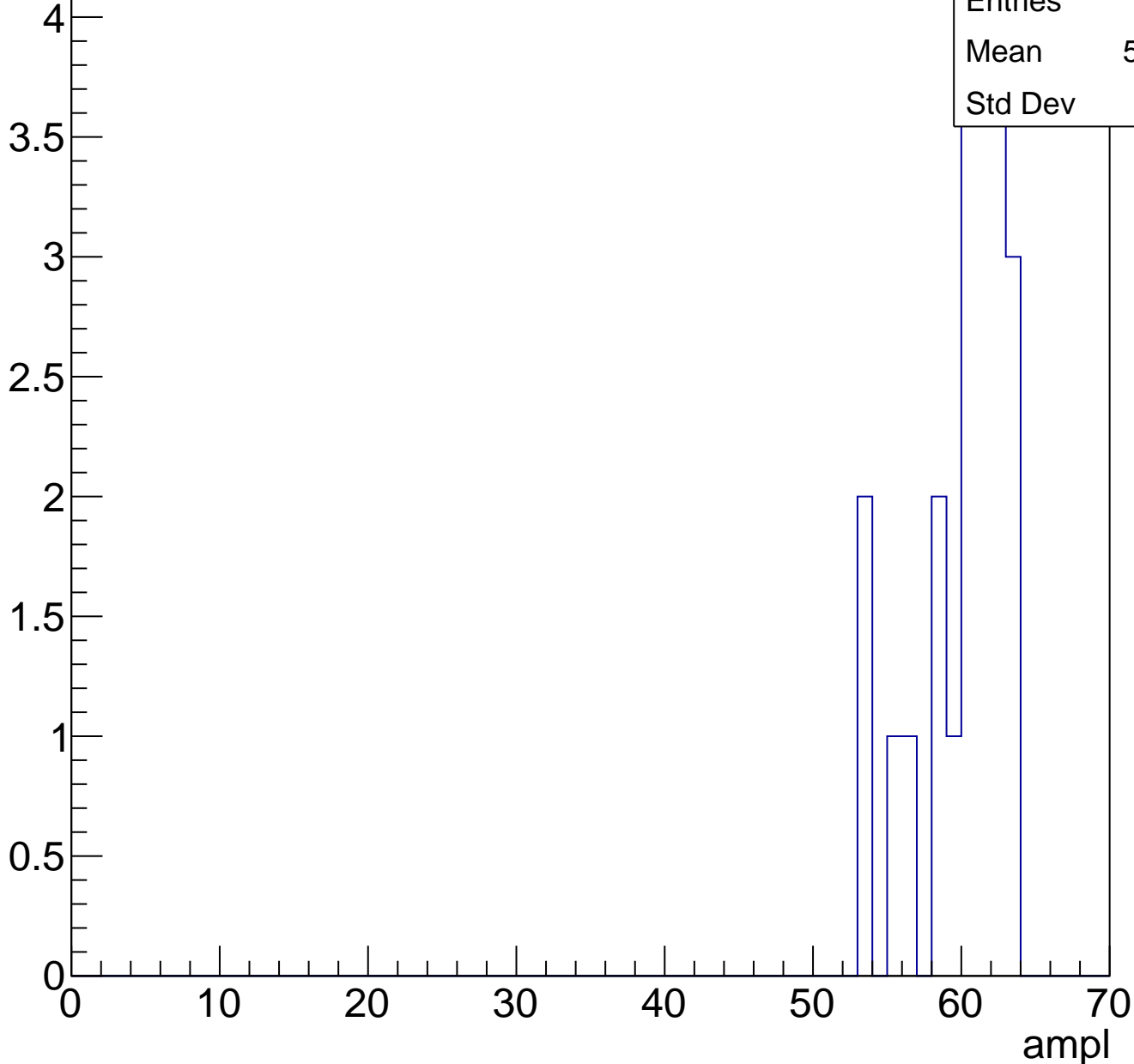
Entry



B1L103S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

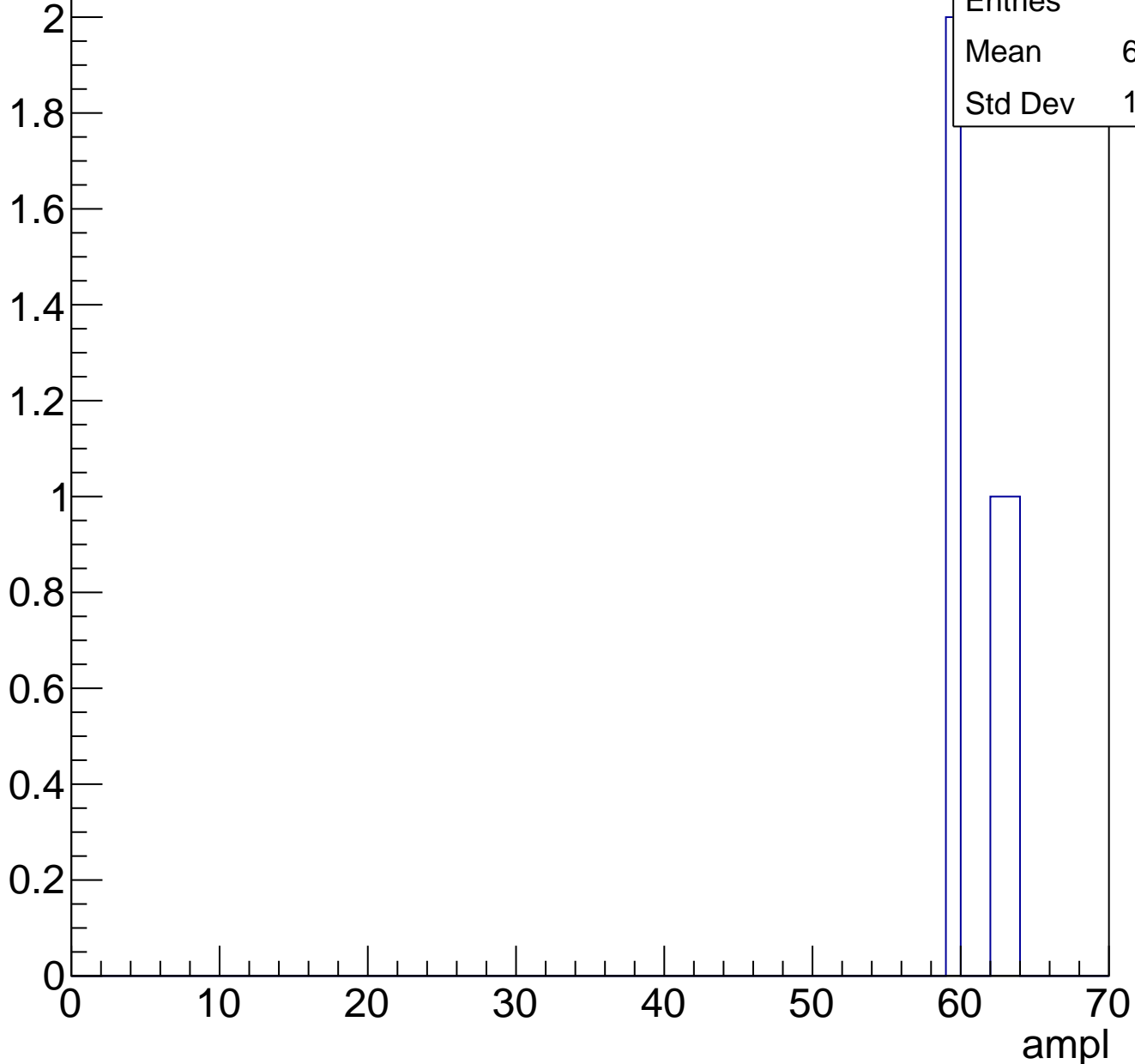
Entry



B1L103S, U15-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	60.75
Std Dev	1.785

B1L103S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

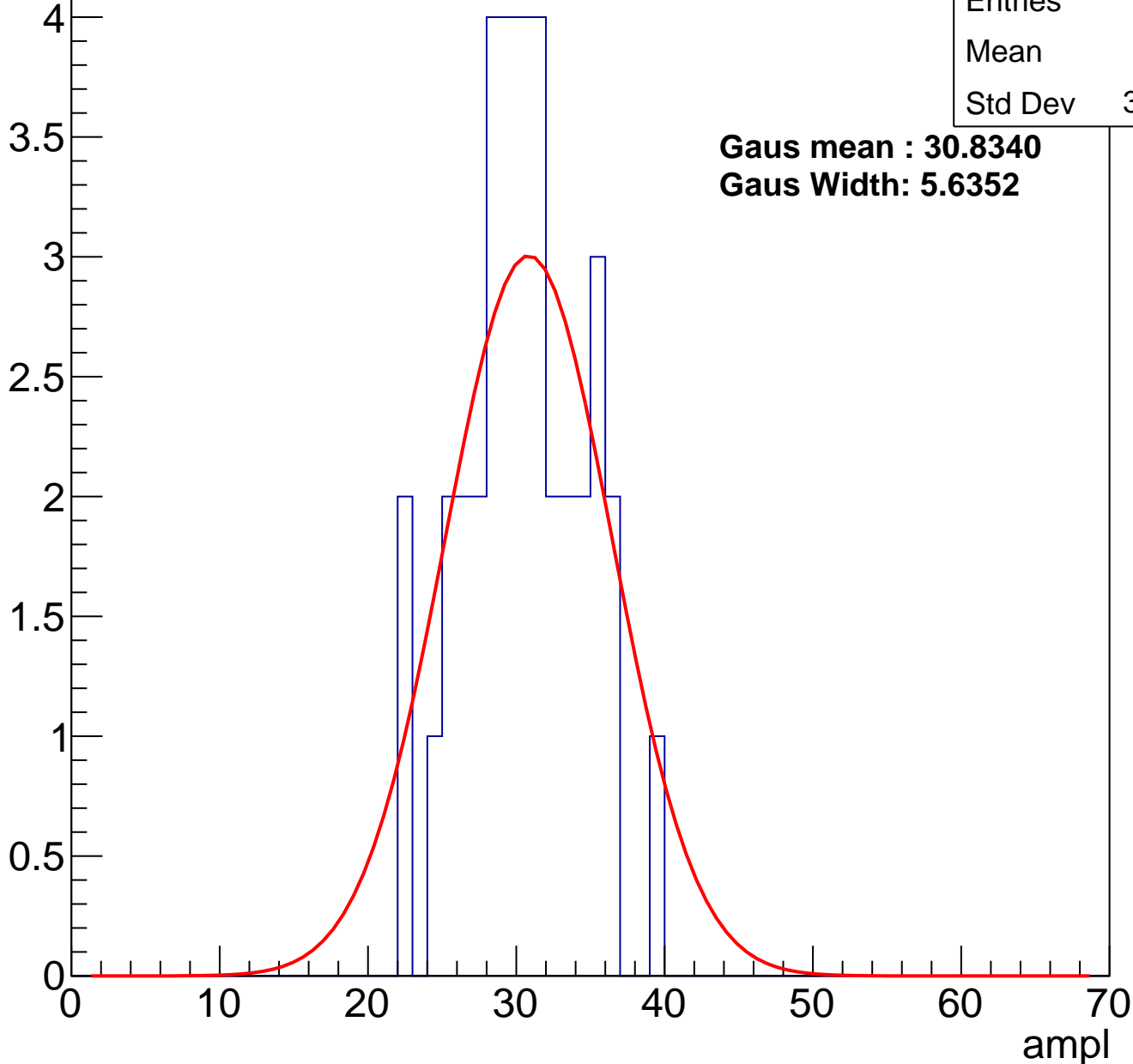


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch34, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

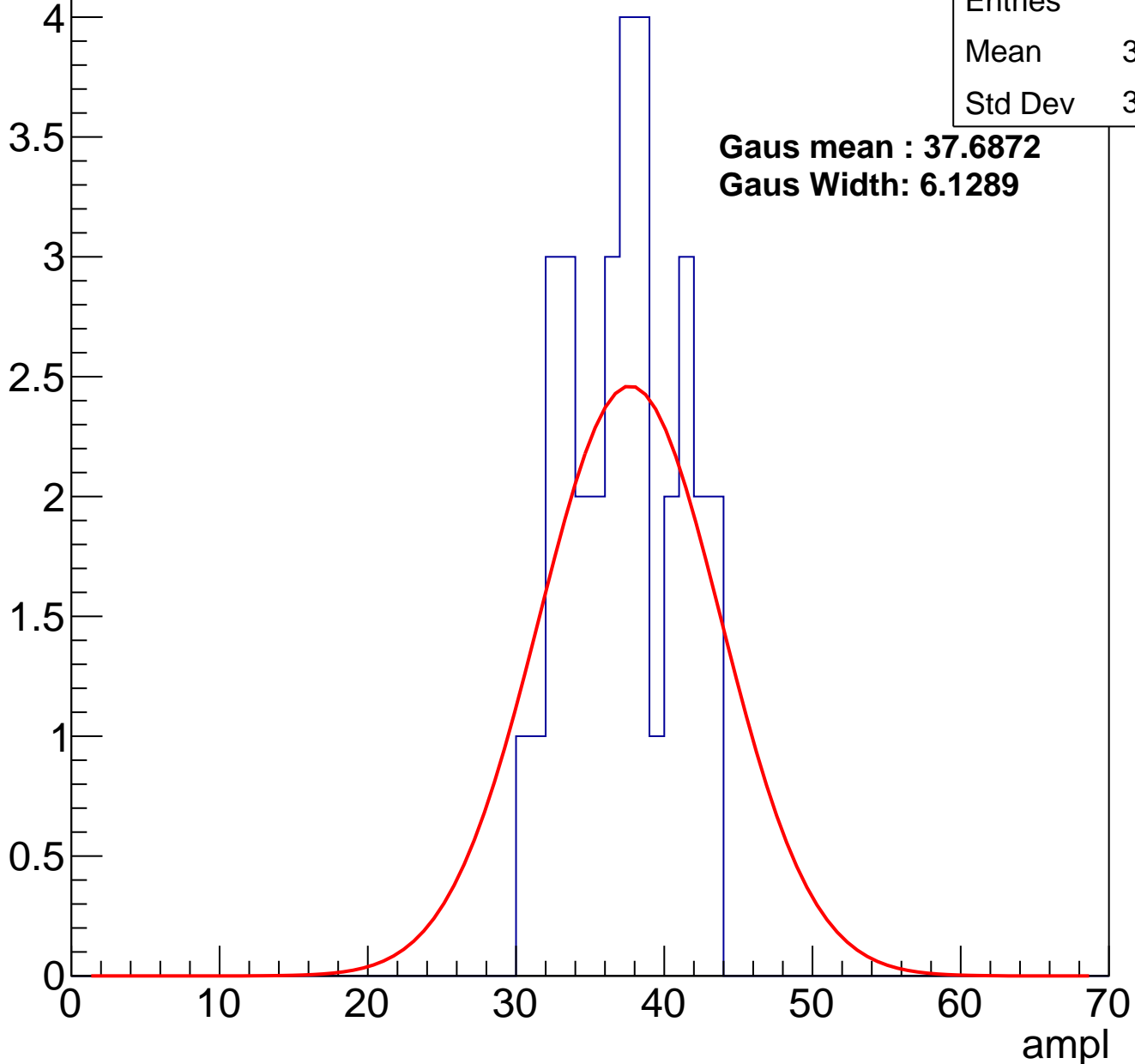


Entries	37
Mean	30
Std Dev	3.939

B1L103S, U15-ch34, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	33
Mean	36.79
Std Dev	3.616

Gaus mean : 37.6872

Gaus Width: 6.1289

B1L103S, U15-ch34, adc2

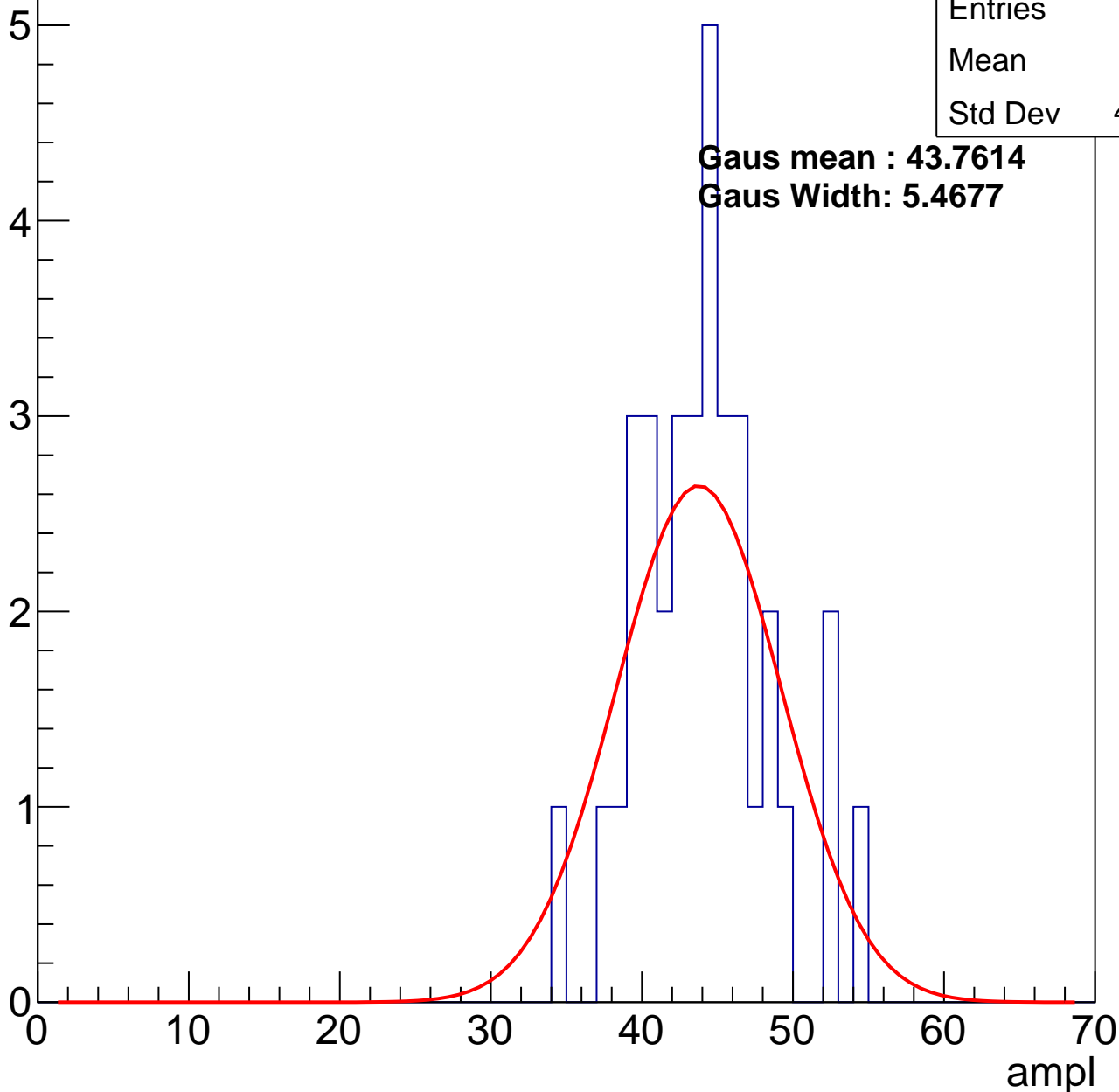
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	43.6
Std Dev	4.291

Gaus mean : 43.7614

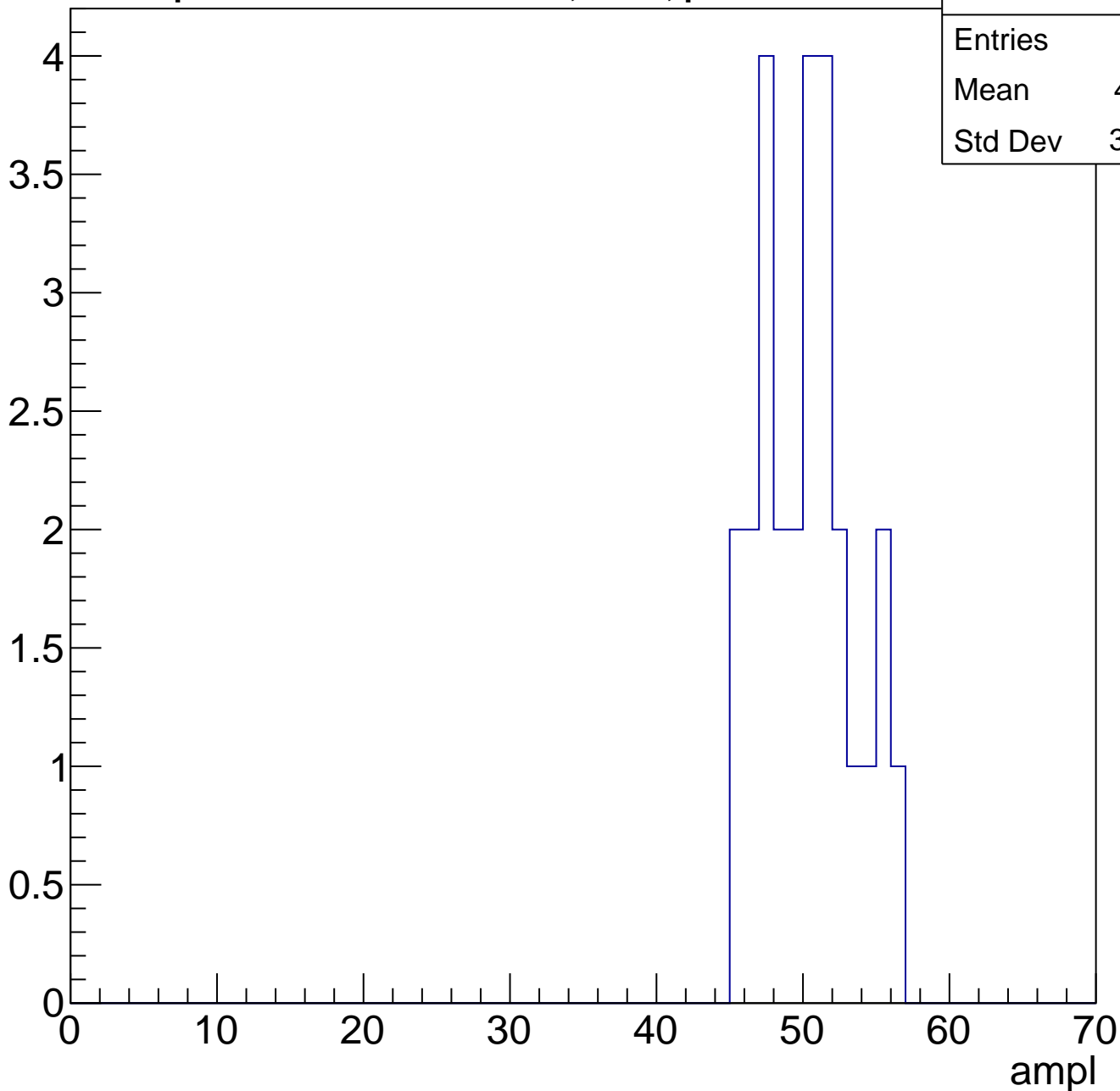
Gaus Width: 5.4677



B1L103S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

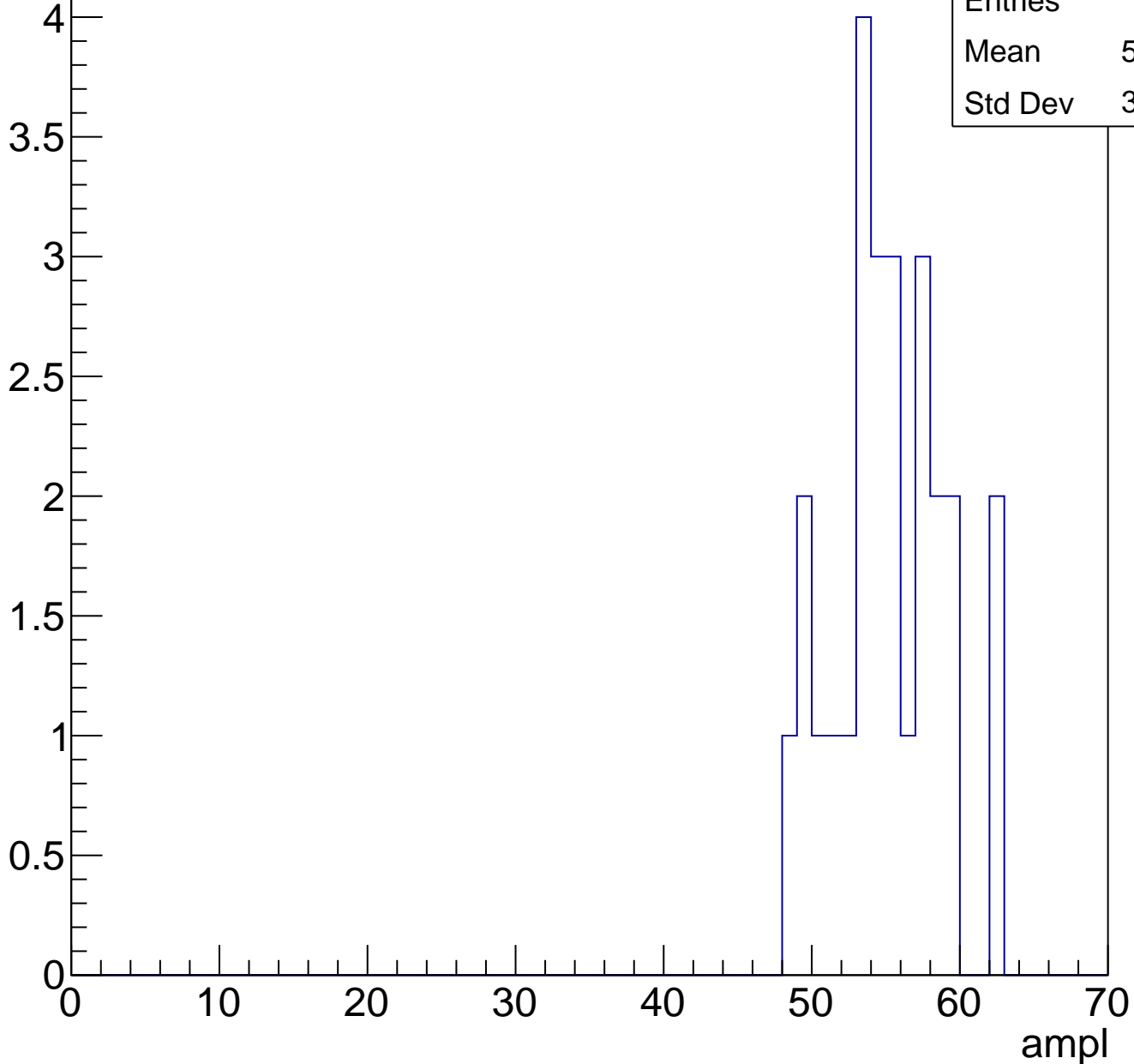
Entry



B1L103S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



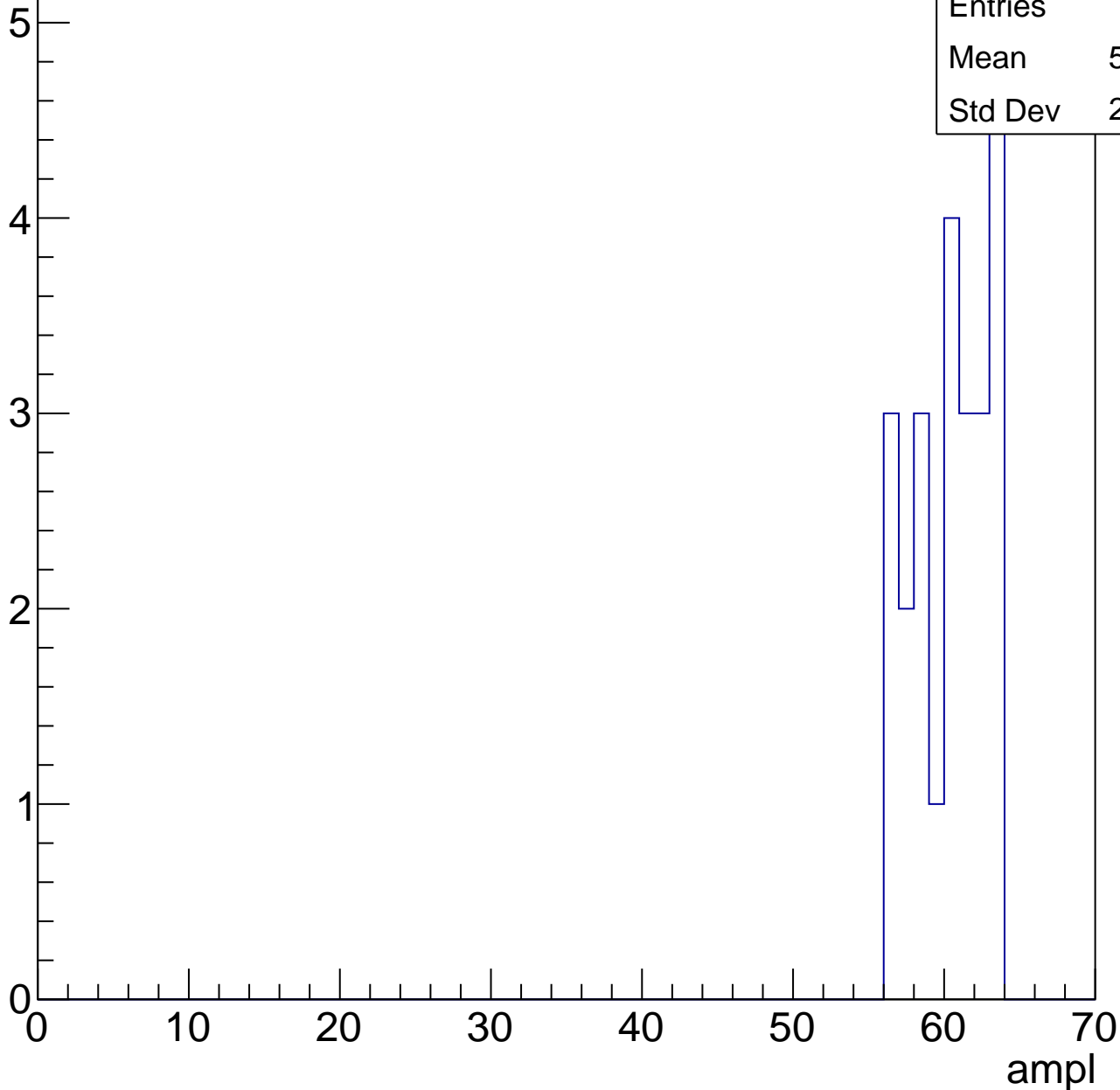
Entries	26
Mean	54.73
Std Dev	3.664

B1L103S, U15-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

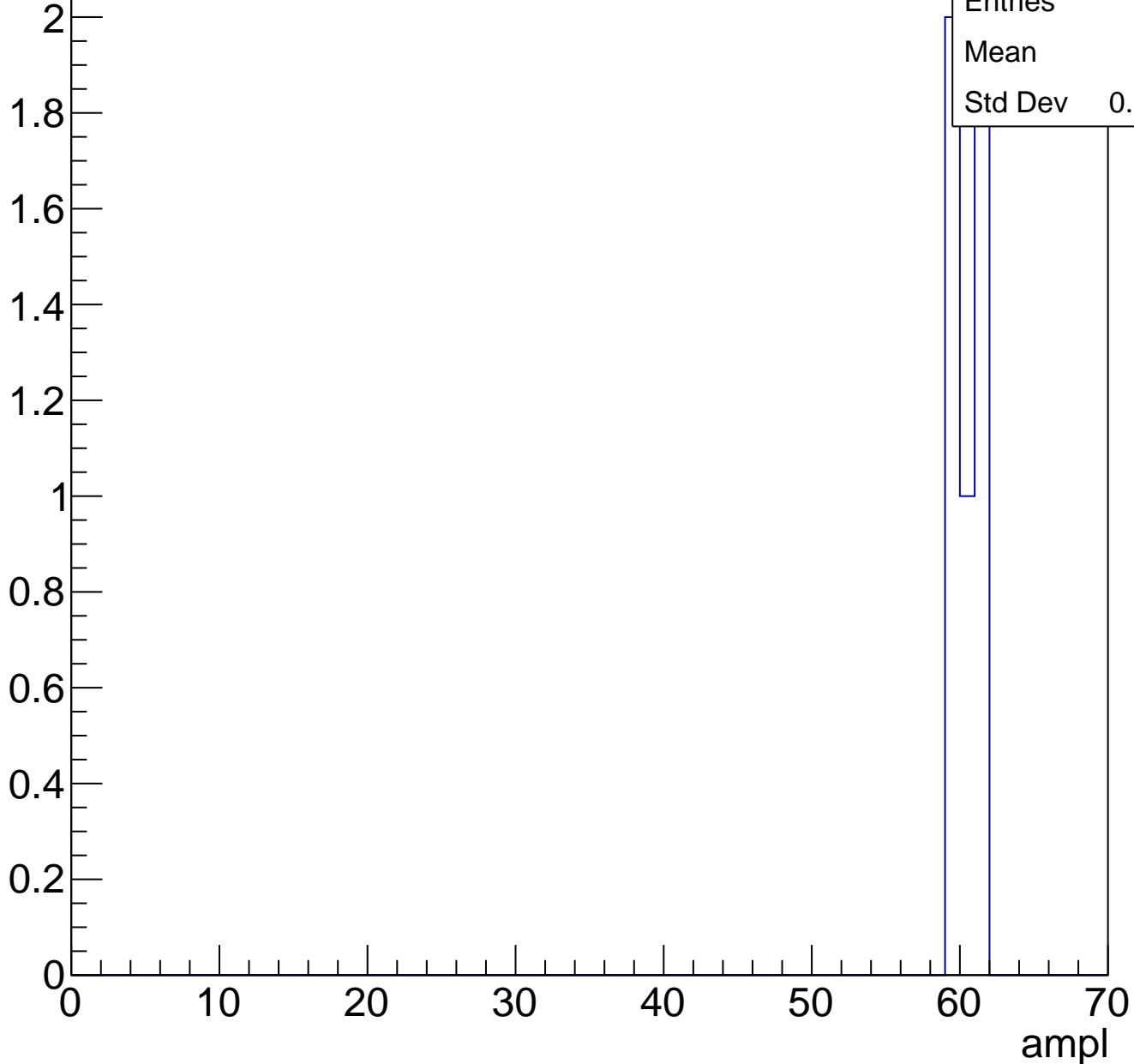
Entries	24
Mean	59.96
Std Dev	2.406



B1L103S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

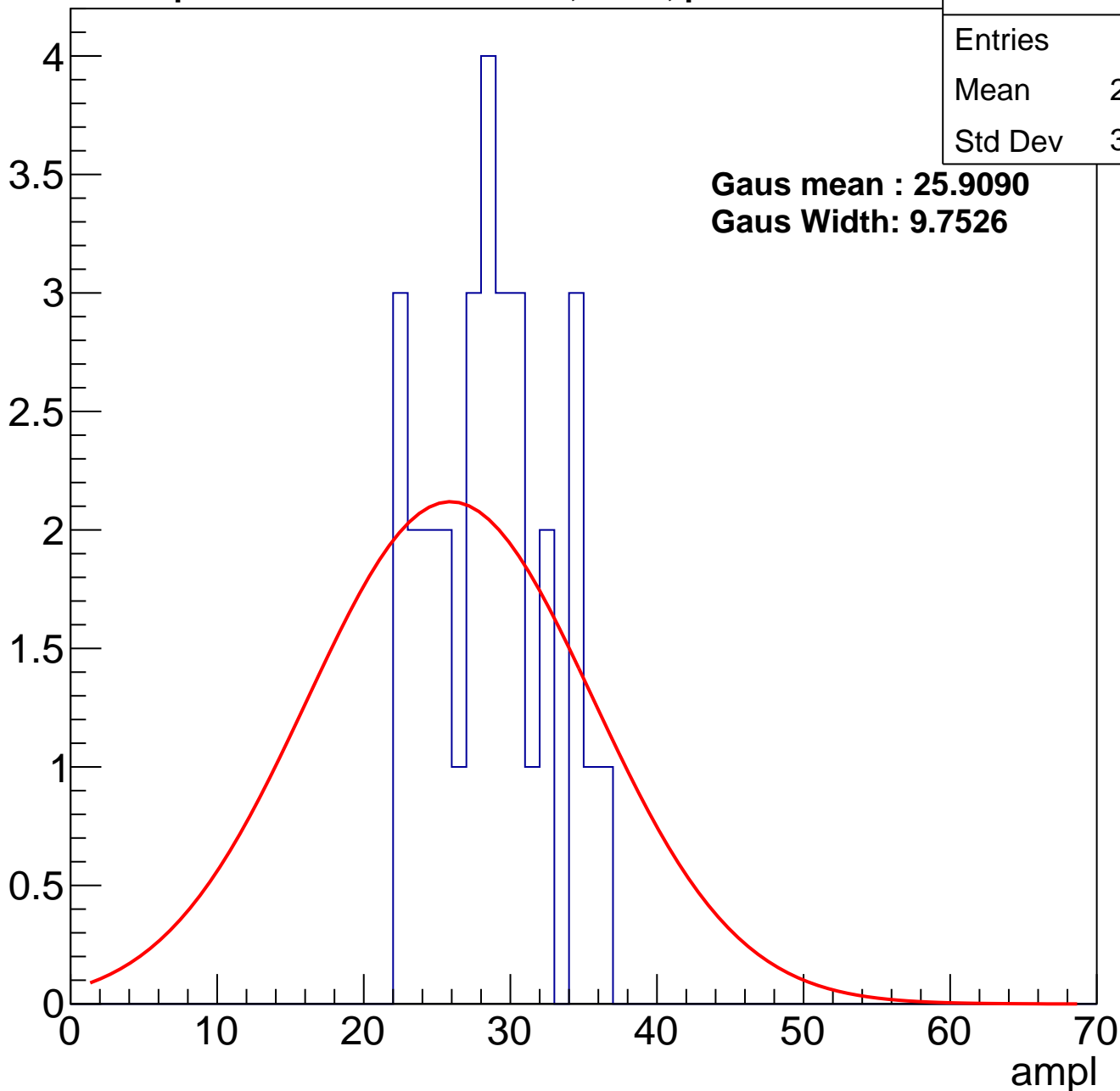


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch35, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch35, adc1

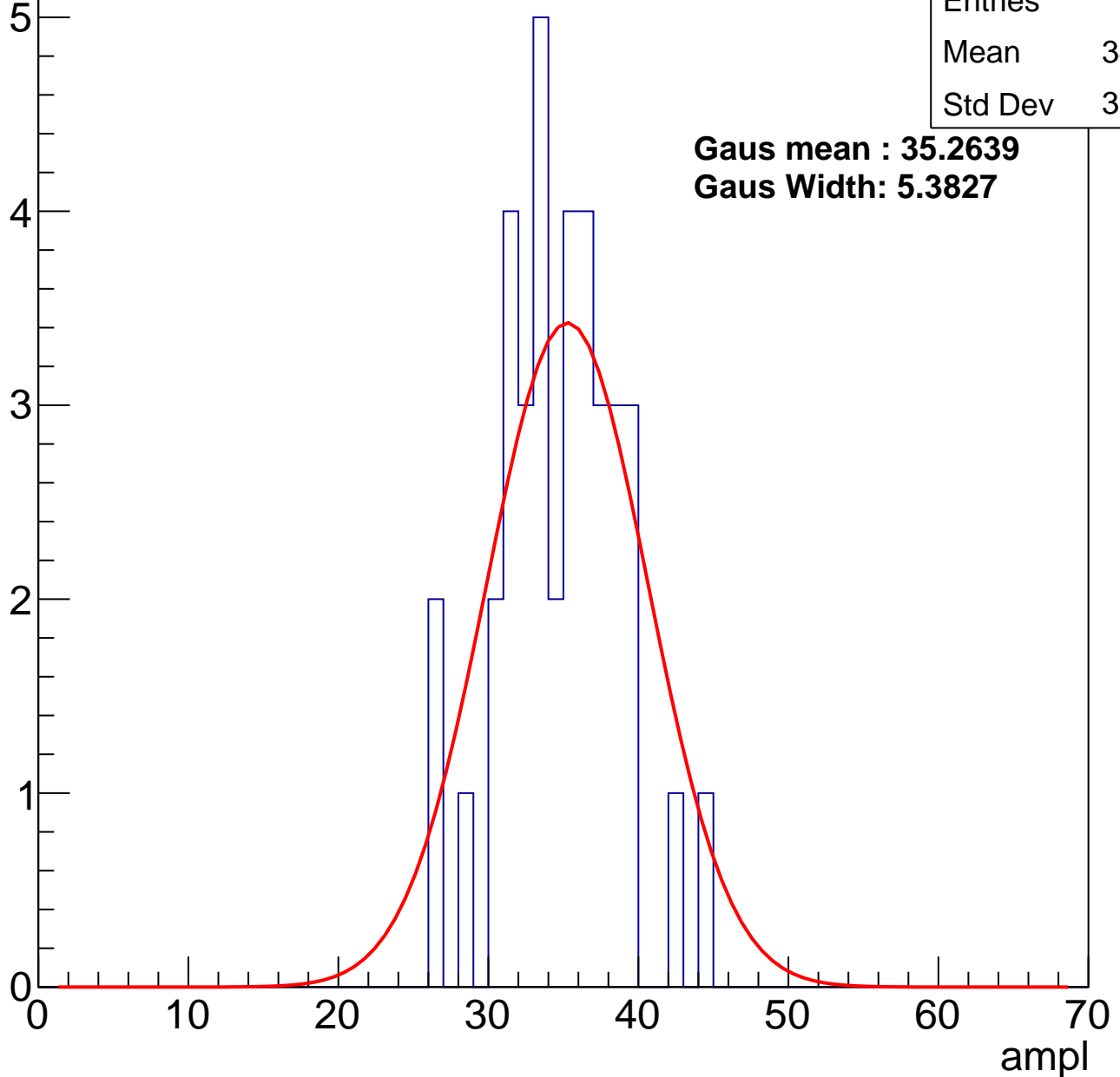
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	34.34
Std Dev	3.902

Gaus mean : 35.2639

Gaus Width: 5.3827



B1L103S, U15-ch35, adc2

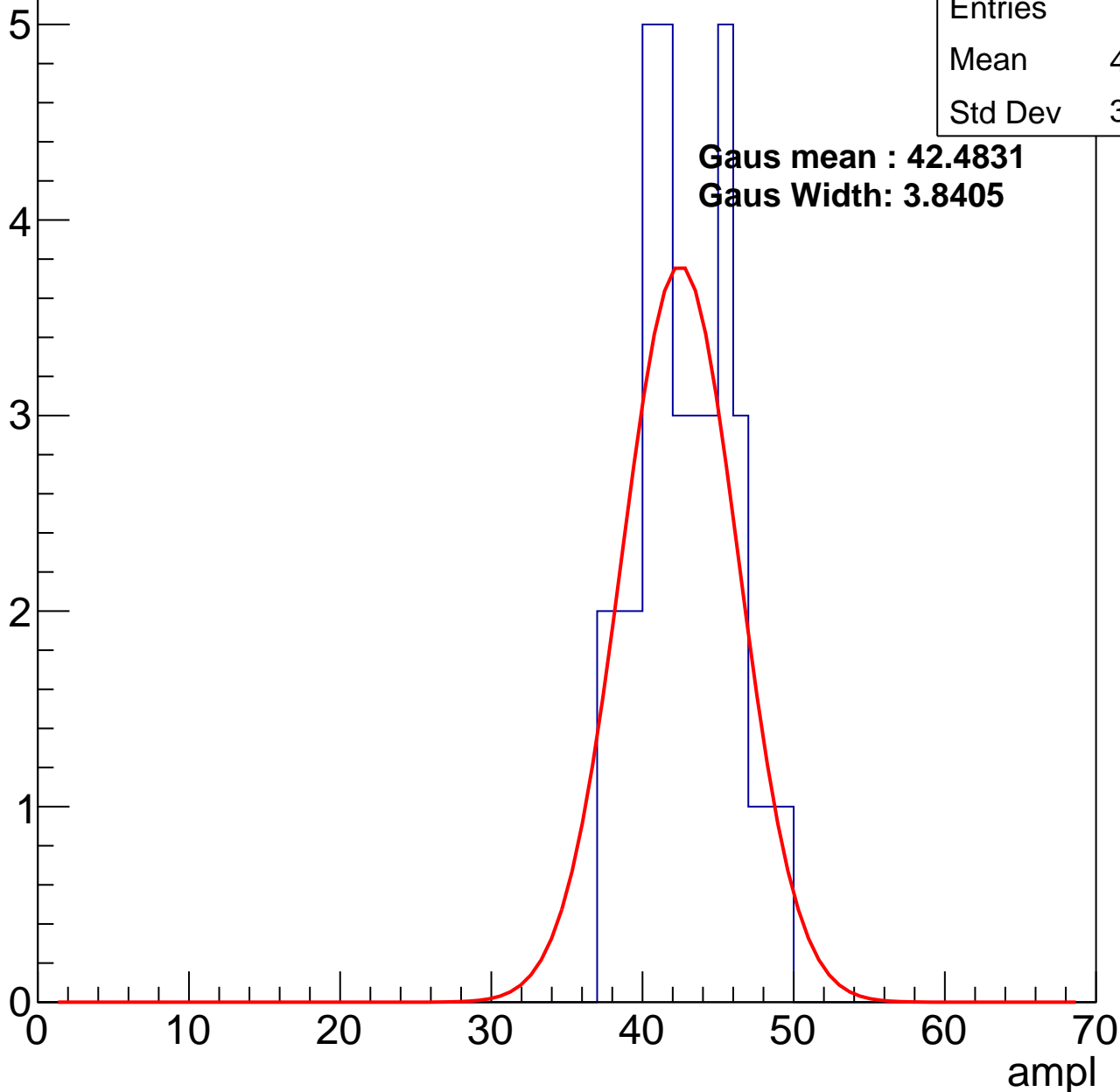
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	42.42
Std Dev	3.058

Gaus mean : 42.4831

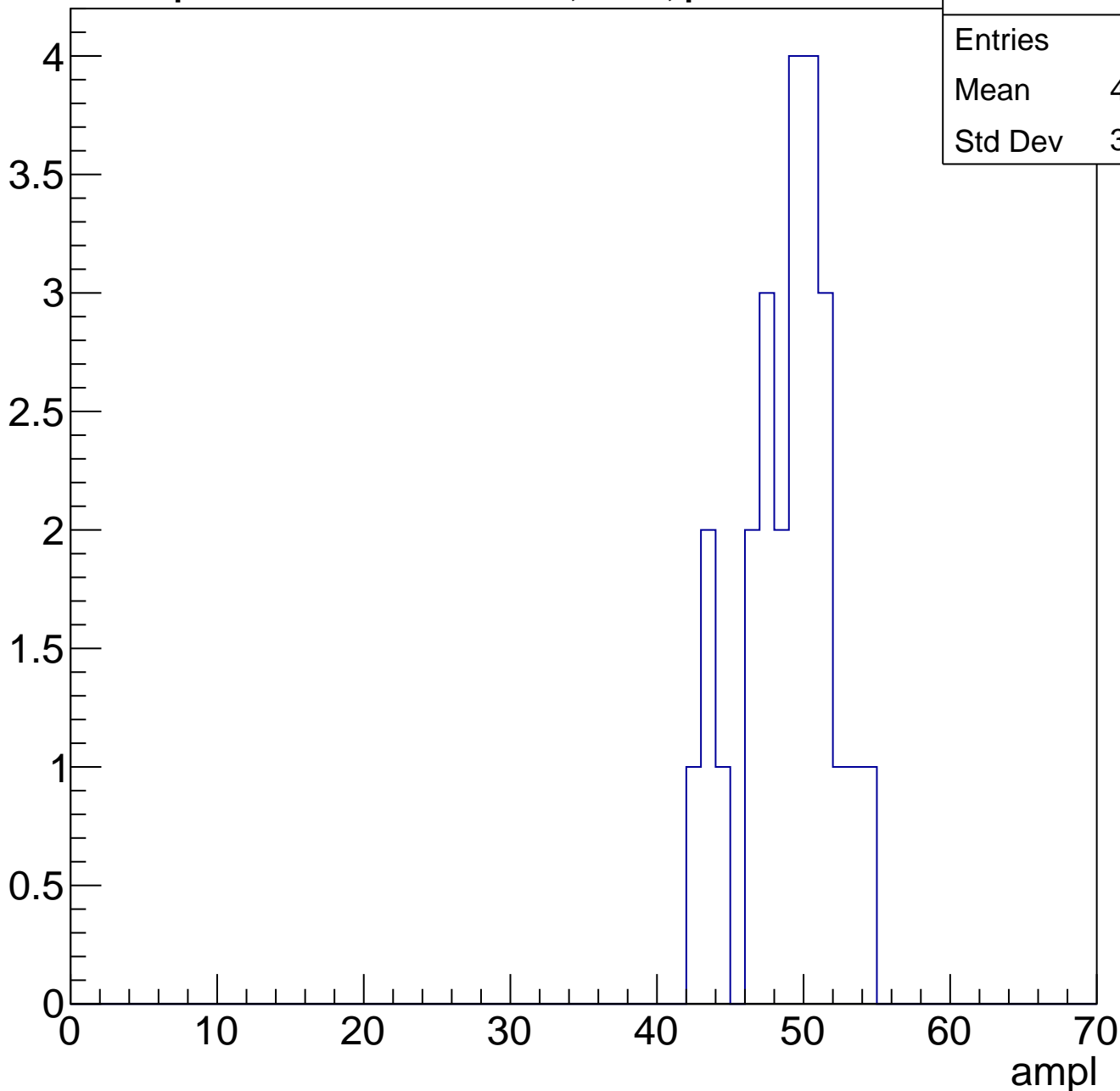
Gaus Width: 3.8405



B1L103S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

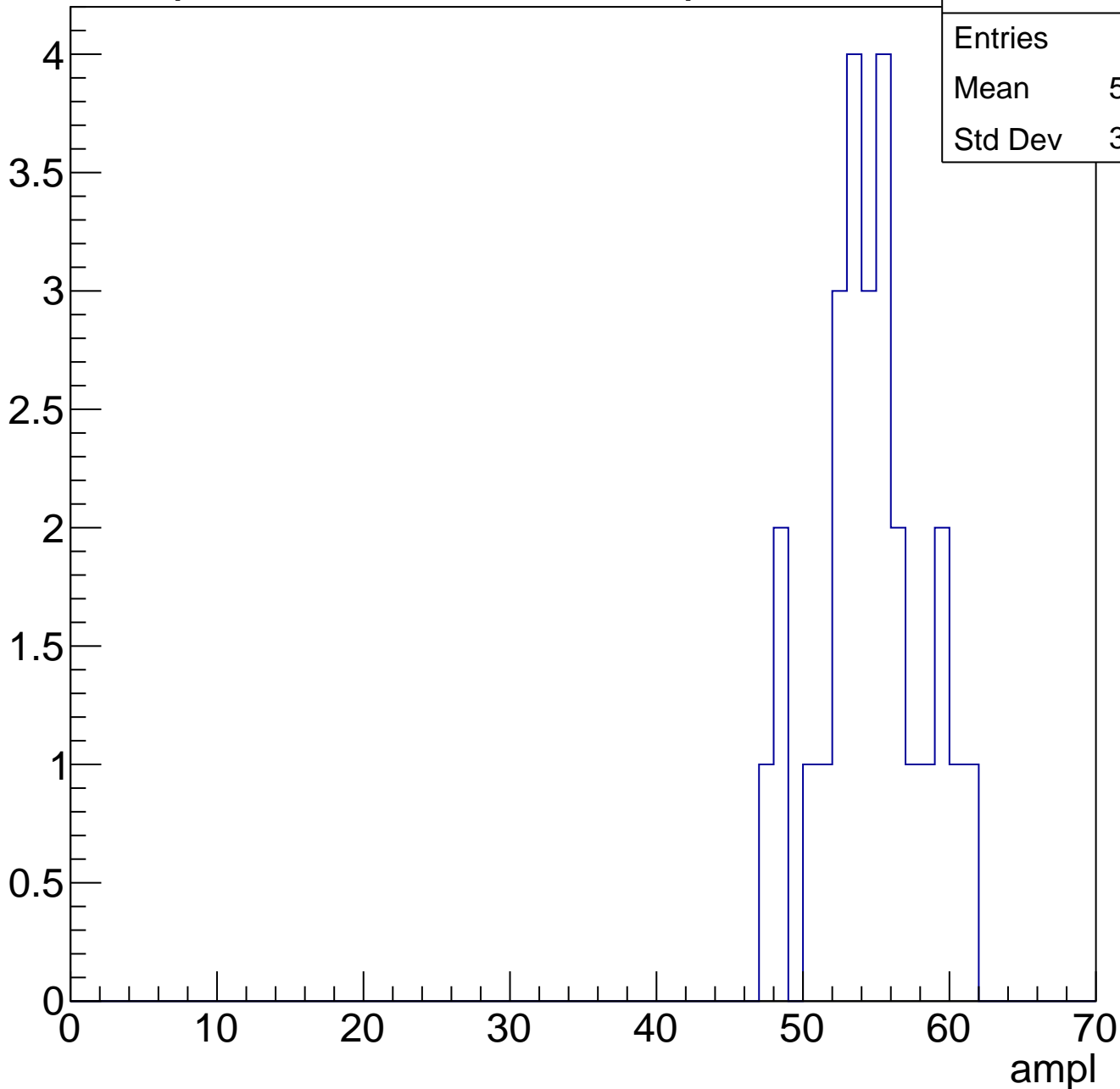


Entries	25
Mean	48.36
Std Dev	3.058

B1L103S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

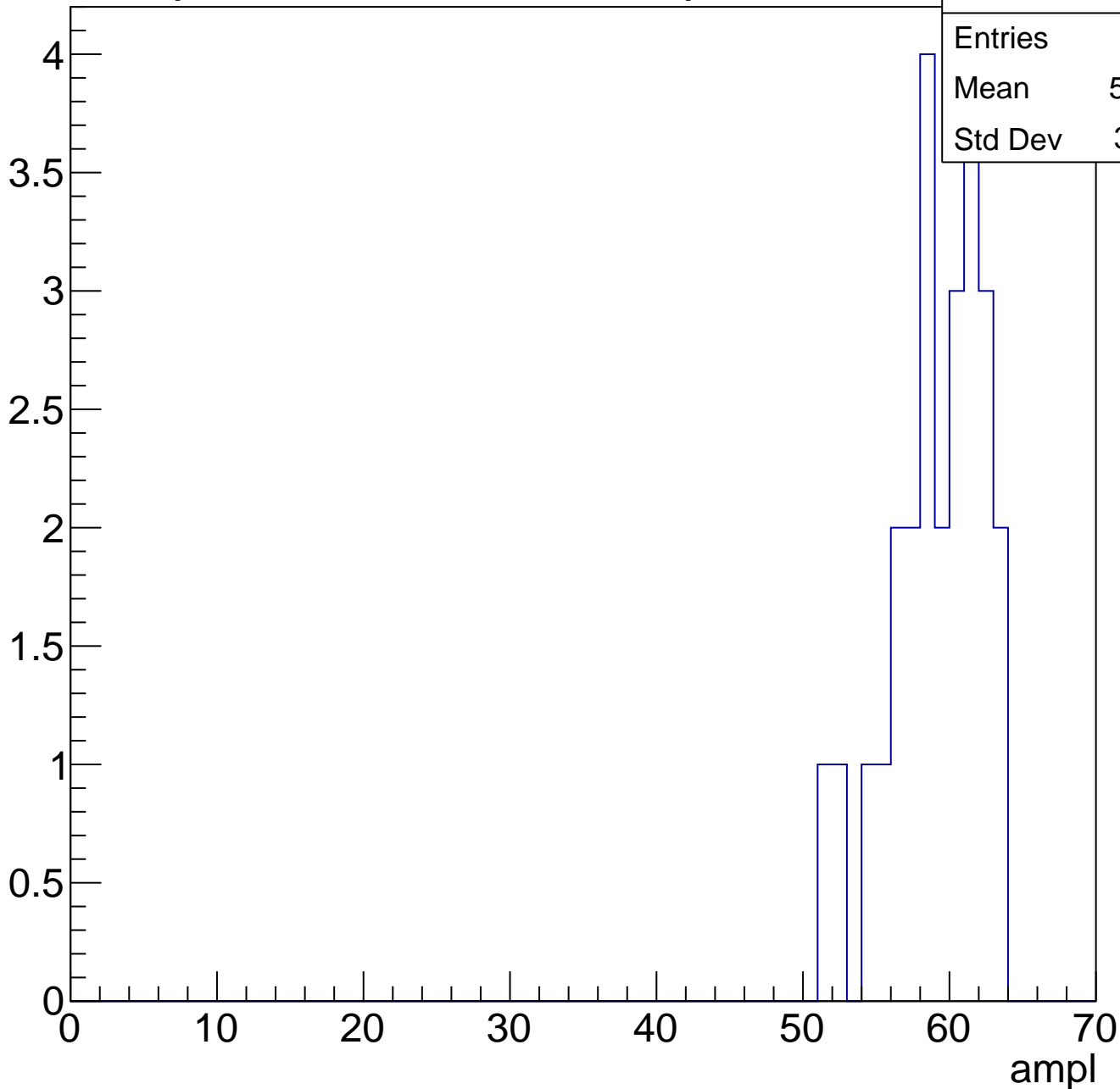
Entry



B1L103S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

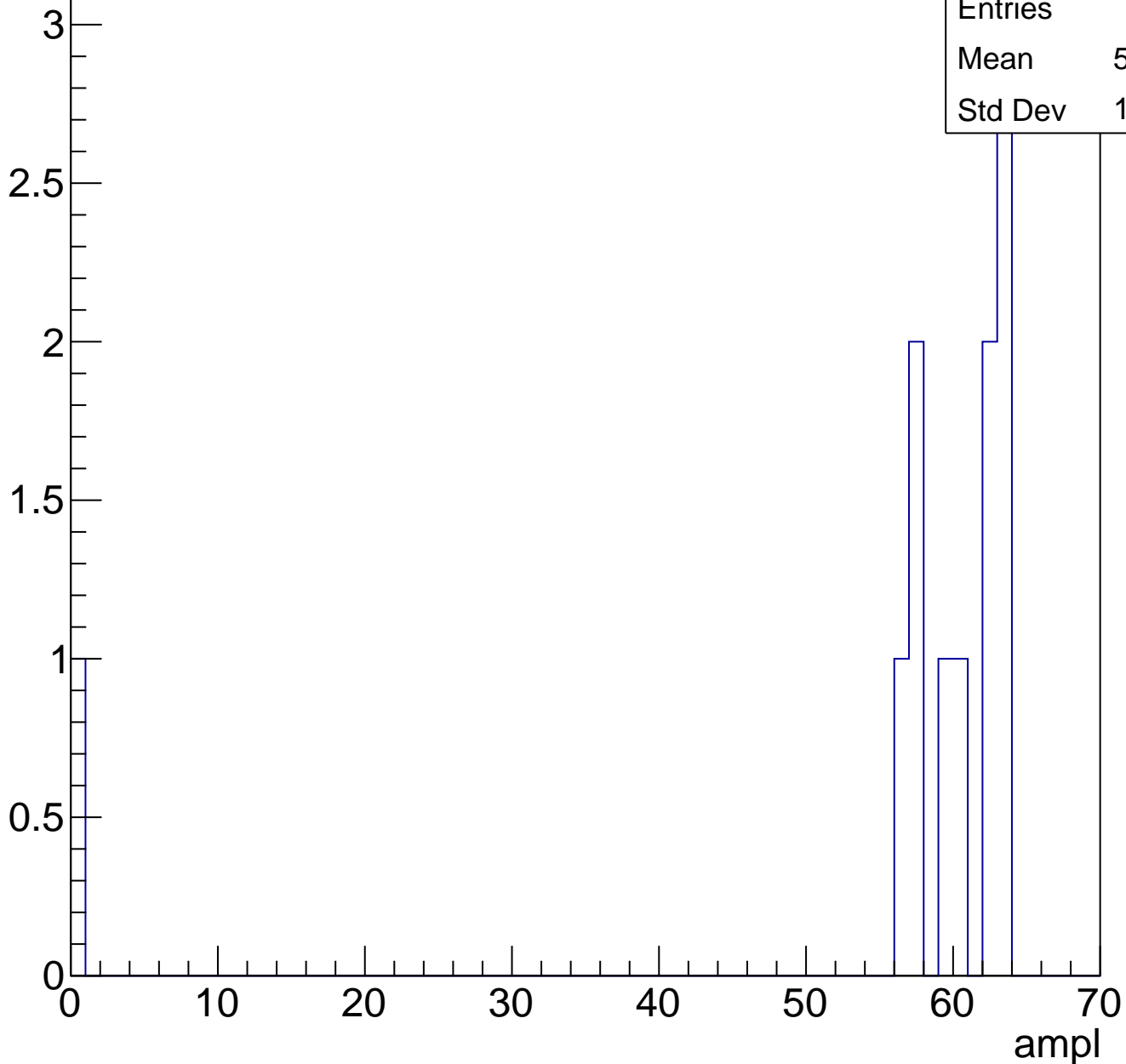
Entry



B1L103S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

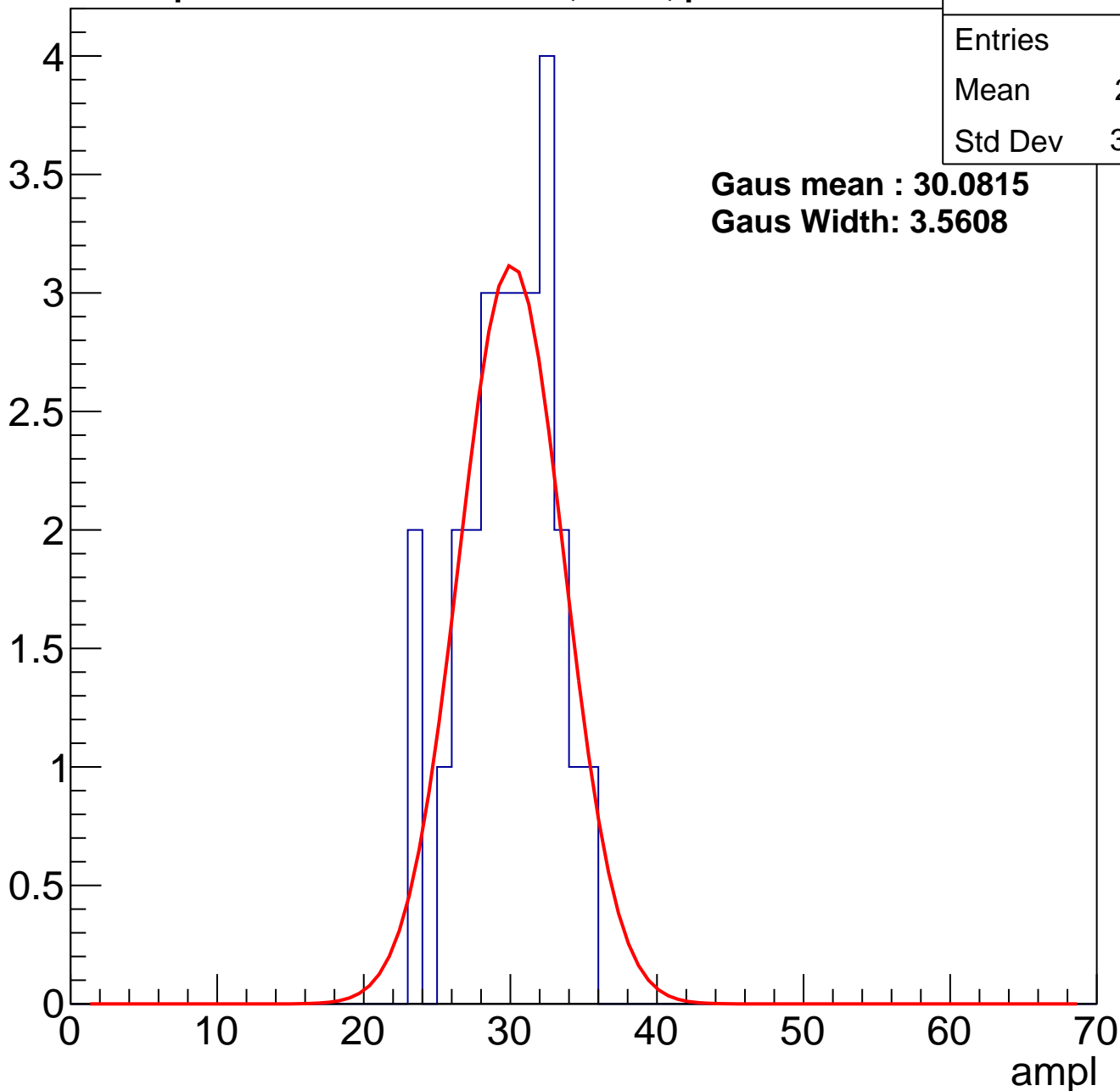


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch36, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch36, adc1

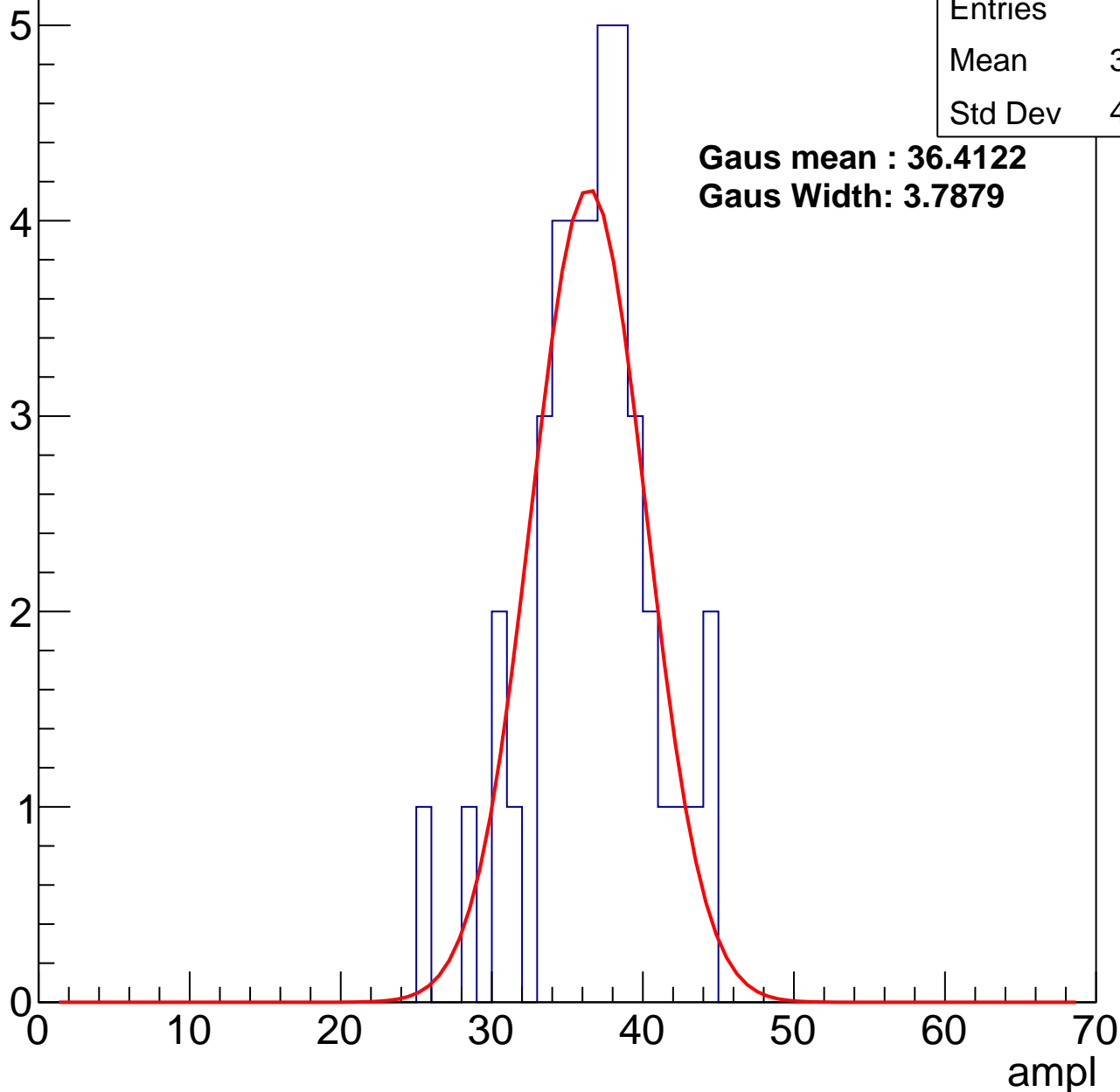
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	36.23
Std Dev	4.028

Gaus mean : 36.4122

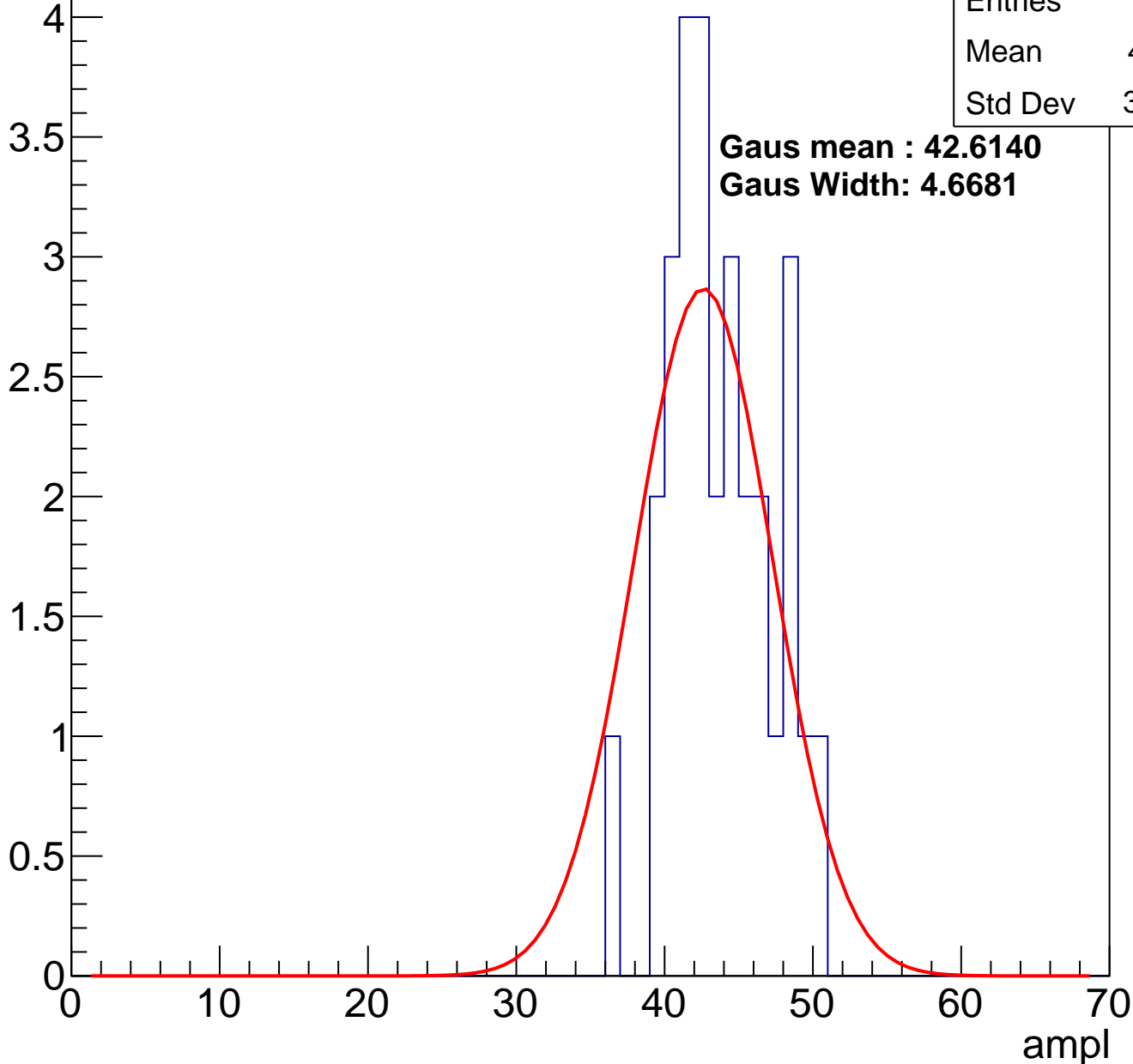
Gaus Width: 3.7879



B1L103S, U15-ch36, adc2

calib_packv5_042523_0143.root, FC#7, port C2

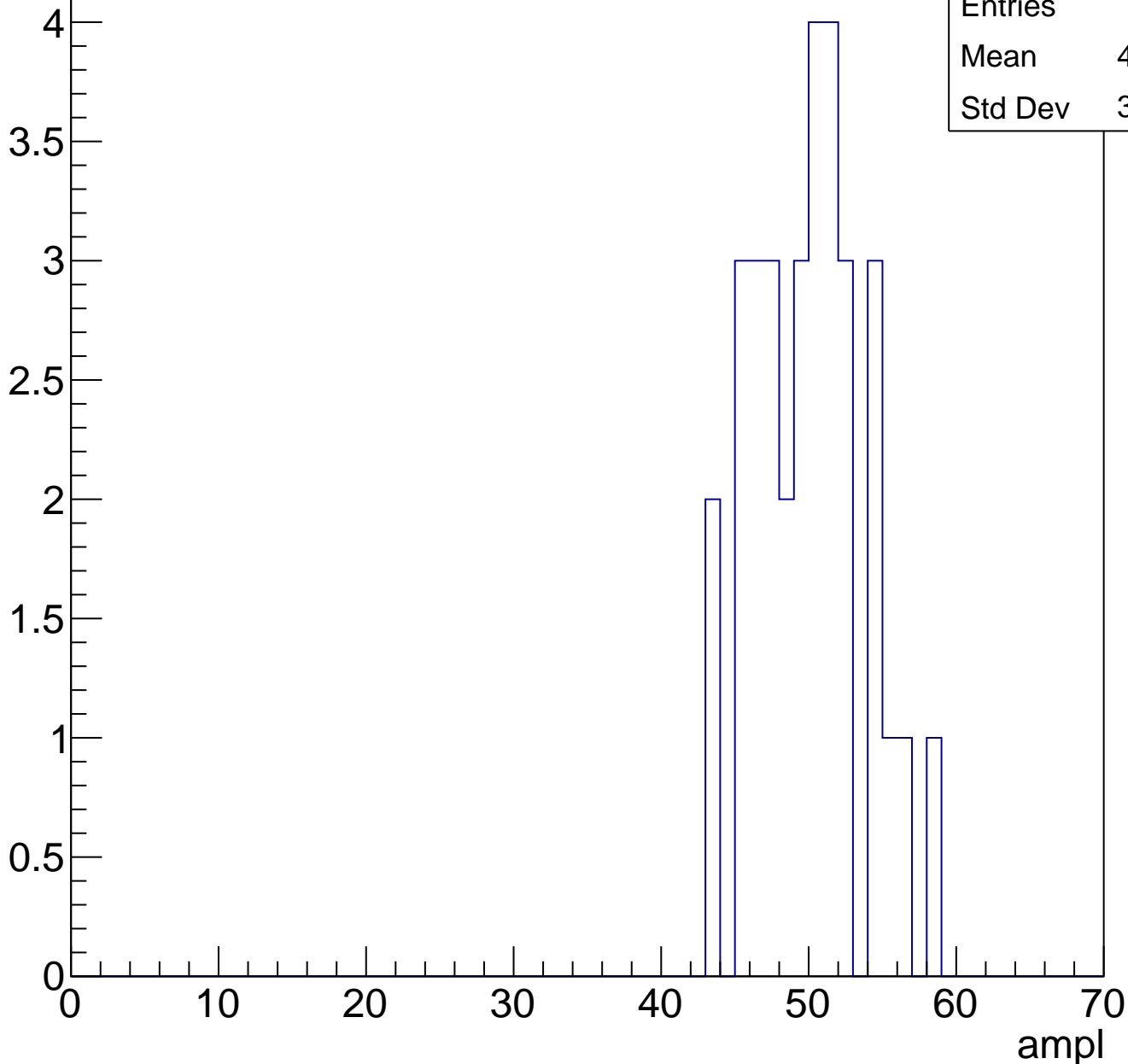
Entry



B1L103S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

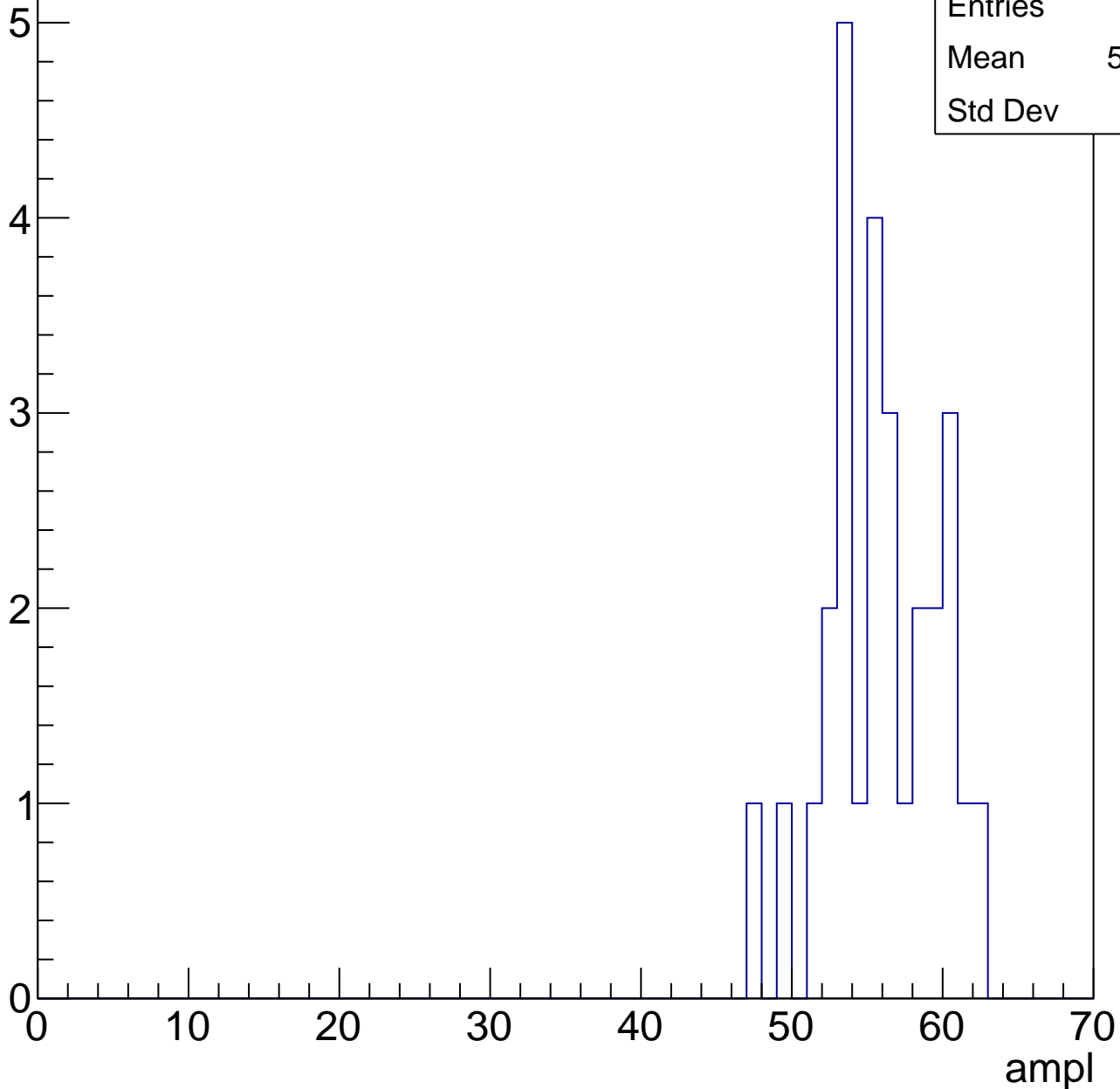


B1L103S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

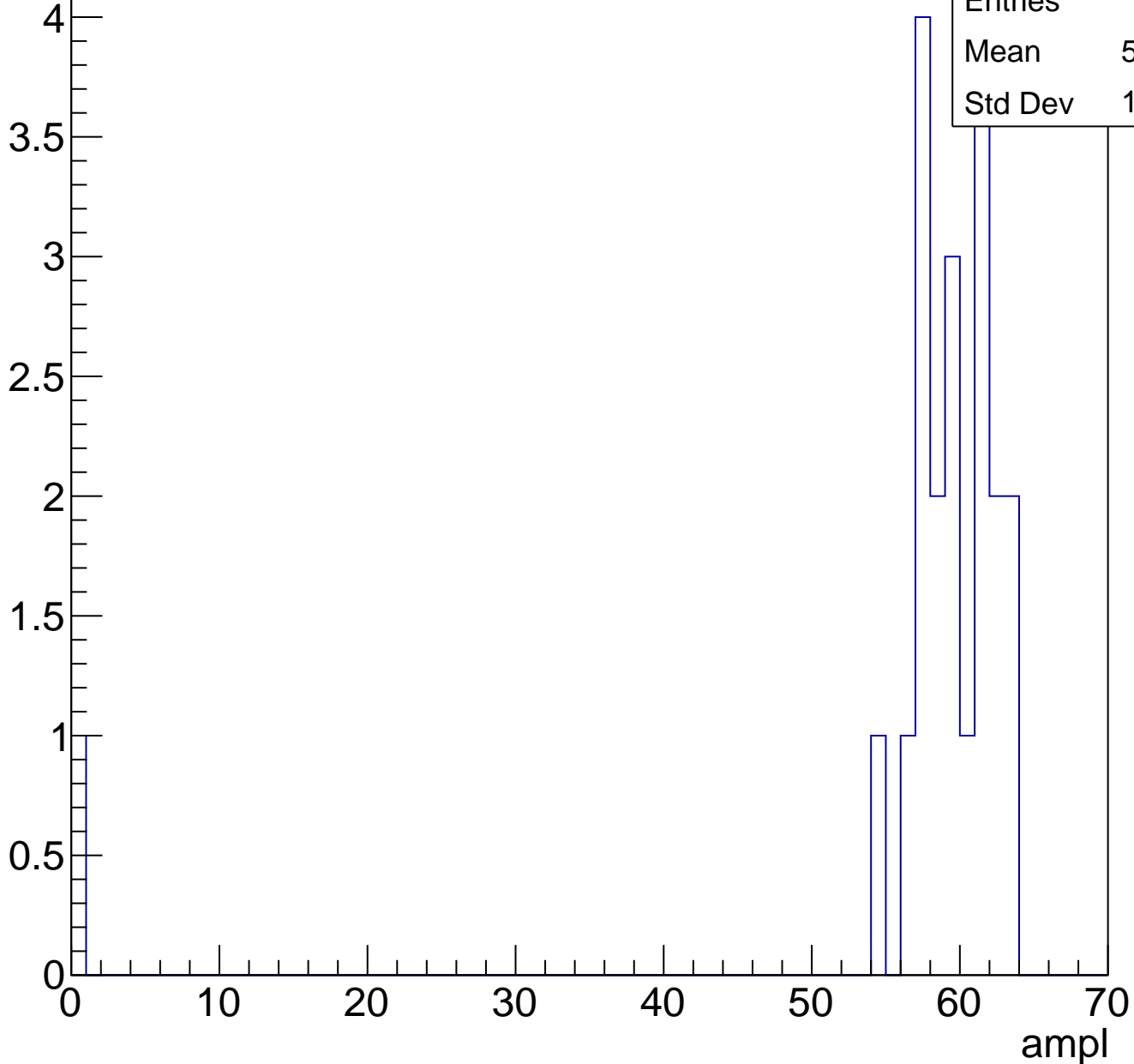
Entries	28
Mean	55.43
Std Dev	3.62



B1L103S, U15-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

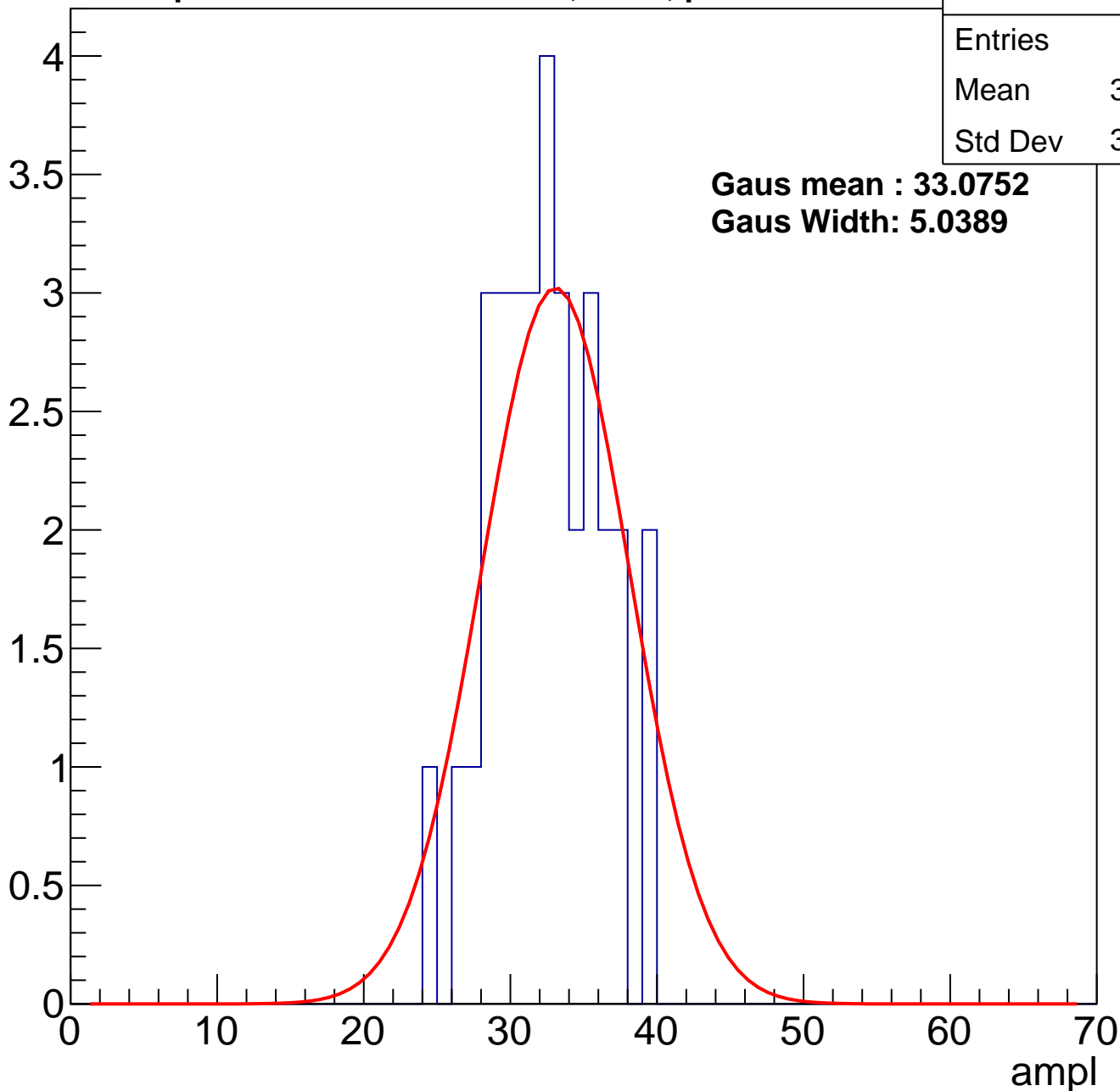
Entry



B1L103S, U15-ch37, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch37, adc1

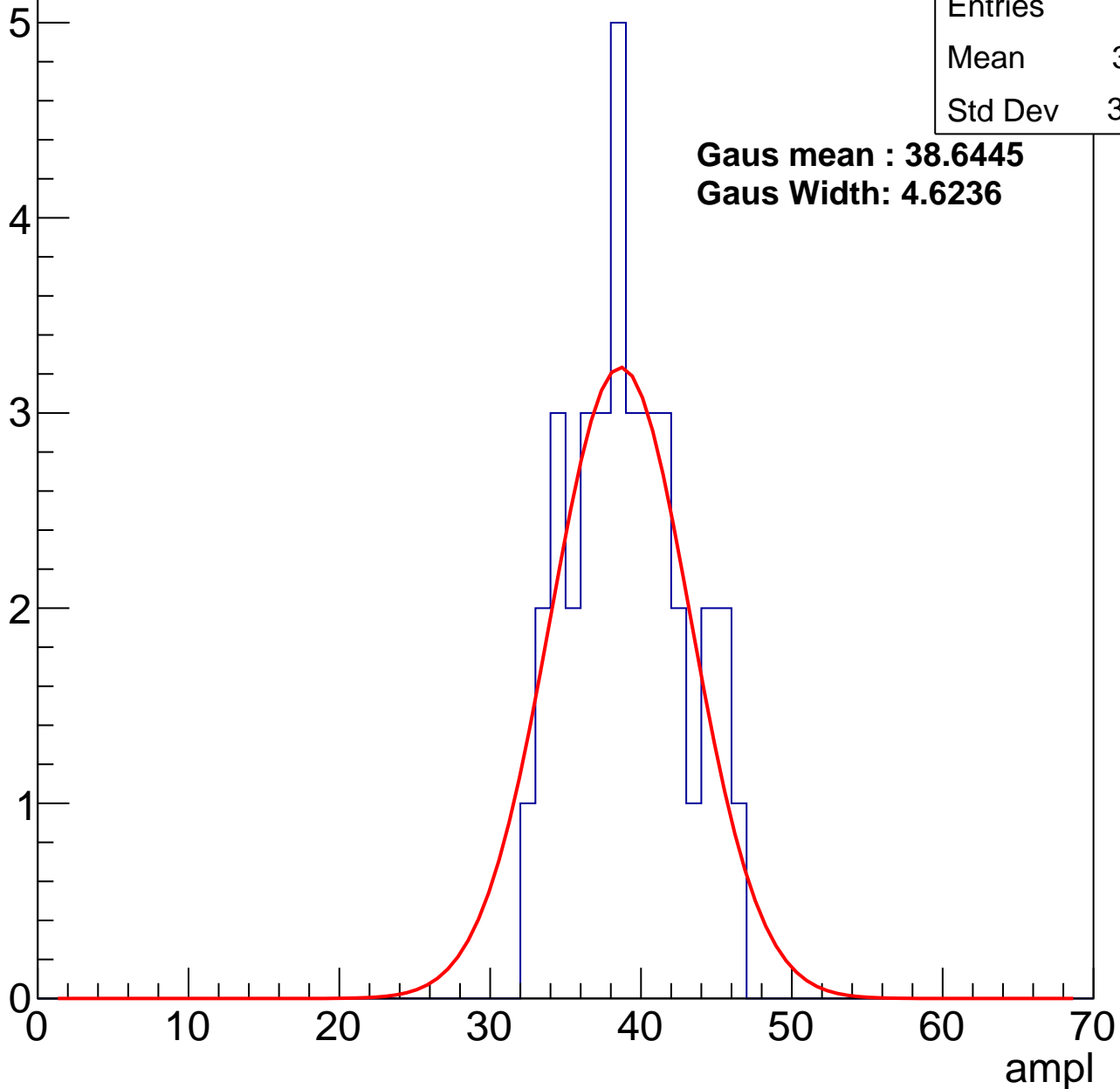
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	38.61
Std Dev	3.676

Gaus mean : 38.6445

Gaus Width: 4.6236



B1L103S, U15-ch37, adc2

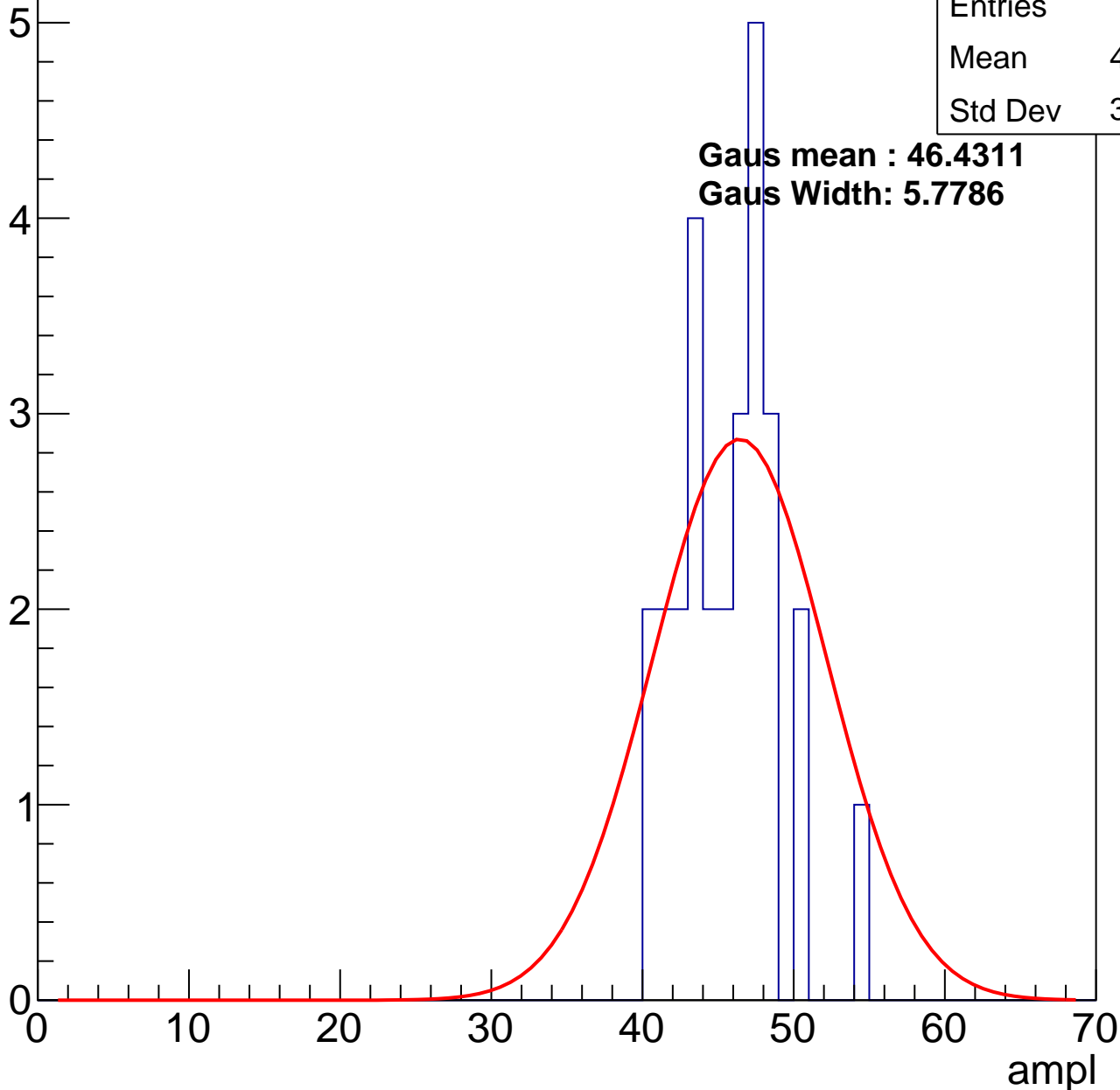
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	45.25
Std Dev	3.258

Gaus mean : 46.4311

Gaus Width: 5.7786

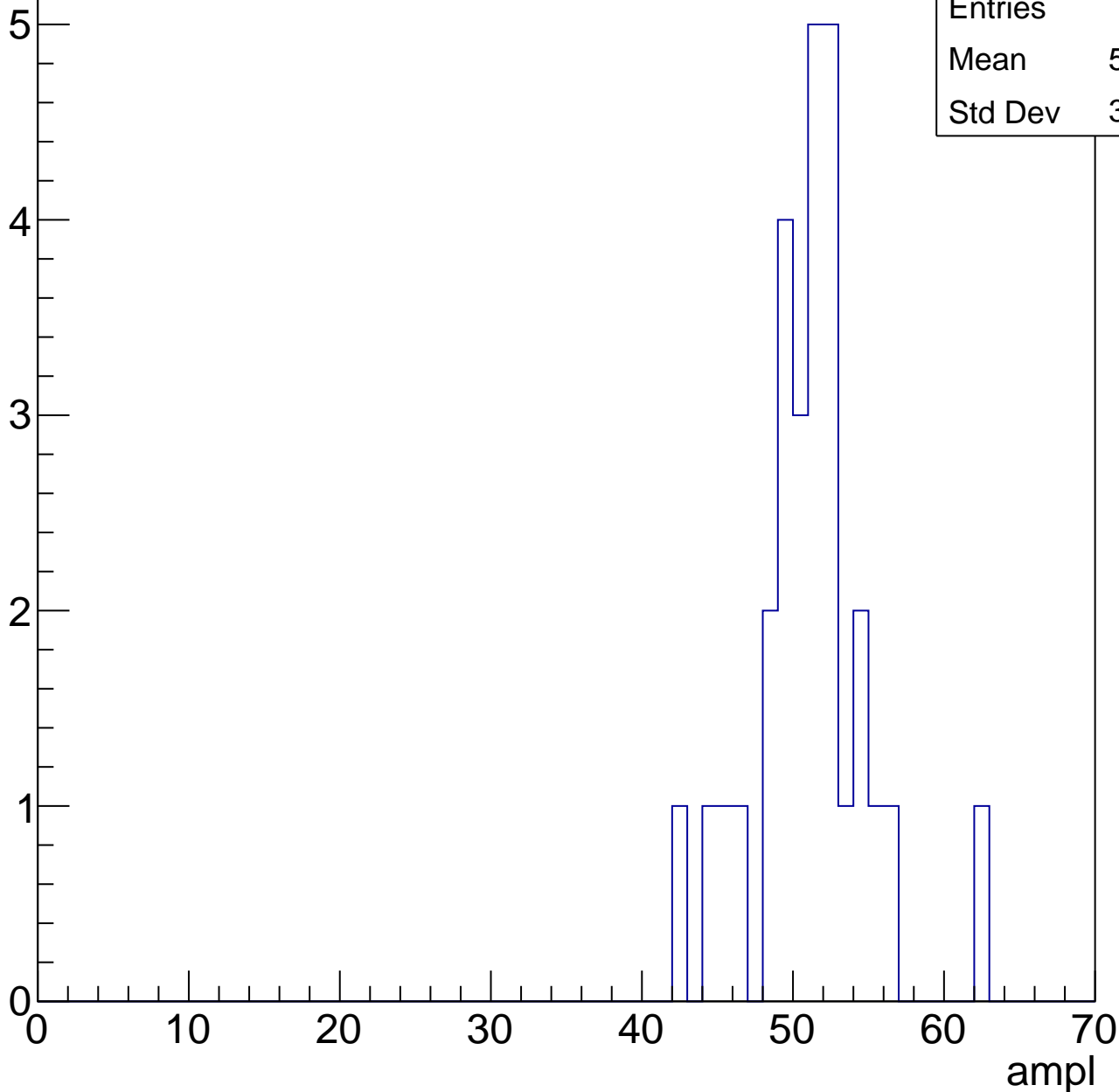


B1L103S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	50.62
Std Dev	3.773

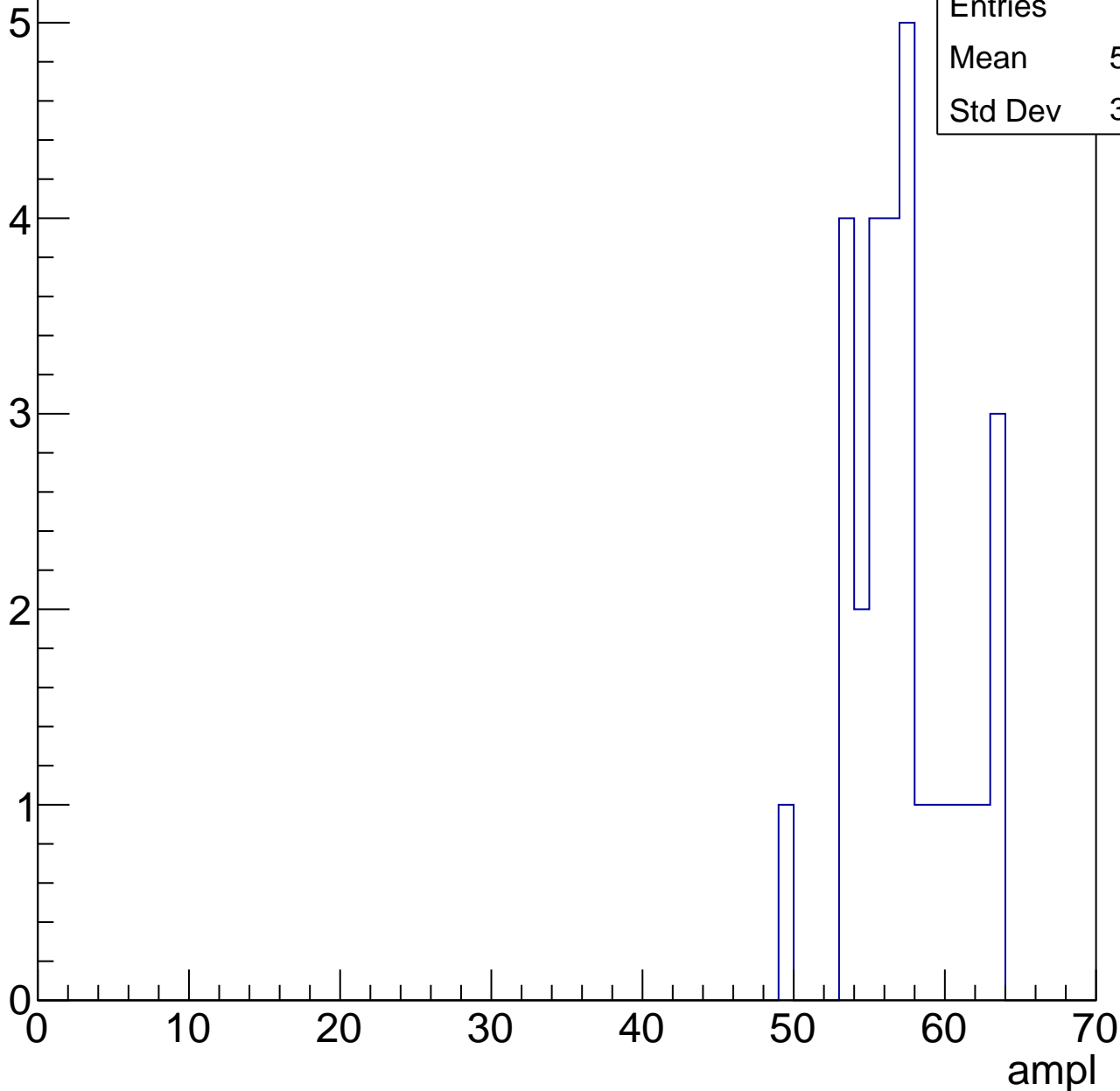


B1L103S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

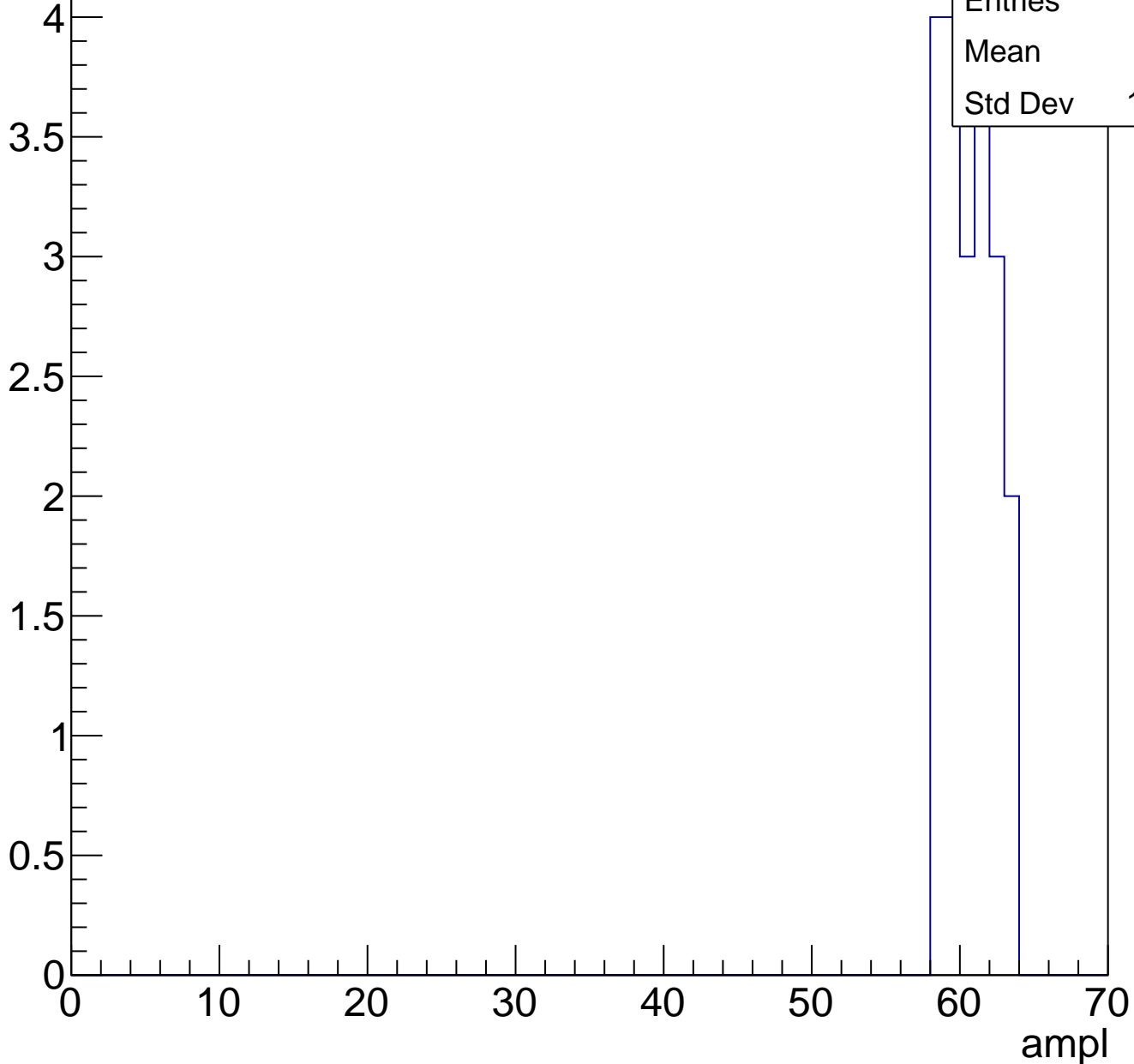
Entries	28
Mean	56.68
Std Dev	3.413



B1L103S, U15-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

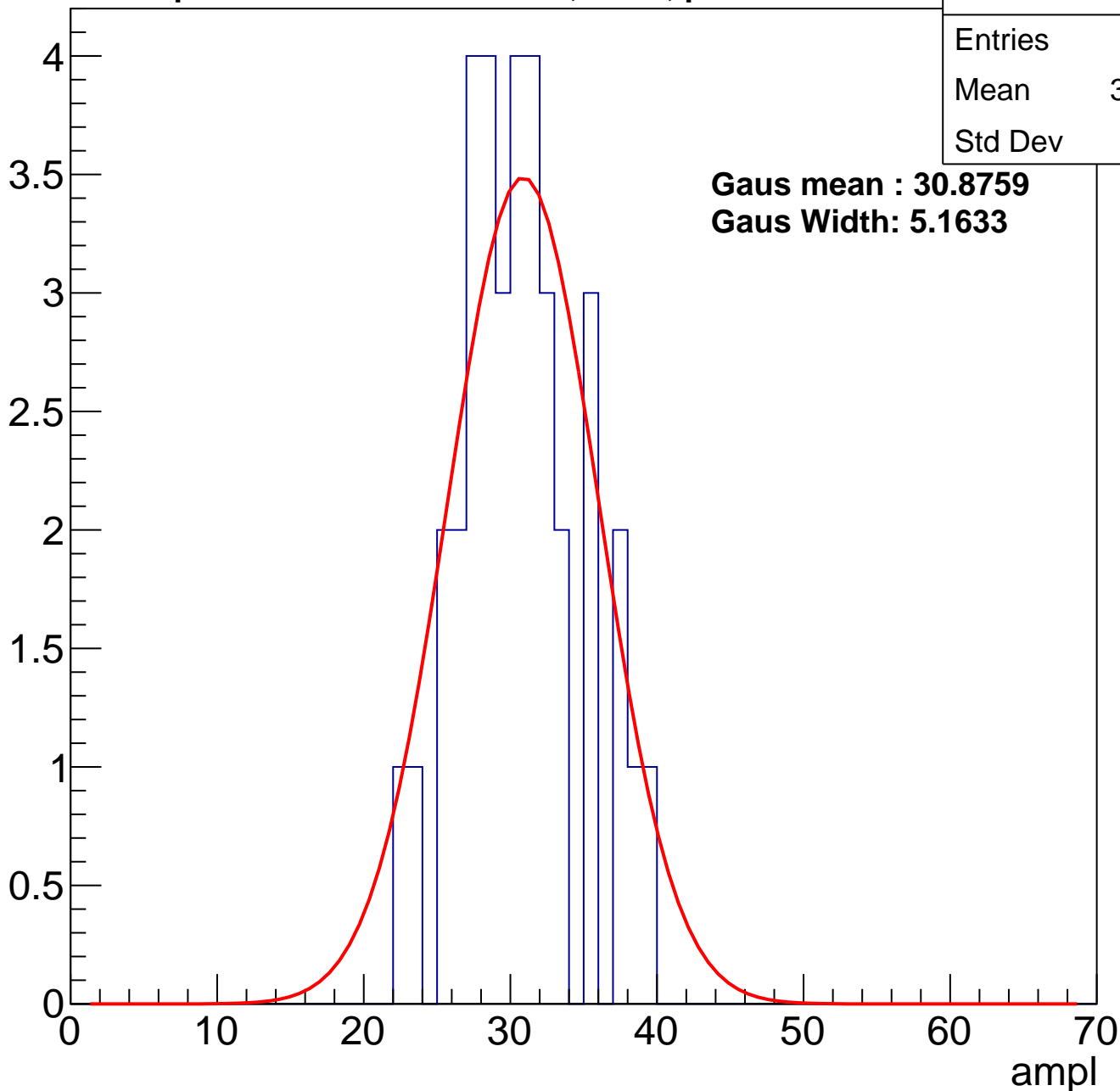


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch38, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch38, adc1

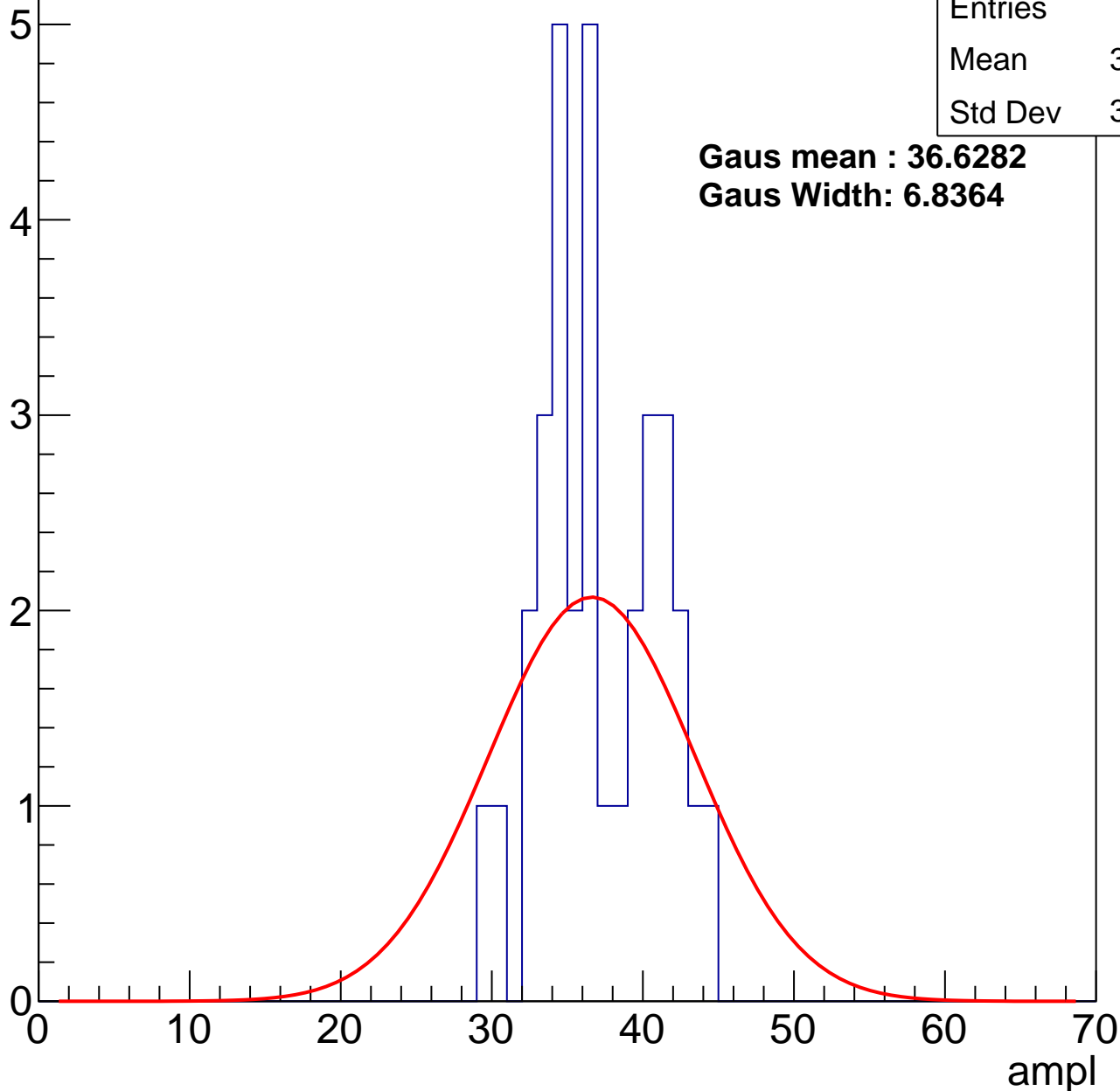
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	36.64
Std Dev	3.828

Gaus mean : 36.6282

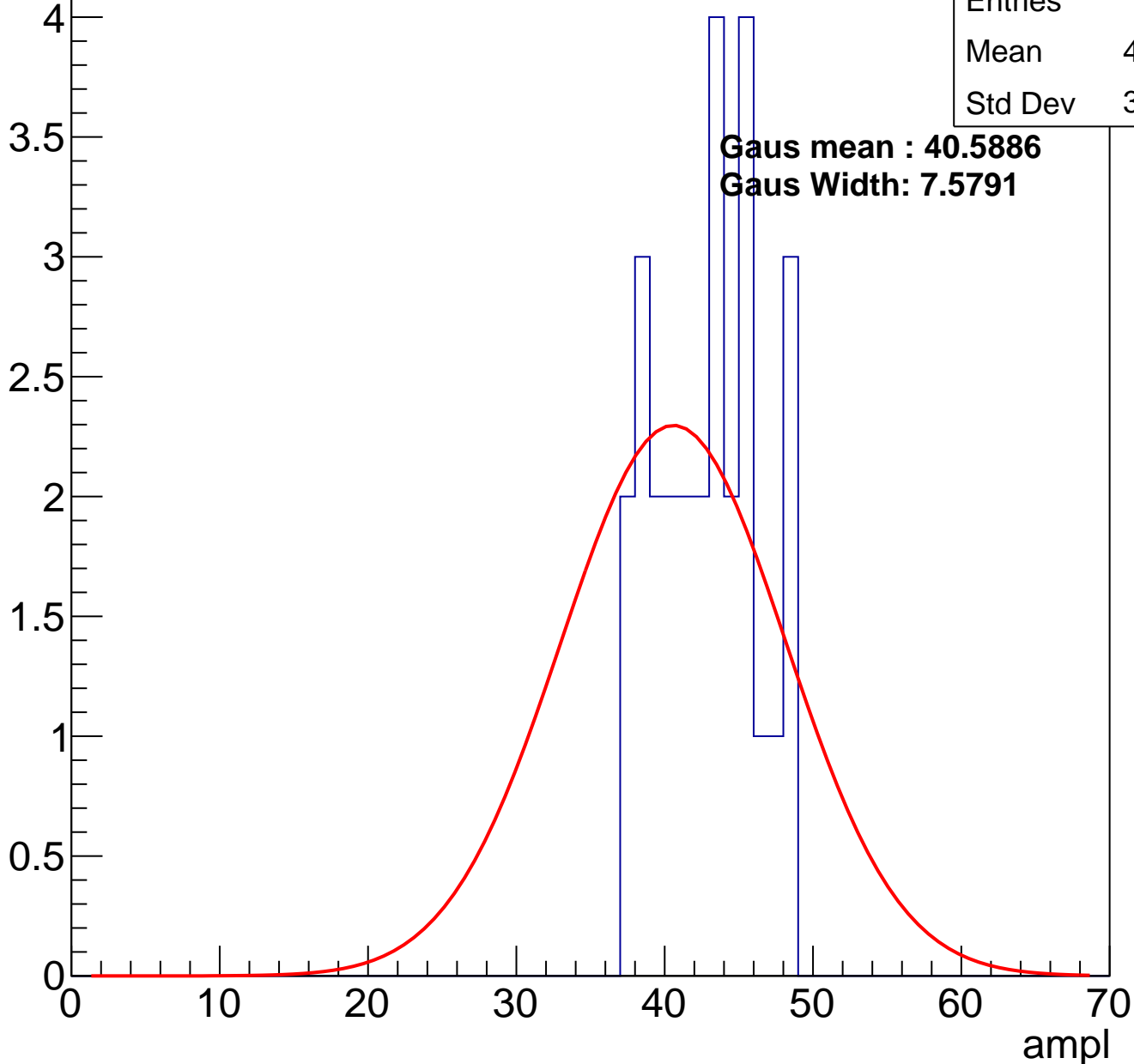
Gaus Width: 6.8364



B1L103S, U15-ch38, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

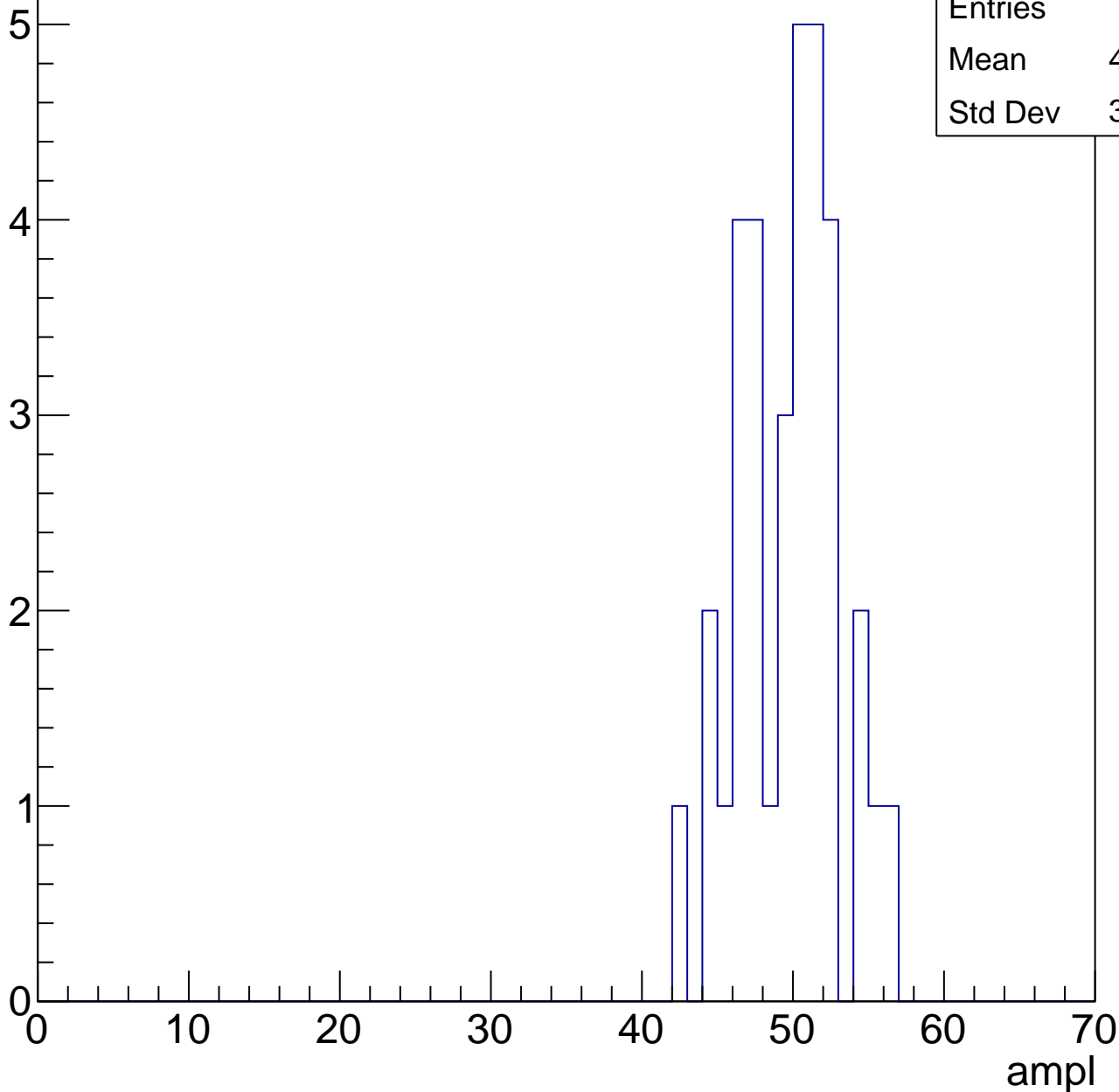


B1L103S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	49.24
Std Dev	3.264

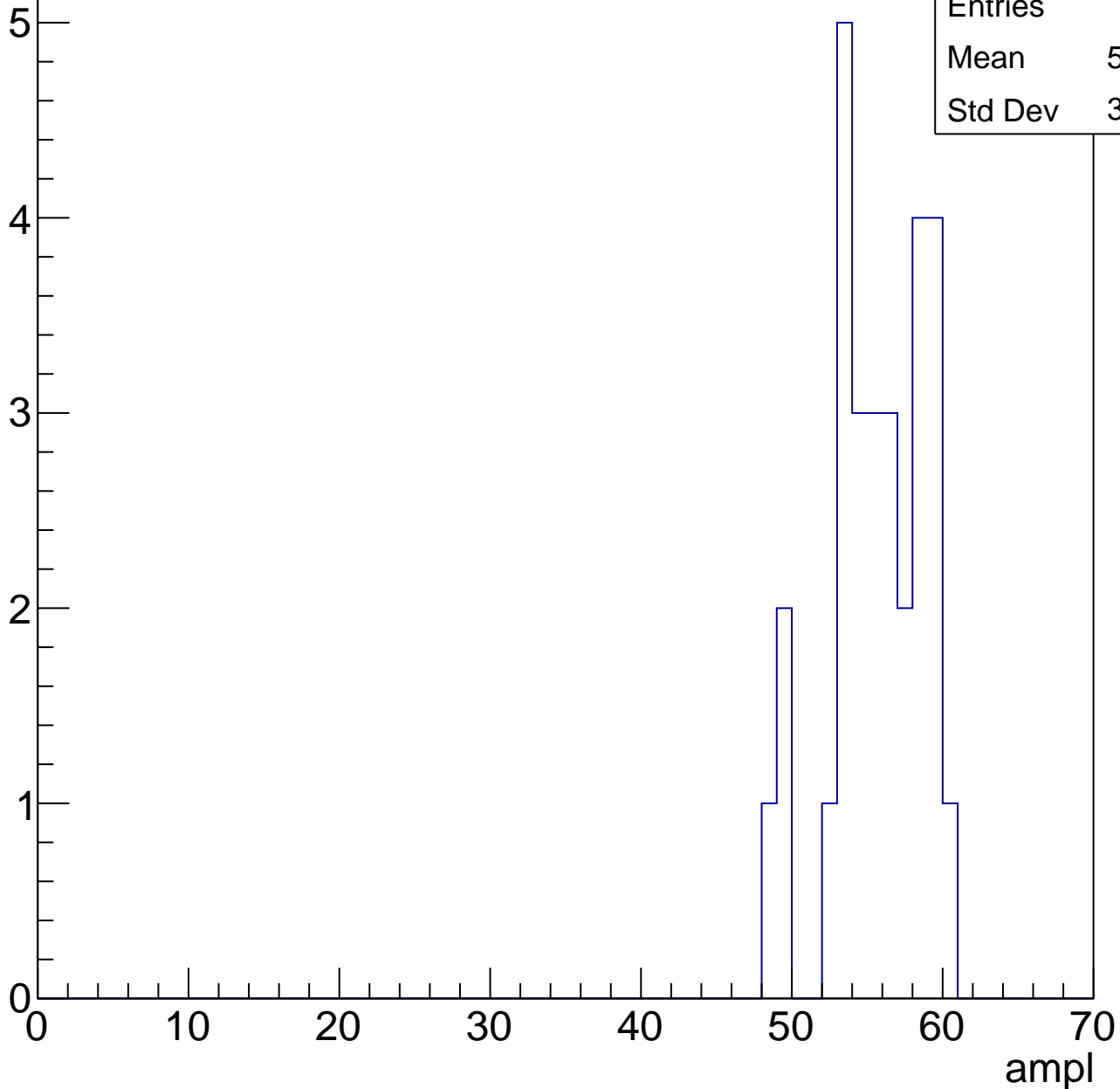


B1L103S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	55.17
Std Dev	3.152

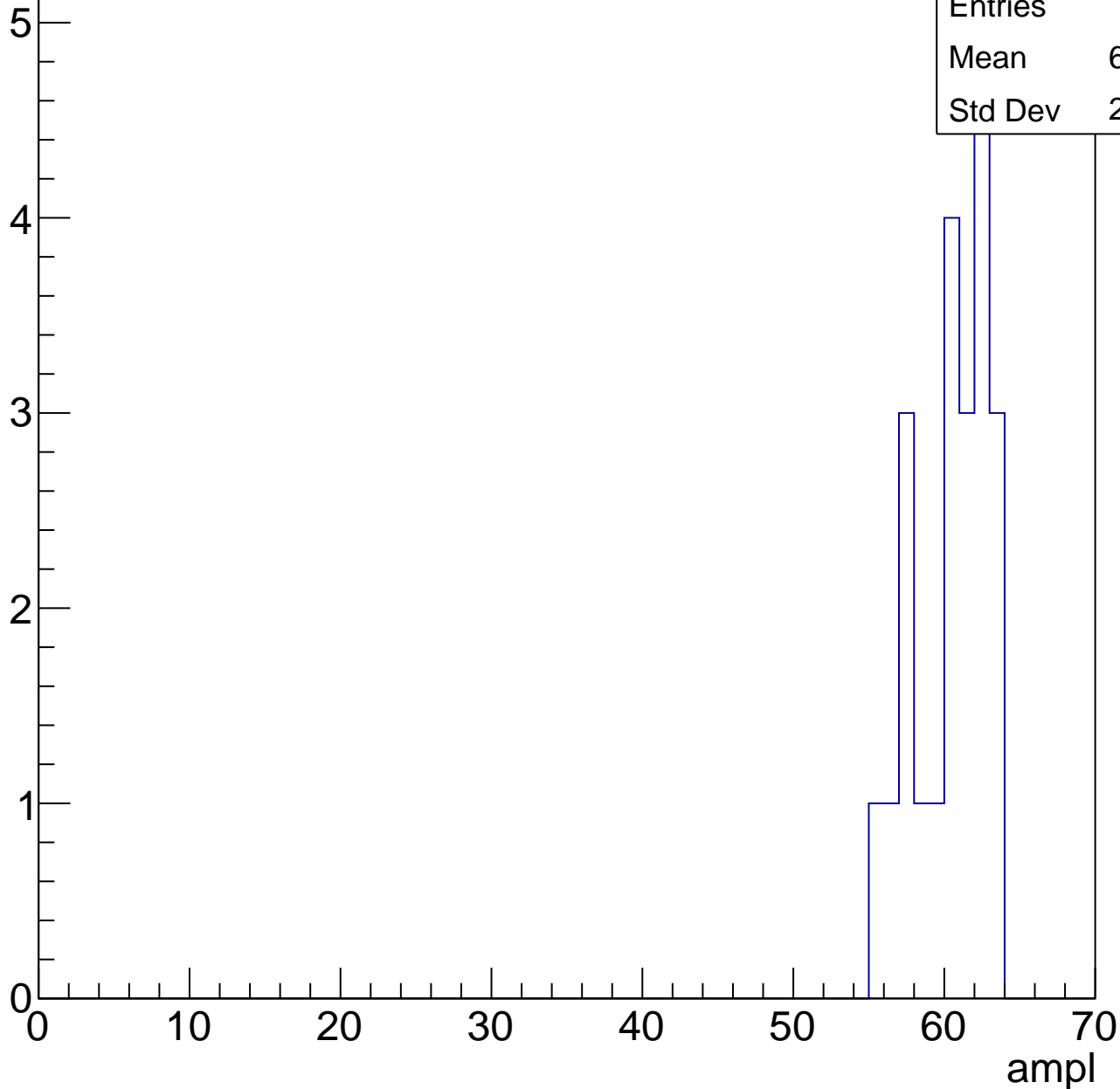


B1L103S, U15-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

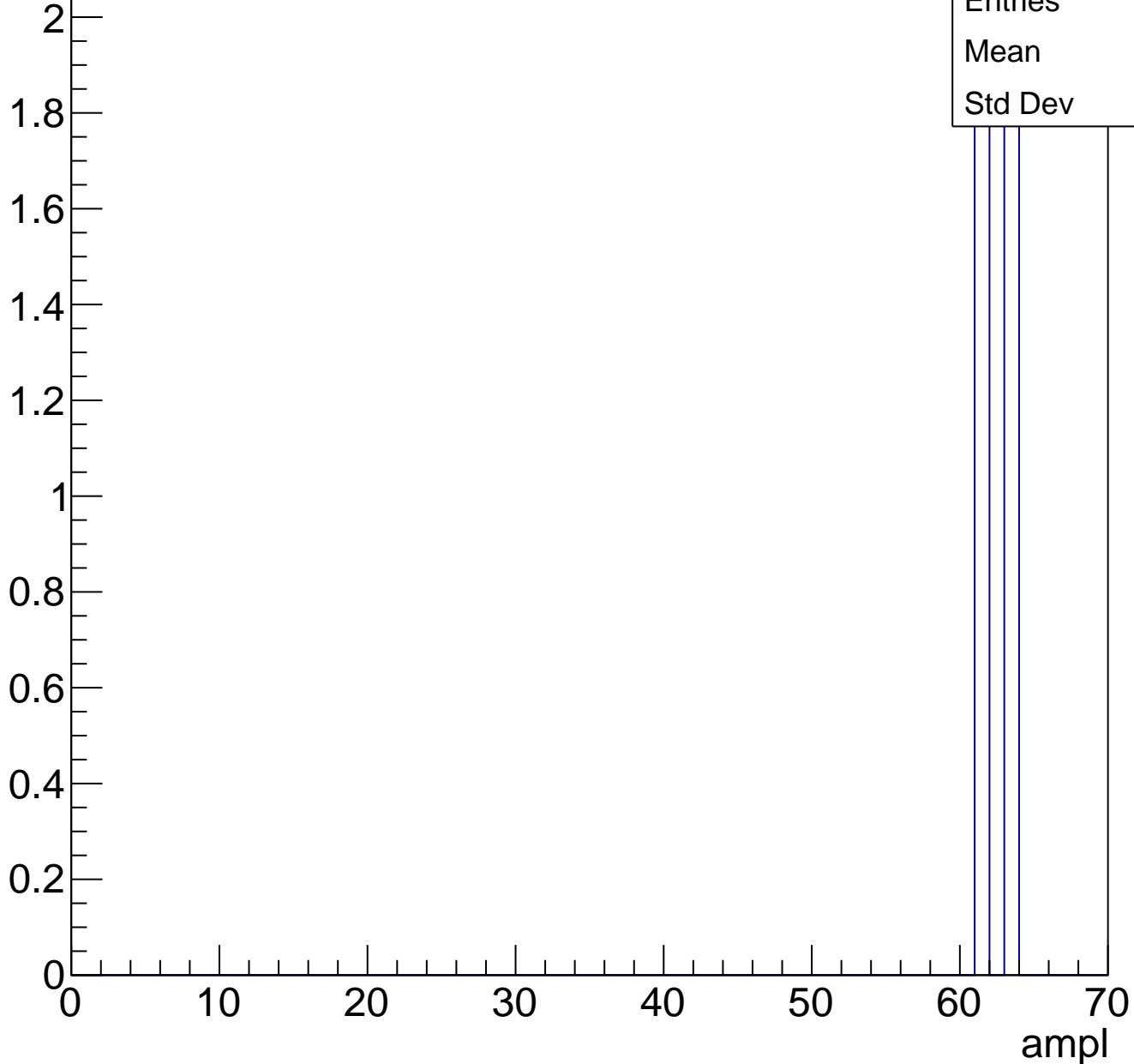
Entries	22
Mean	60.05
Std Dev	2.364



B1L103S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch39, adc0

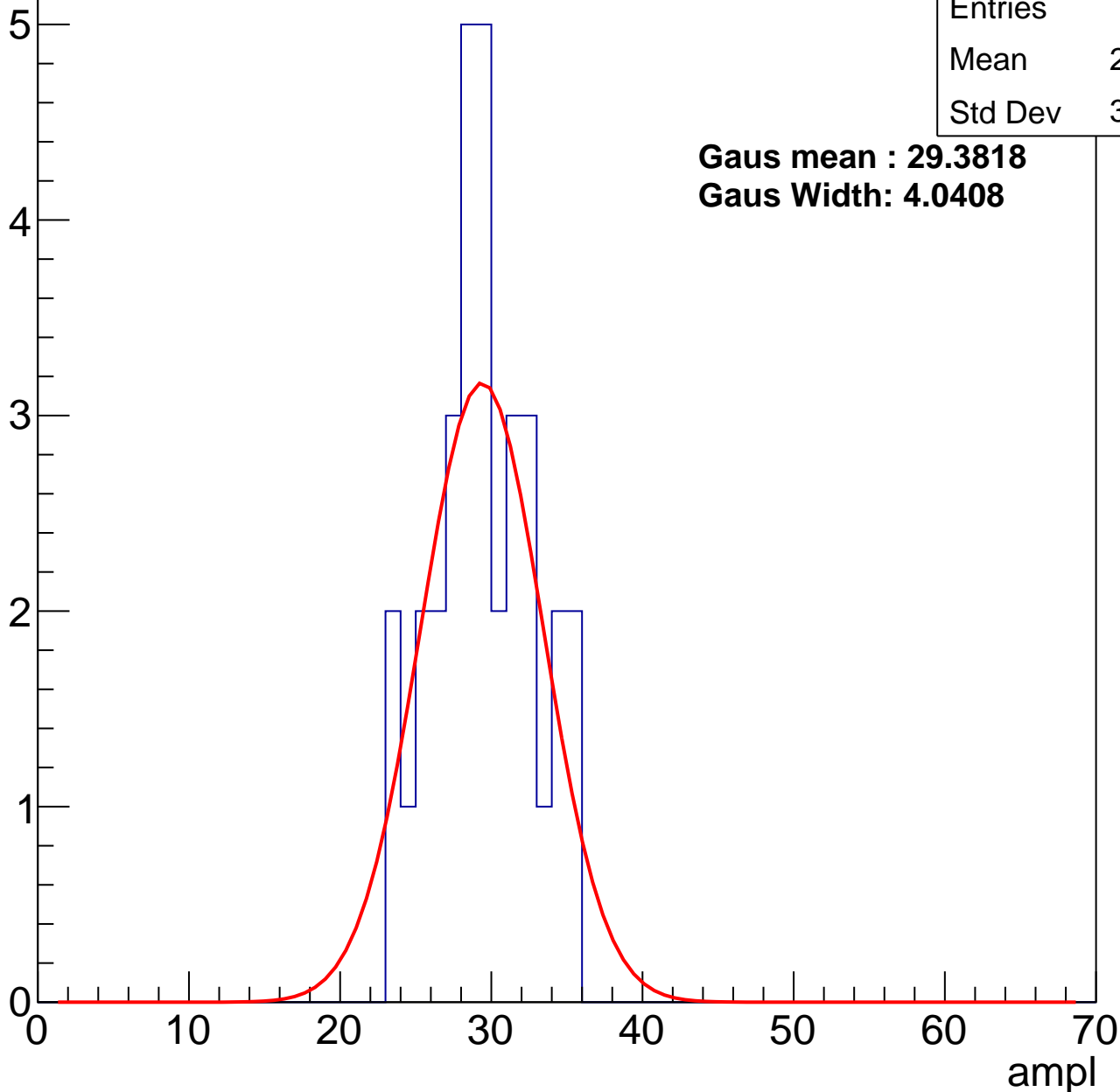
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	29.03
Std Dev	3.224

Gaus mean : 29.3818

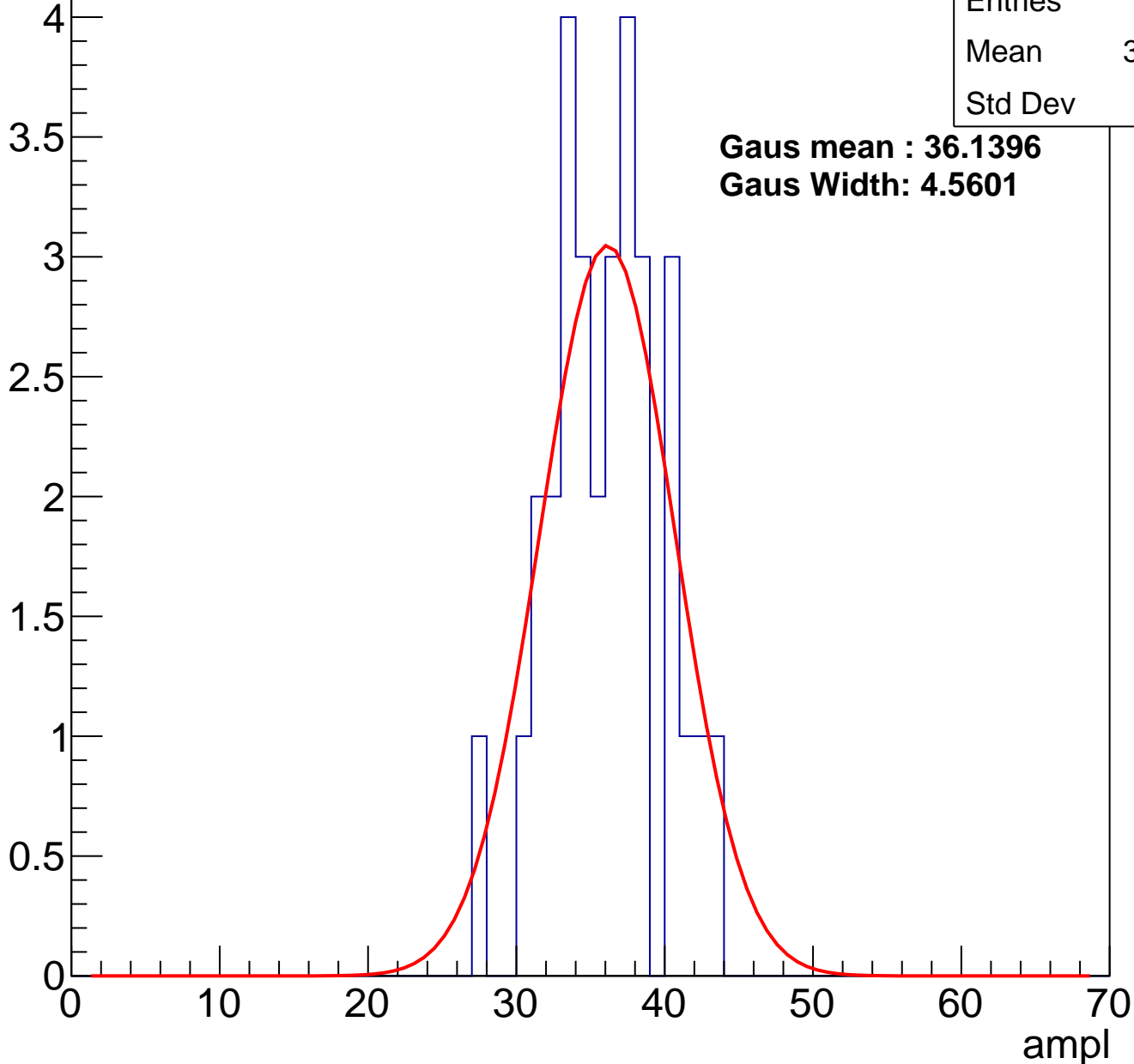
Gaus Width: 4.0408



B1L103S, U15-ch39, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch39, adc2

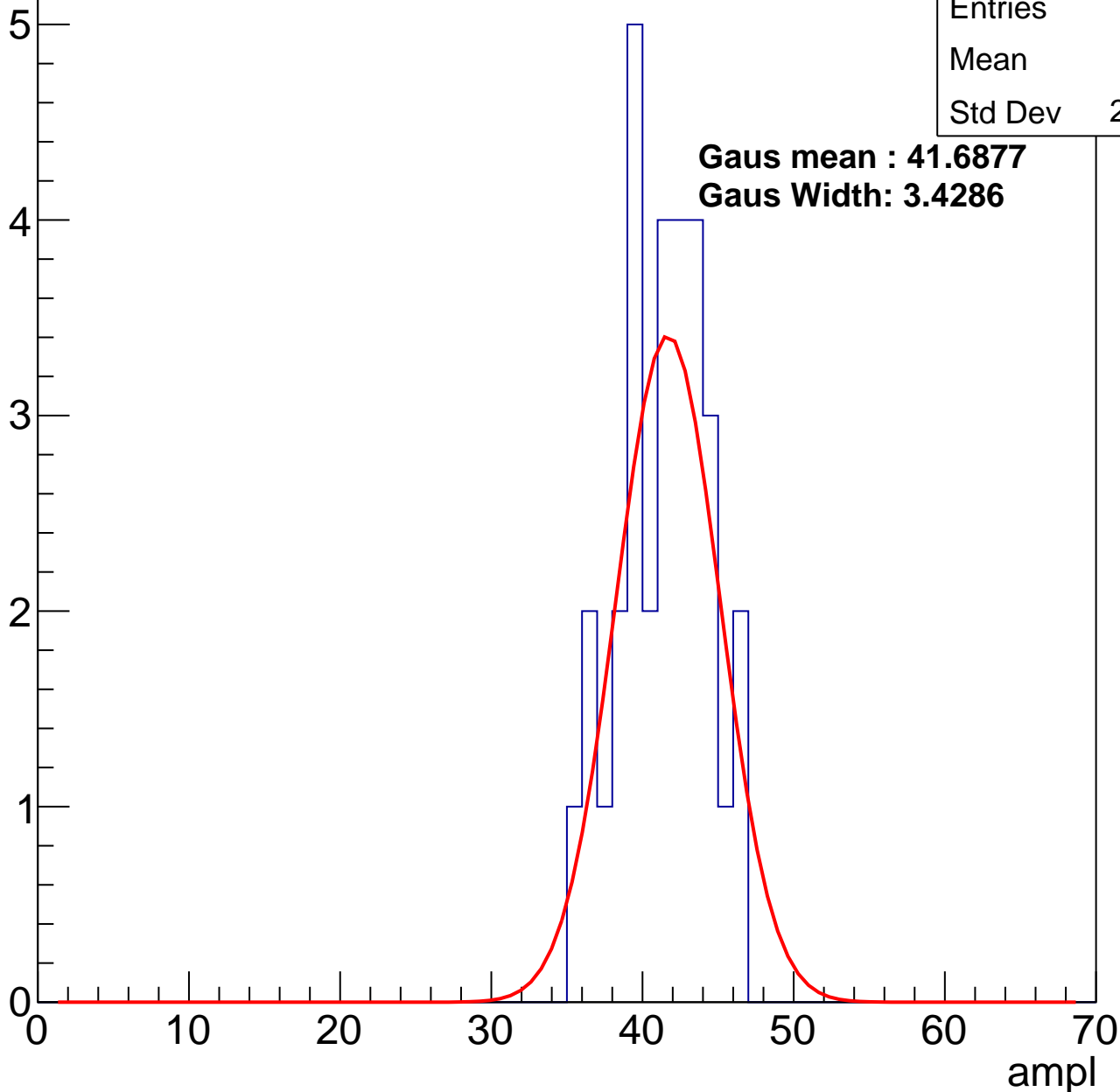
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	40.9
Std Dev	2.866

Gaus mean : 41.6877

Gaus Width: 3.4286

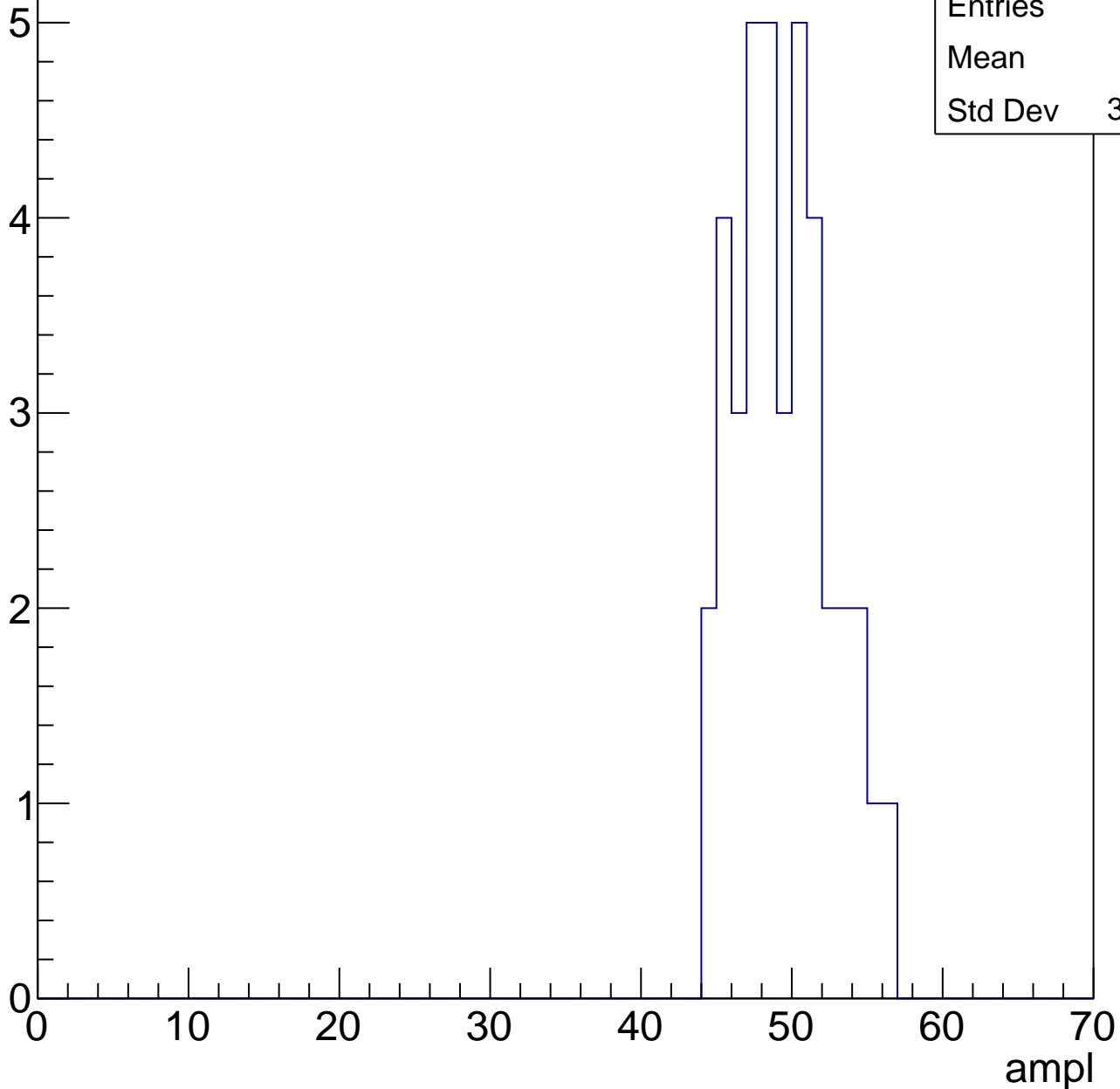


B1L103S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

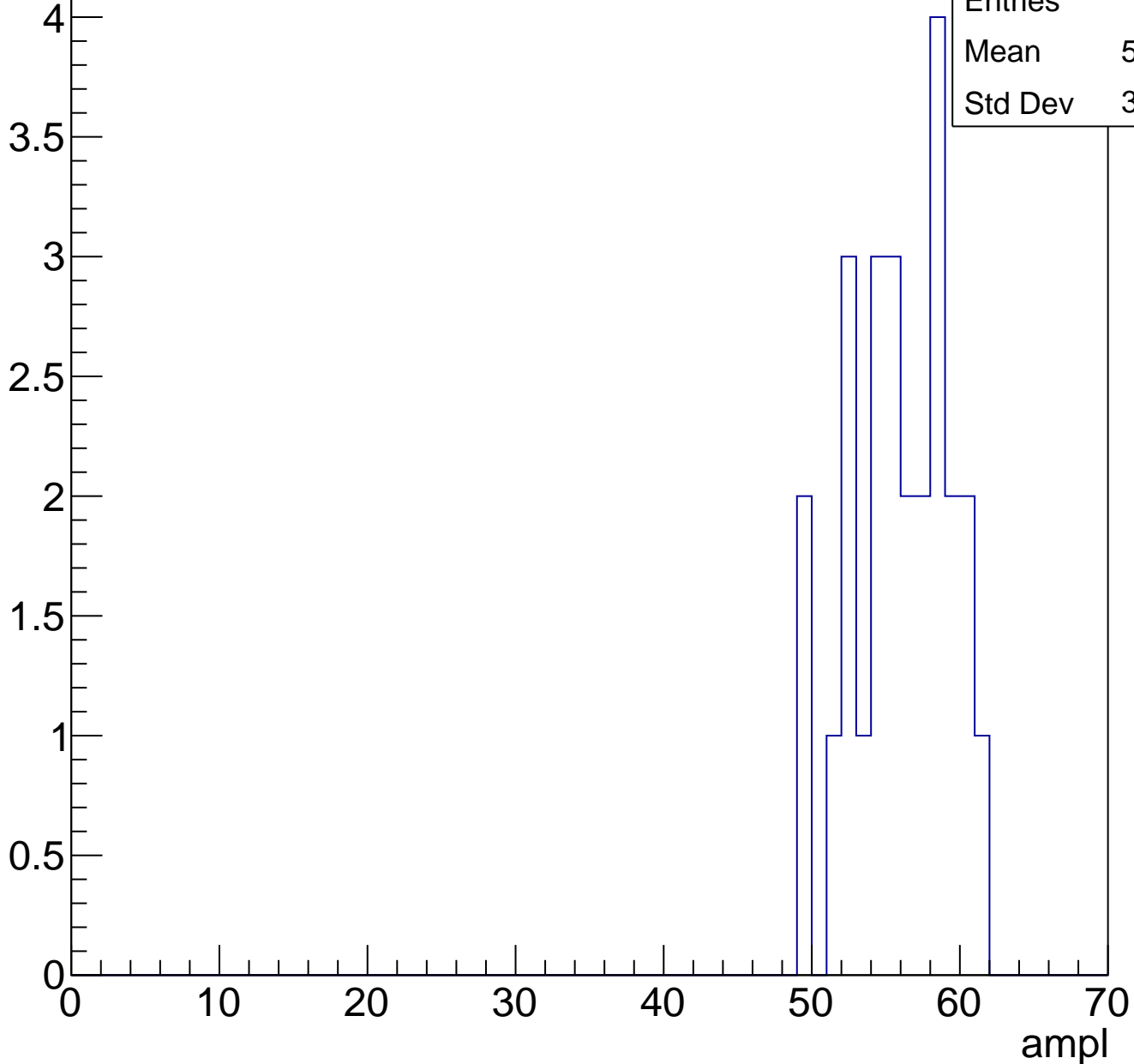
Entries	39
Mean	49
Std Dev	3.088



B1L103S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

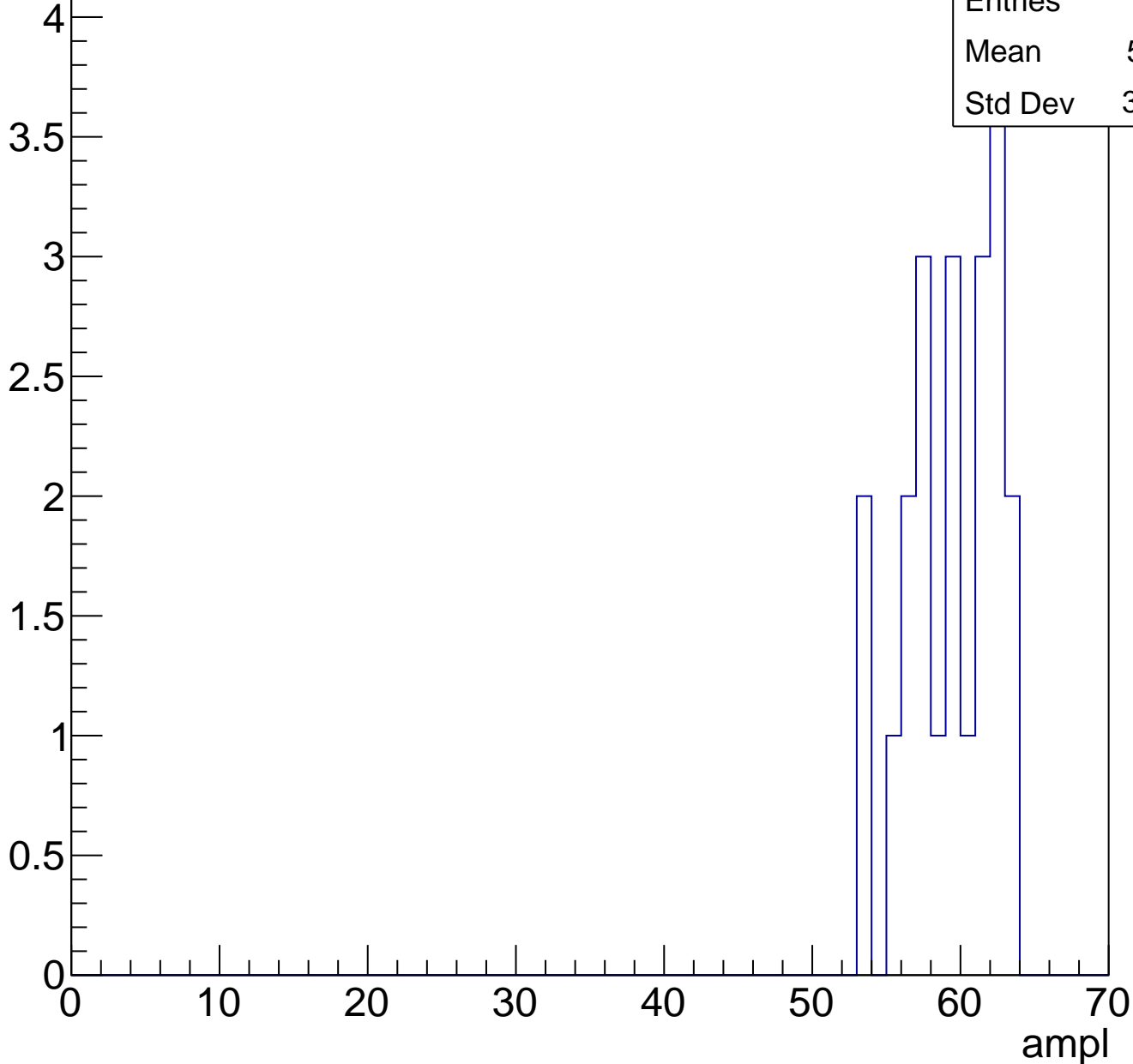
Entry



B1L103S, U15-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

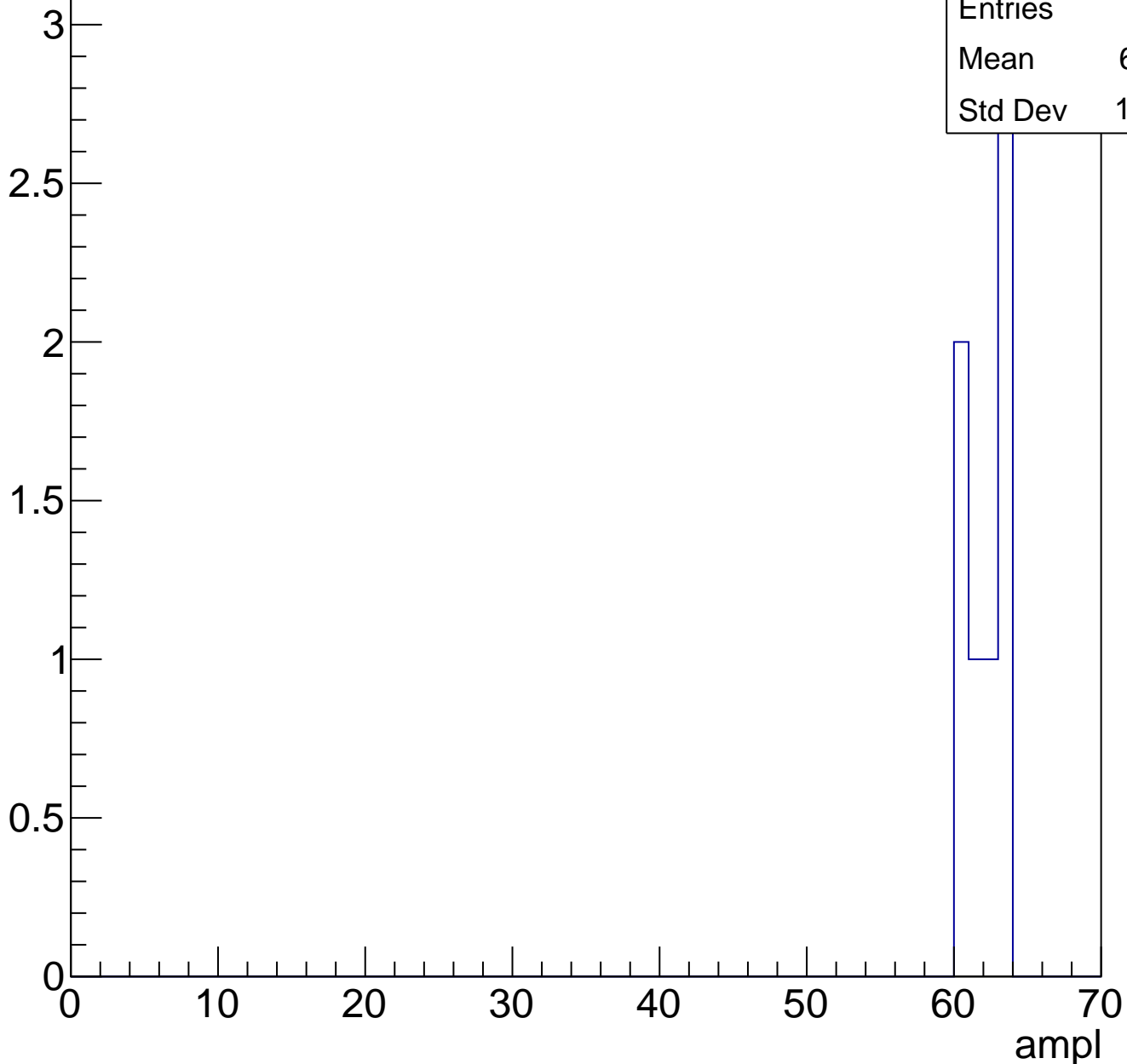
Entry



B1L103S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch40, adc0

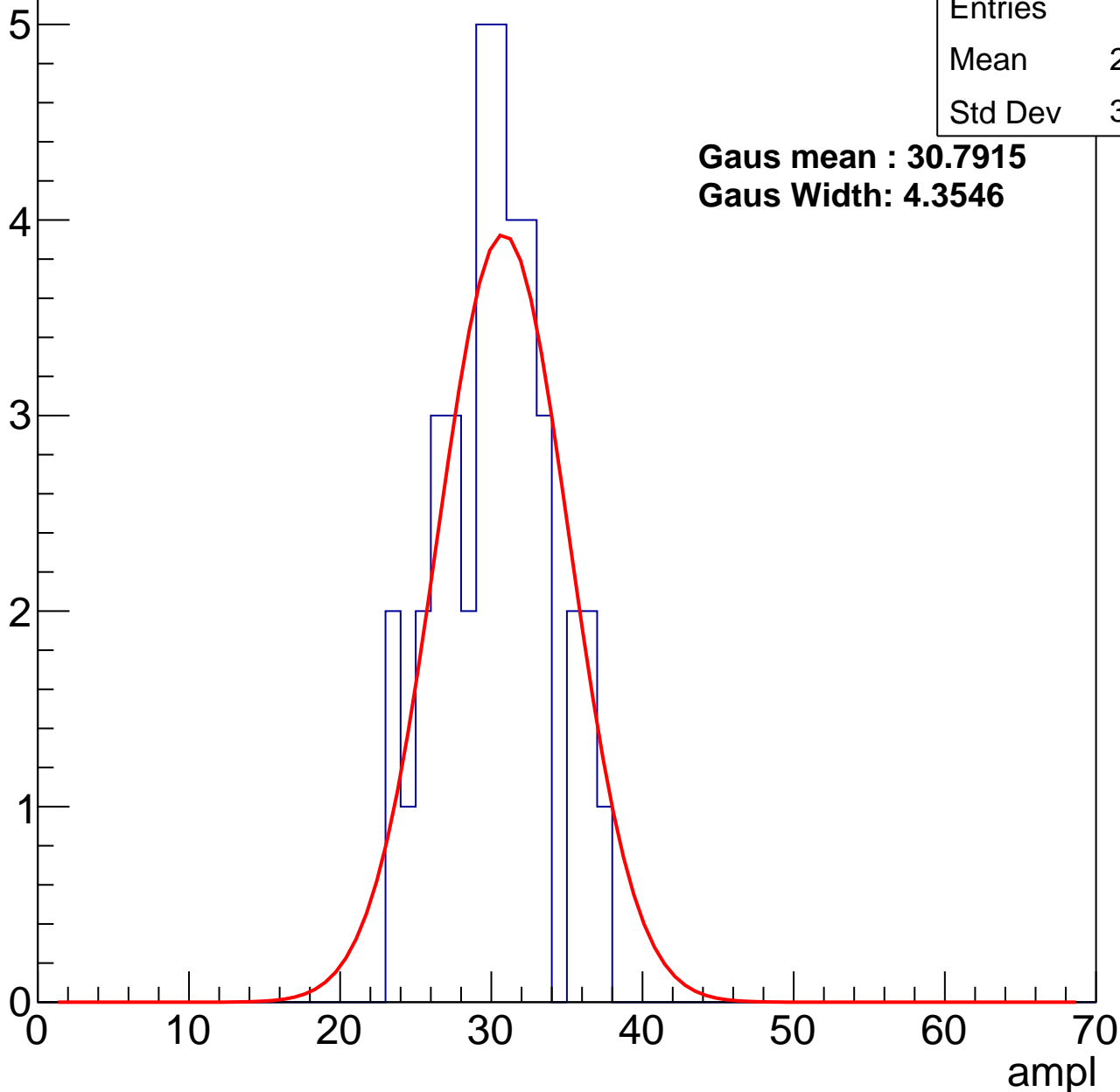
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	29.74
Std Dev	3.535

Gaus mean : 30.7915

Gaus Width: 4.3546



B1L103S, U15-ch40, adc1

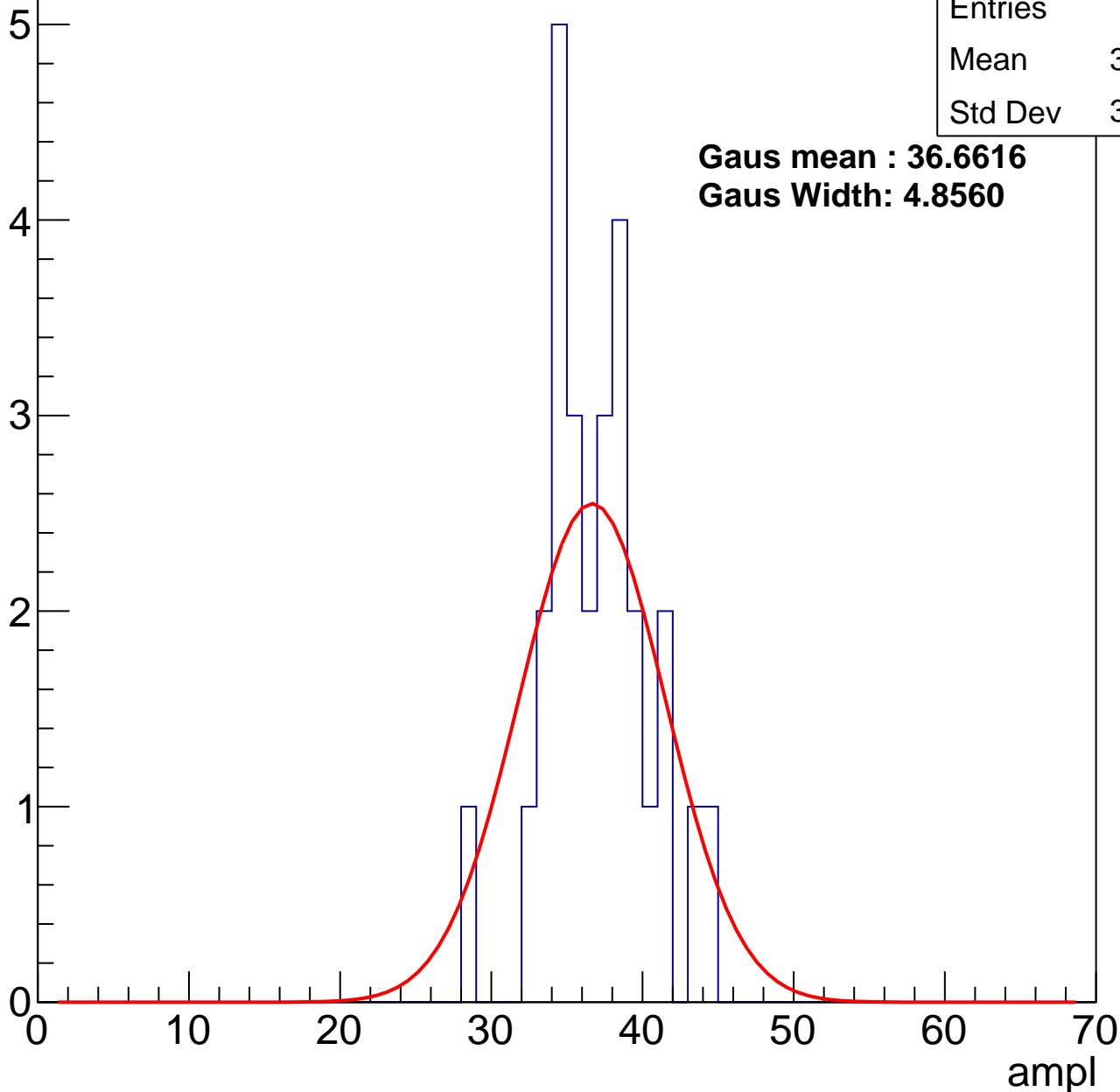
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	36.54
Std Dev	3.428

Gaus mean : 36.6616

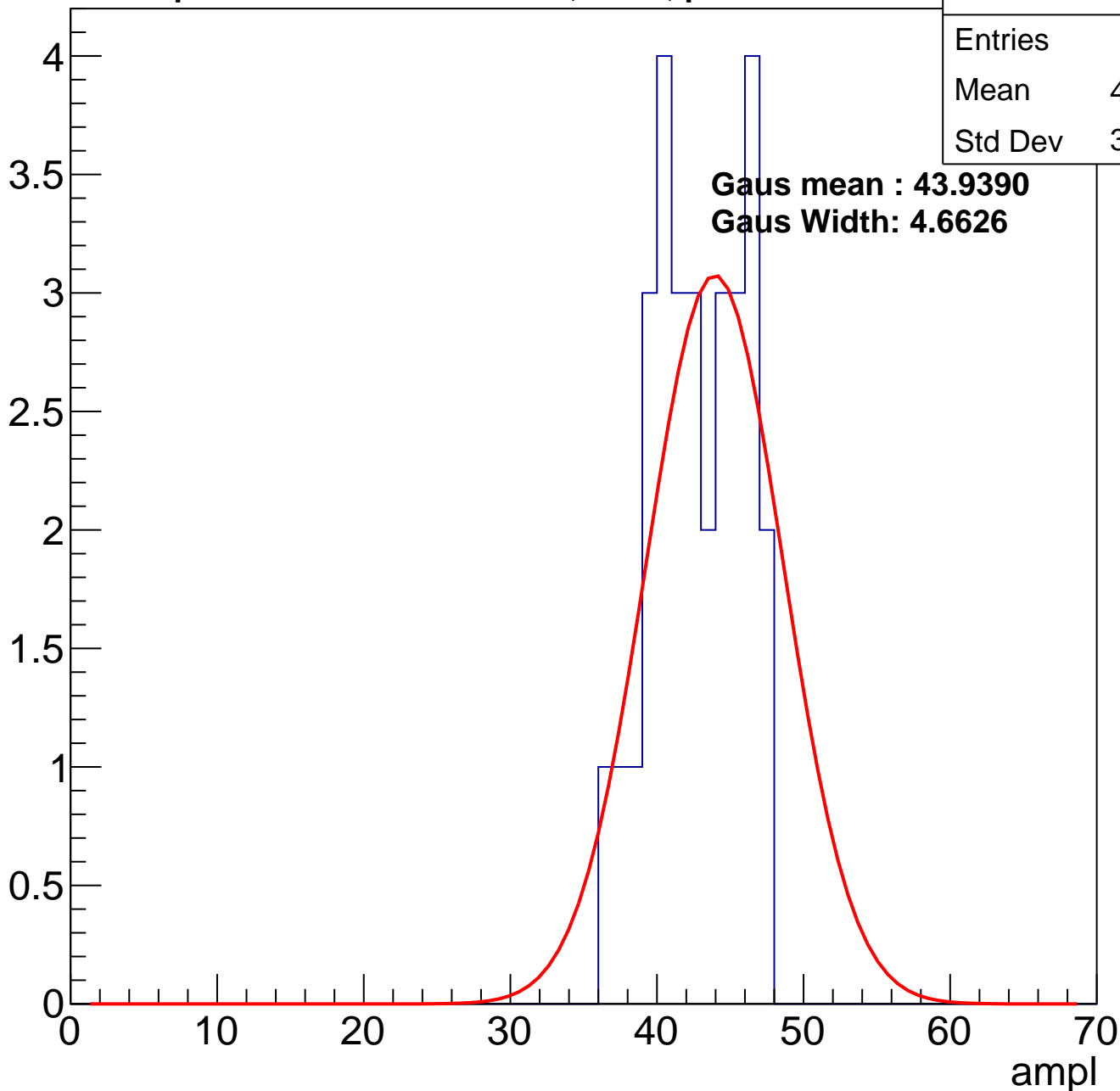
Gaus Width: 4.8560



B1L103S, U15-ch40, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

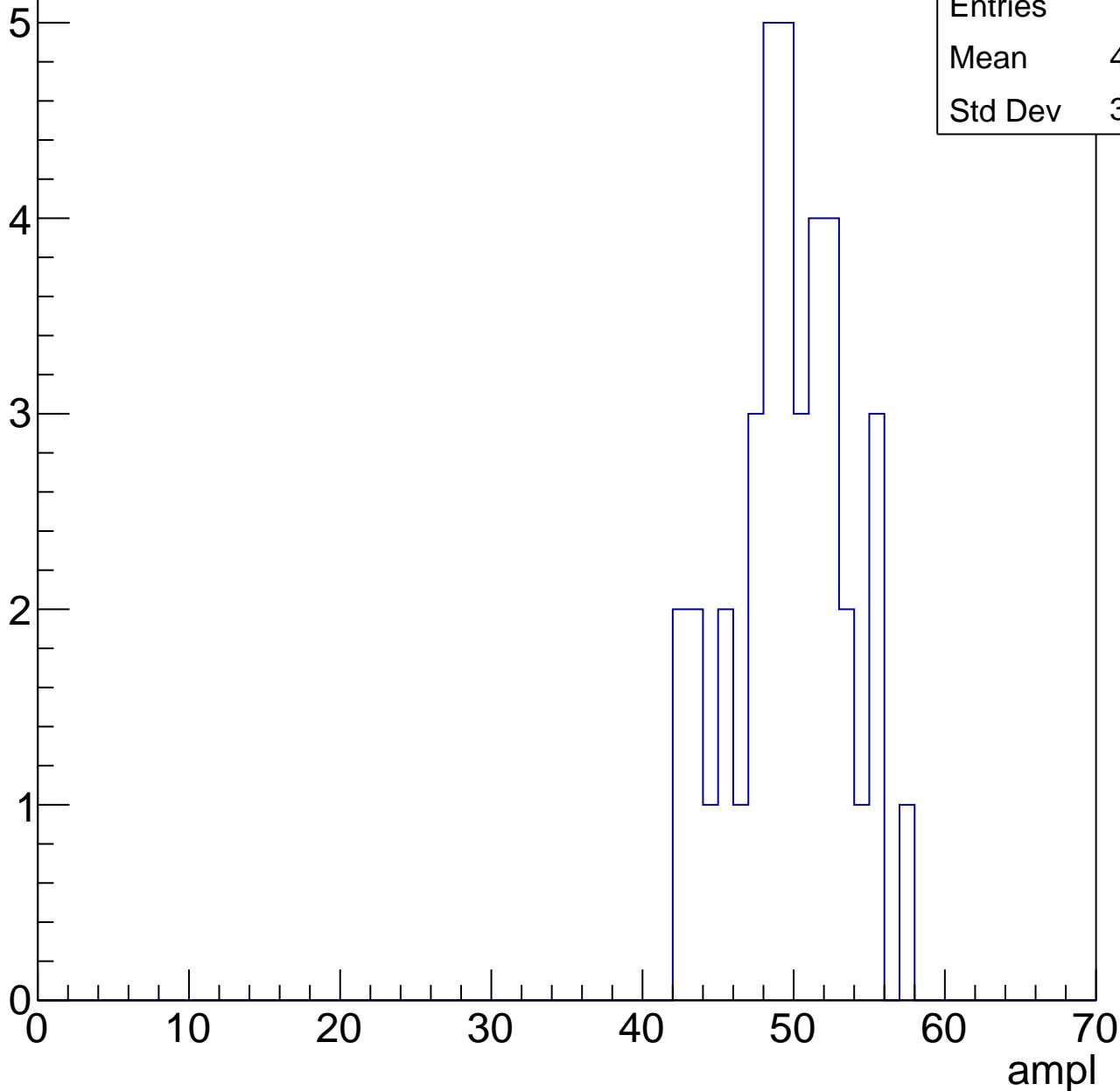


B1L103S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

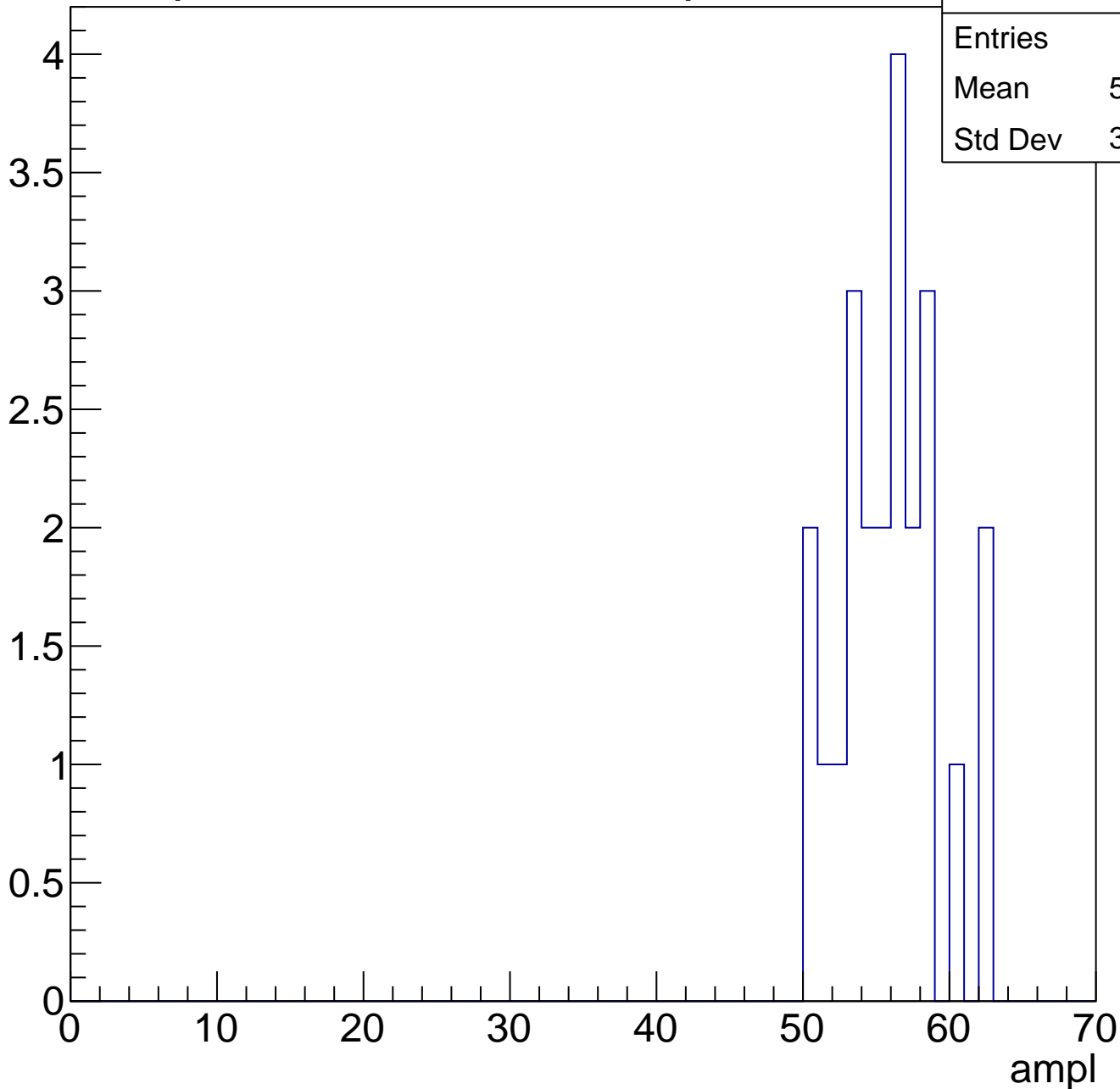
Entries	39
Mean	49.23
Std Dev	3.704



B1L103S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

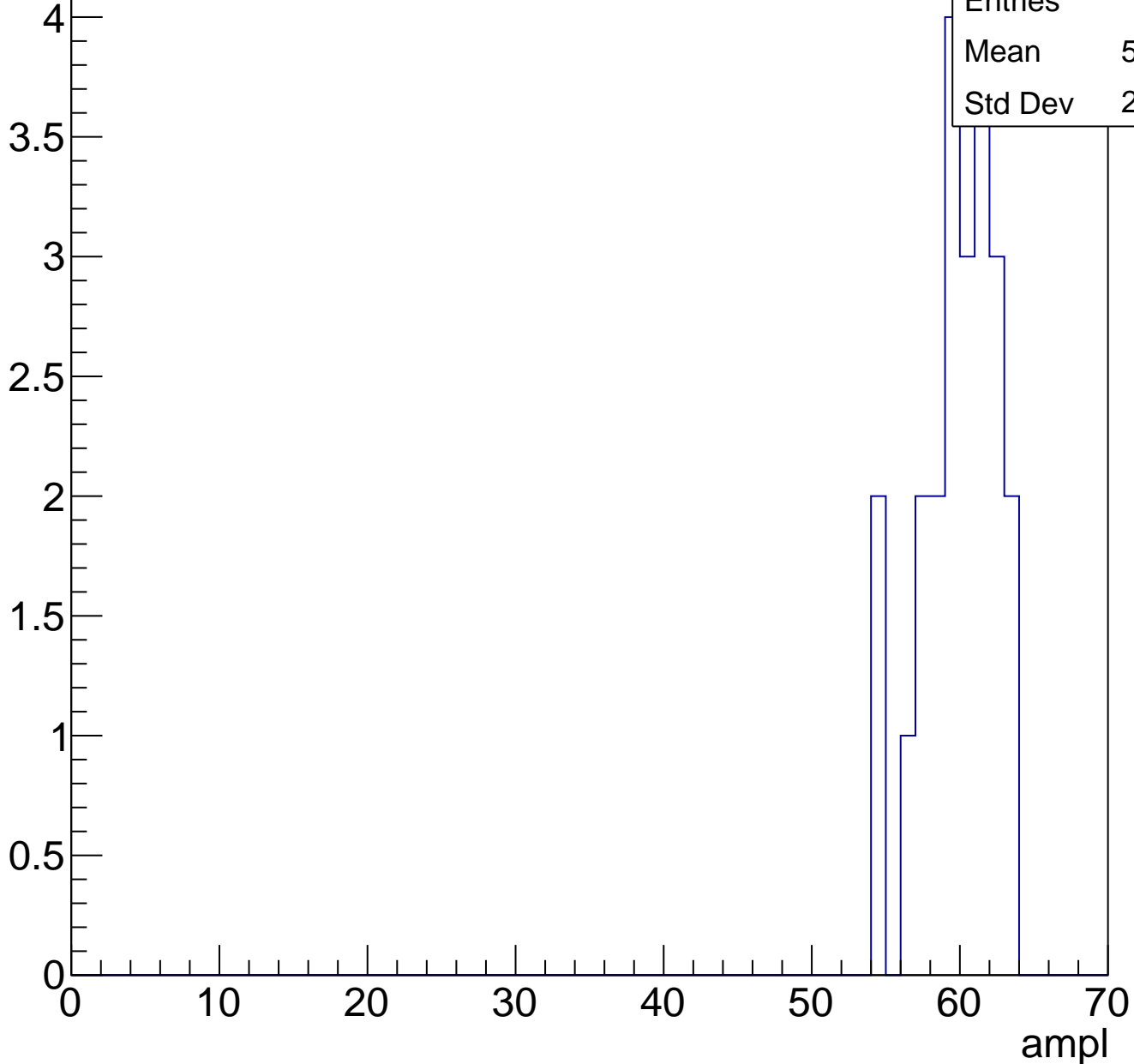
Entry



B1L103S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

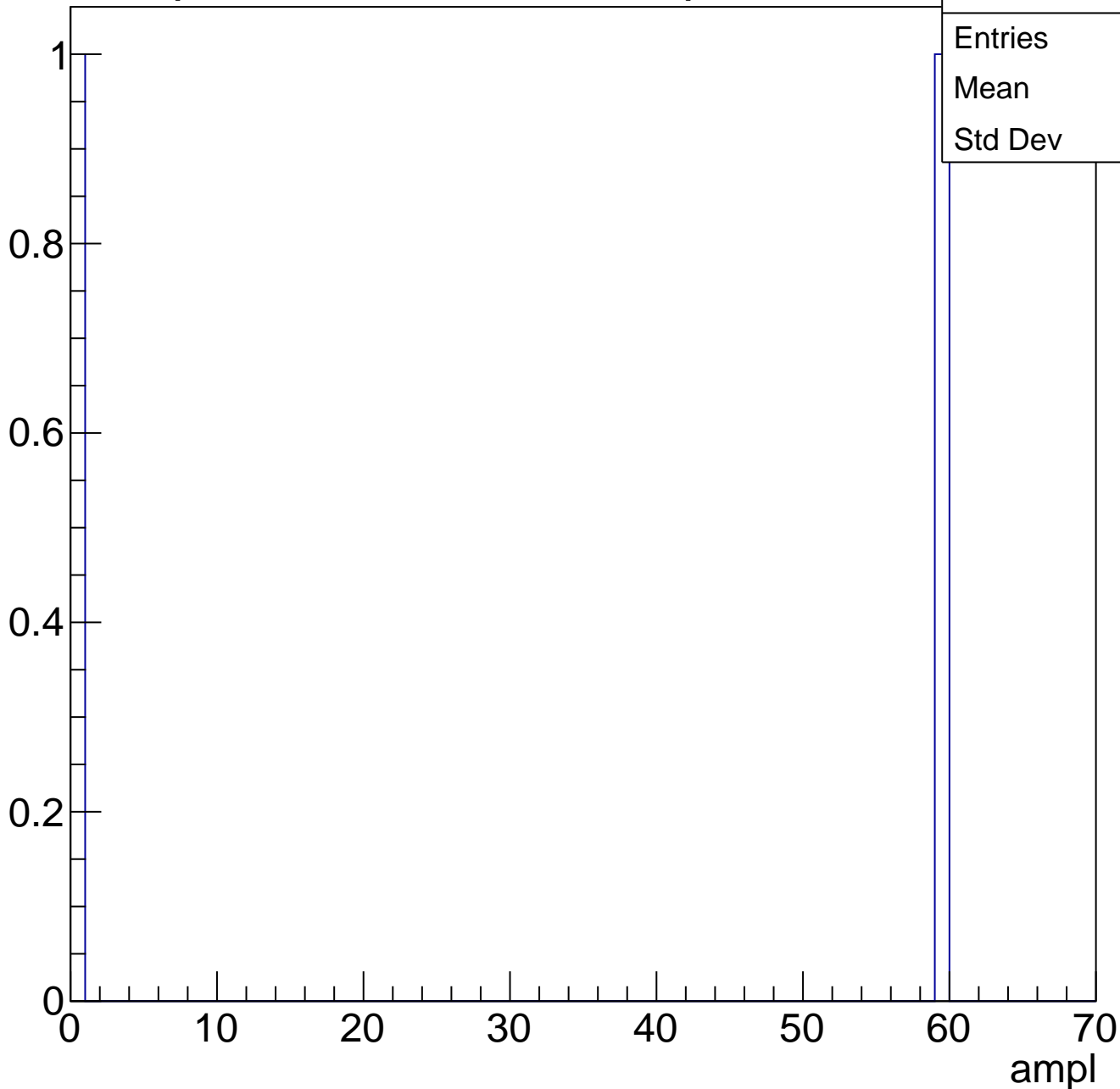
Entry



B1L103S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

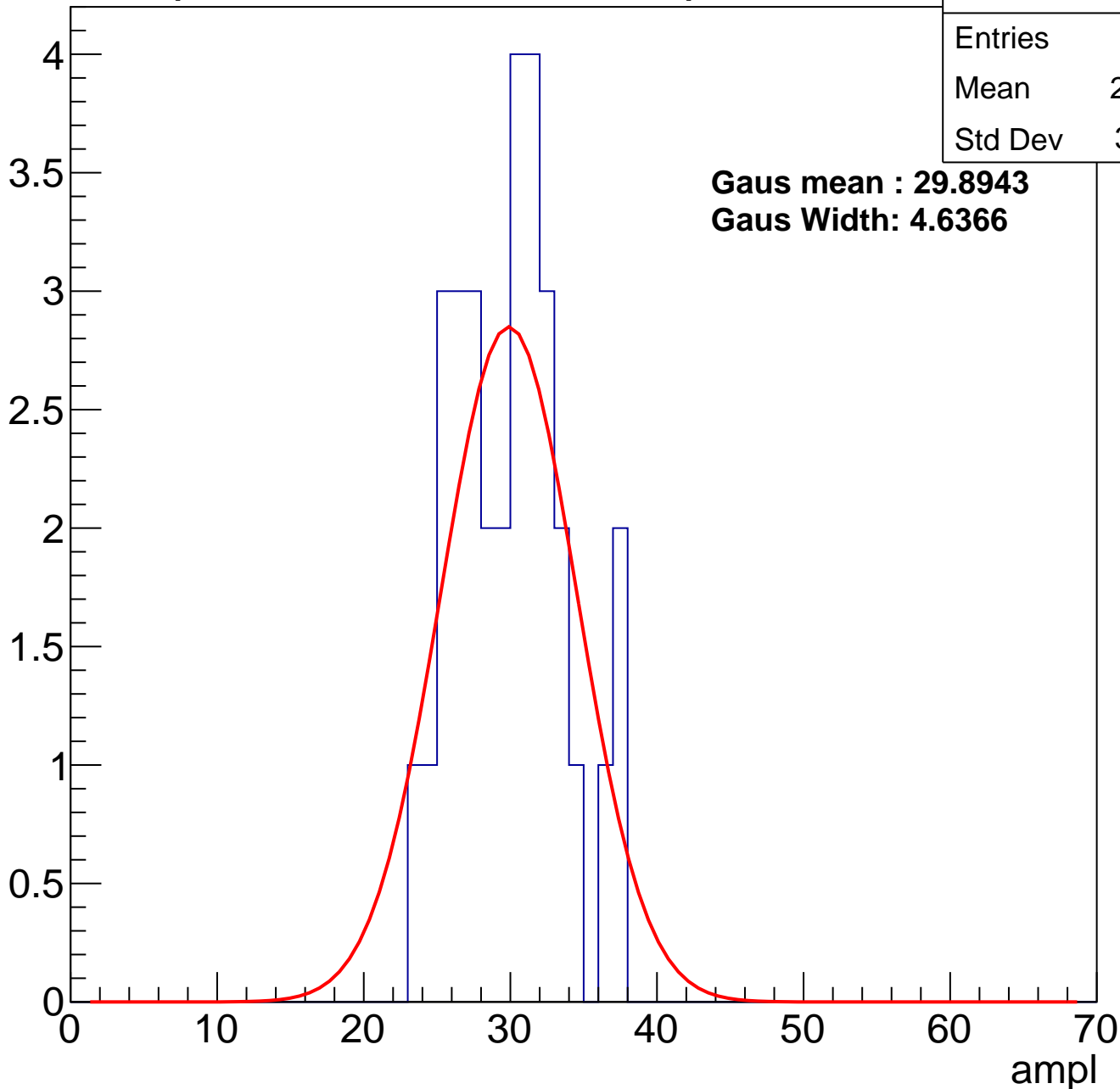
Entry



B1L103S, U15-ch41, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch41, adc1

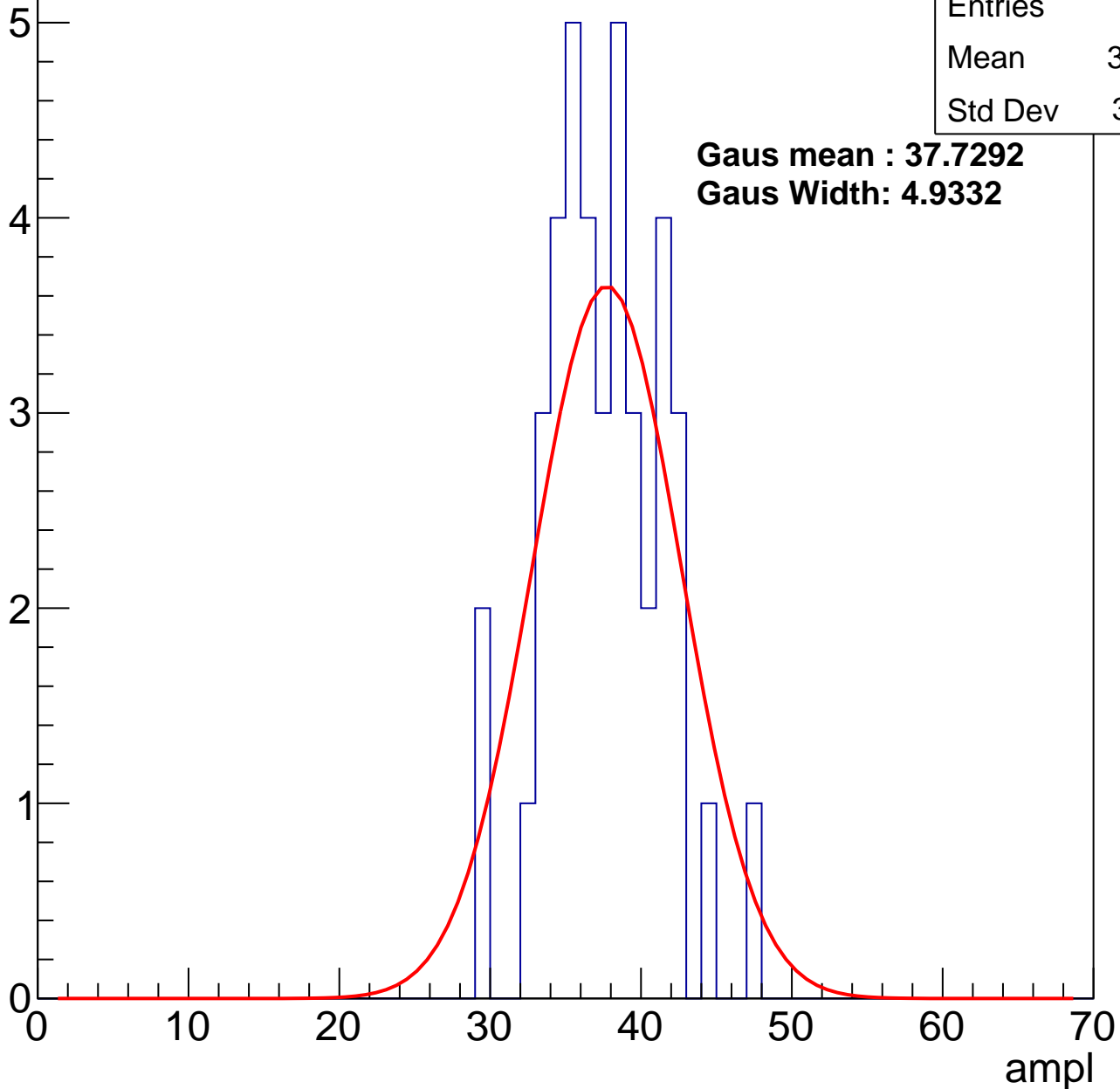
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	37.15
Std Dev	3.771

Gaus mean : 37.7292

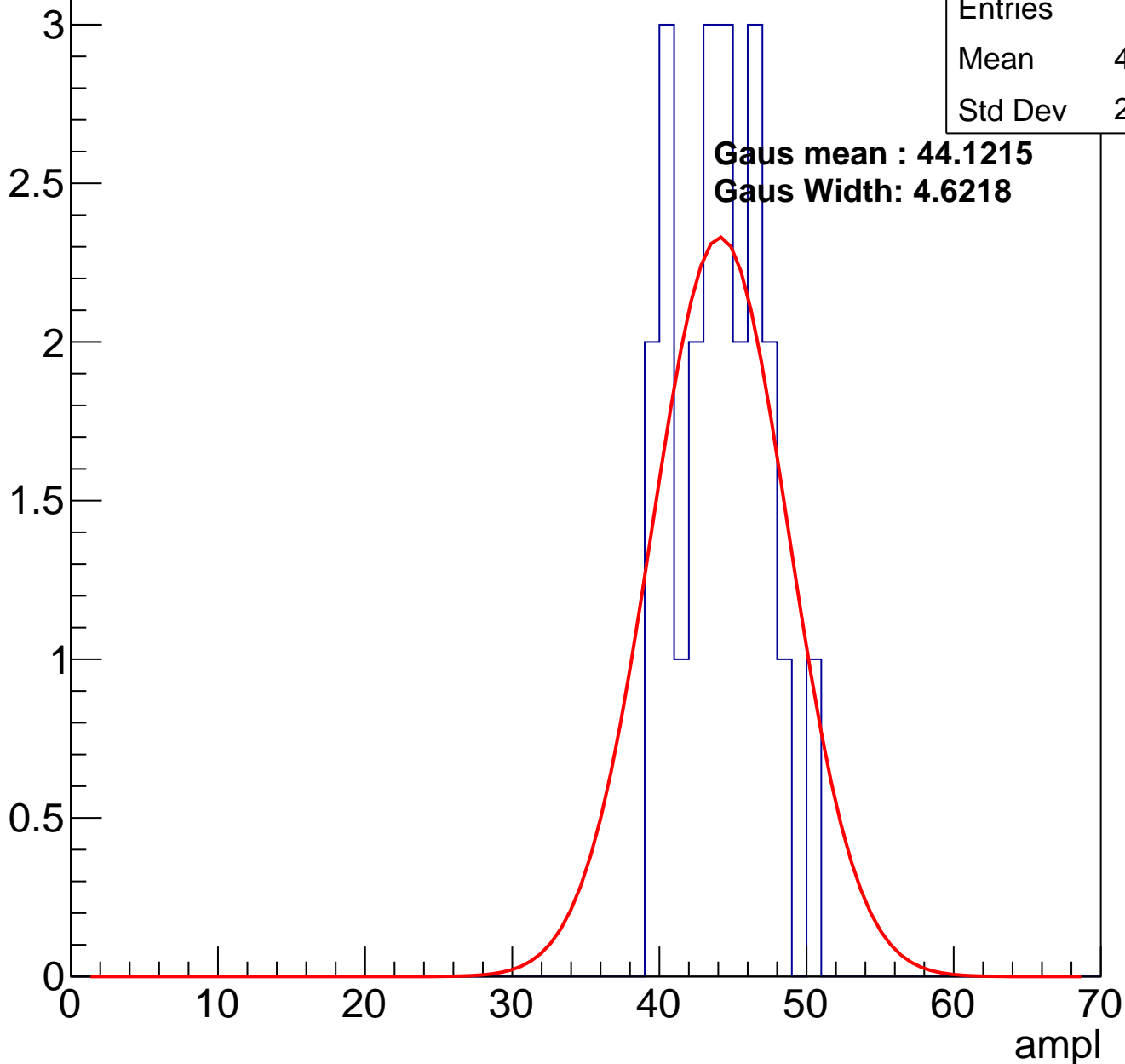
Gaus Width: 4.9332



B1L103S, U15-ch41, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

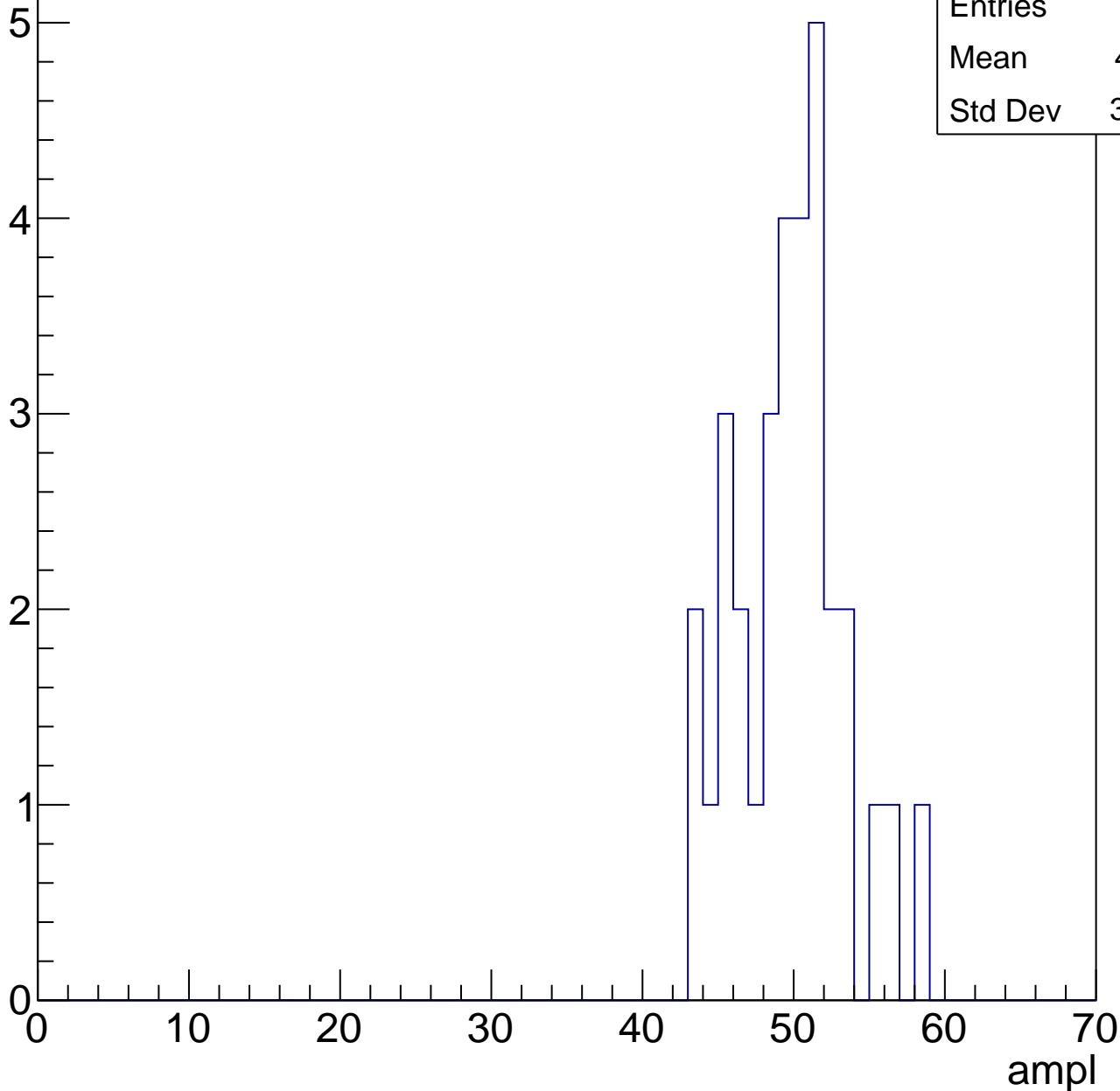


B1L103S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

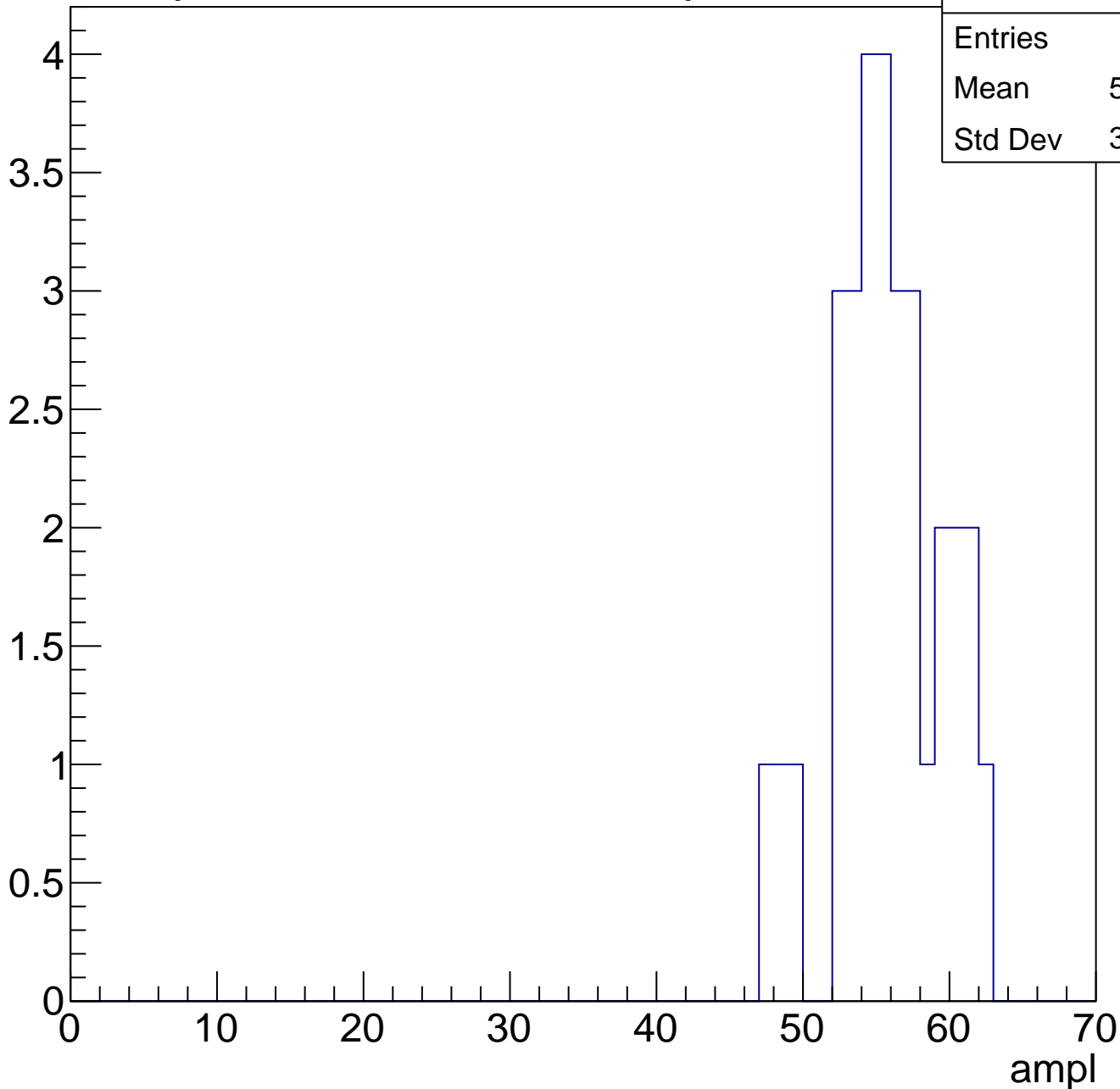
Entries	32
Mean	49.31
Std Dev	3.566



B1L103S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

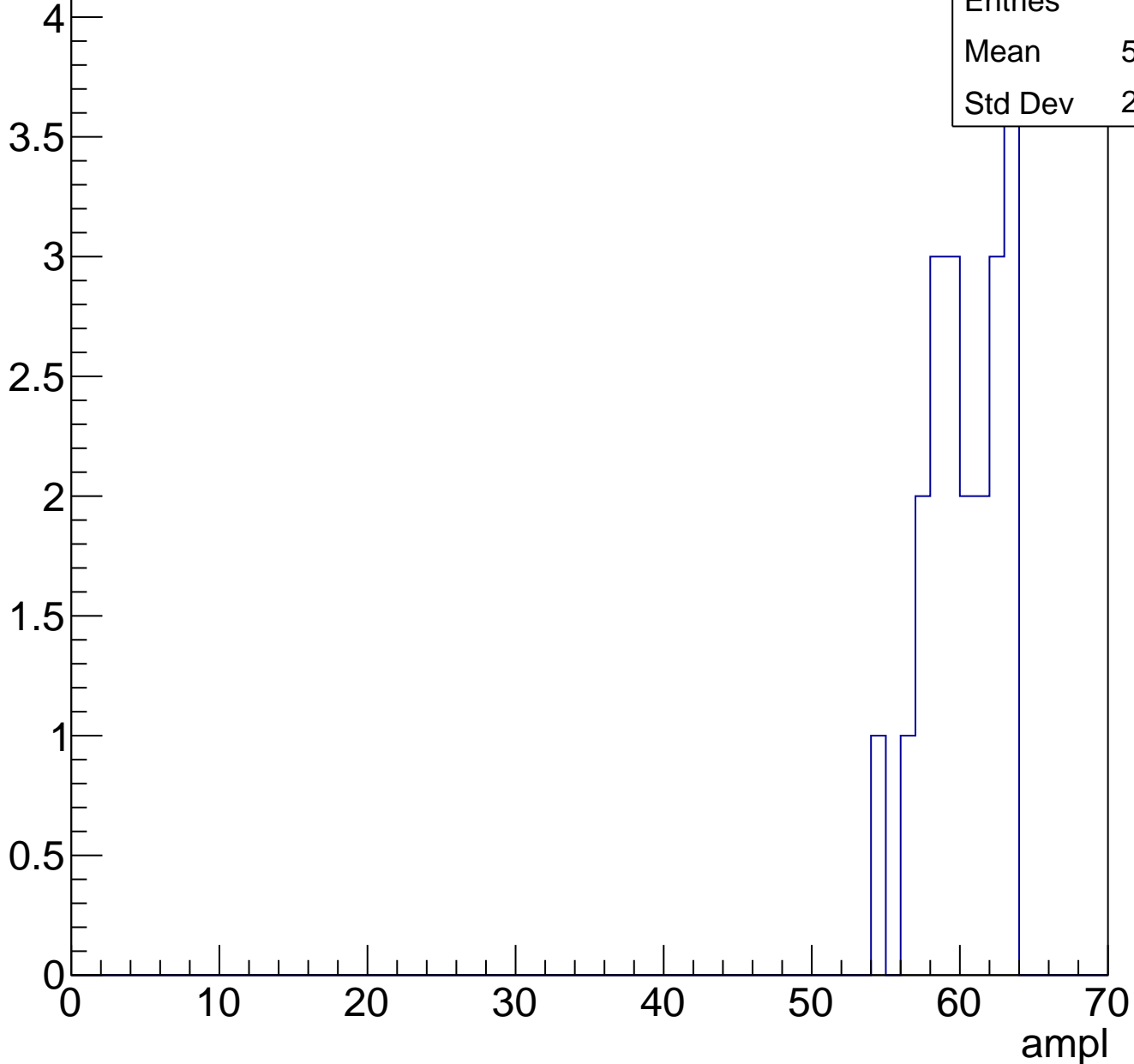
Entry



B1L103S, U15-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

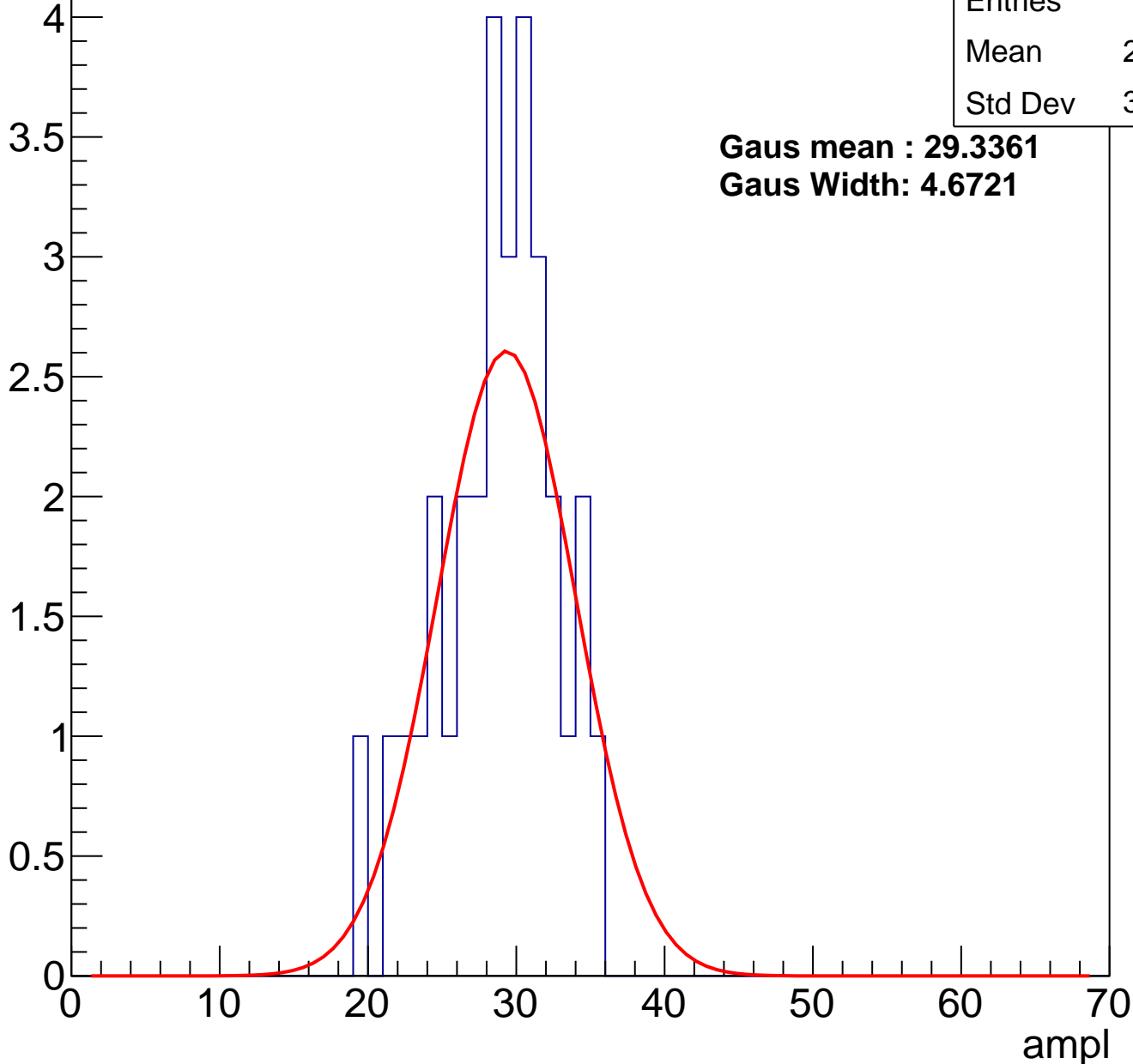


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch42, adc0

calib_packv5_042523_0143.root, FC#7, port C2

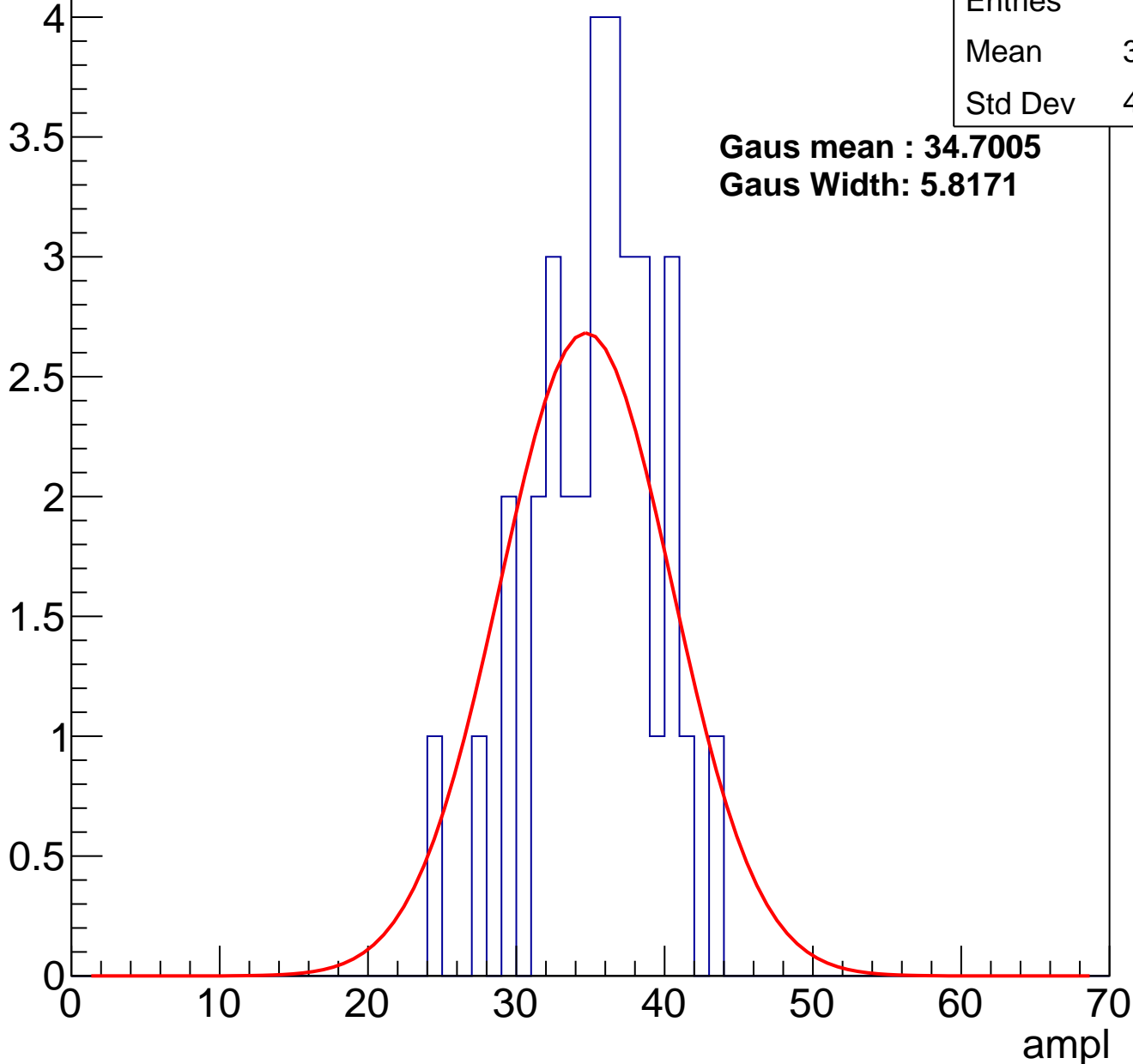
Entry



B1L103S, U15-ch42, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch42, adc2

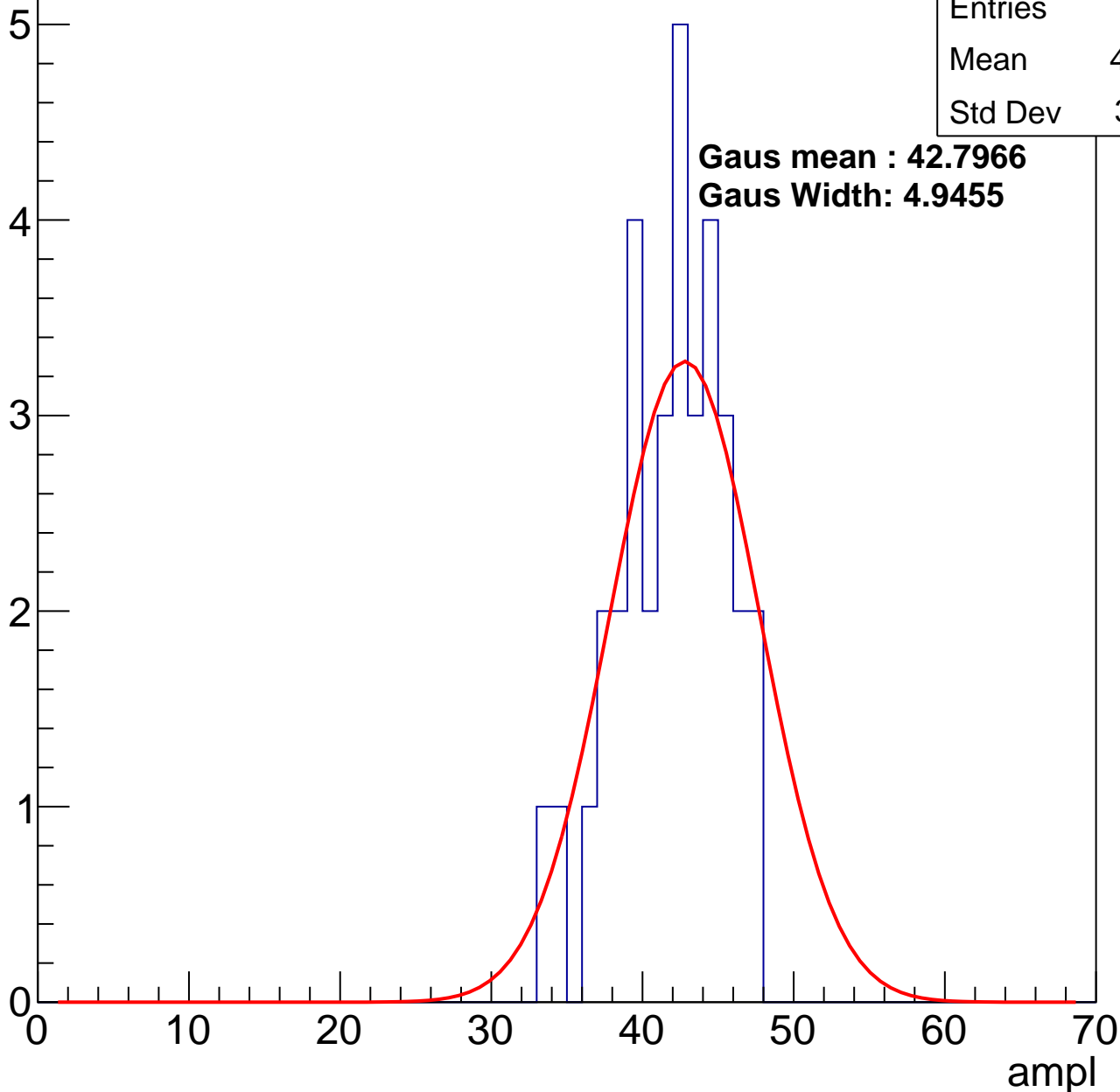
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	41.37
Std Dev	3.481

Gaus mean : 42.7966

Gaus Width: 4.9455



B1L103S, U15-ch42, adc3

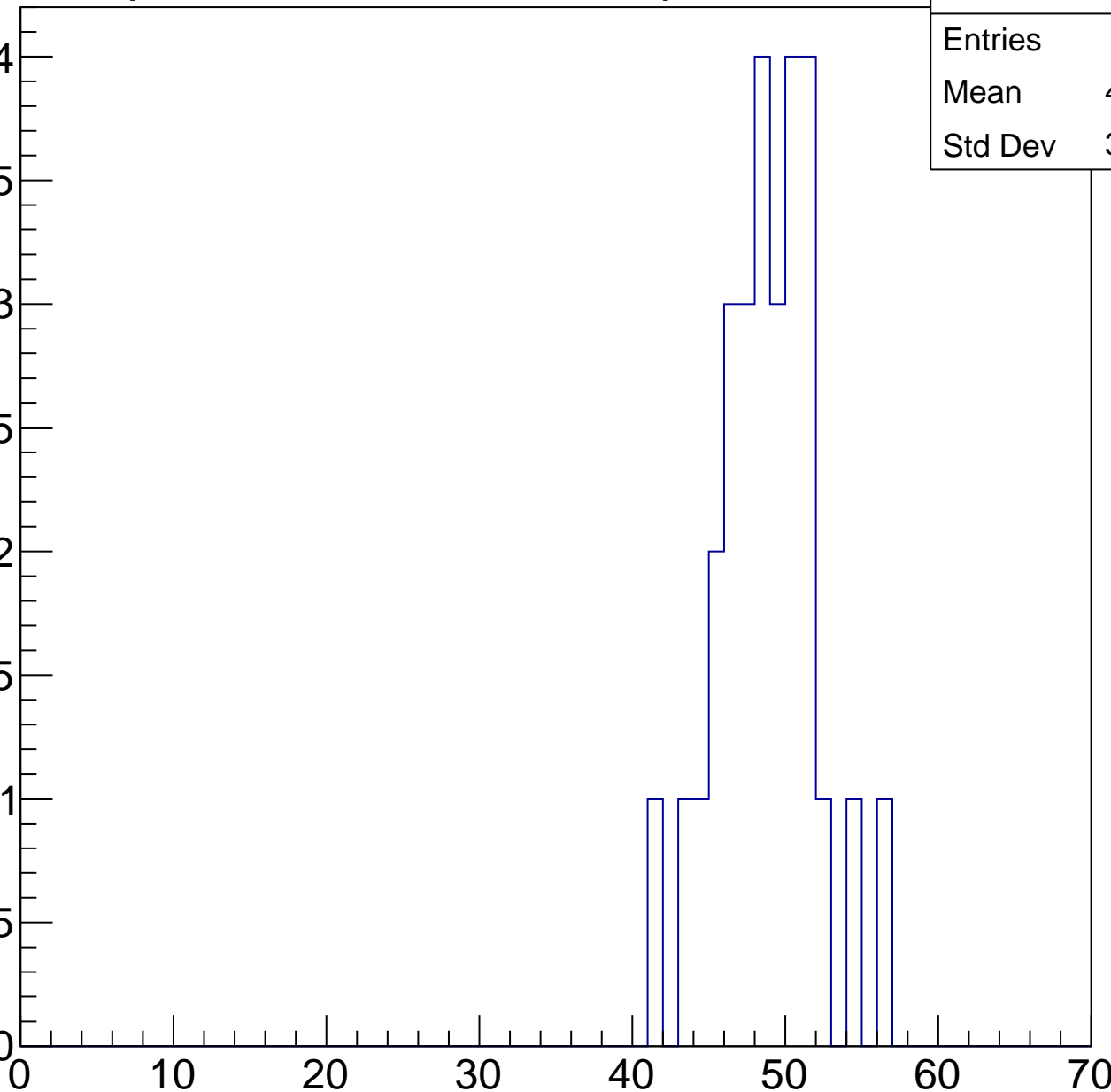
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	29
Mean	48.34
Std Dev	3.165

ampl

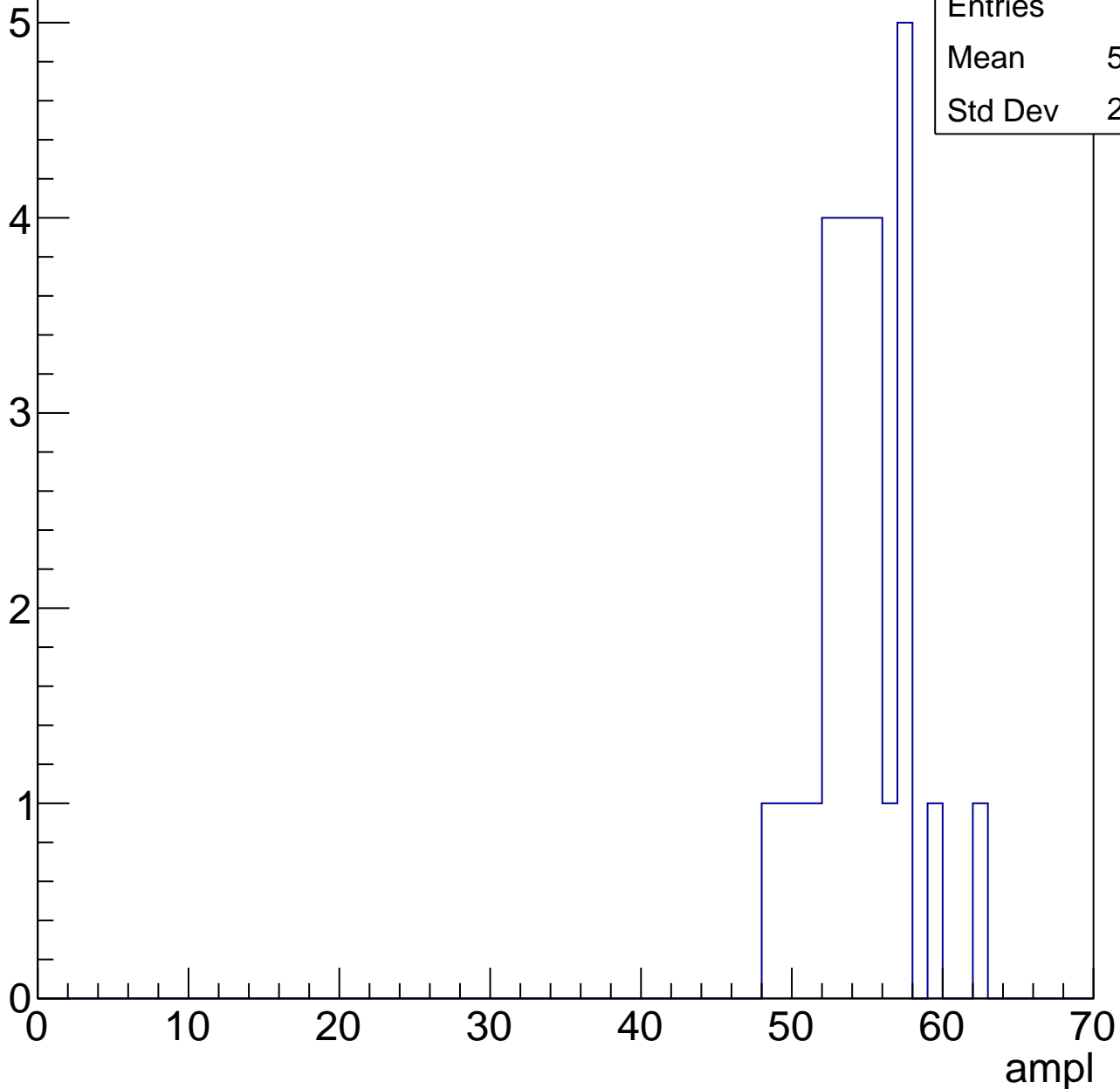


B1L103S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	54.14
Std Dev	2.973

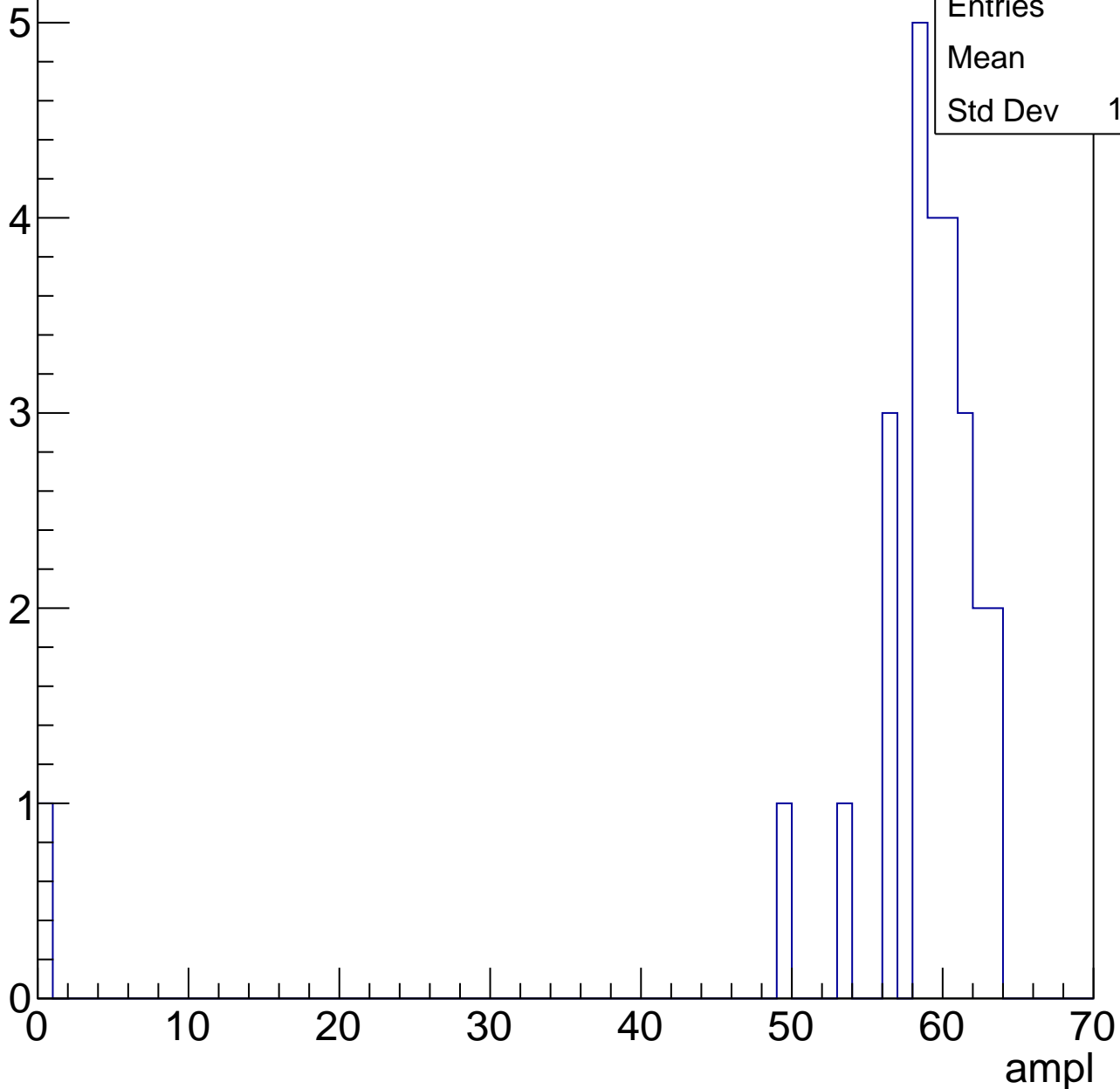


B1L103S, U15-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

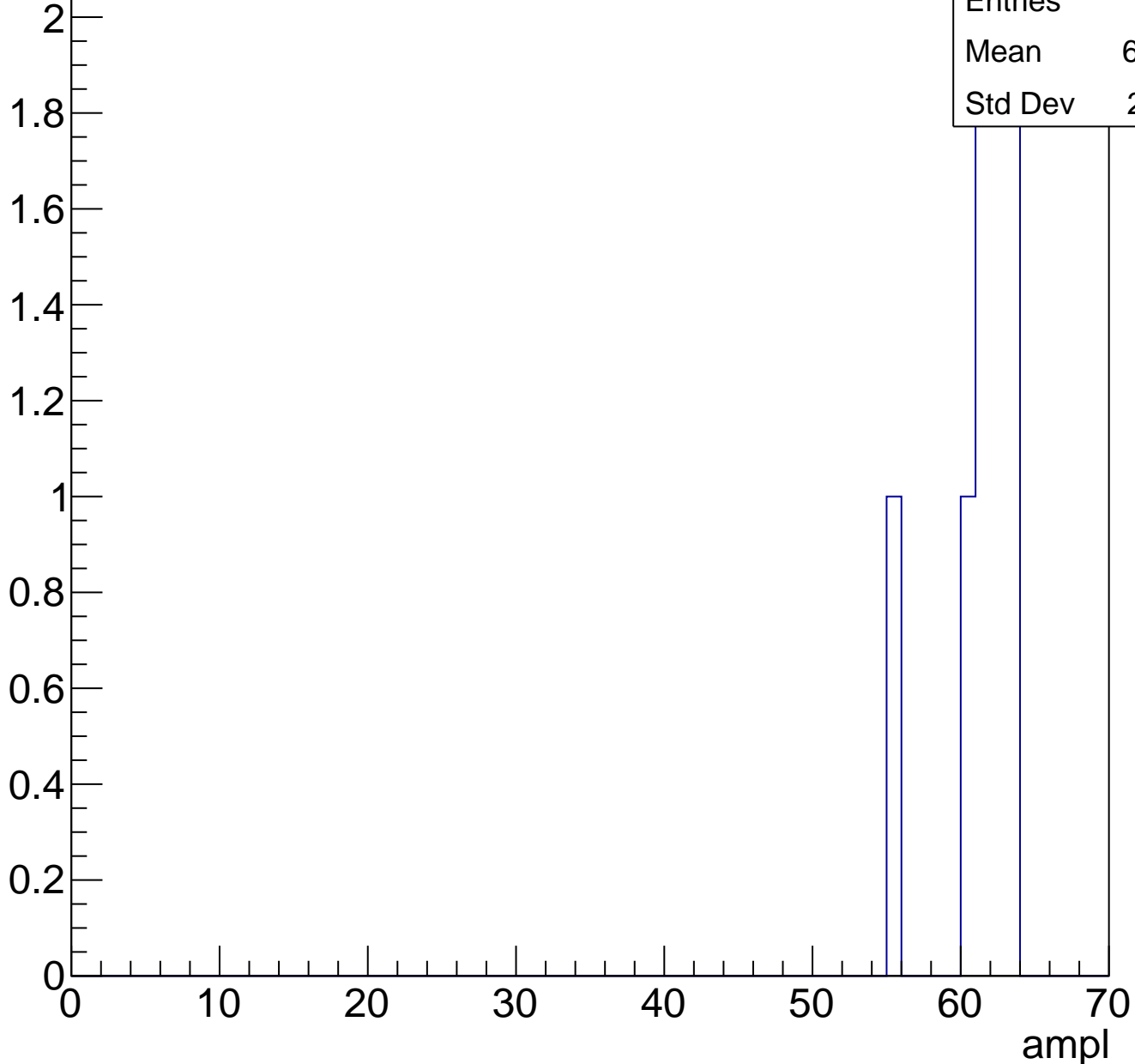
Entries	26
Mean	56.5
Std Dev	11.69



B1L103S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch43, adc0

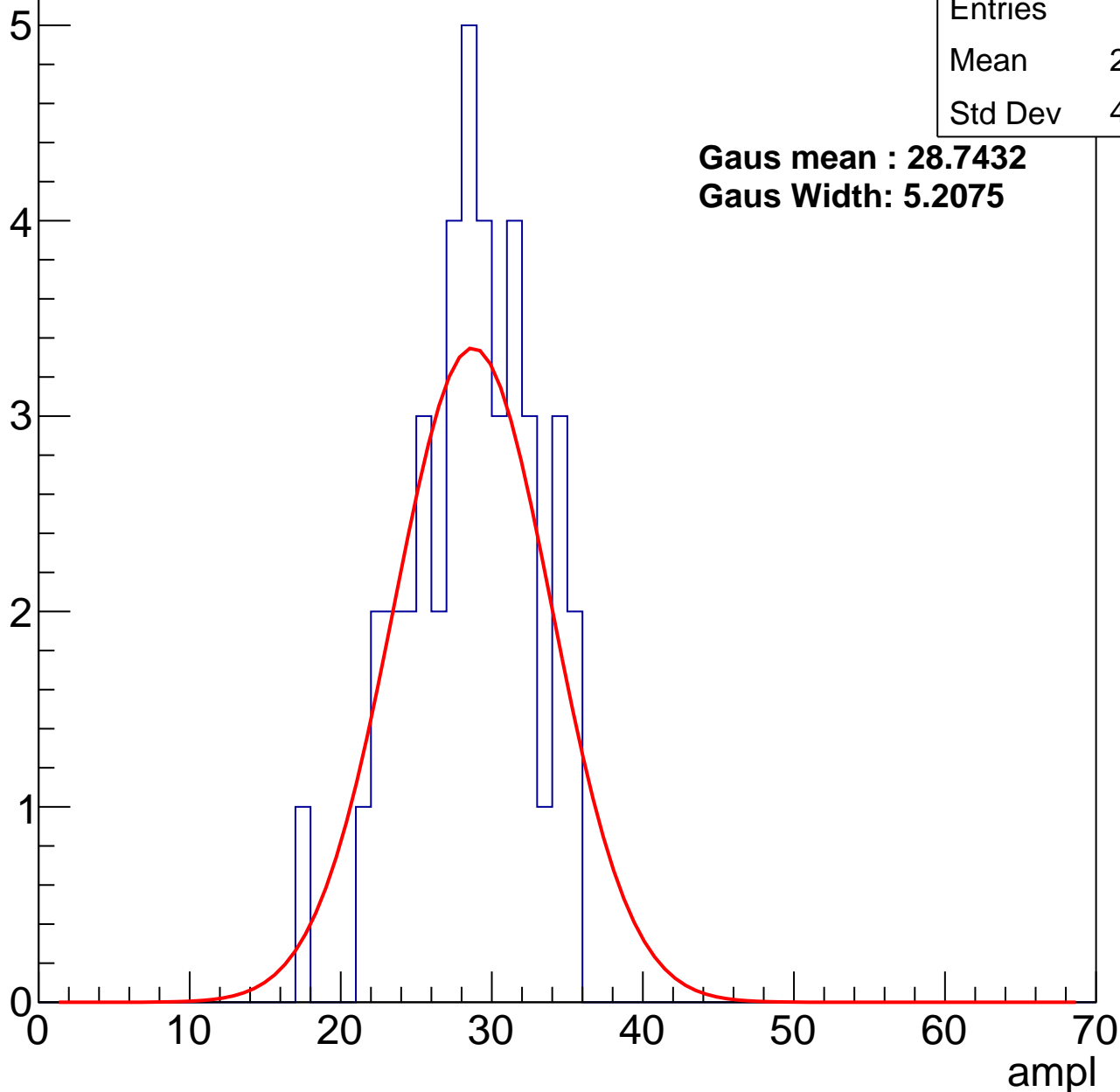
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	28.14
Std Dev	4.062

Gaus mean : 28.7432

Gaus Width: 5.2075



B1L103S, U15-ch43, adc1

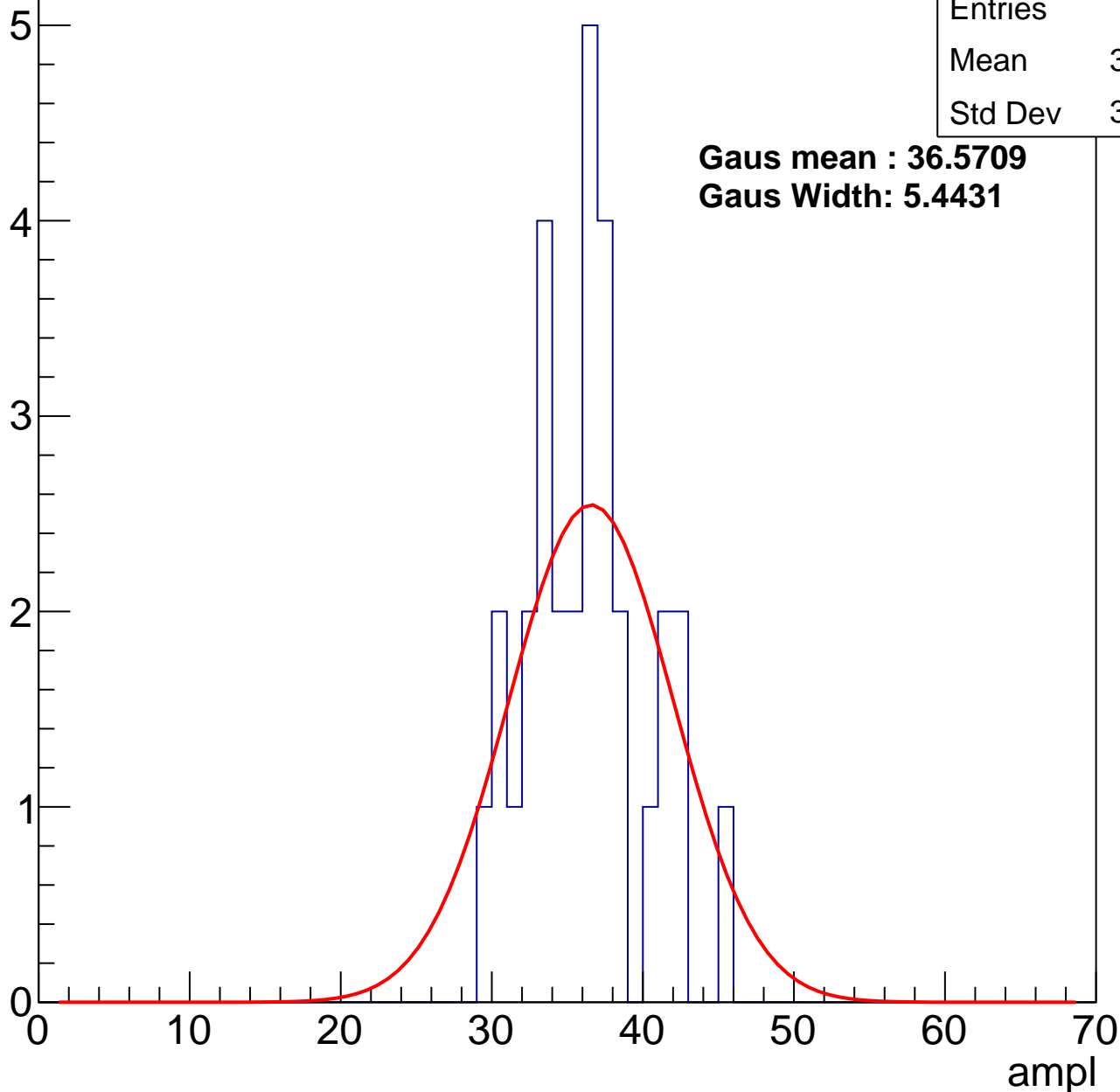
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	35.77
Std Dev	3.824

Gaus mean : 36.5709

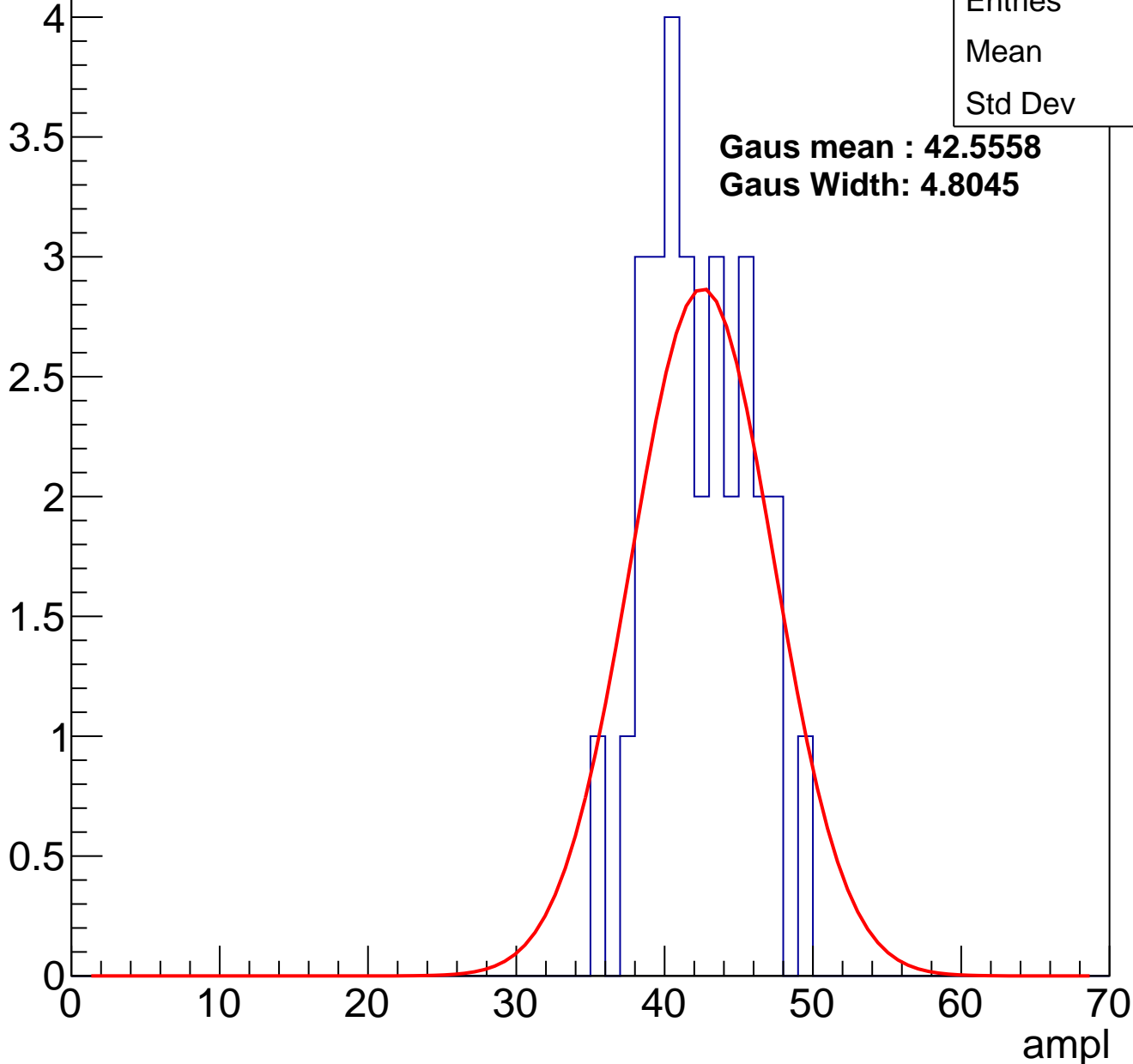
Gaus Width: 5.4431



B1L103S, U15-ch43, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

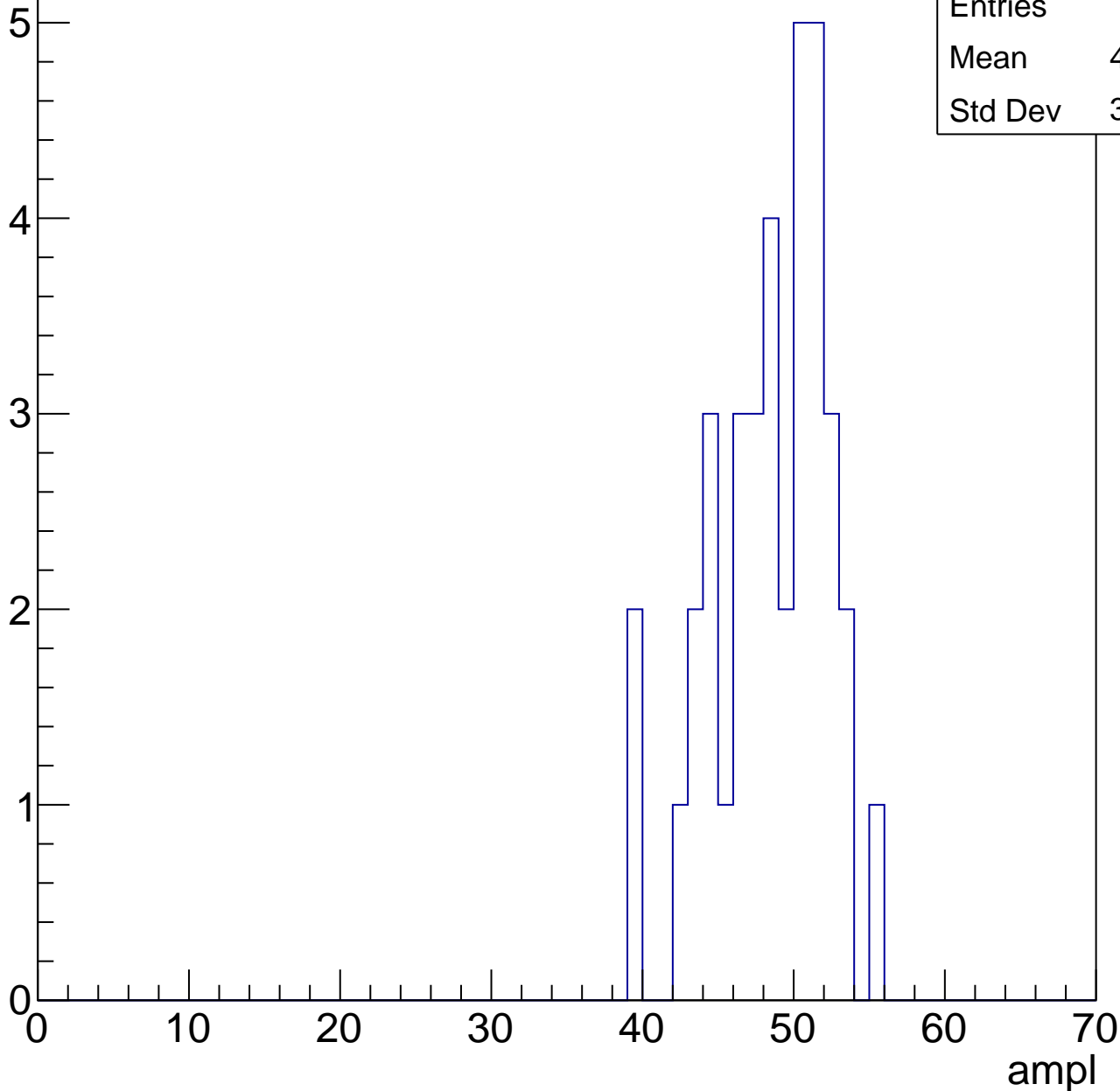


B1L103S, U15-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	47.95
Std Dev	3.799

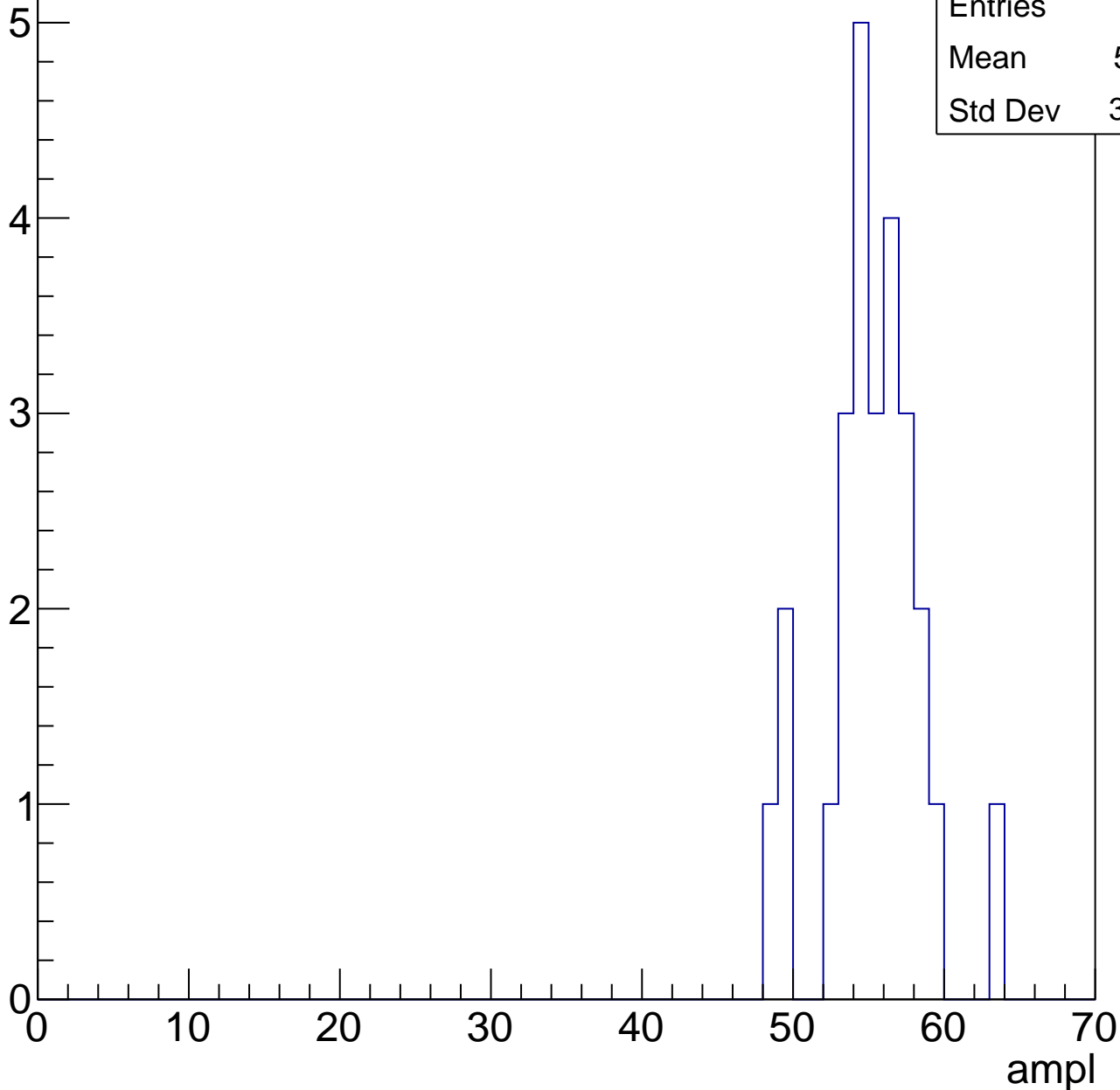


B1L103S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	54.81
Std Dev	3.163

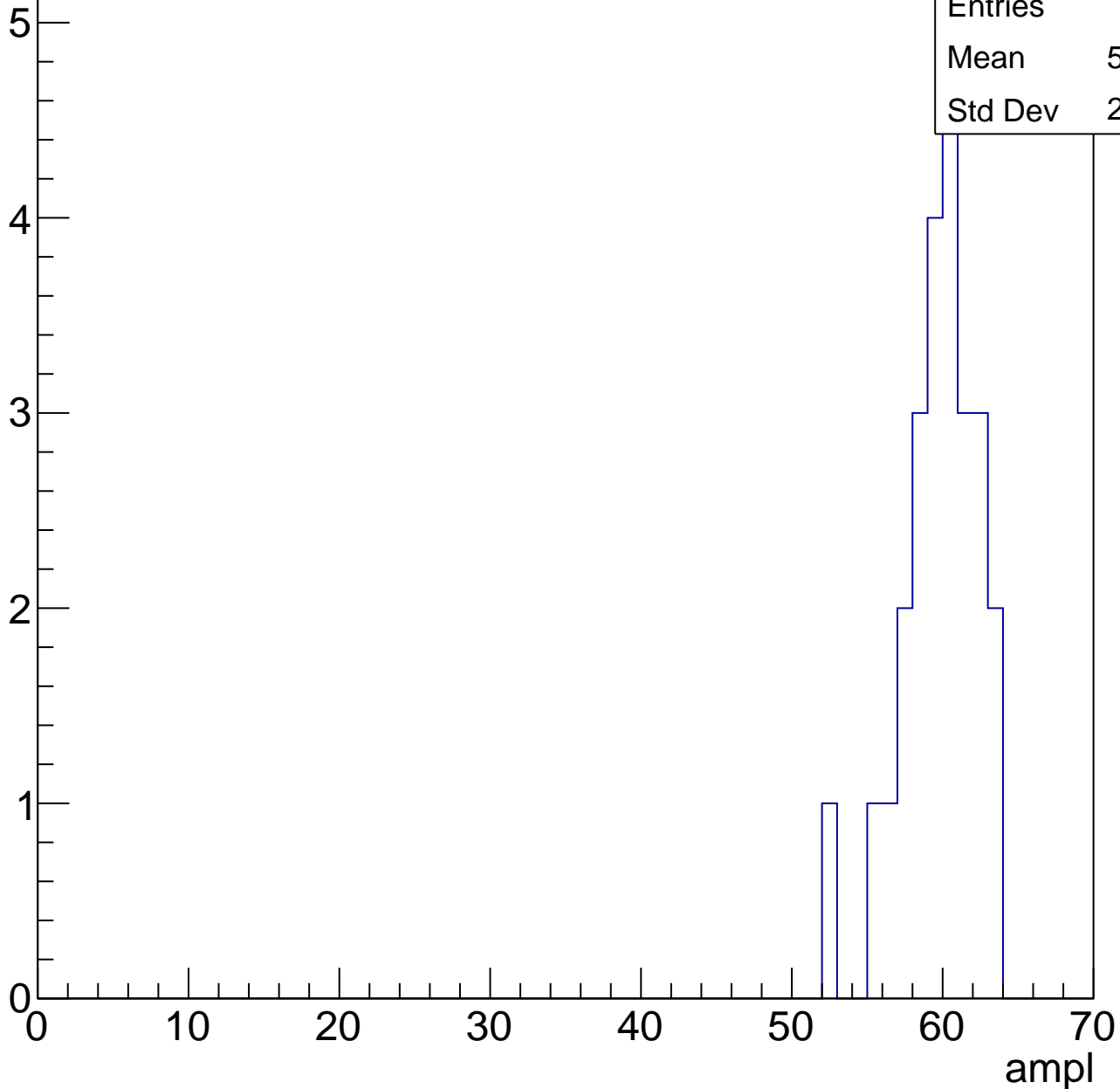


B1L103S, U15-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	59.28
Std Dev	2.522



B1L103S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

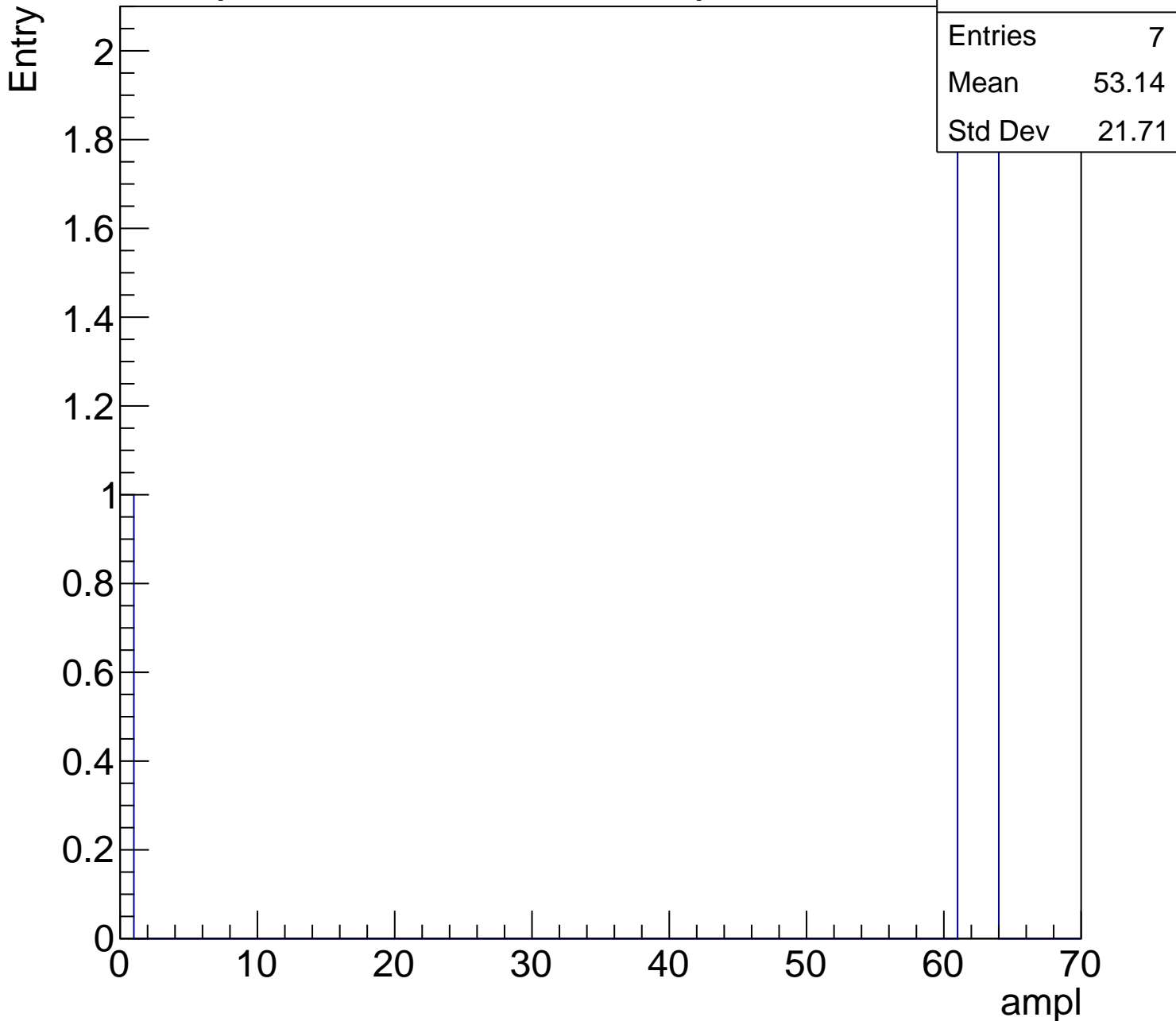
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	53.14
Std Dev	21.71

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

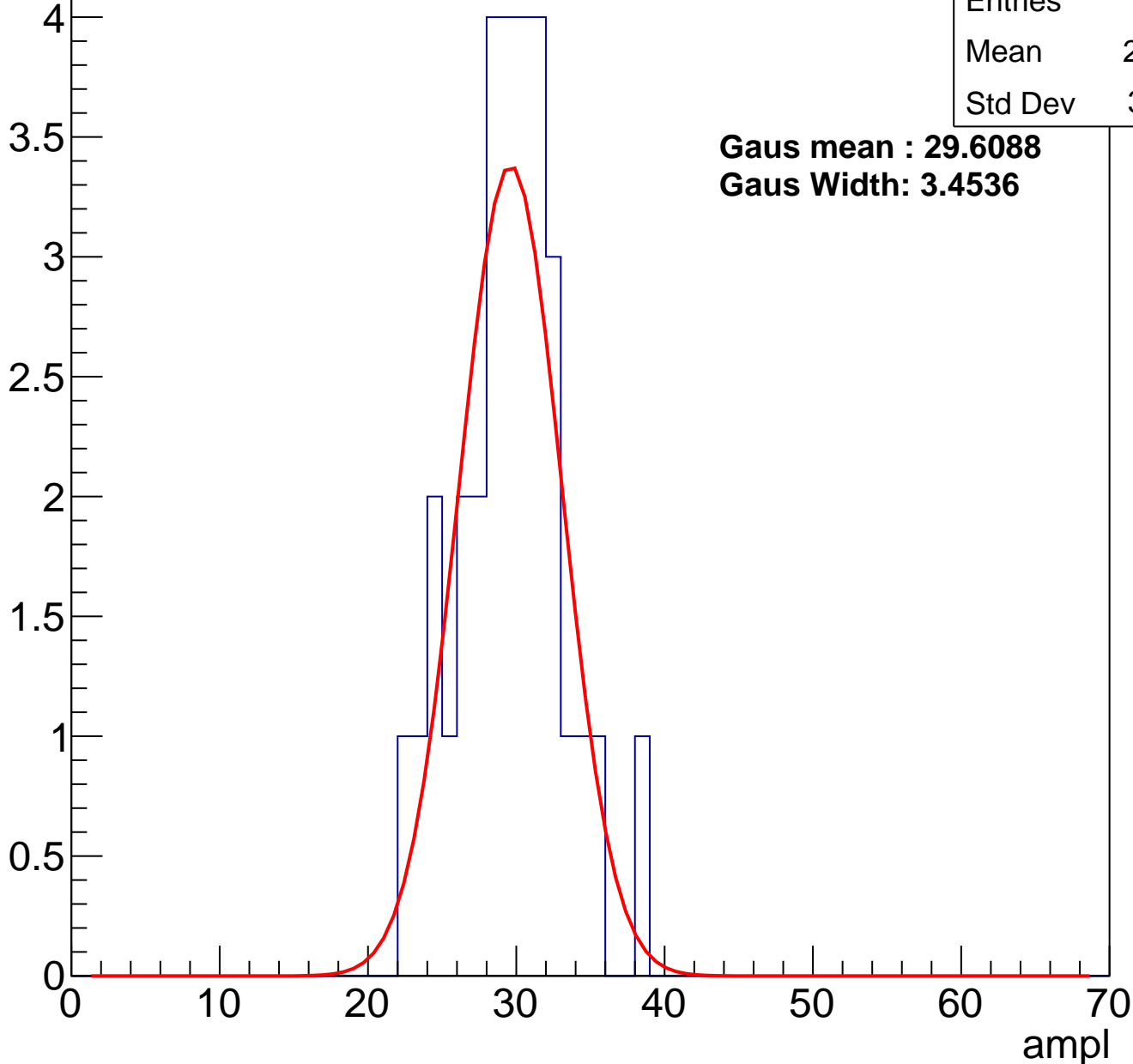


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch44, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch44, adc1

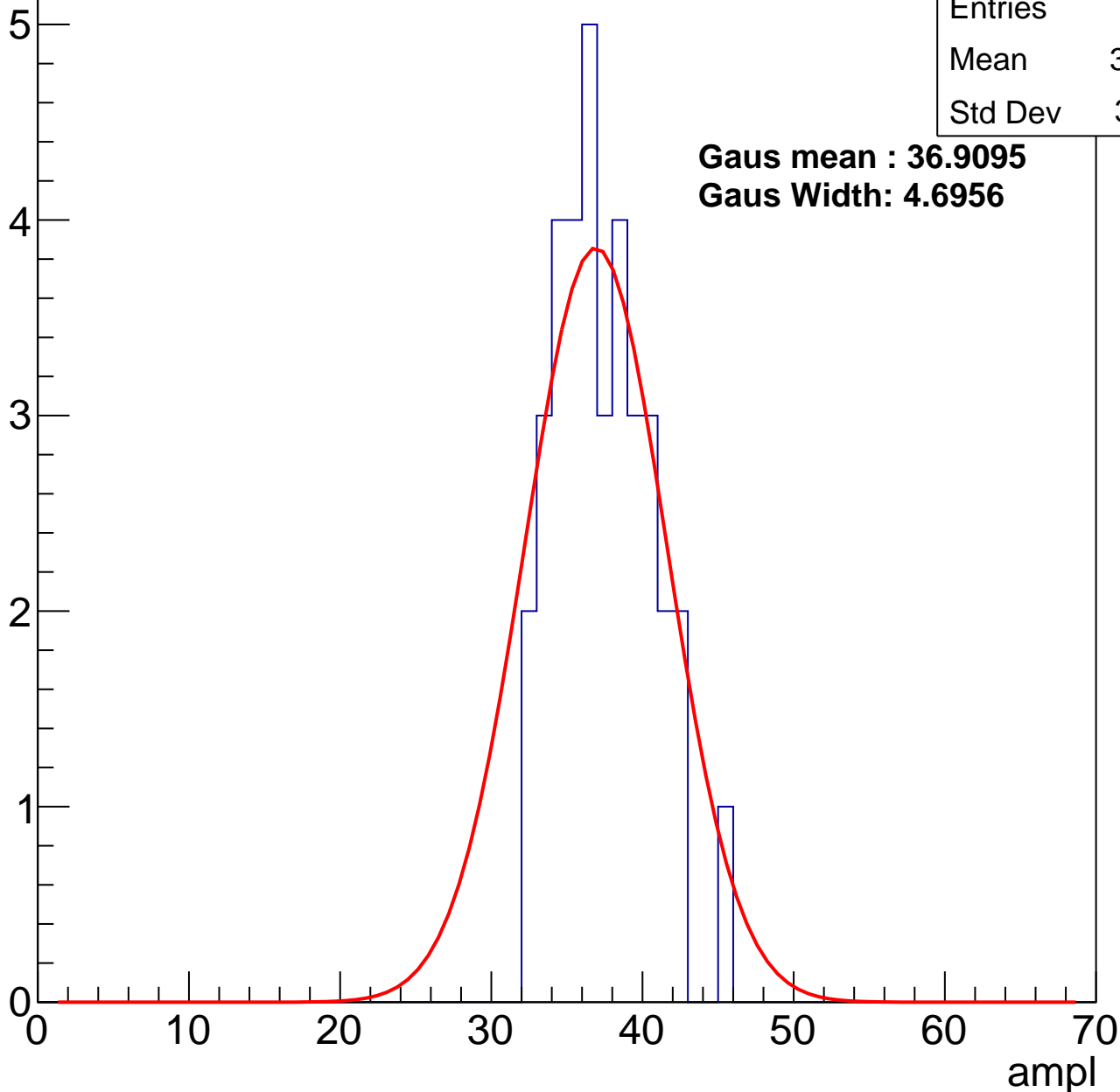
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	36.94
Std Dev	3.091

Gaus mean : 36.9095

Gaus Width: 4.6956



B1L103S, U15-ch44, adc2

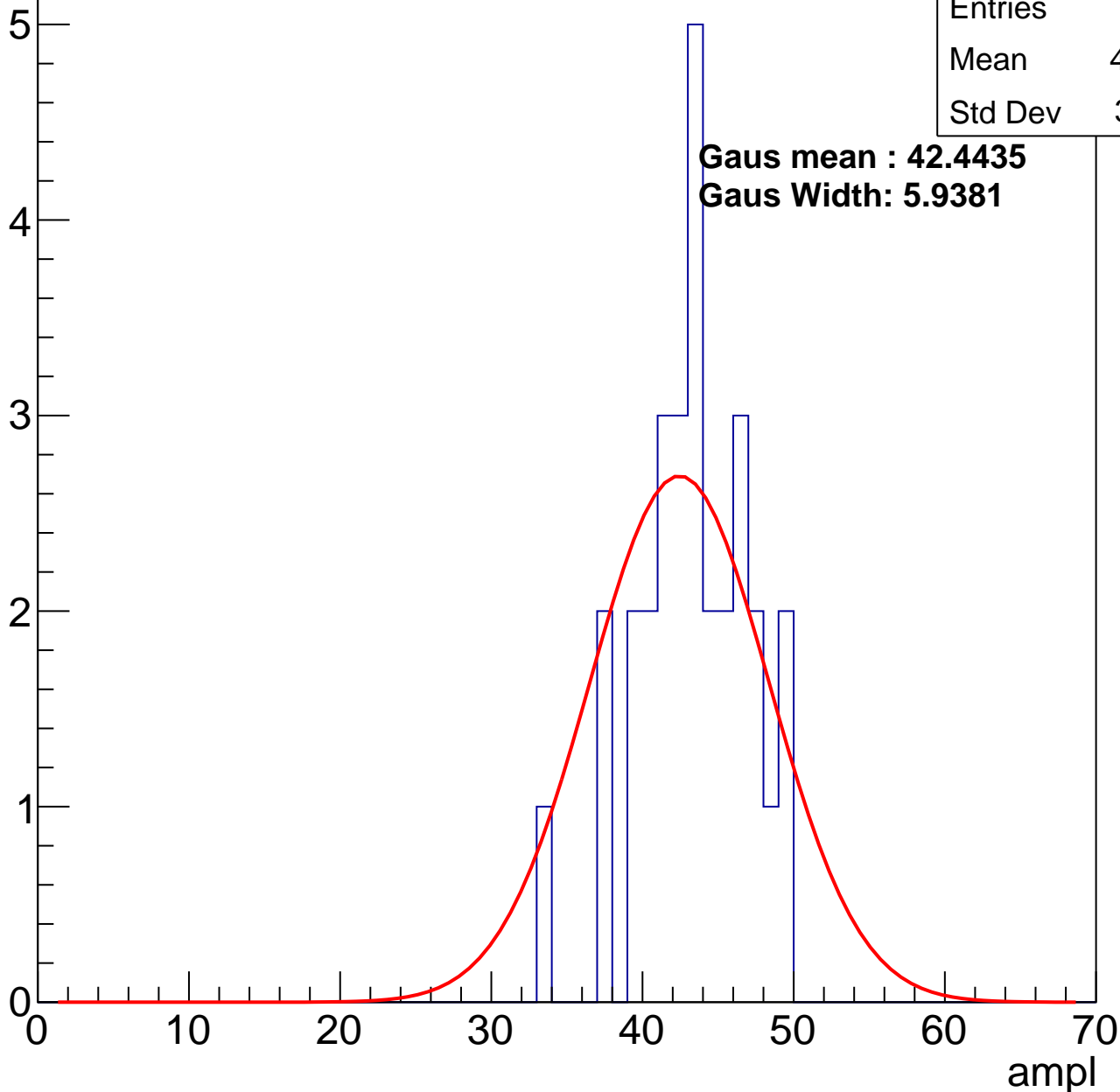
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	42.83
Std Dev	3.661

Gaus mean : 42.4435

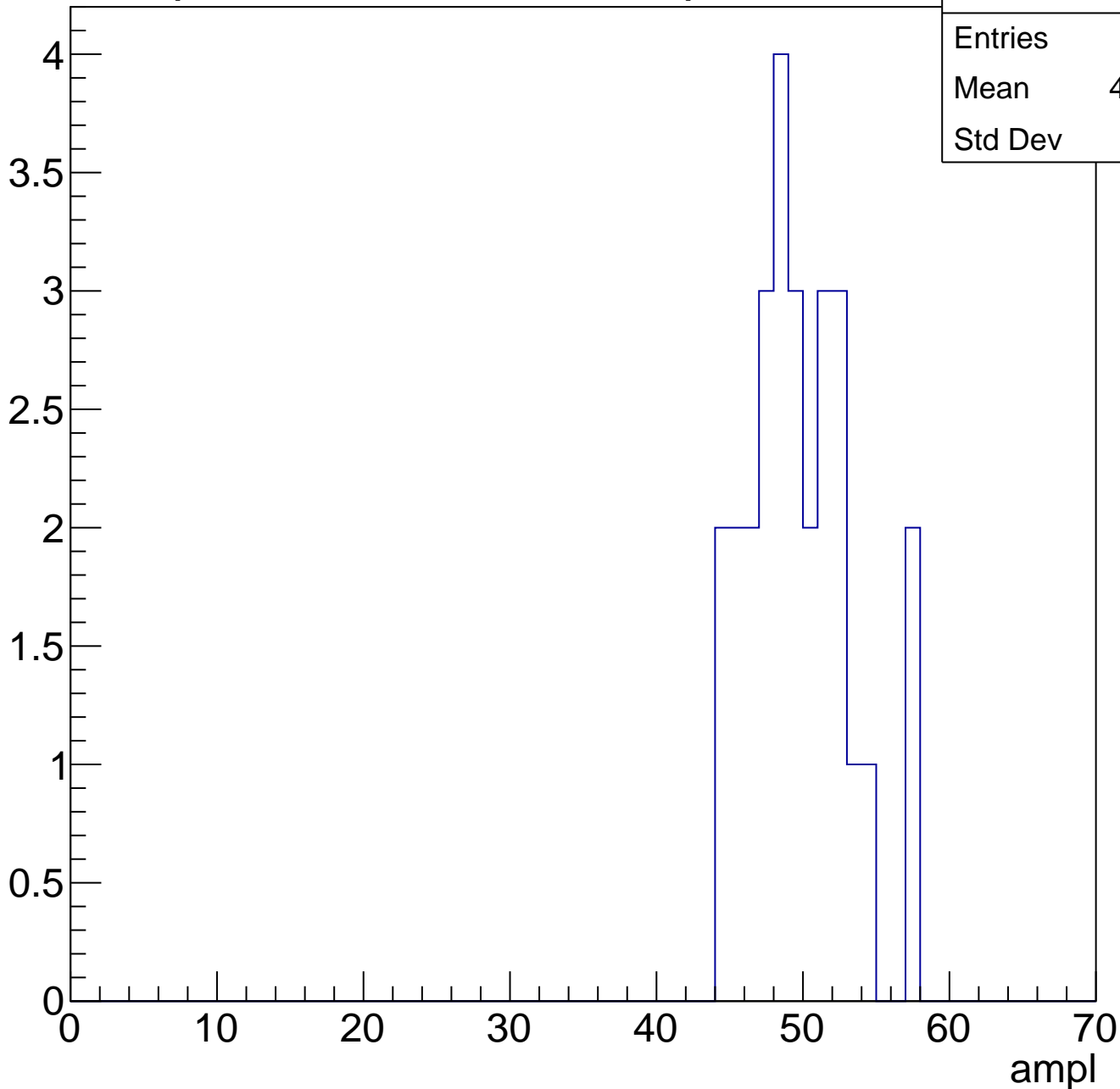
Gaus Width: 5.9381



B1L103S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

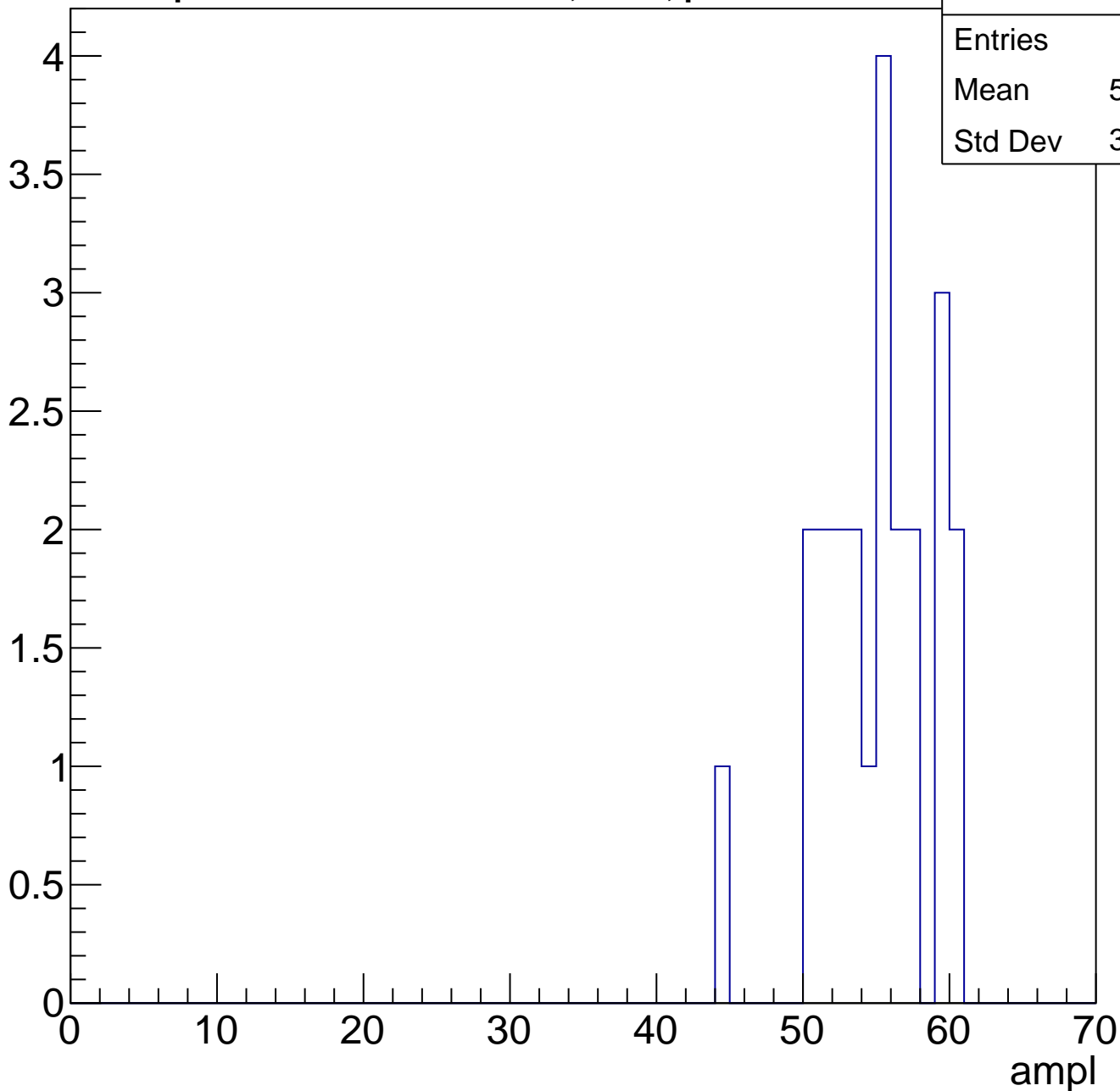
Entry



B1L103S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

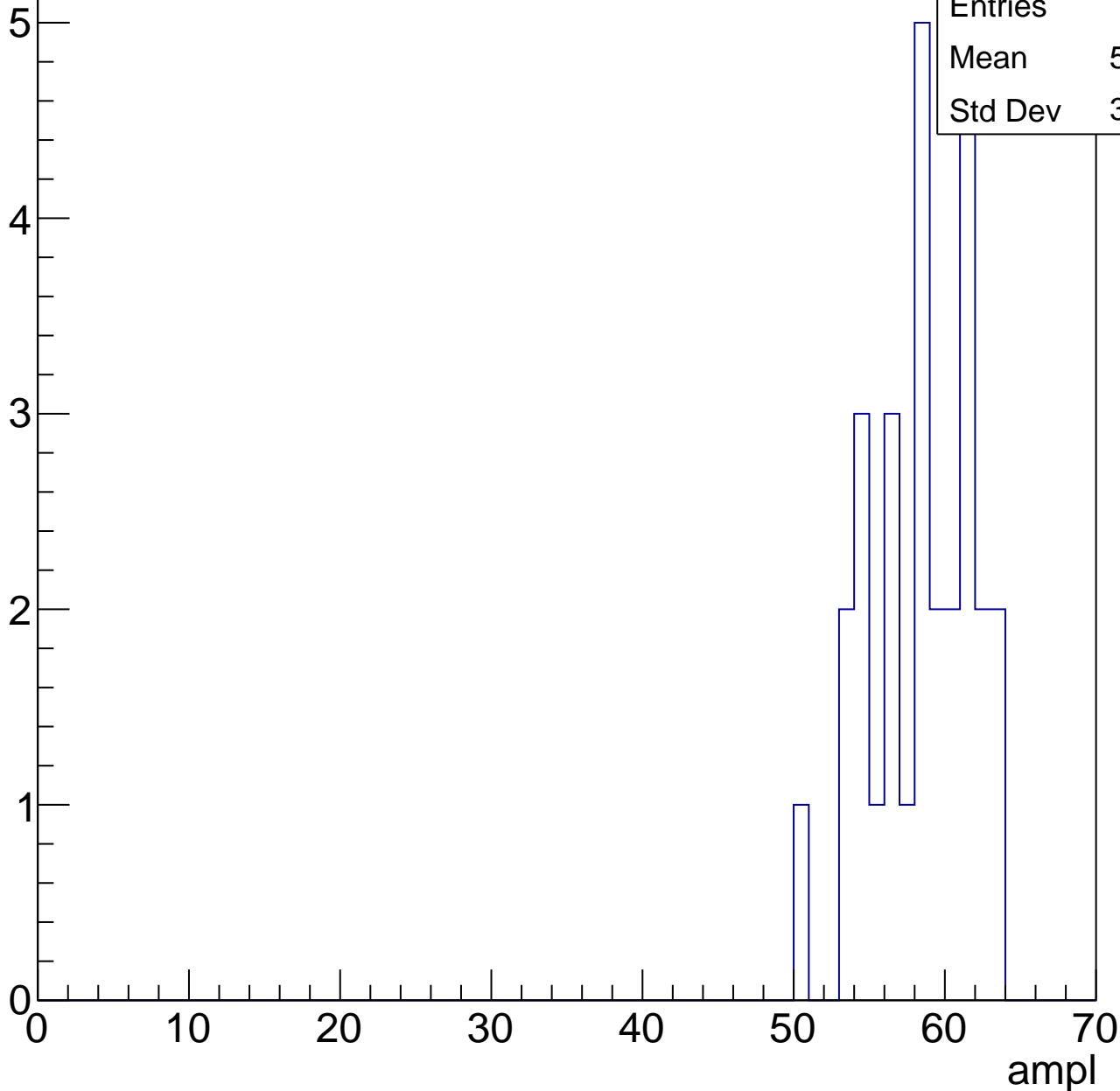


B1L103S, U15-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

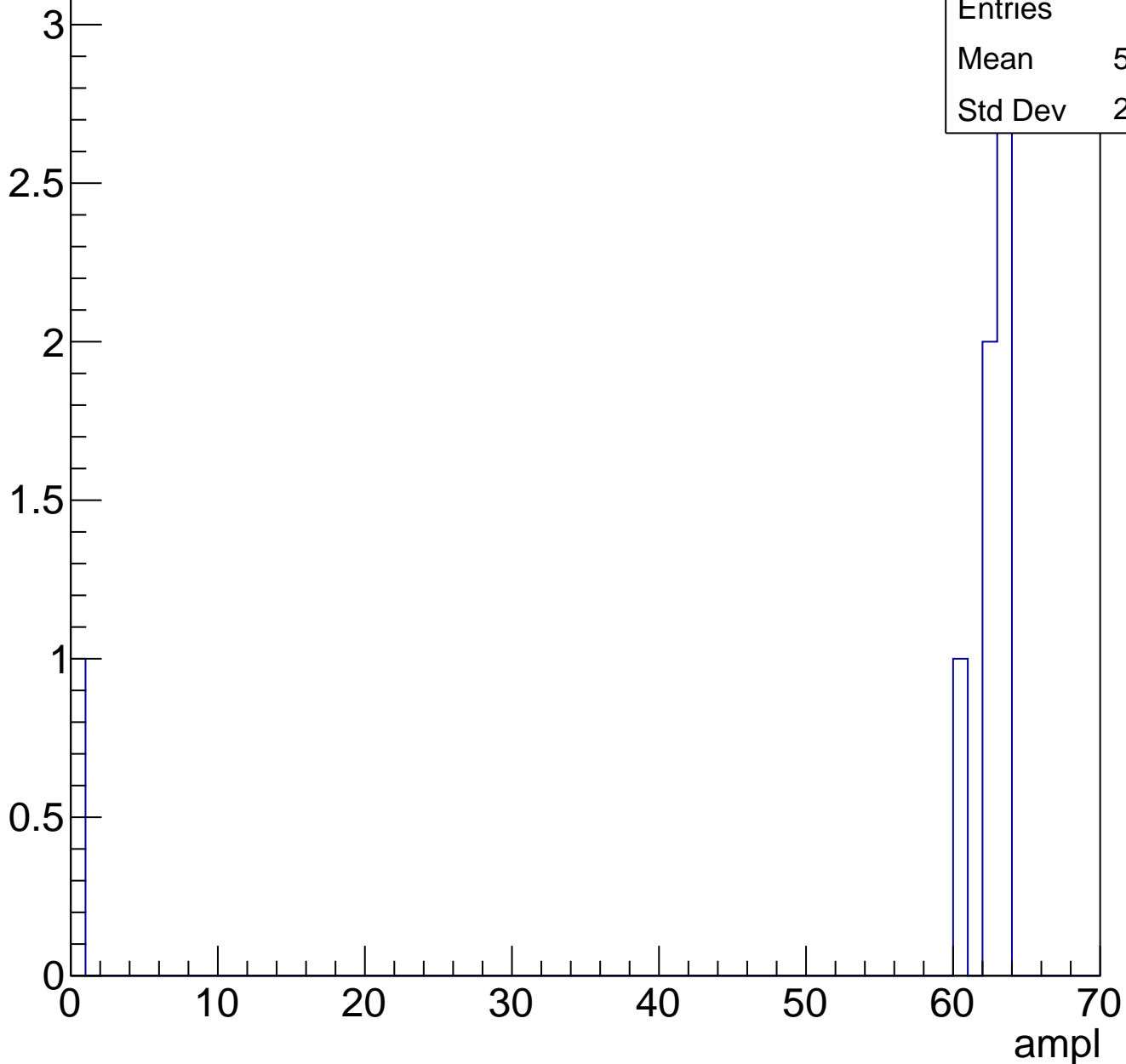
Entries	29
Mean	57.97
Std Dev	3.327



B1L103S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch45, adc0

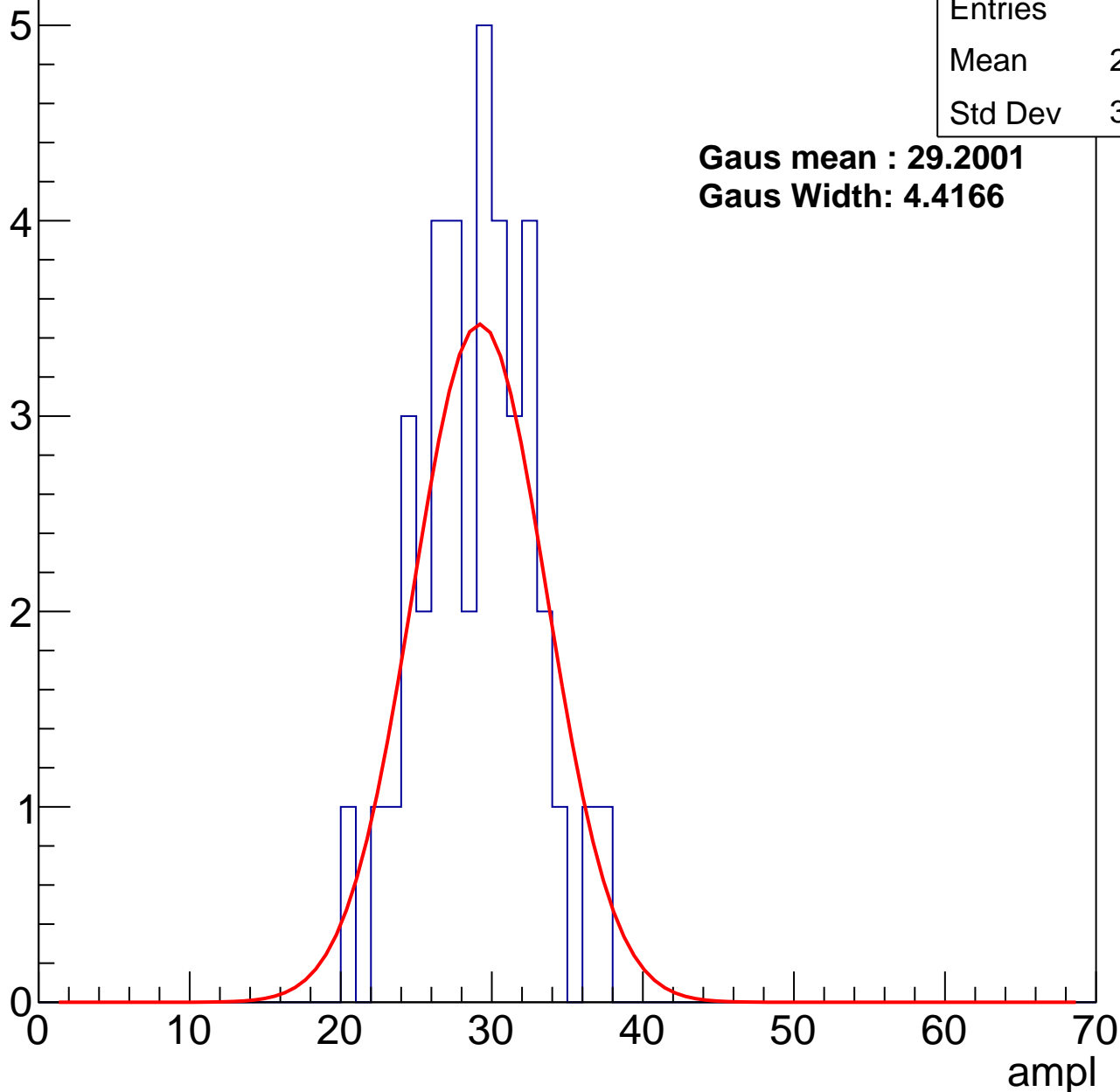
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	28.56
Std Dev	3.727

Gaus mean : 29.2001

Gaus Width: 4.4166



B1L103S, U15-ch45, adc1

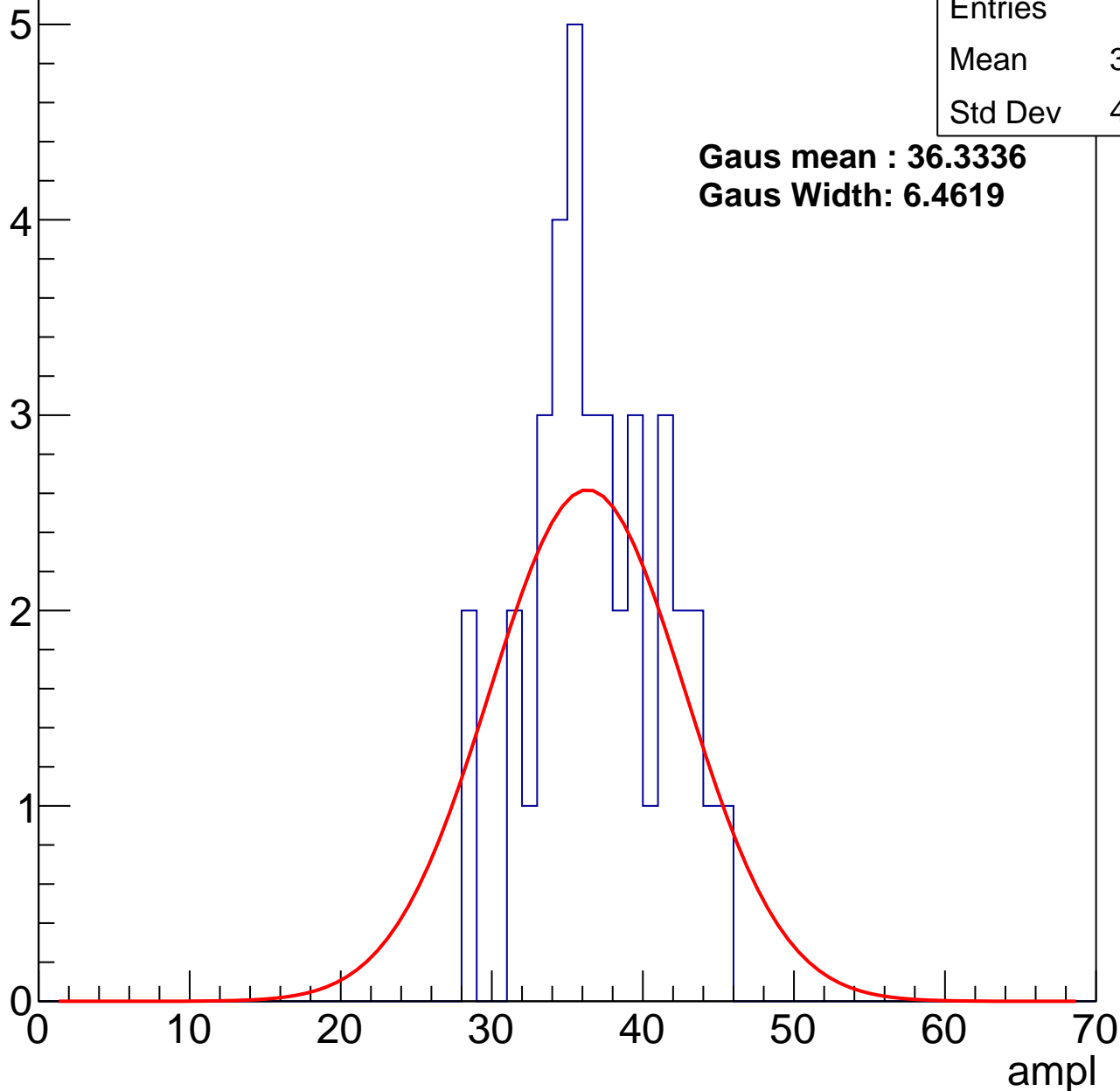
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	36.68
Std Dev	4.193

Gaus mean : 36.3336

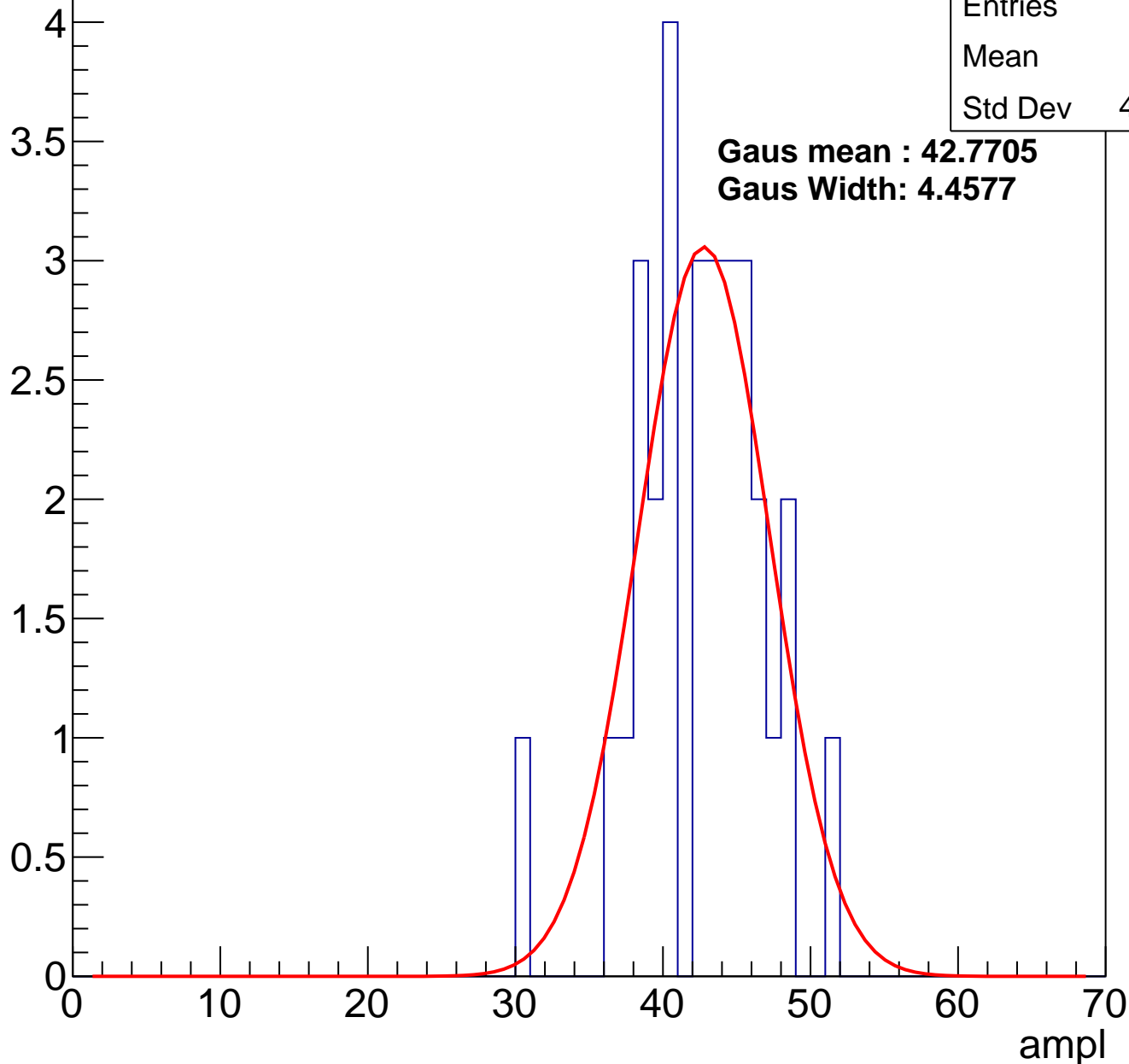
Gaus Width: 6.4619



B1L103S, U15-ch45, adc2

calib_packv5_042523_0143.root, FC#7, port C2

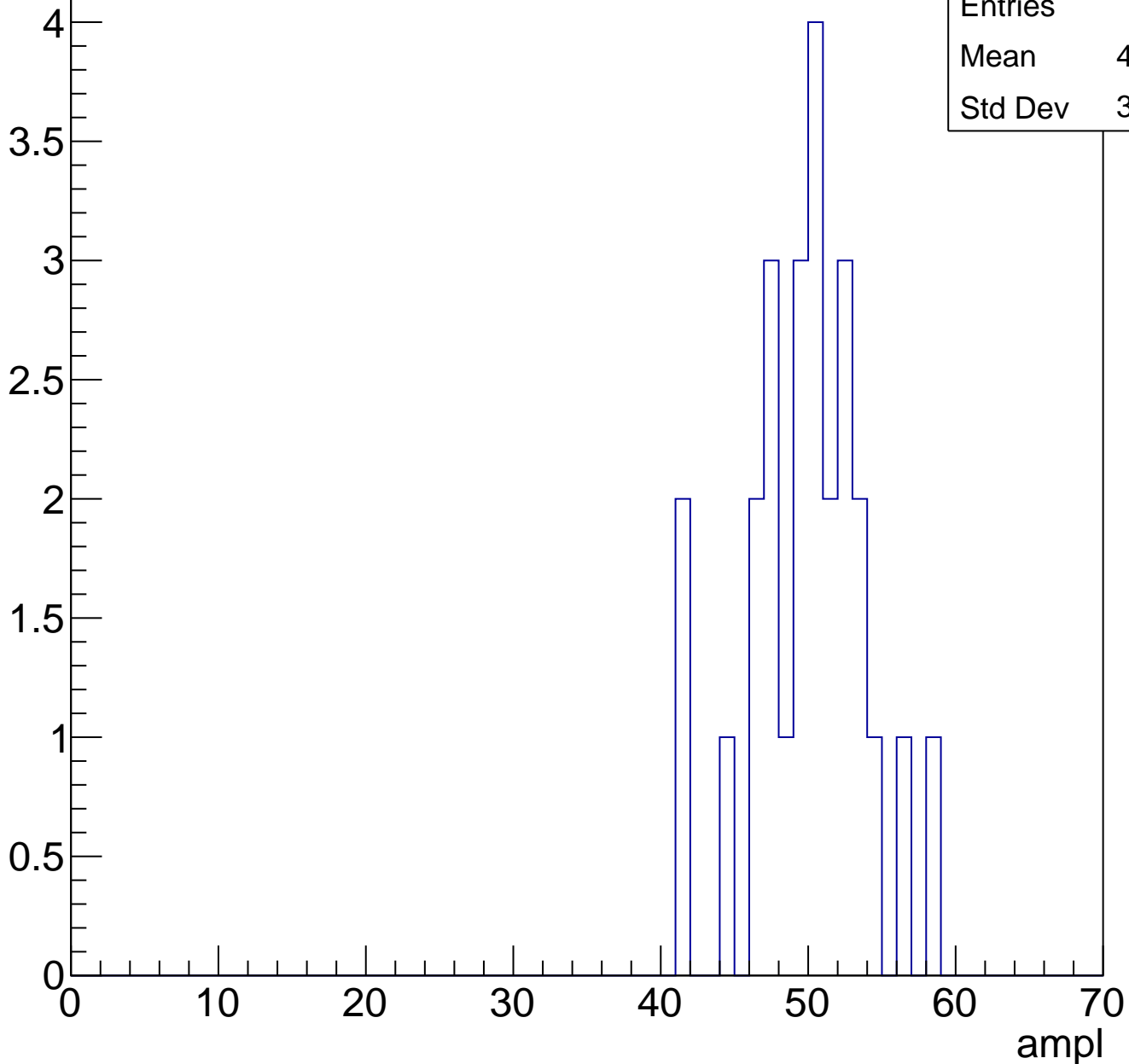
Entry



B1L103S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

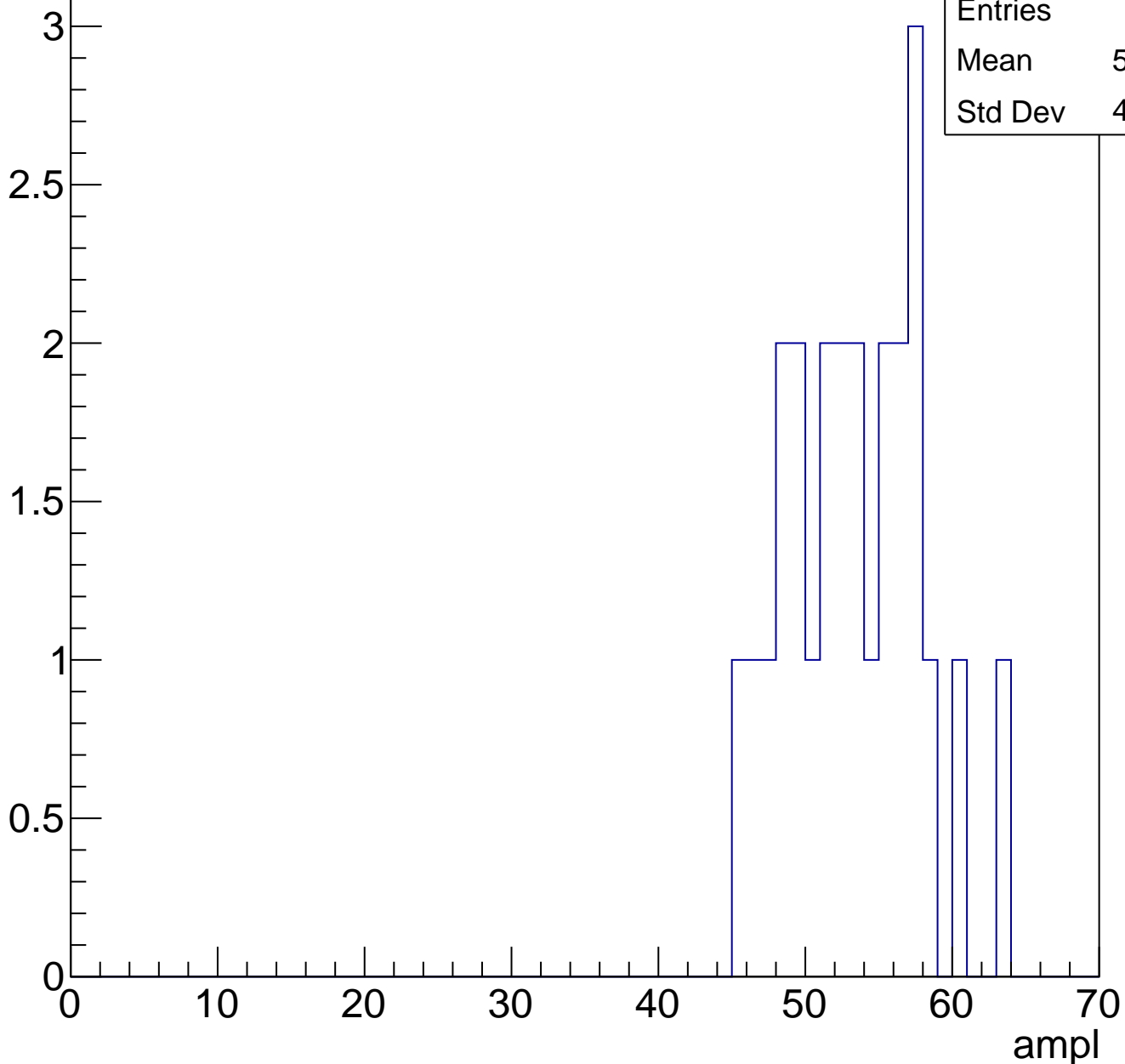
Entry



B1L103S, U15-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

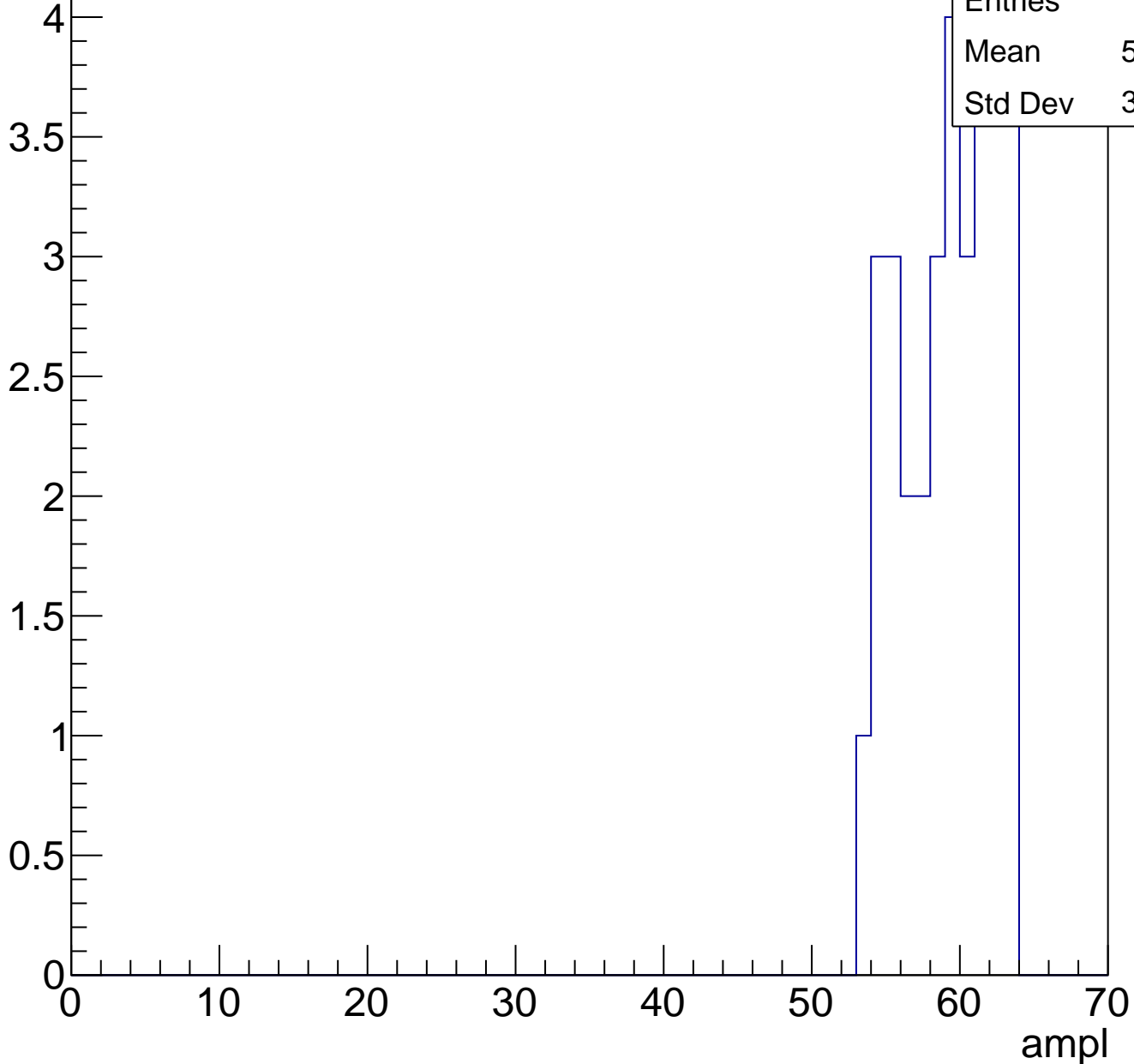
Entry



B1L103S, U15-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

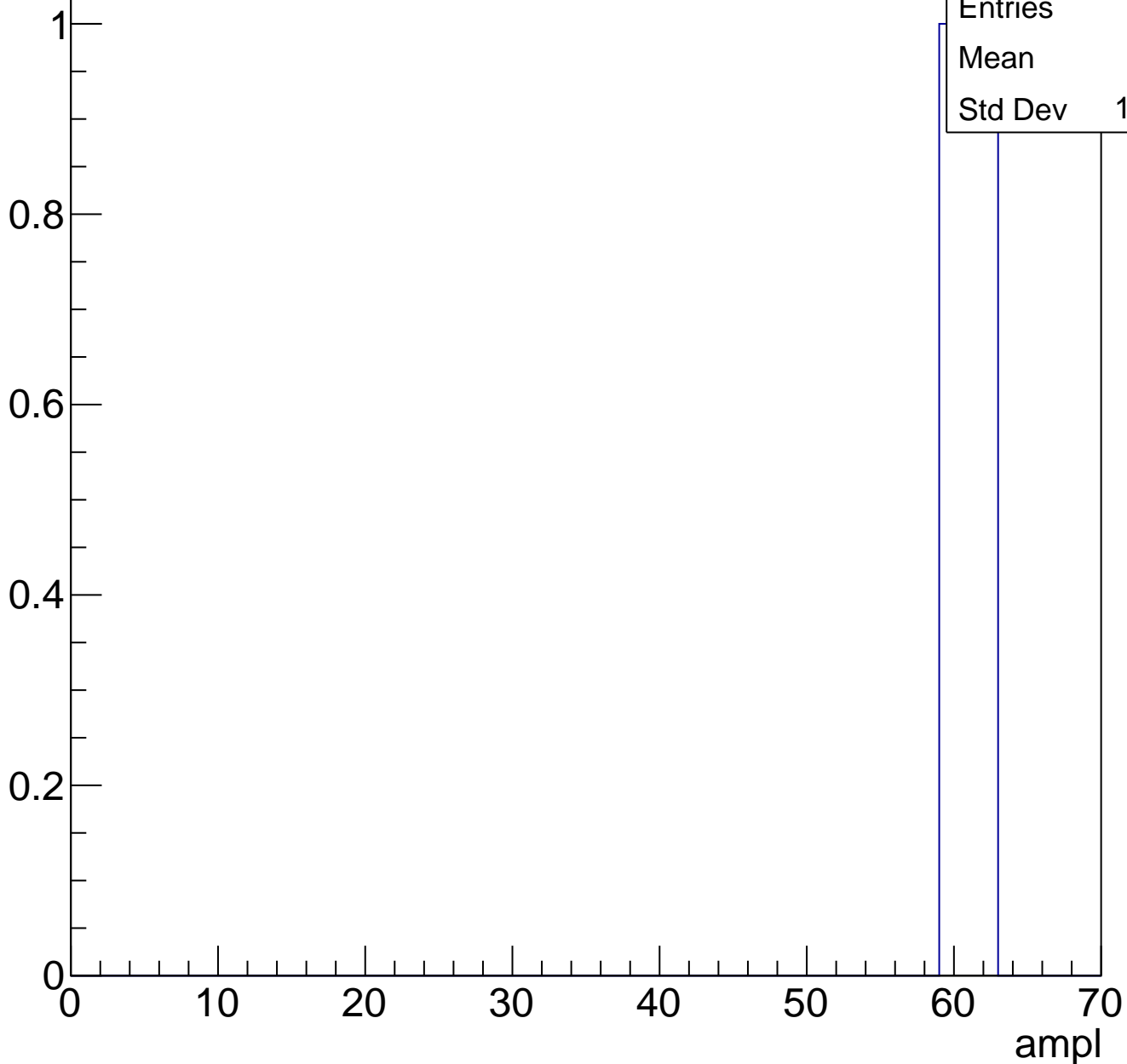


Entries	33
Mean	58.79
Std Dev	3.043

B1L103S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L103S, U15-ch46, adc0

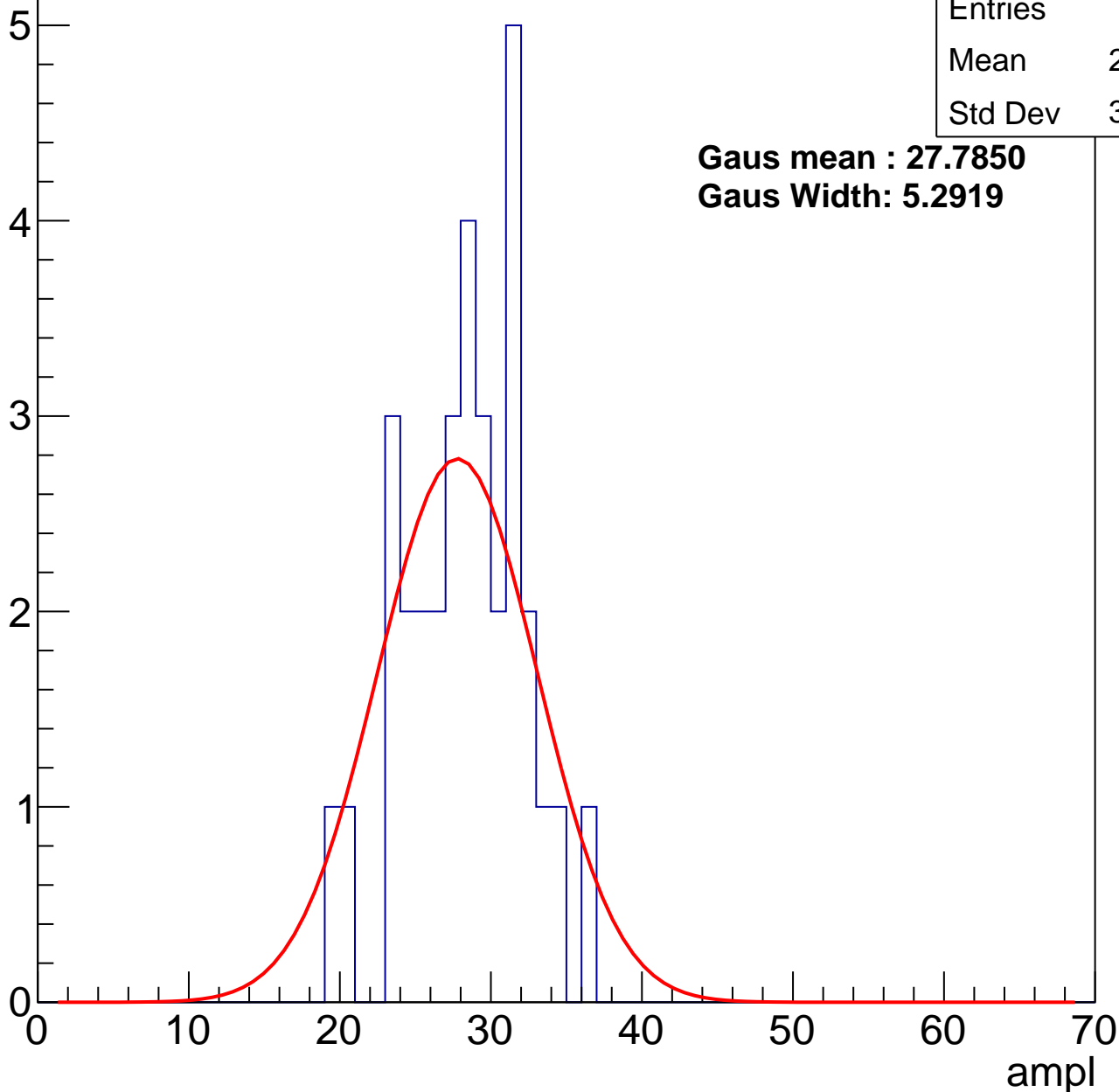
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	27.88
Std Dev	3.867

Gaus mean : 27.7850

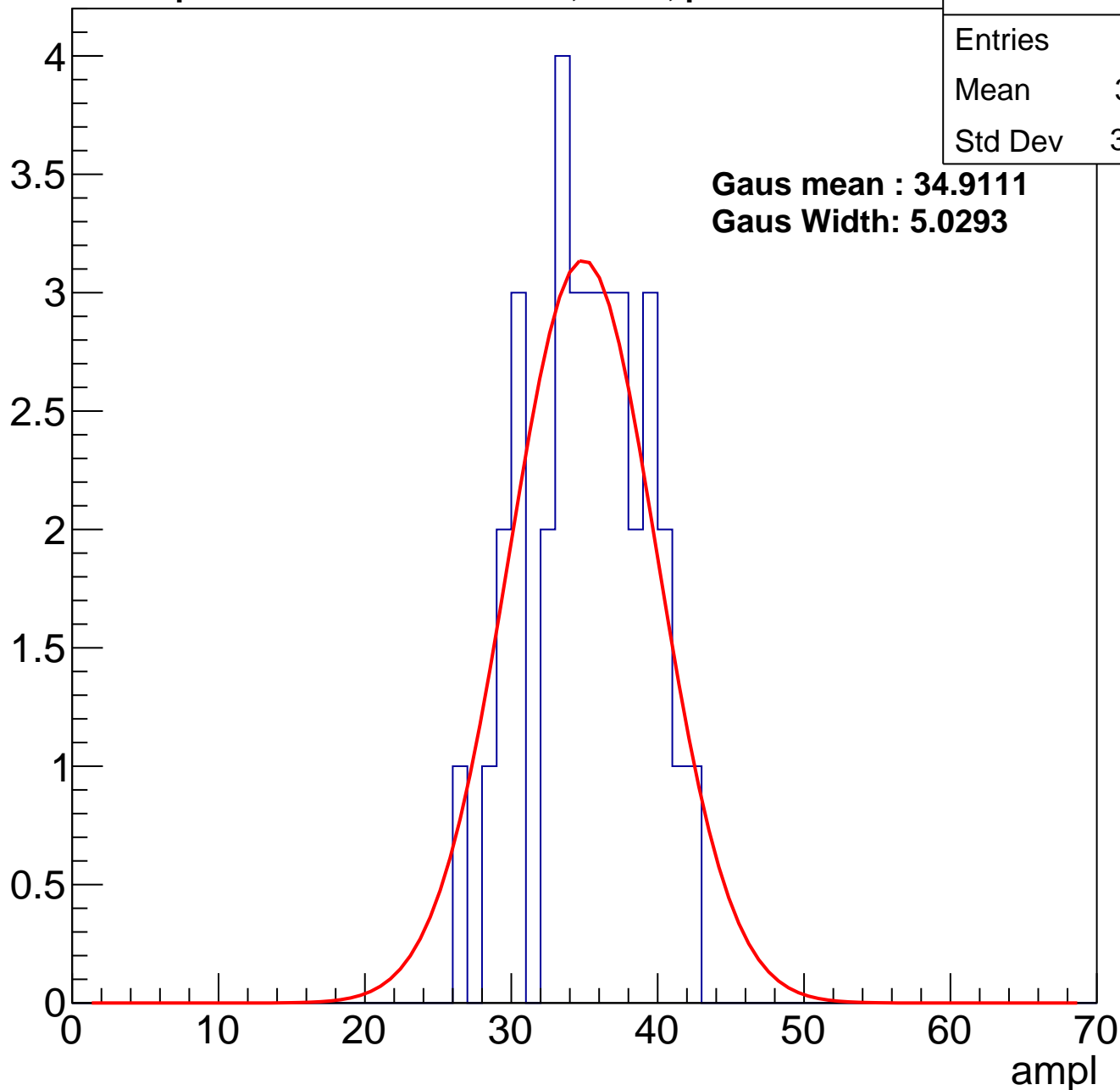
Gaus Width: 5.2919



B1L103S, U15-ch46, adc1

calib_packv5_042523_0143.root, FC#7, port C2

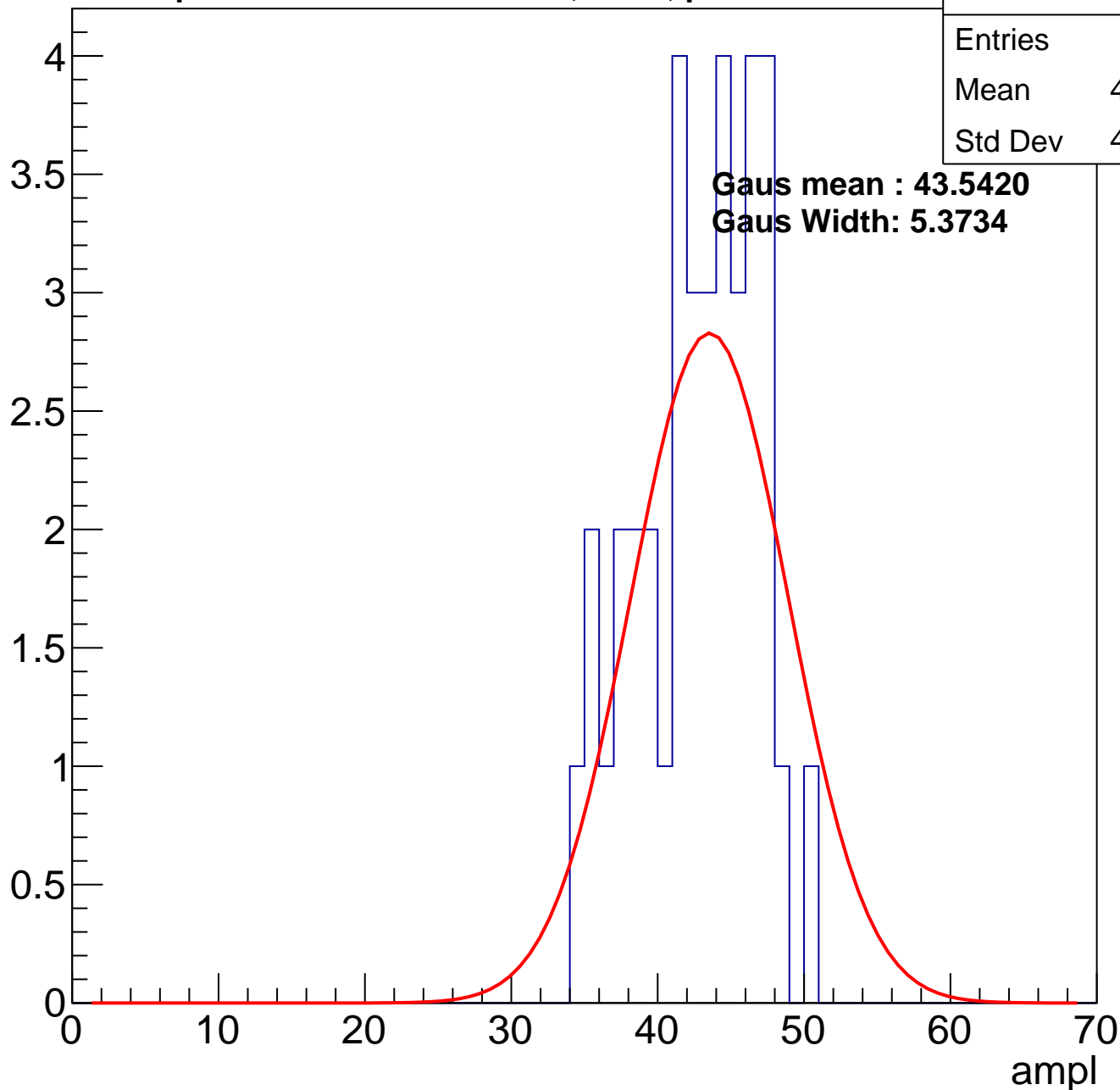
Entry



B1L103S, U15-ch46, adc2

calib_packv5_042523_0143.root, FC#7, port C2

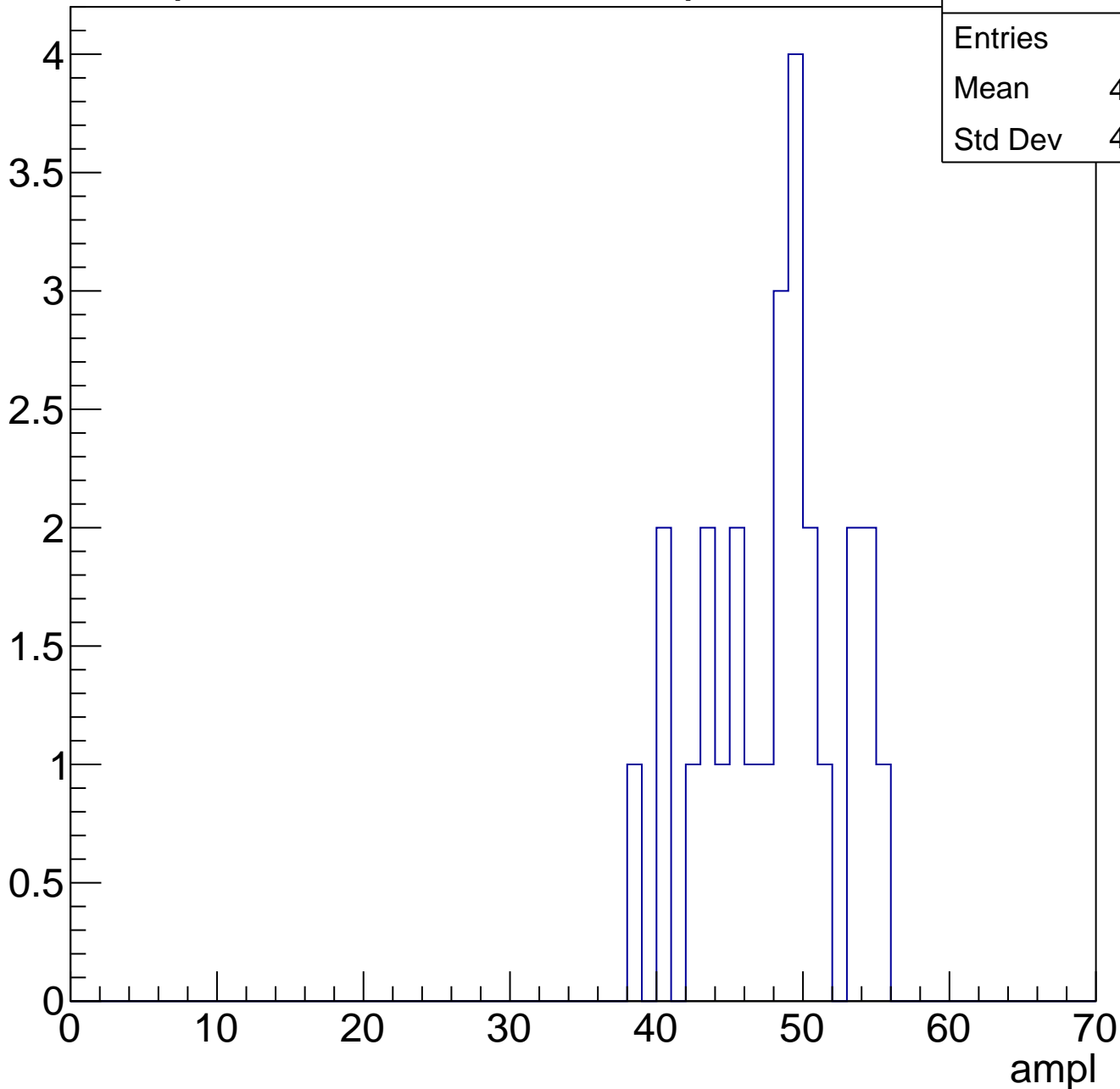
Entry



B1L103S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

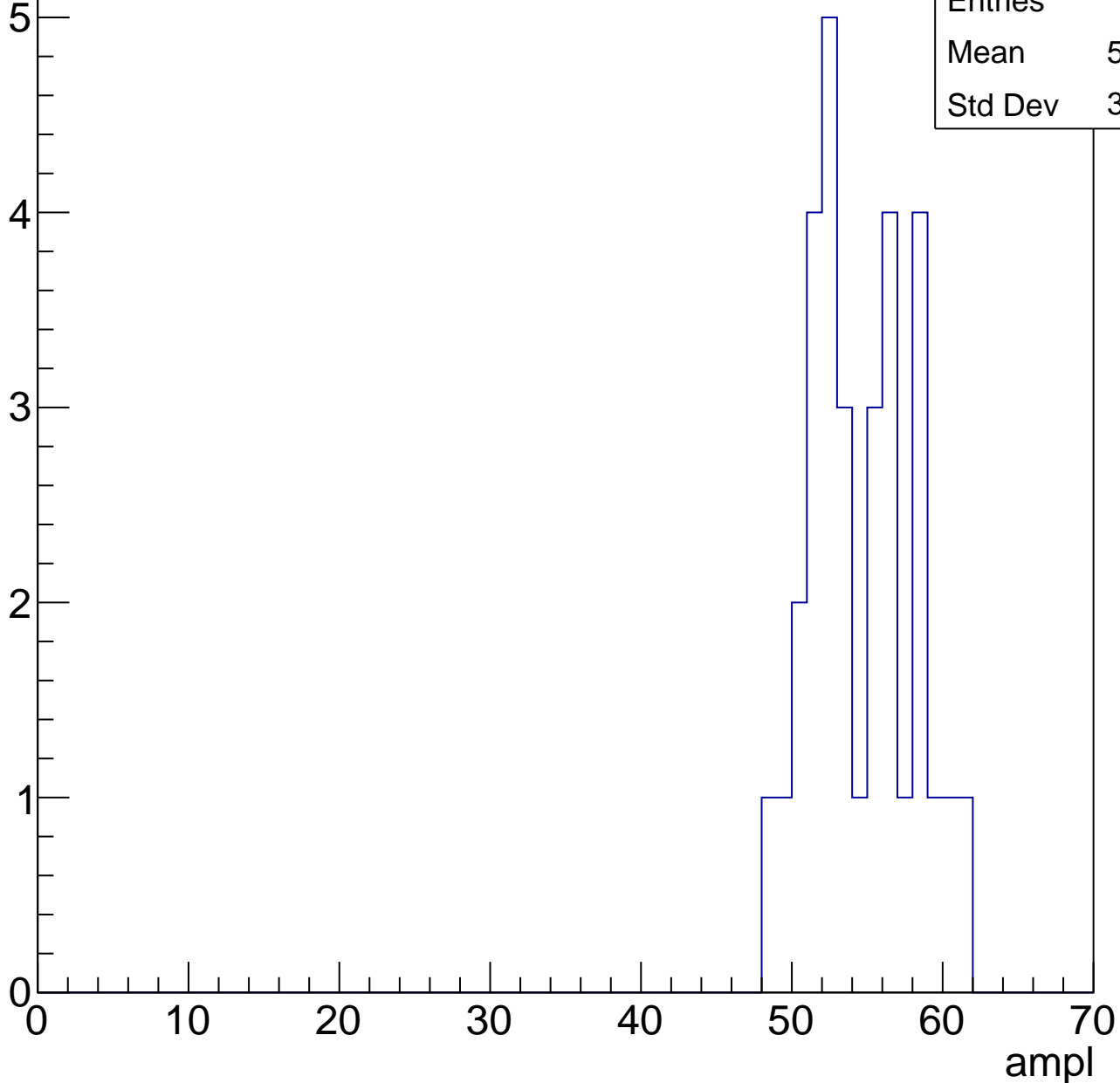


B1L103S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	54.12
Std Dev	3.324

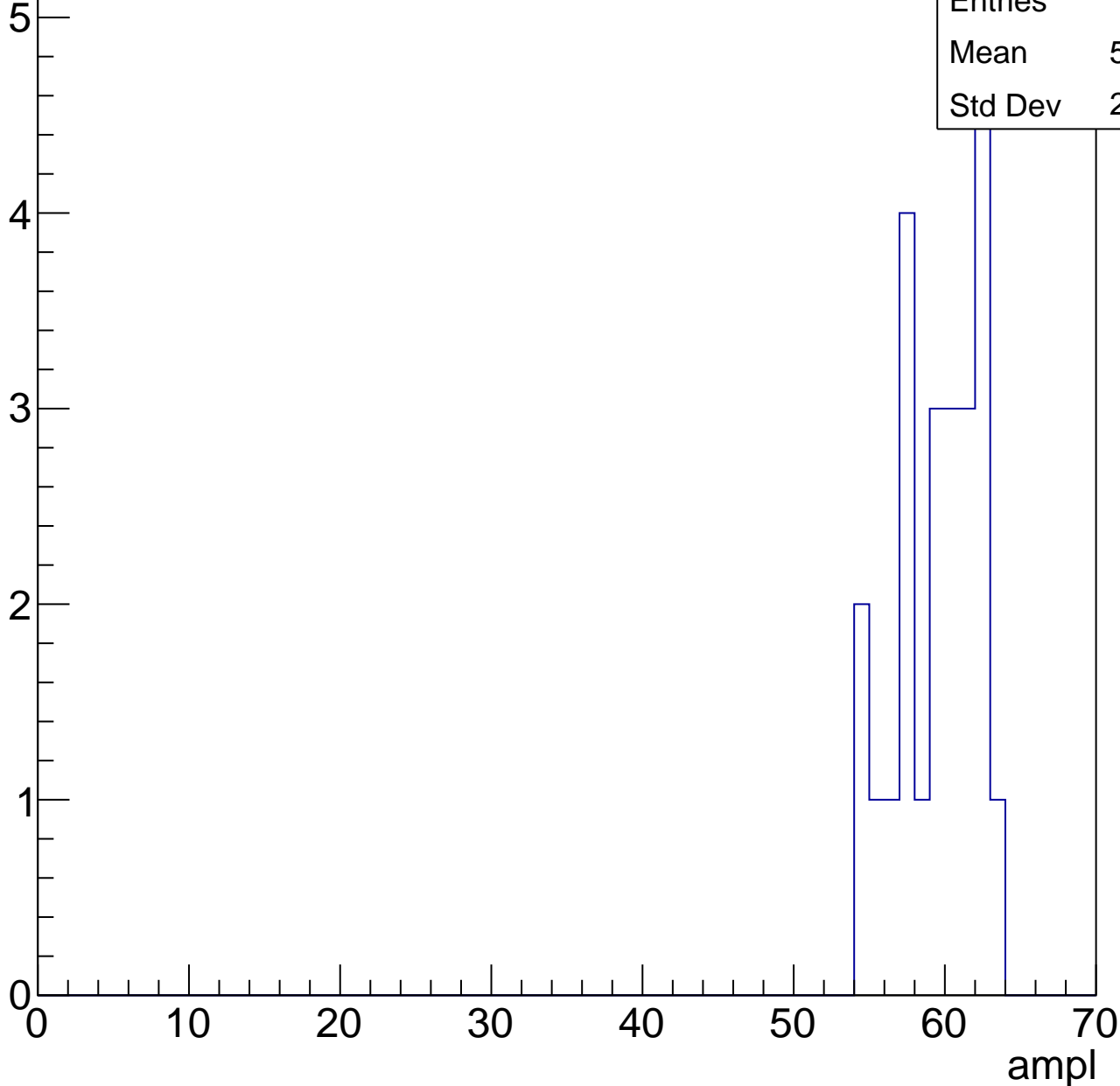


B1L103S, U15-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

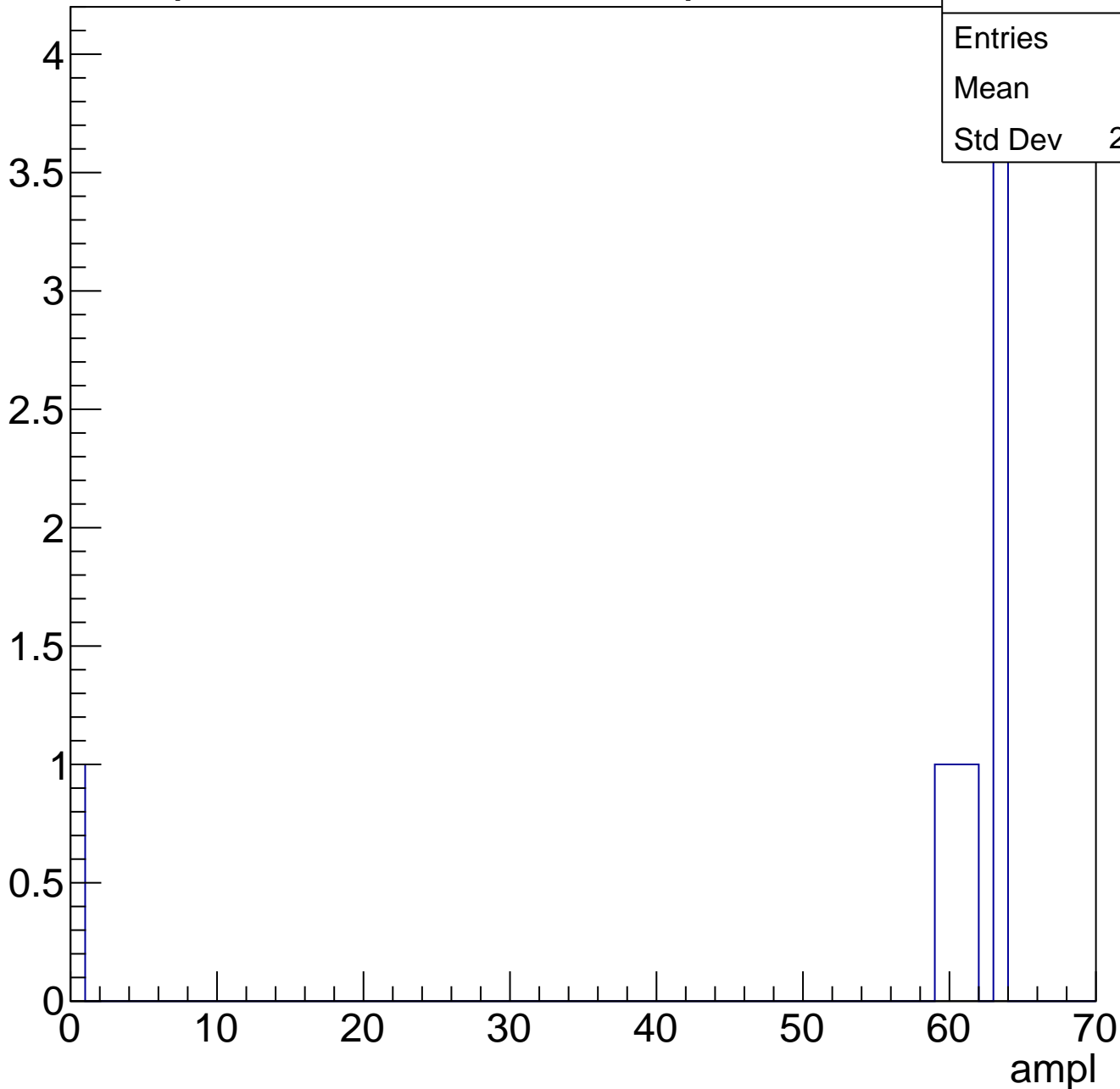
Entries	24
Mean	59.08
Std Dev	2.644



B1L103S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch47, adc0

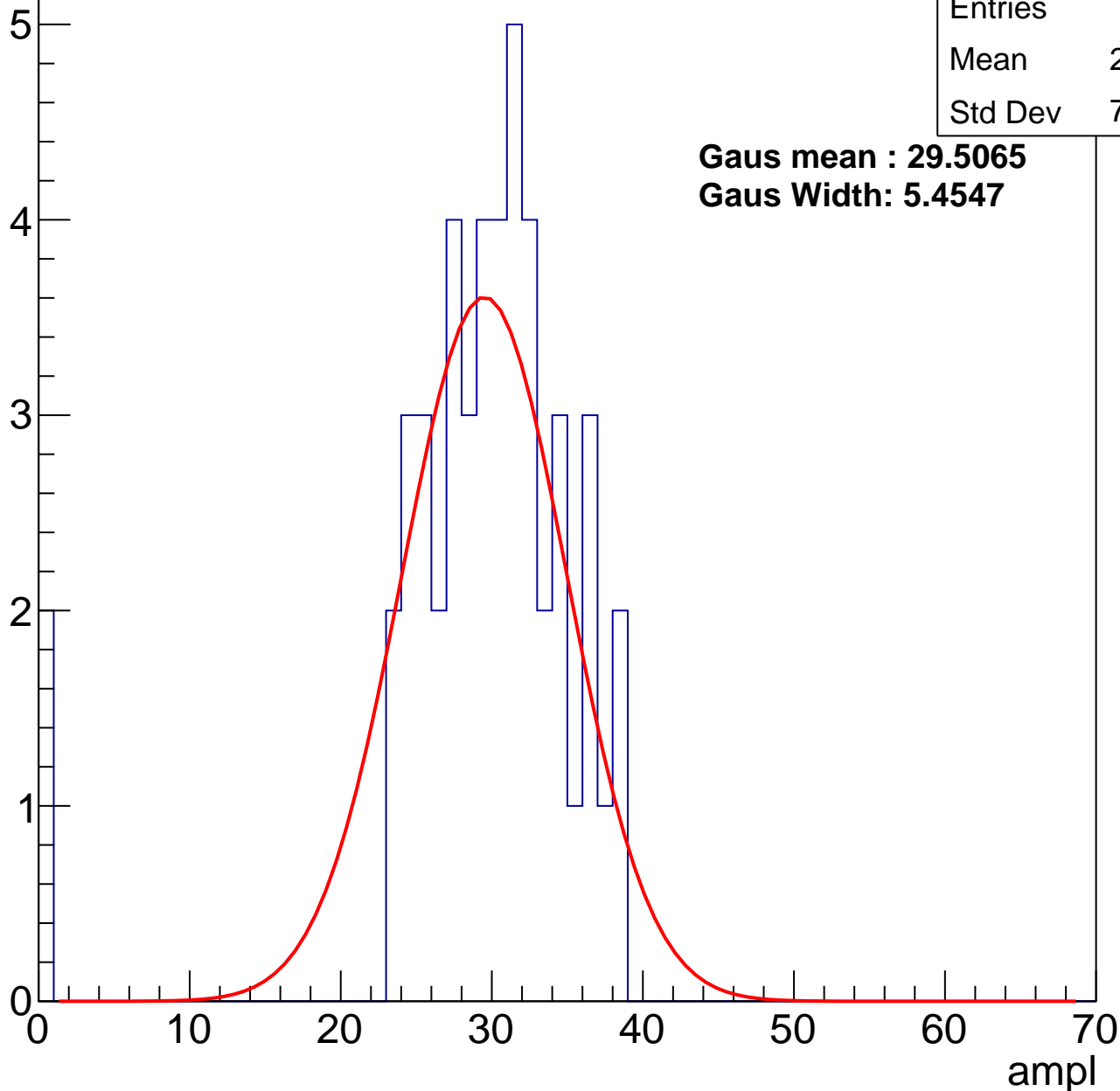
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	28.75
Std Dev	7.198

Gaus mean : 29.5065

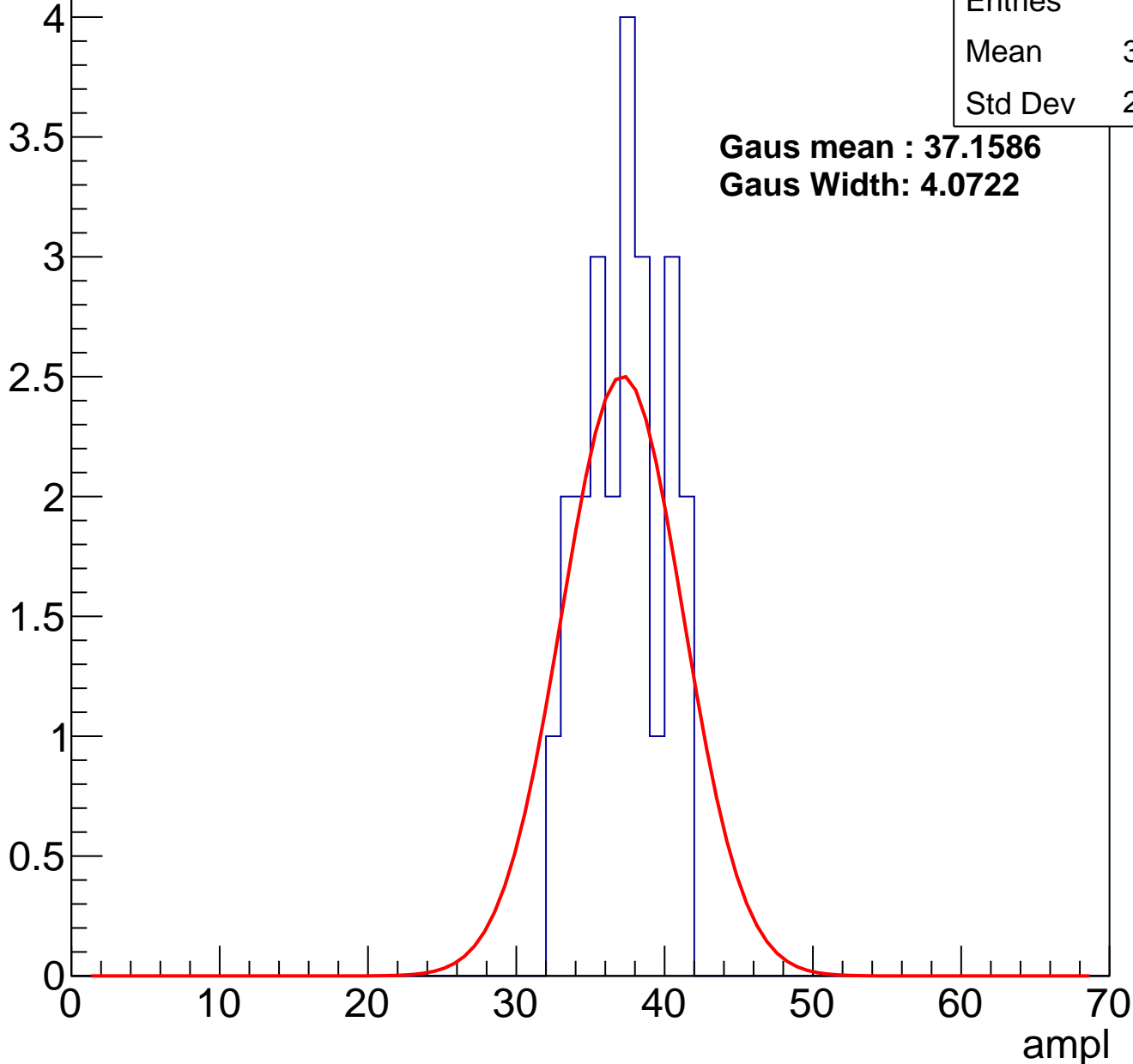
Gaus Width: 5.4547



B1L103S, U15-ch47, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch47, adc2

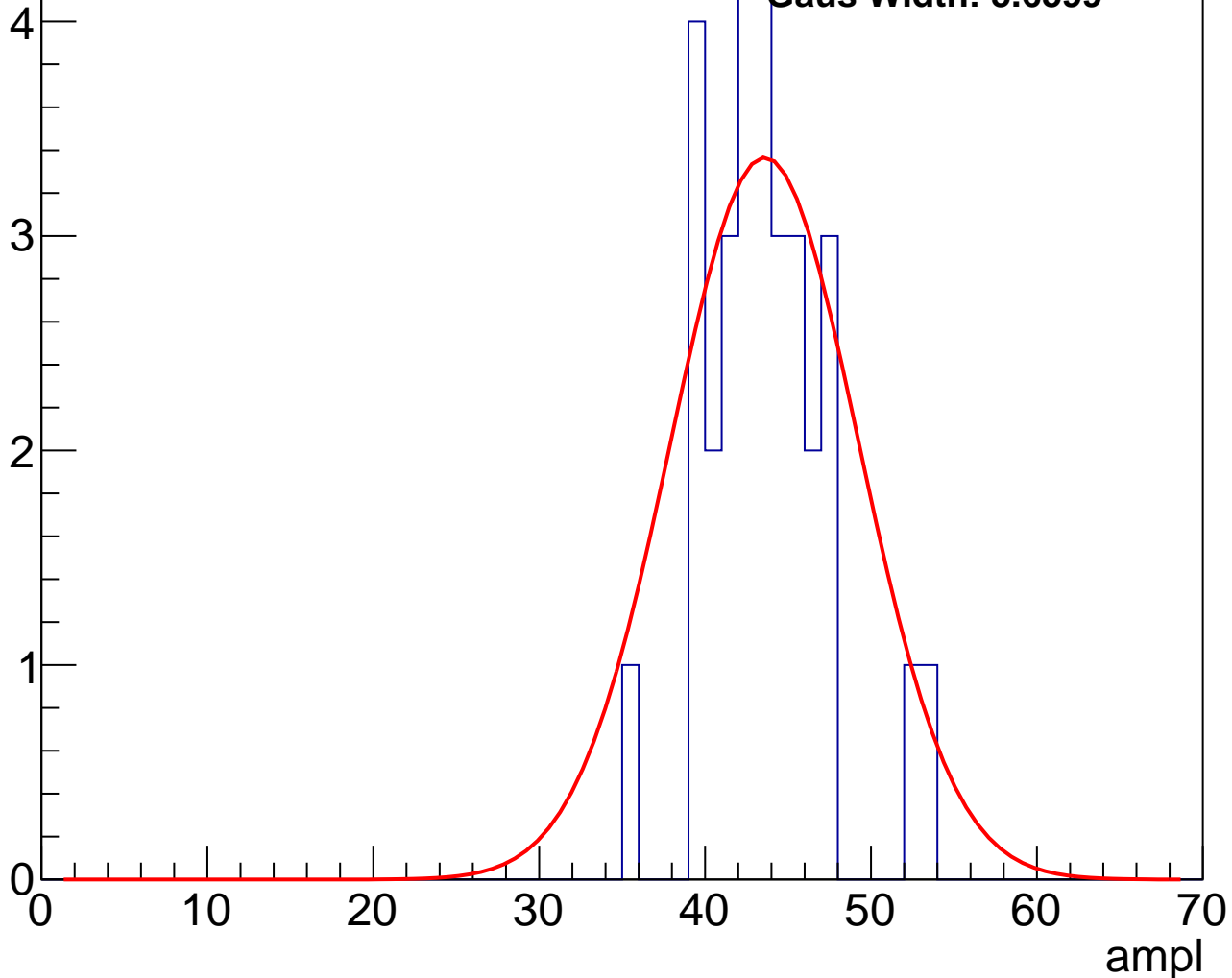
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	43.15
Std Dev	3.586

Gaus mean : 43.5898

Gaus Width: 5.6599

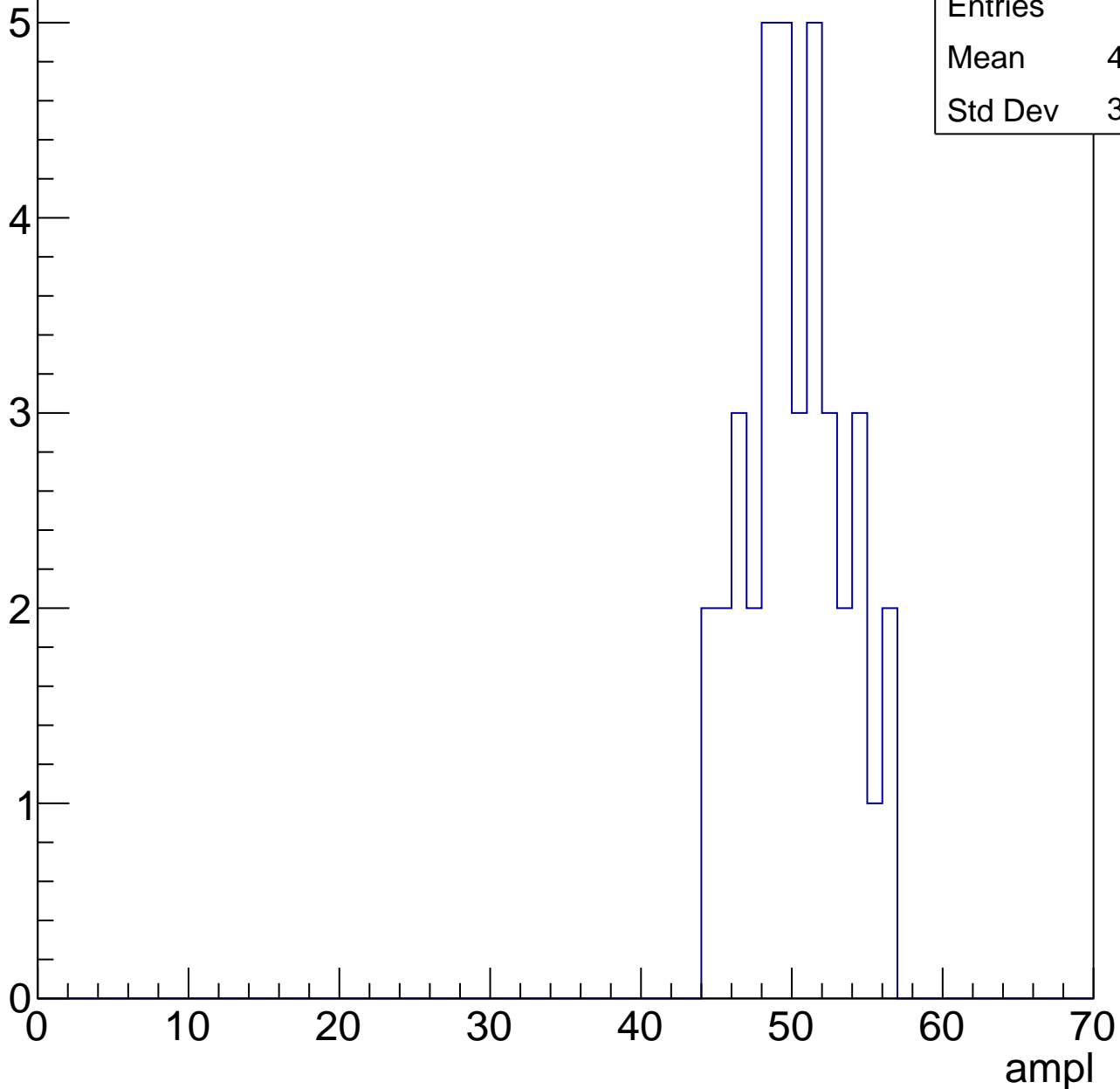


B1L103S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

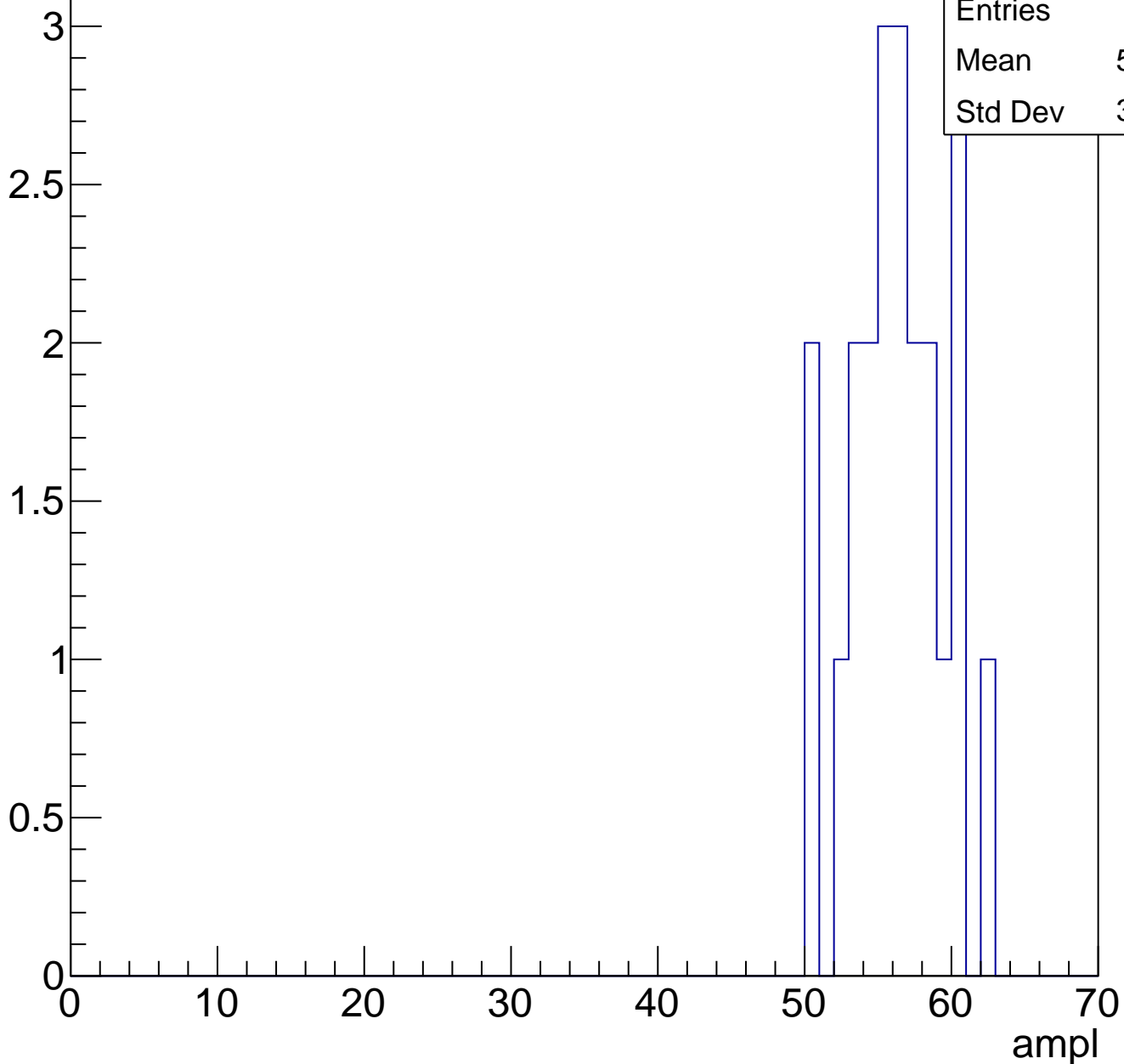
Entries	38
Mean	49.76
Std Dev	3.207



B1L103S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

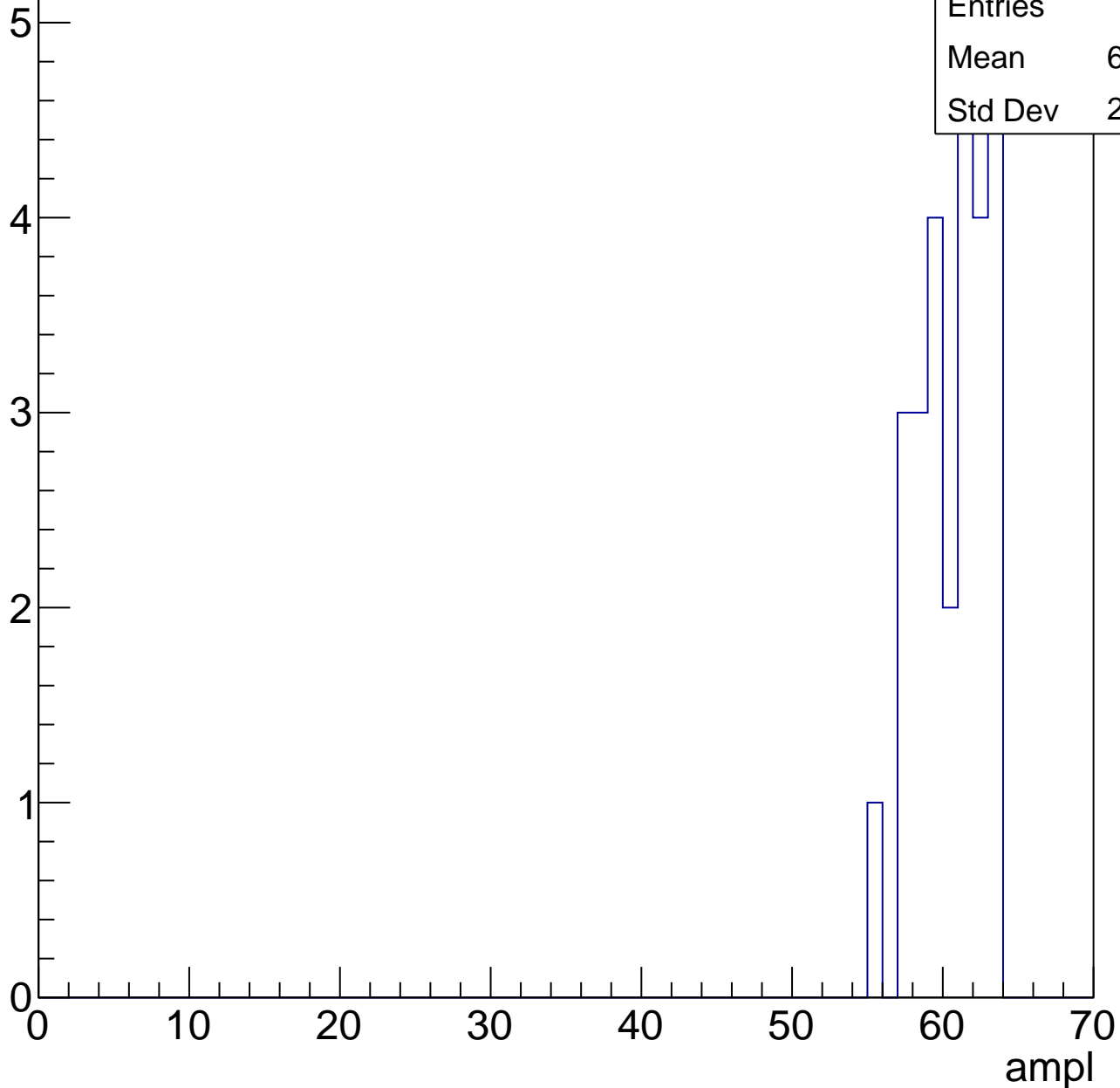


B1L103S, U15-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	60.15
Std Dev	2.223



B1L103S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch48, adc0

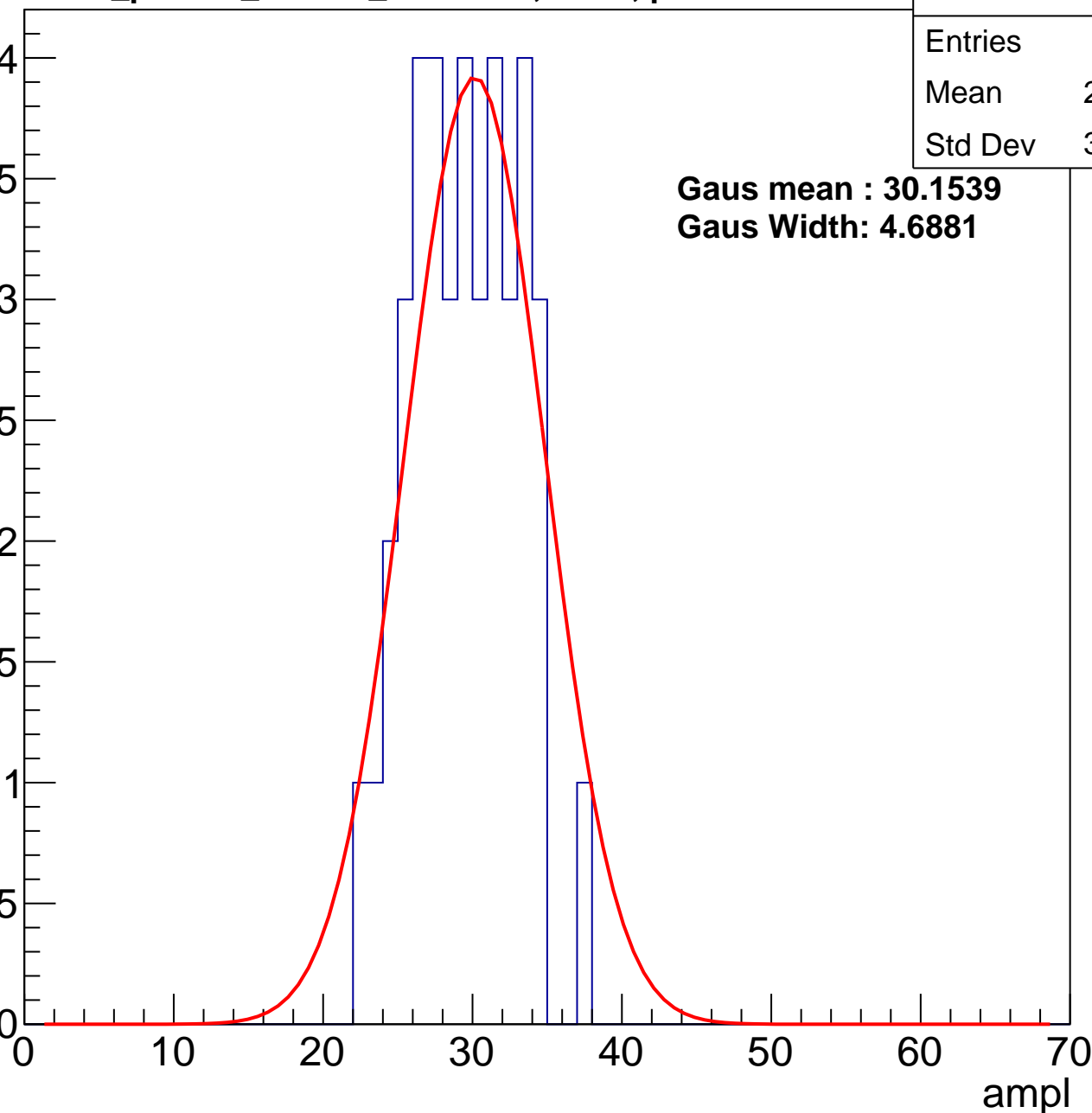
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	40
Mean	29.02
Std Dev	3.489

Gaus mean : 30.1539
Gaus Width: 4.6881



B1L103S, U15-ch48, adc1

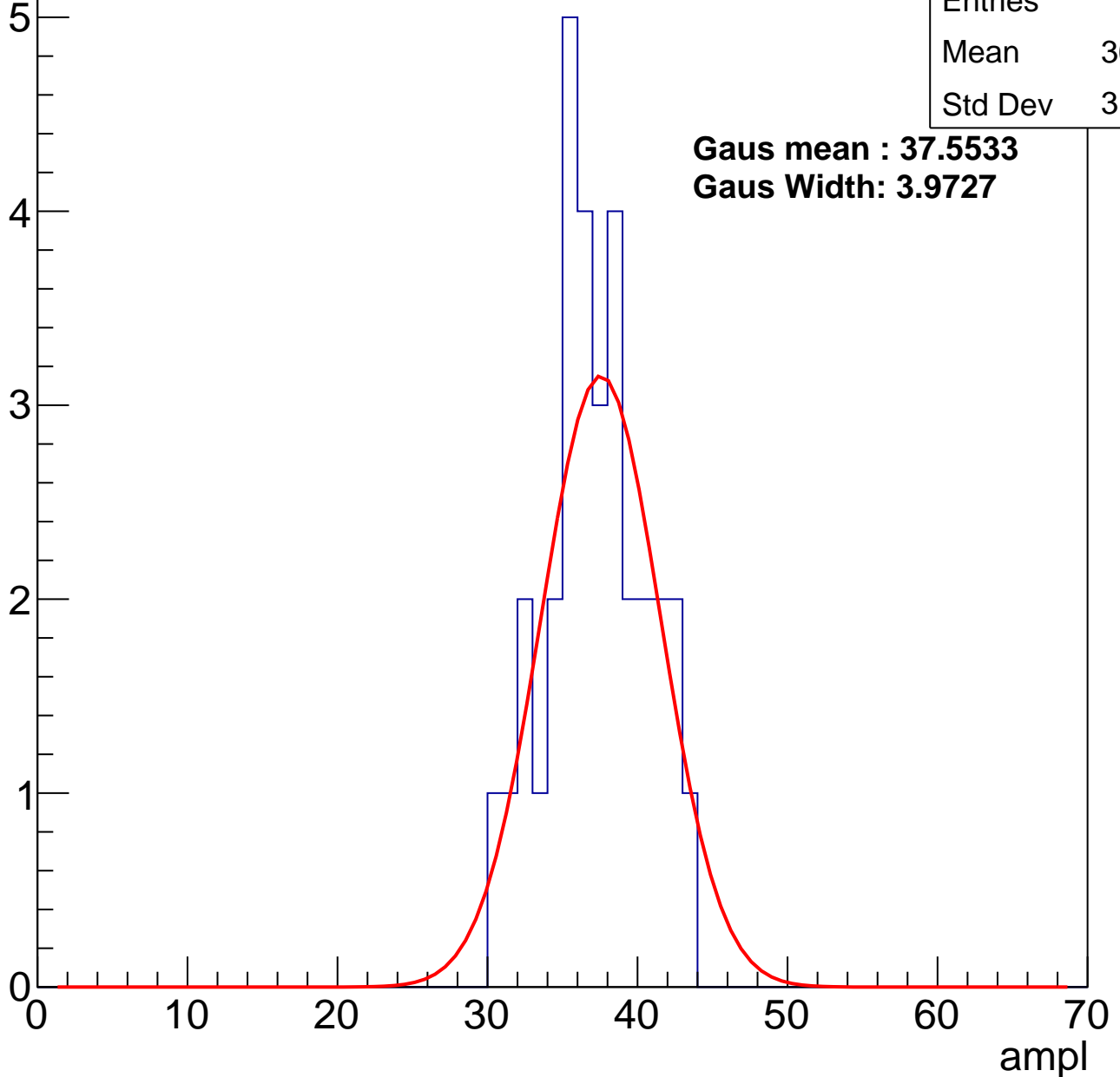
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	36.72
Std Dev	3.252

Gaus mean : 37.5533

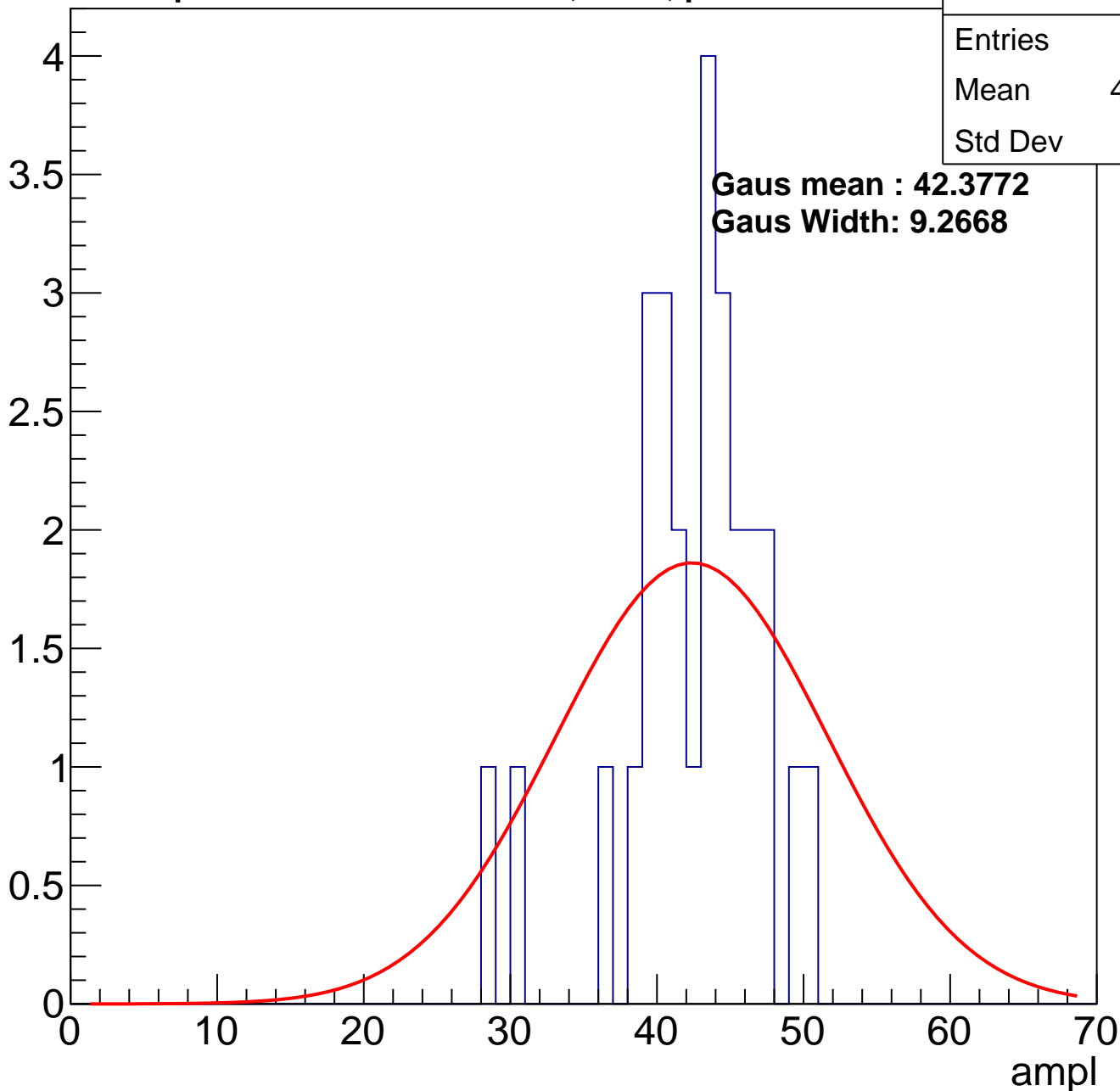
Gaus Width: 3.9727



B1L103S, U15-ch48, adc2

calib_packv5_042523_0143.root, FC#7, port C2

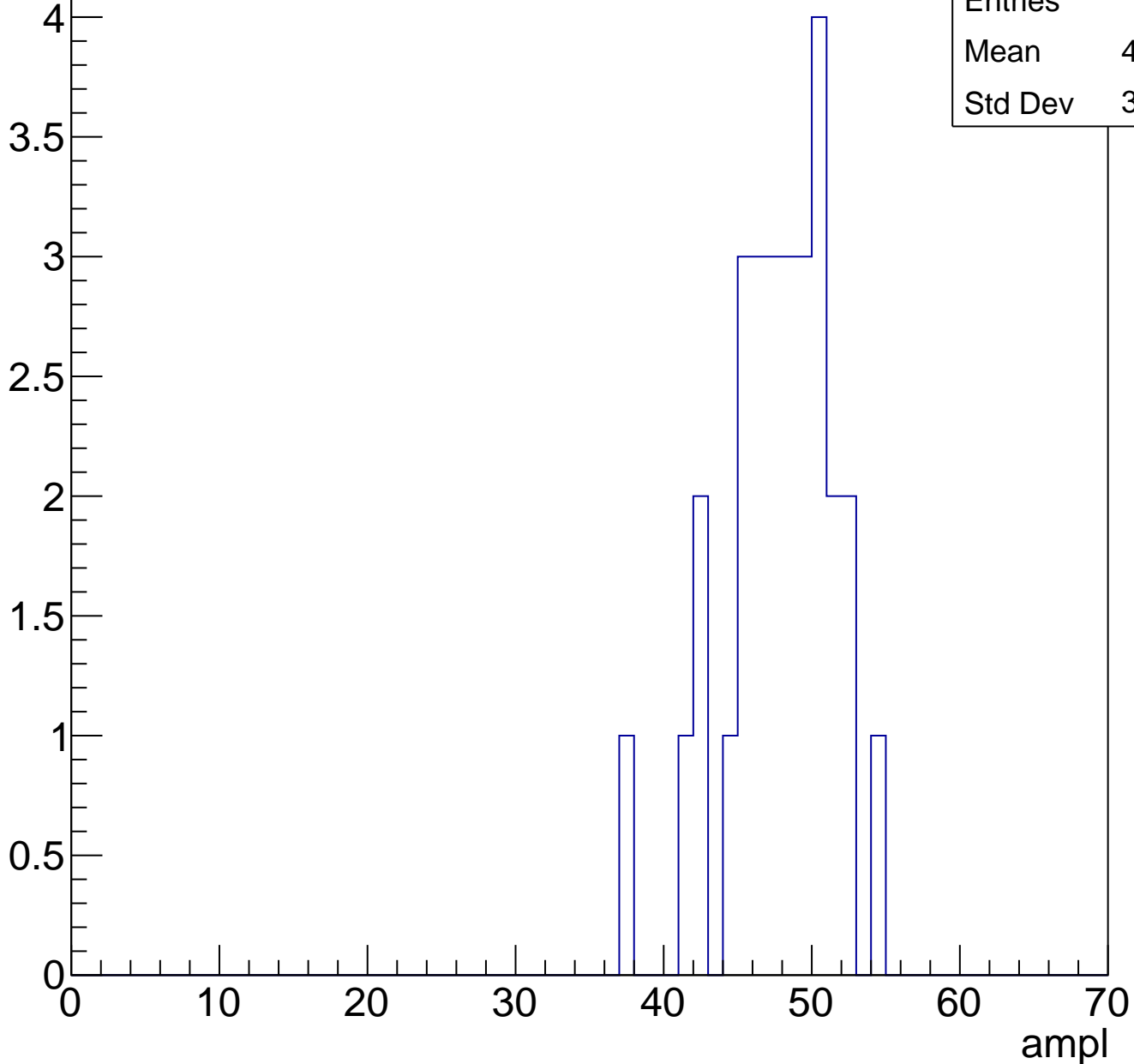
Entry



B1L103S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

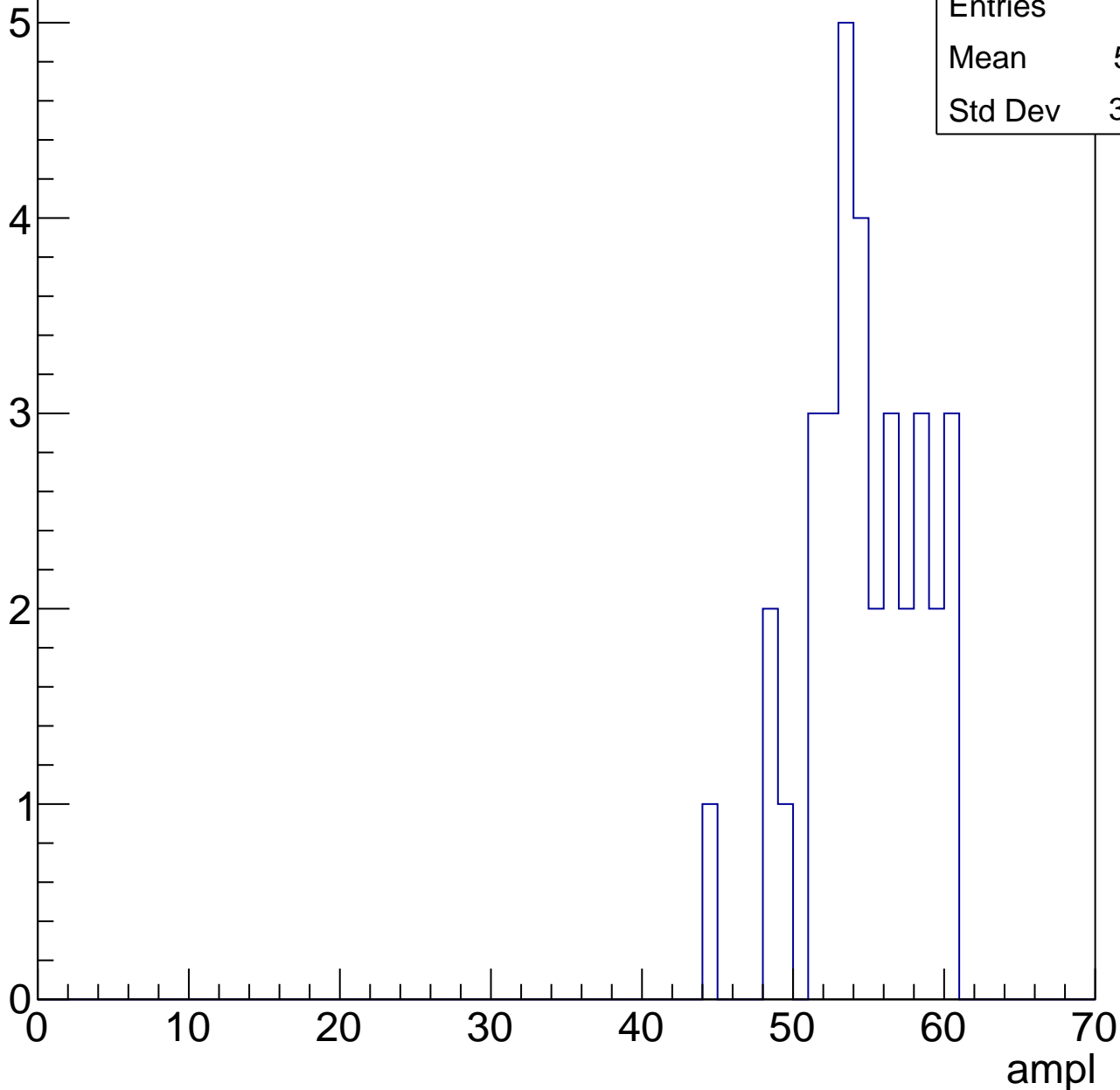


B1L103S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	54.21
Std Dev	3.748

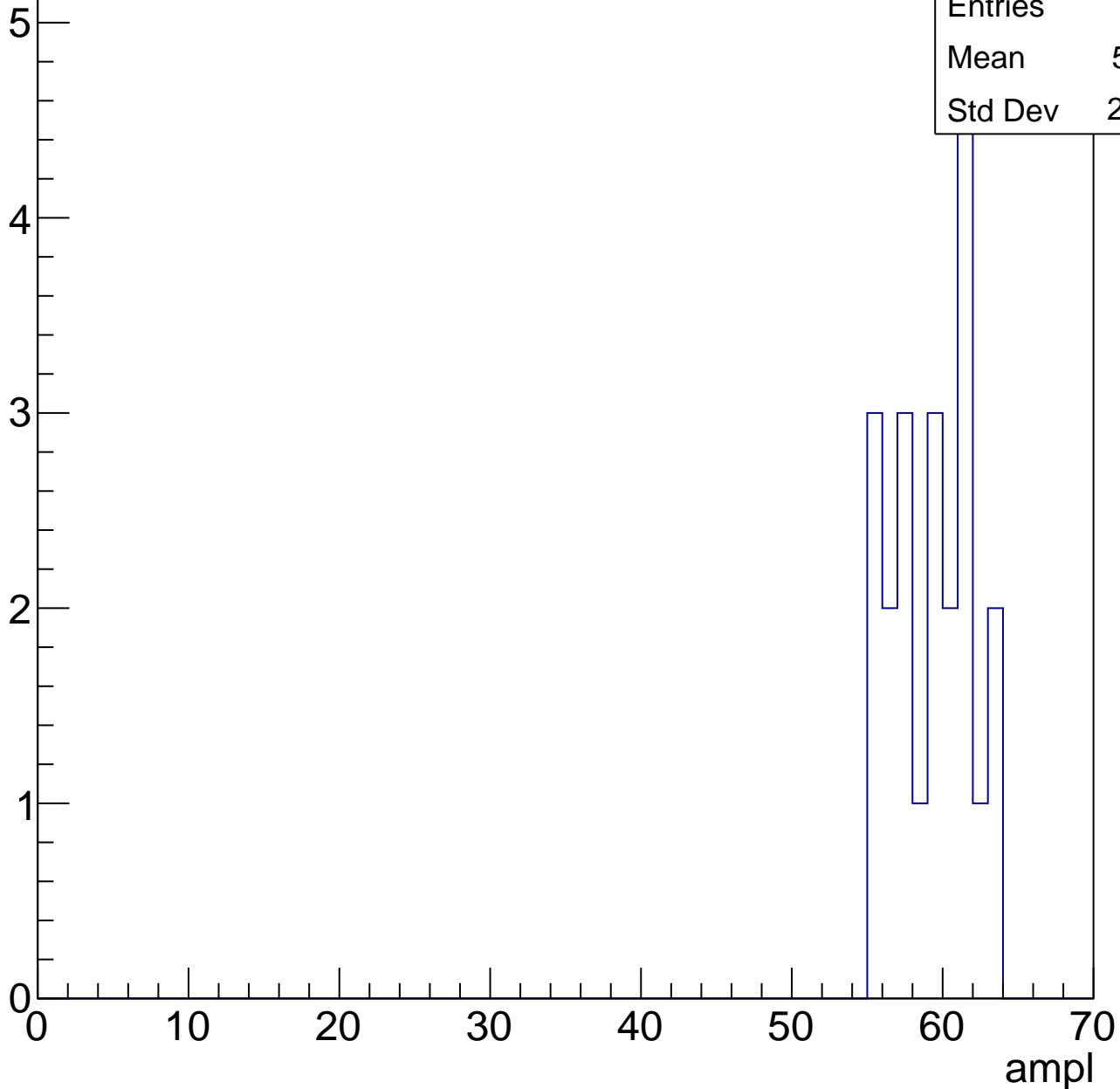


B1L103S, U15-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

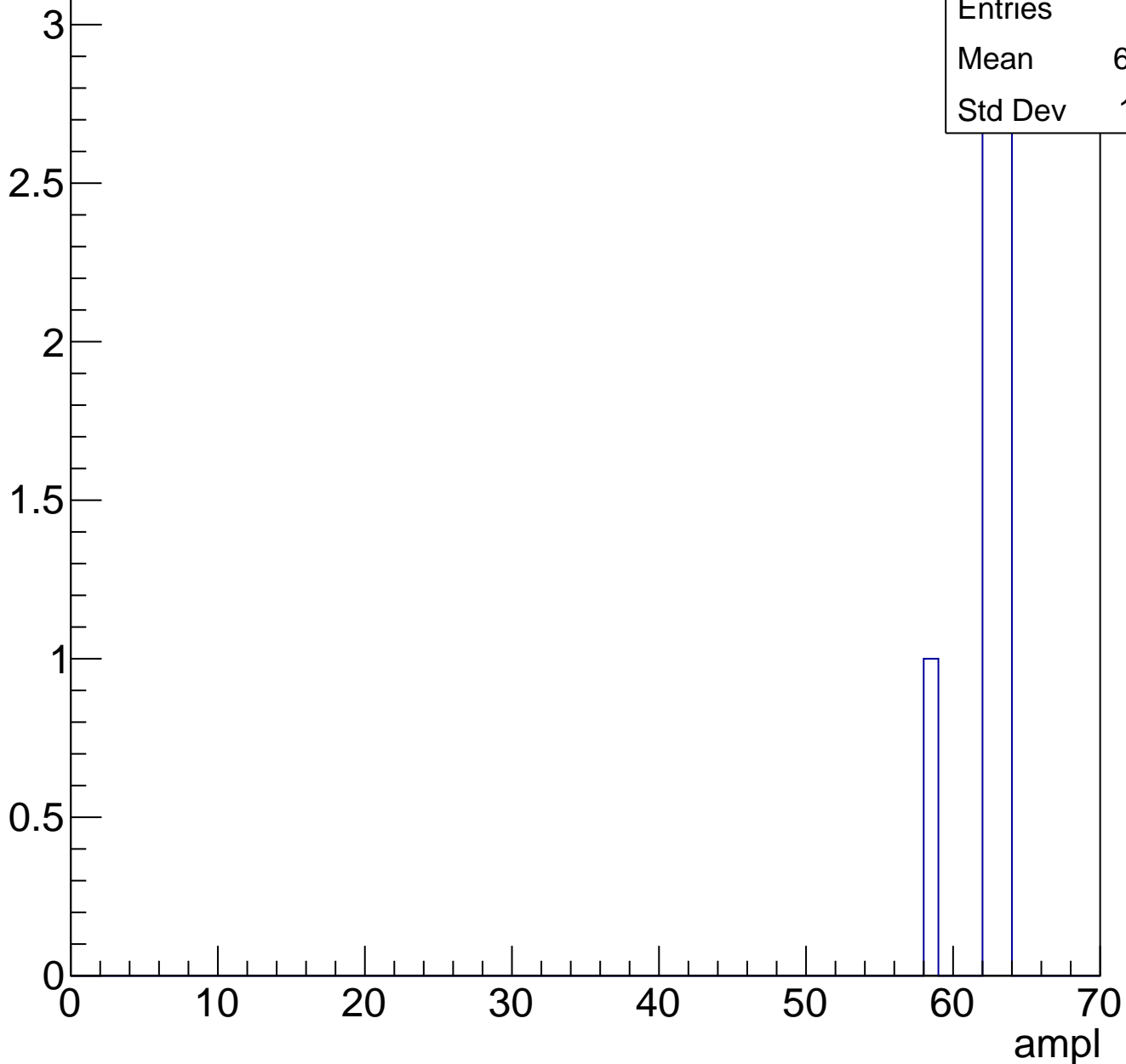
Entries	22
Mean	58.91
Std Dev	2.539



B1L103S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch49, adc0

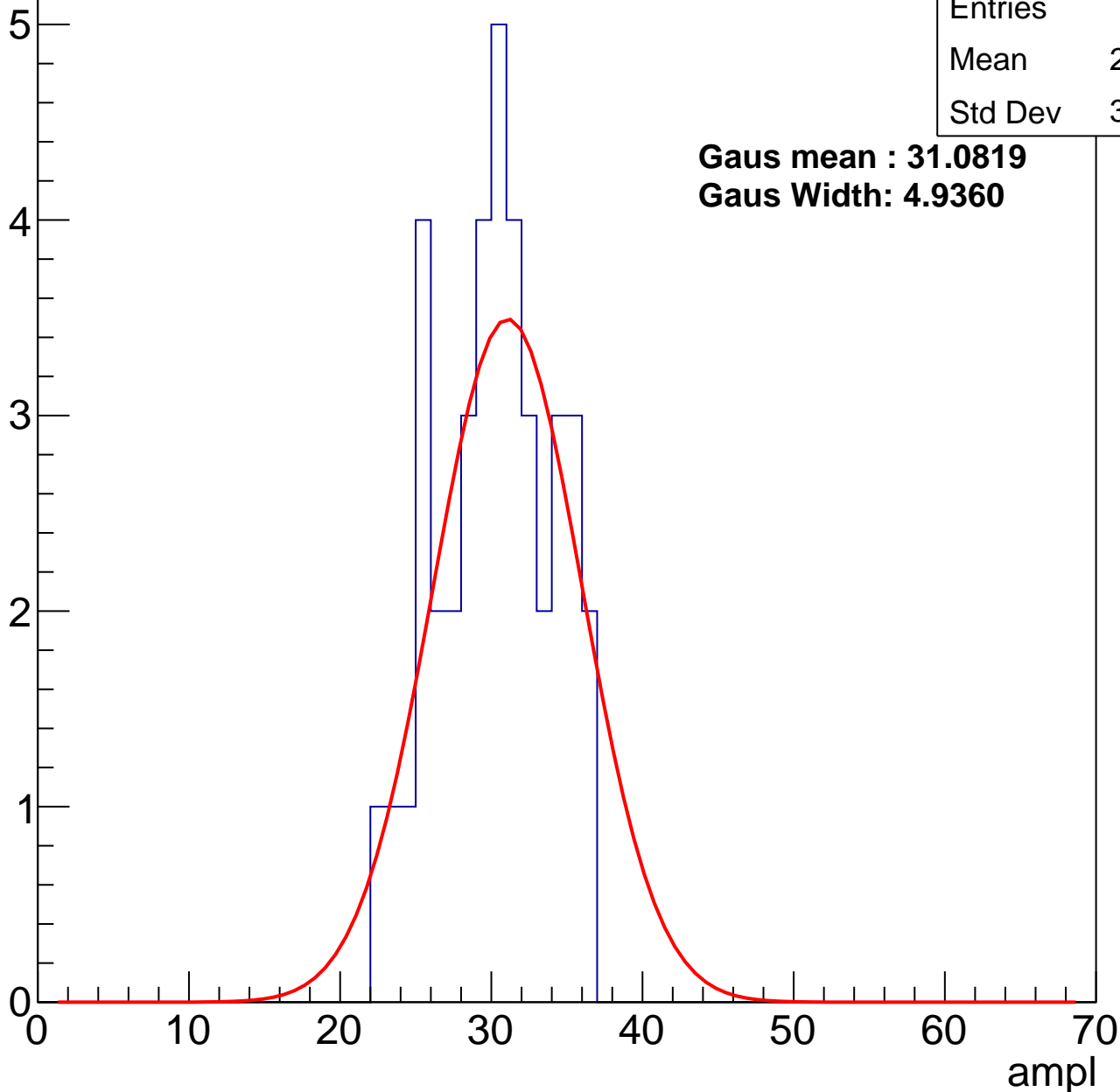
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	29.75
Std Dev	3.679

Gaus mean : 31.0819

Gaus Width: 4.9360



B1L103S, U15-ch49, adc1

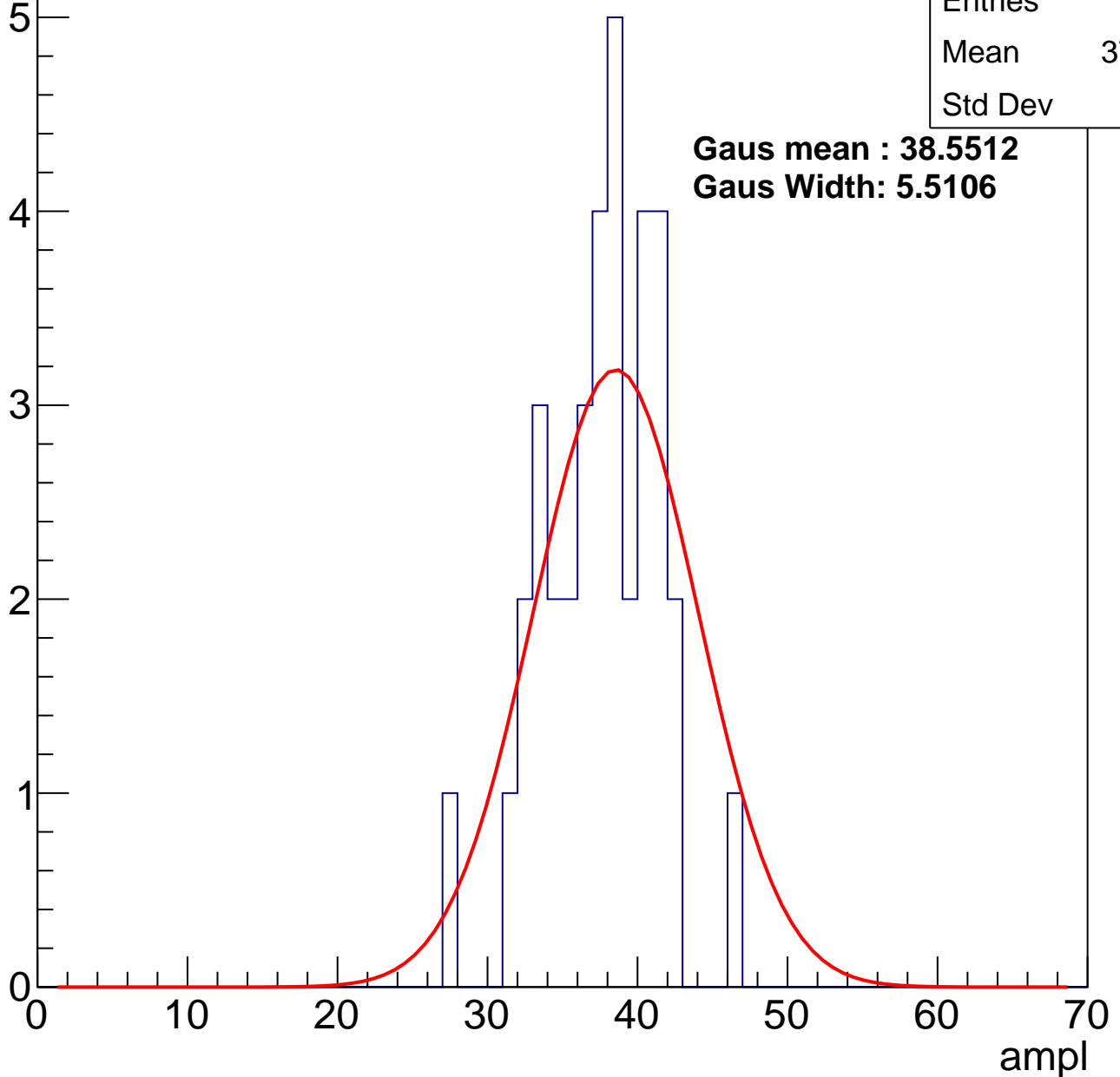
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	37.14
Std Dev	3.75

Gaus mean : 38.5512

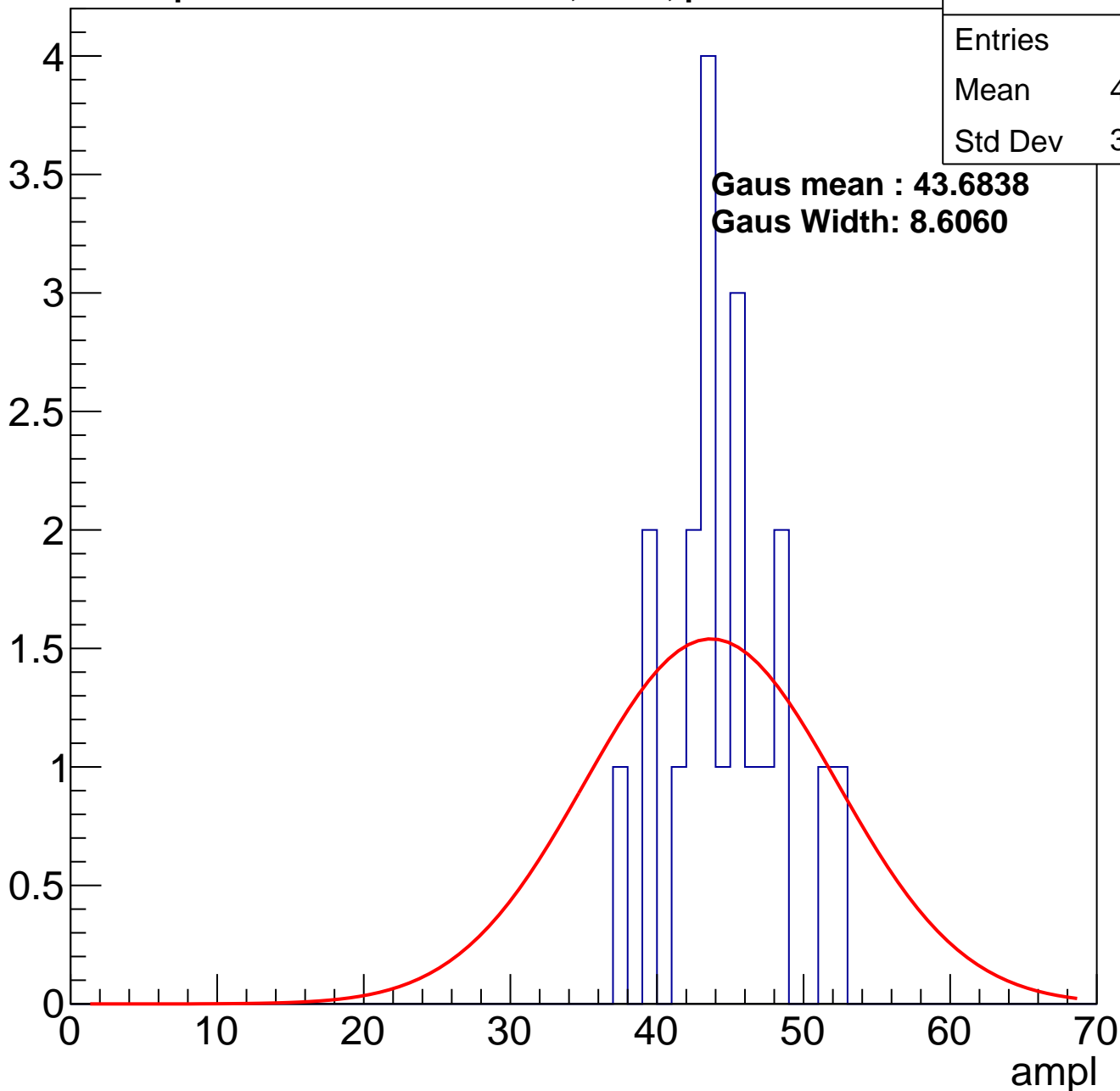
Gaus Width: 5.5106



B1L103S, U15-ch49, adc2

calib_packv5_042523_0143.root, FC#7, port C2

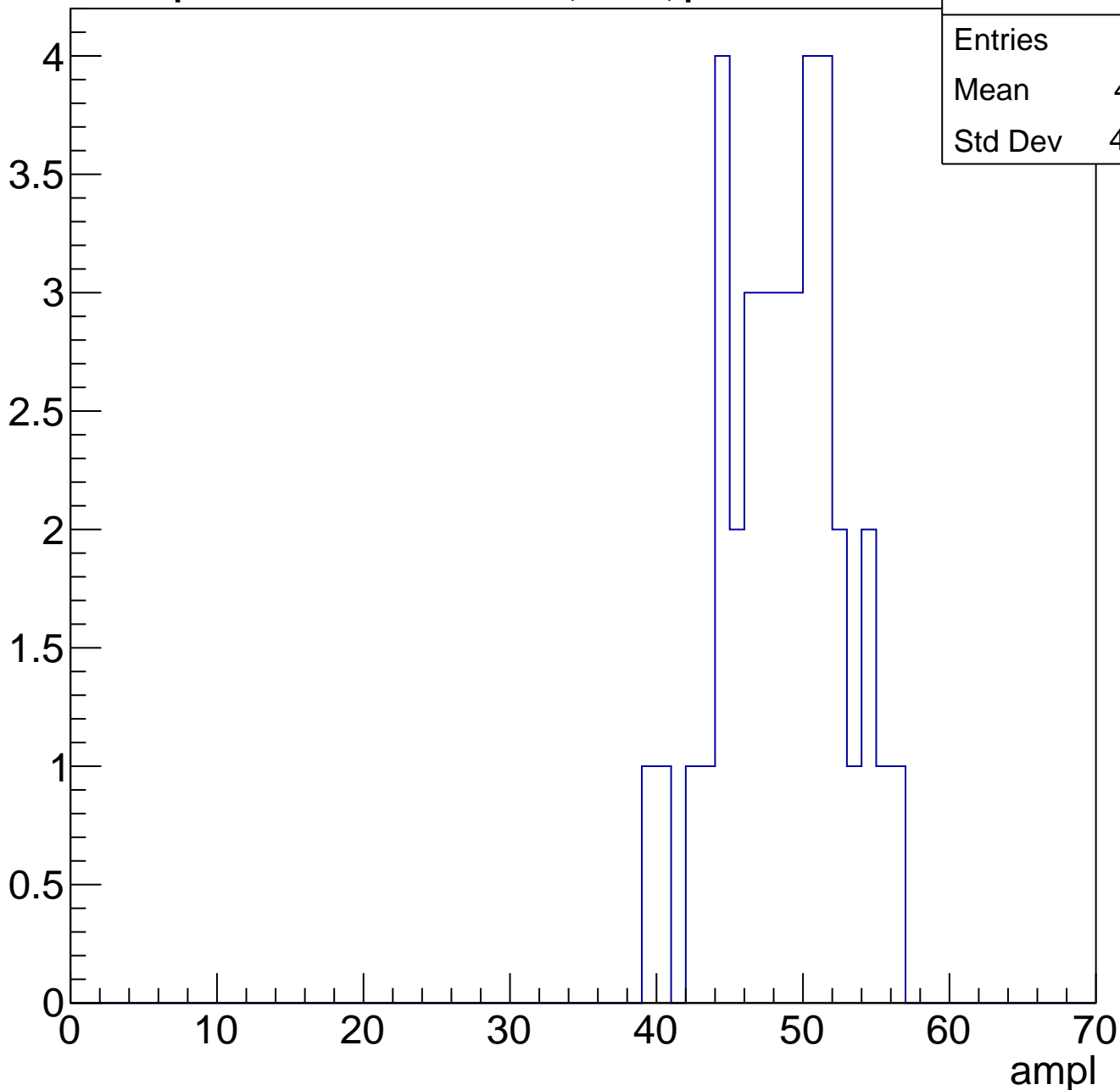
Entry



B1L103S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

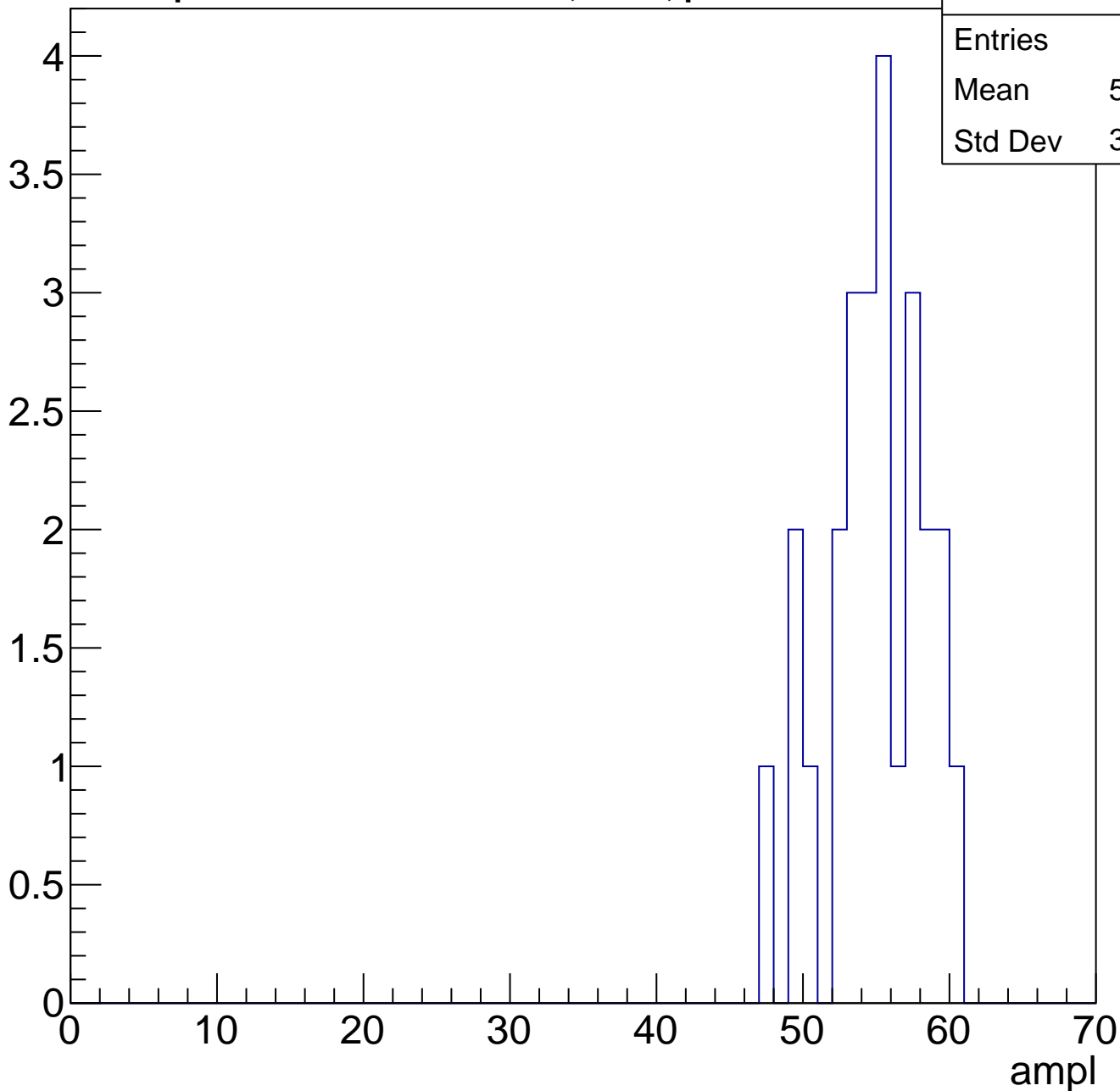


Entries	37
Mean	48.11
Std Dev	4.025

B1L103S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

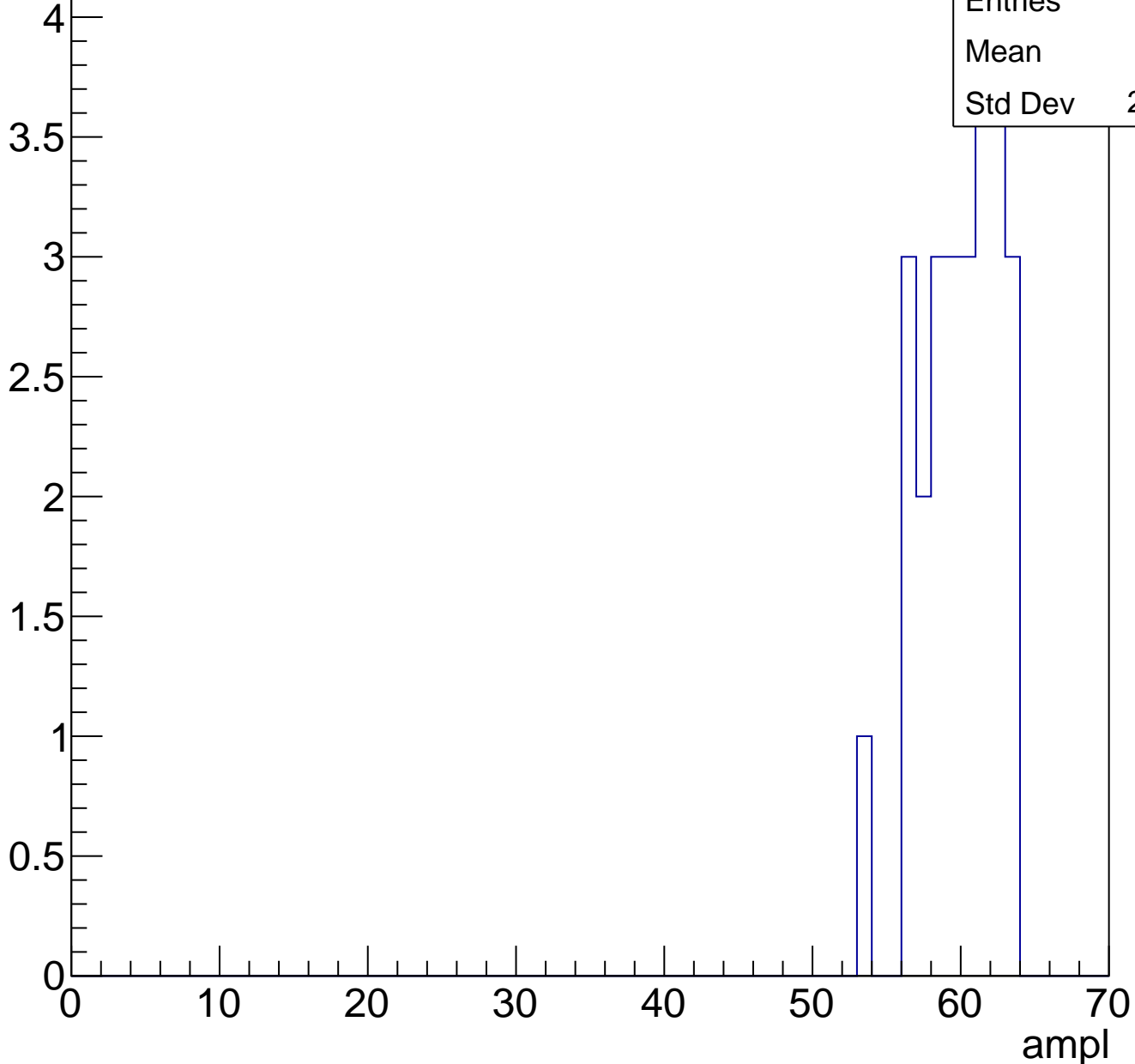
Entry



B1L103S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	26
Mean	59.5
Std Dev	2.561

B1L103S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

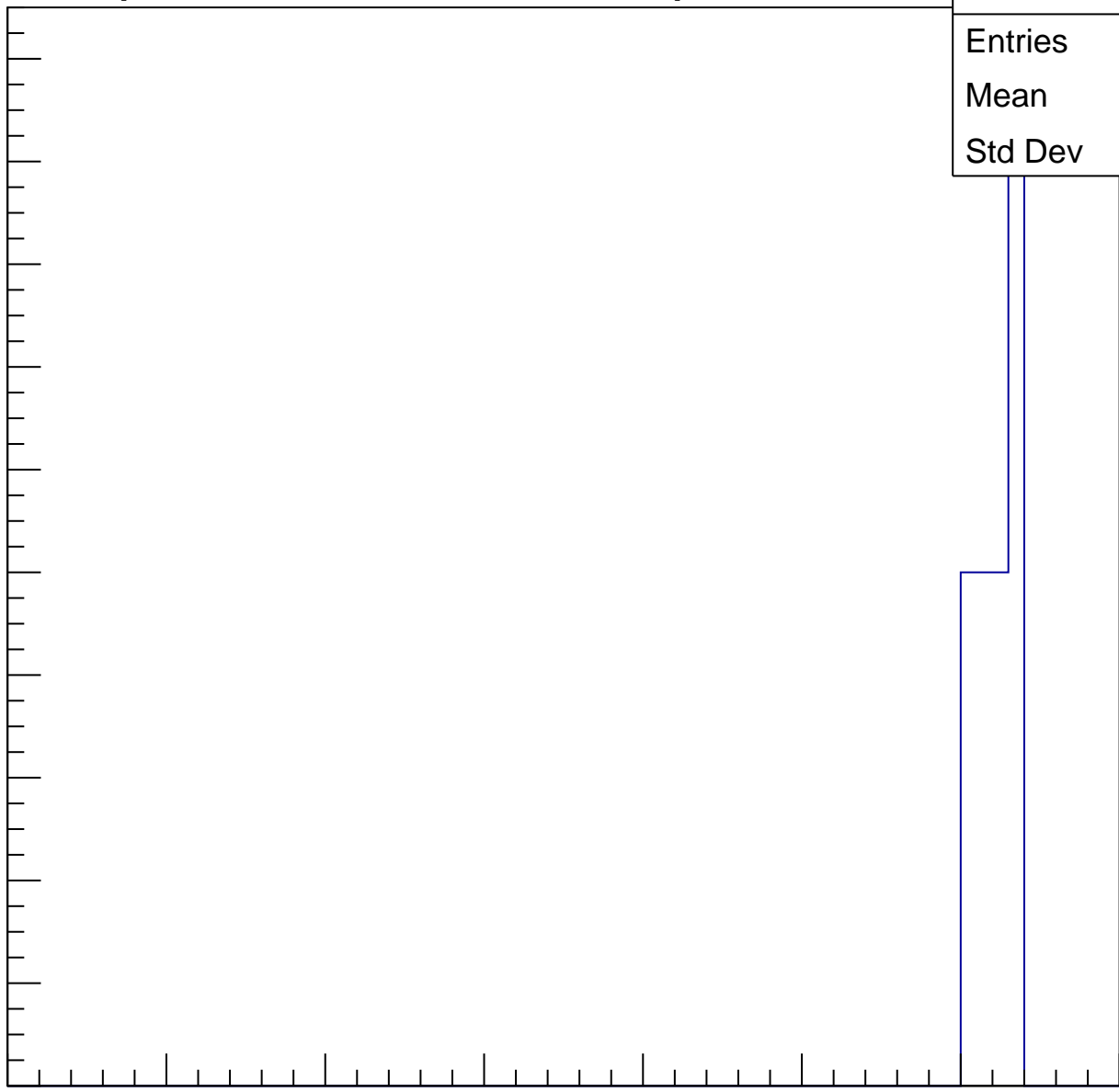
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L103S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

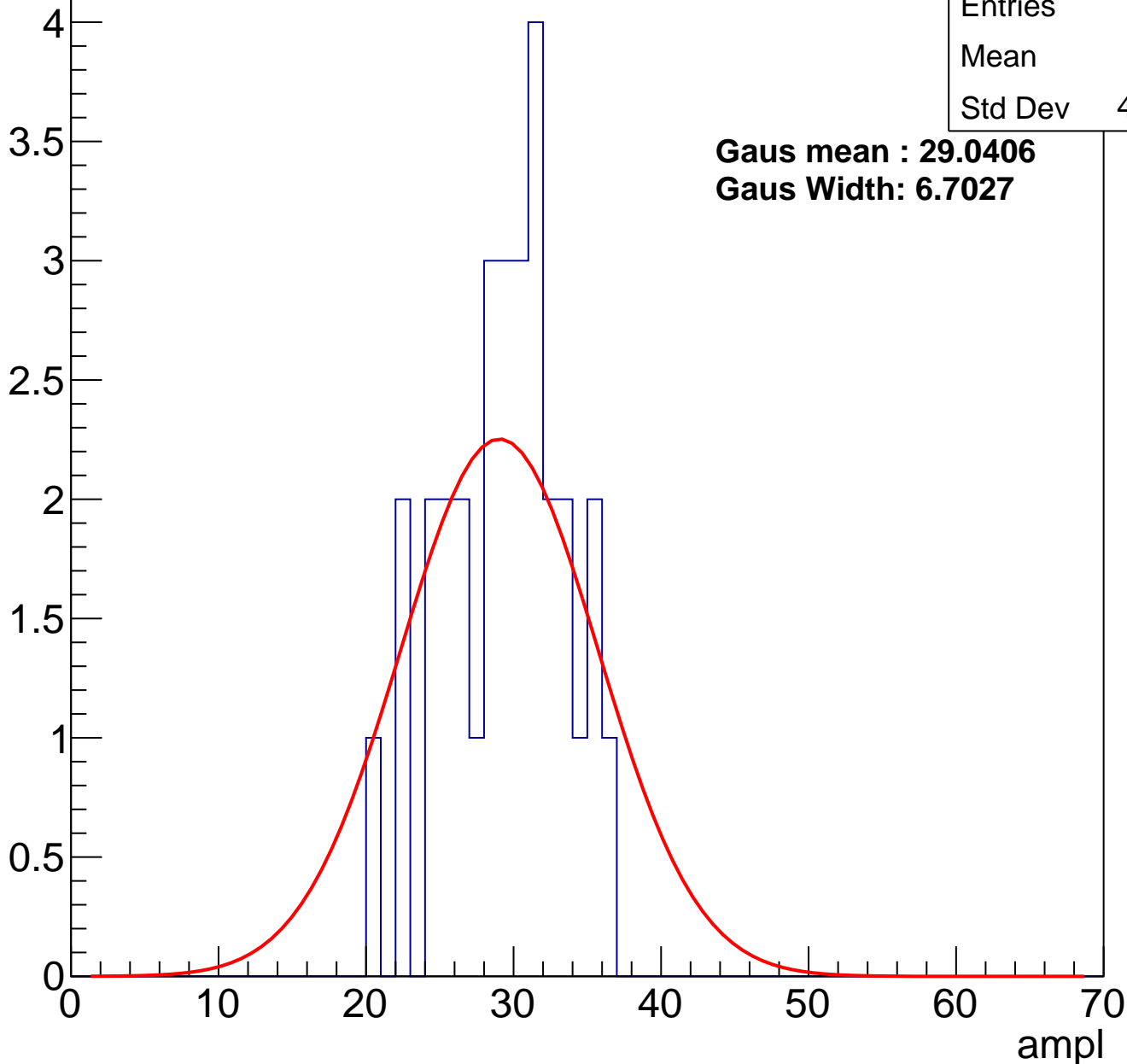


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch50, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch50, adc1

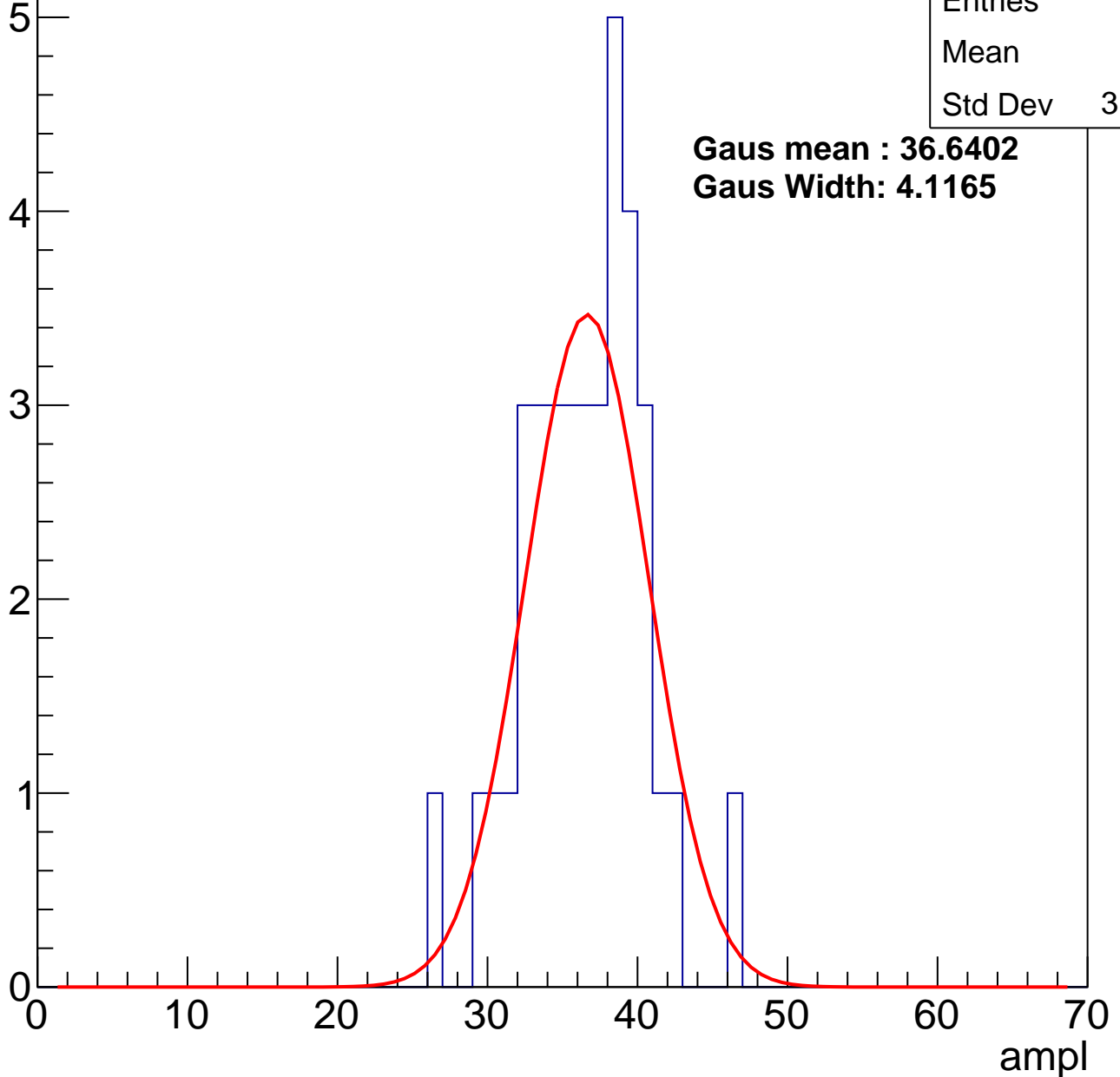
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	36
Std Dev	3.918

Gaus mean : 36.6402

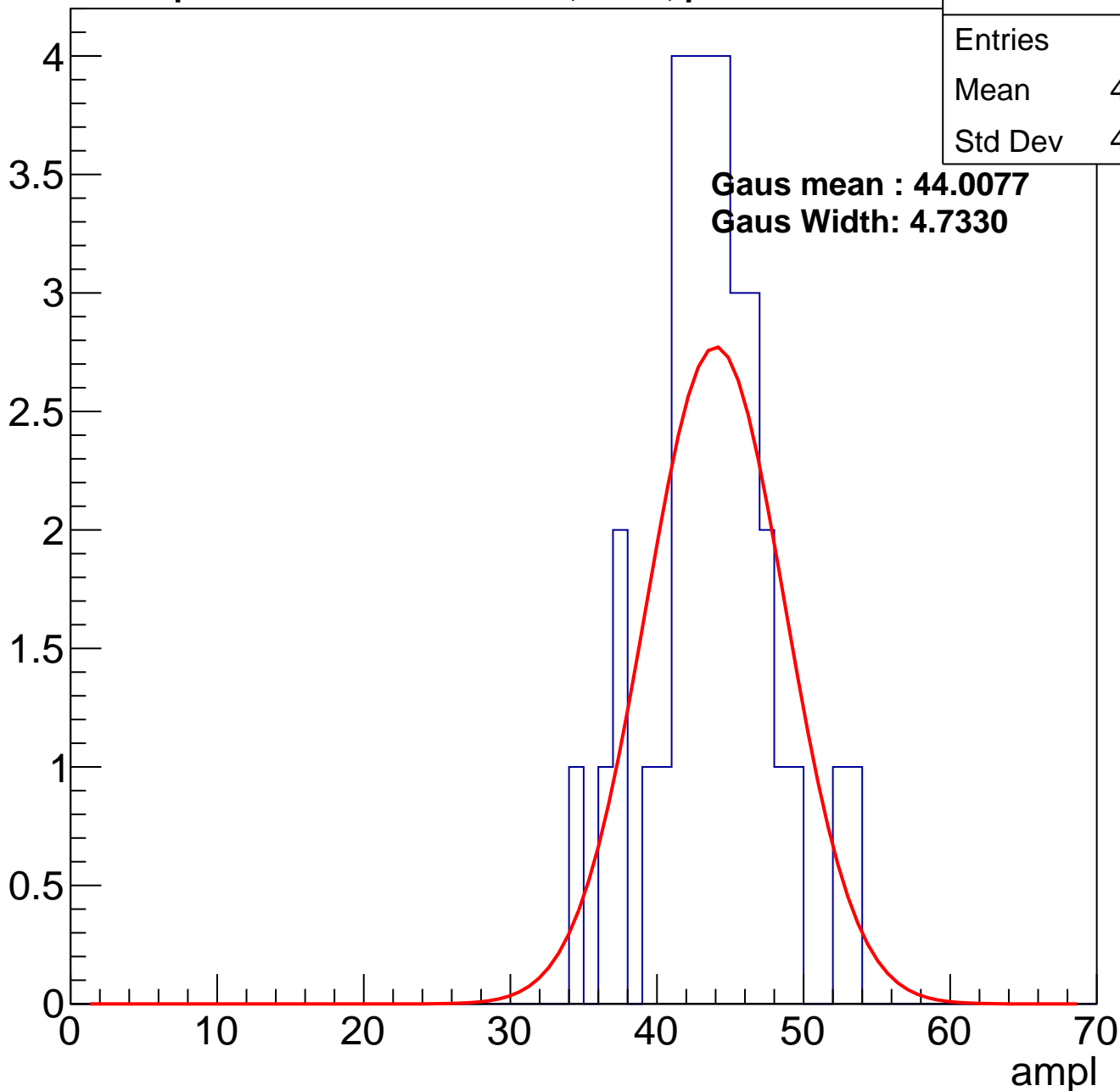
Gaus Width: 4.1165



B1L103S, U15-ch50, adc2

calib_packv5_042523_0143.root, FC#7, port C2

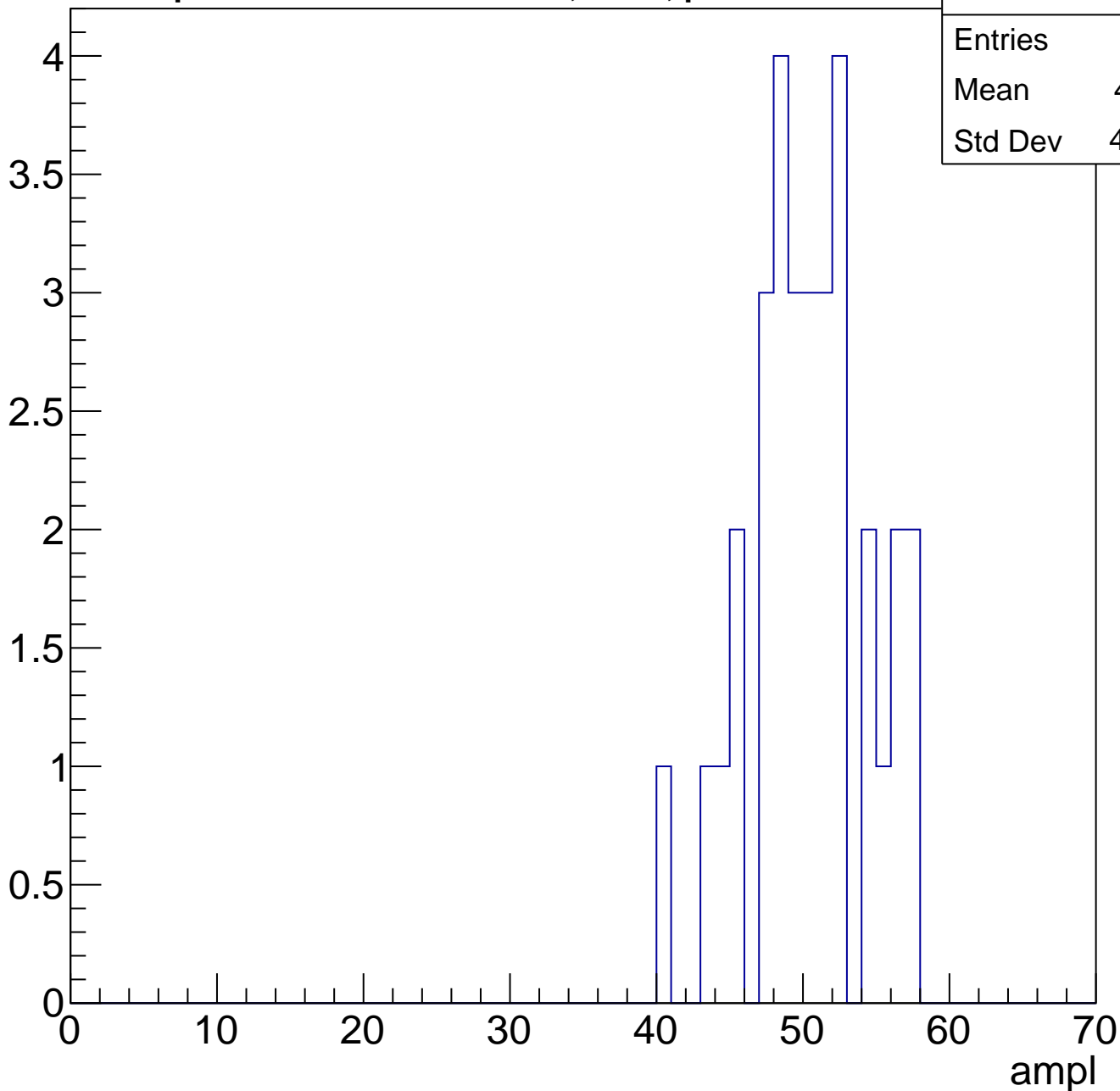
Entry



B1L103S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

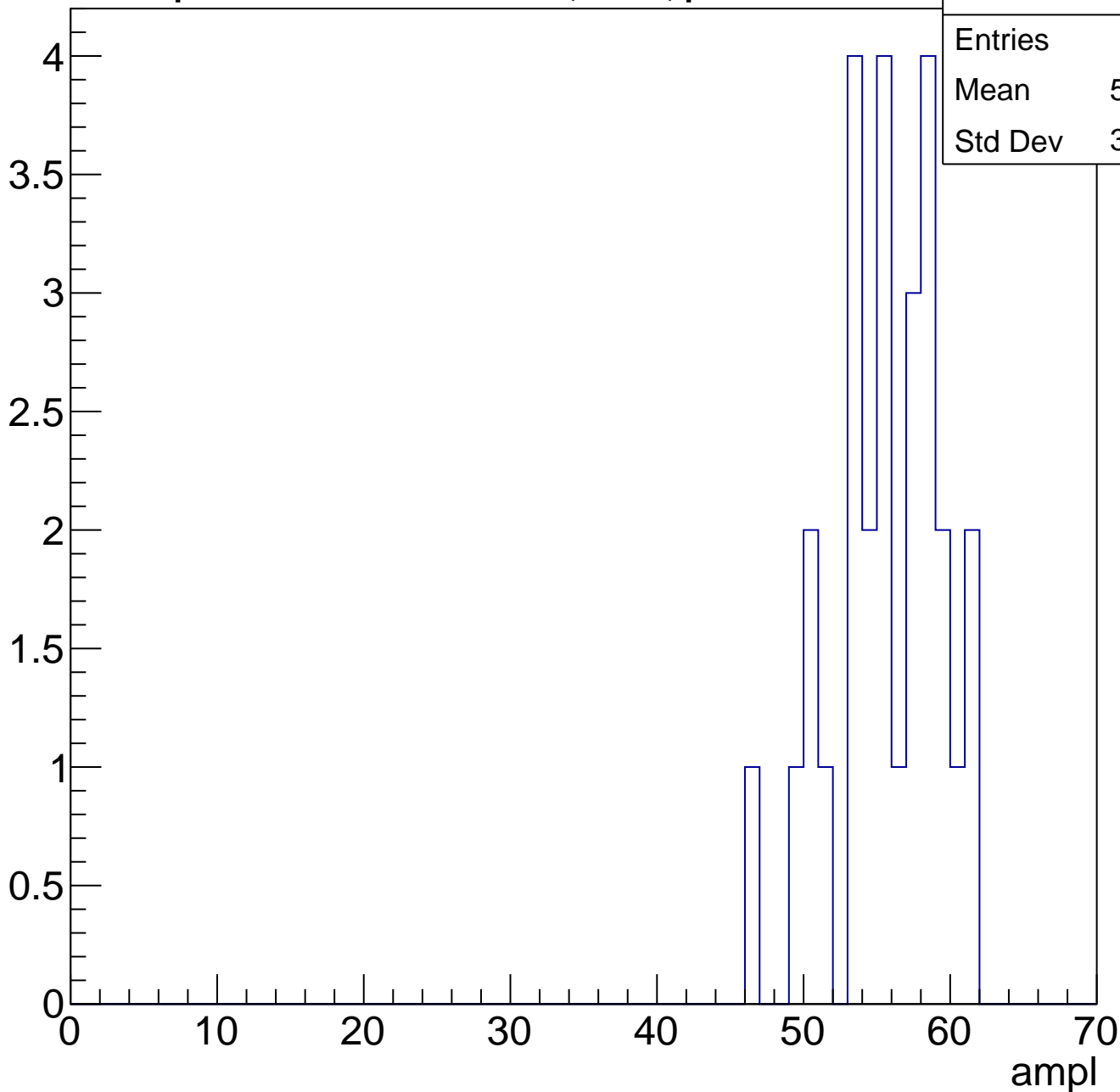
Entry



B1L103S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

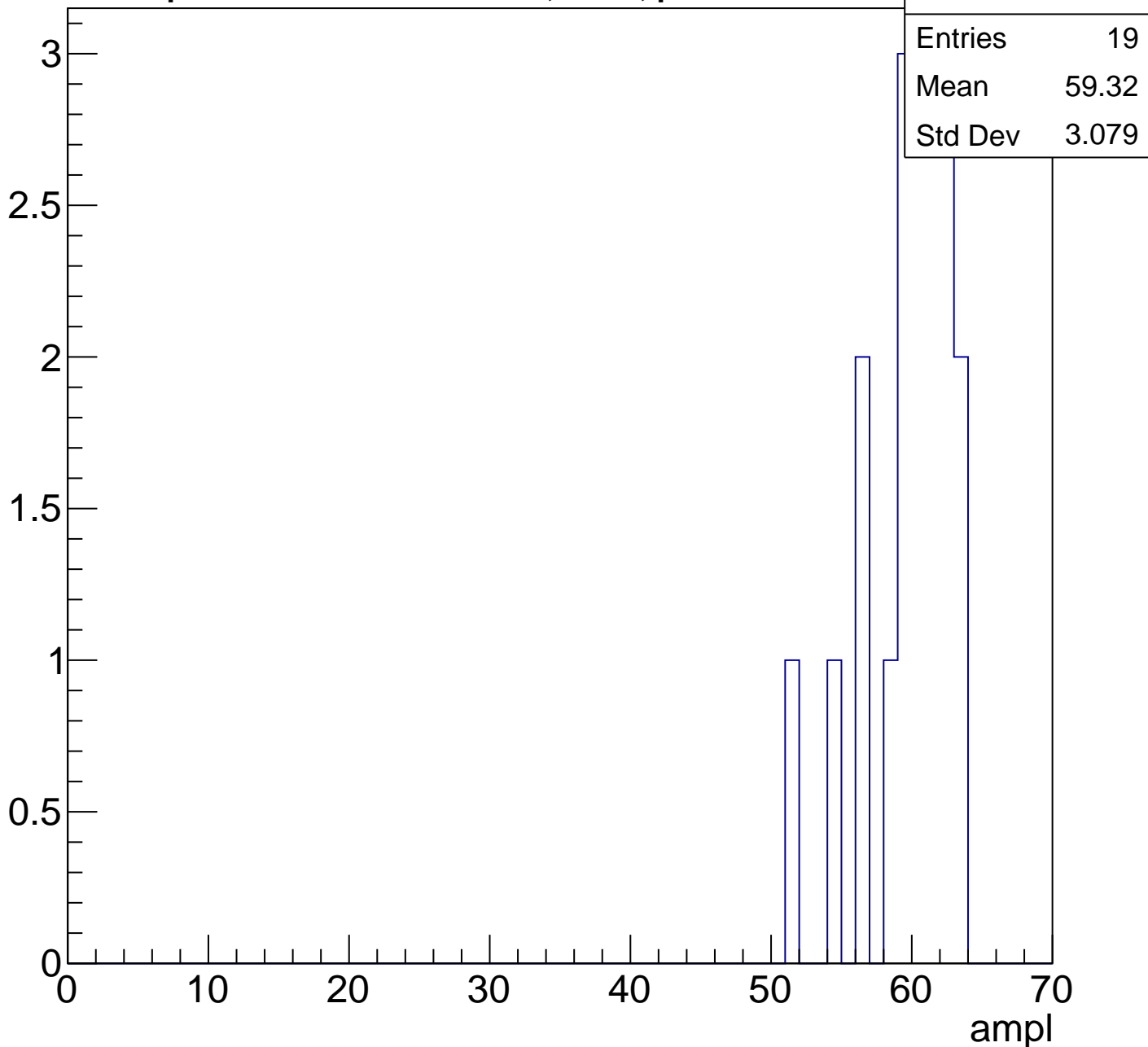
50

60

70

ampl

Entries	19
Mean	59.32
Std Dev	3.079



B1L103S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	62
Std Dev	1.095

B1L103S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch51, adc0

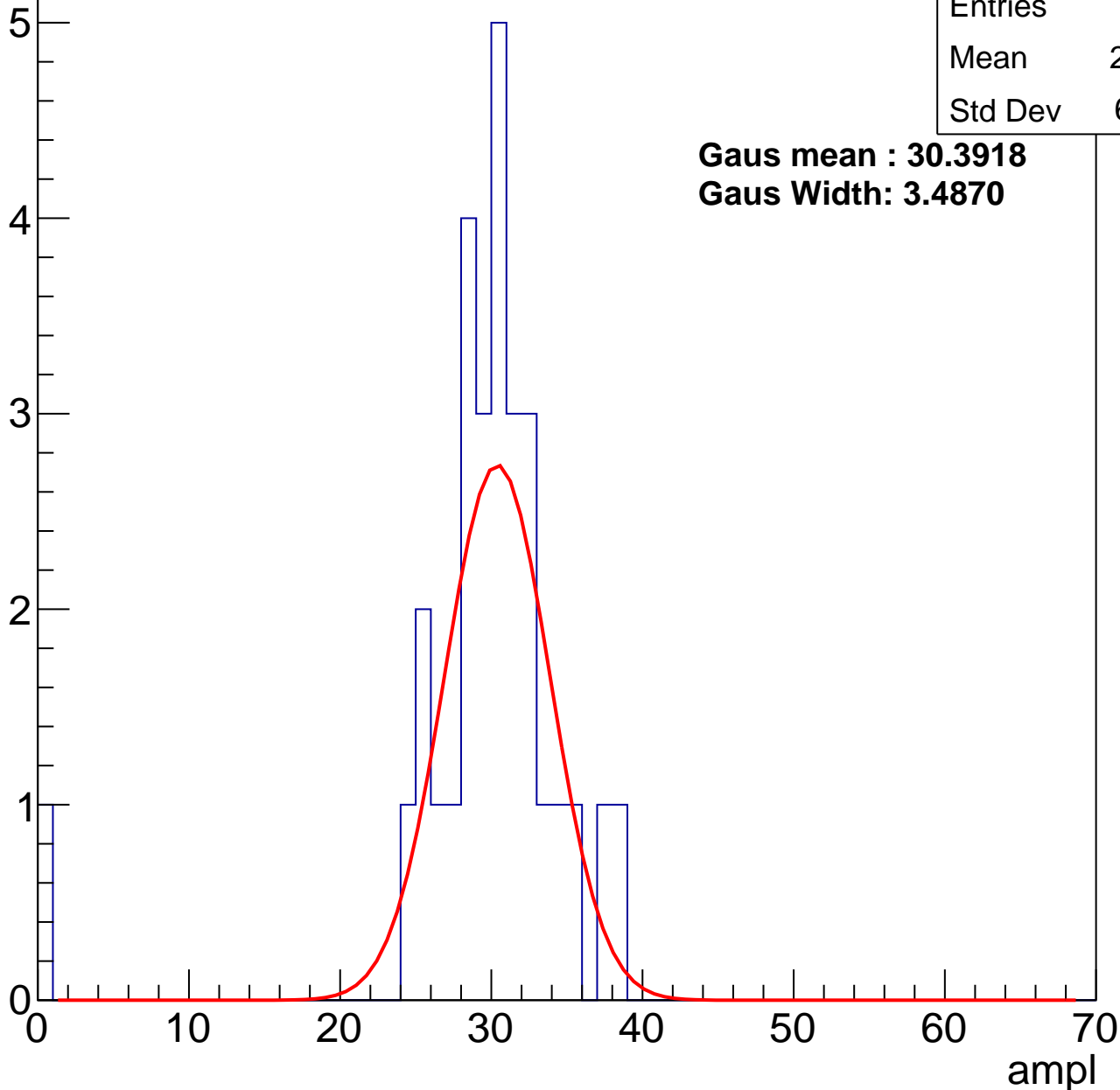
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	29.03
Std Dev	6.381

Gaus mean : 30.3918

Gaus Width: 3.4870



B1L103S, U15-ch51, adc1

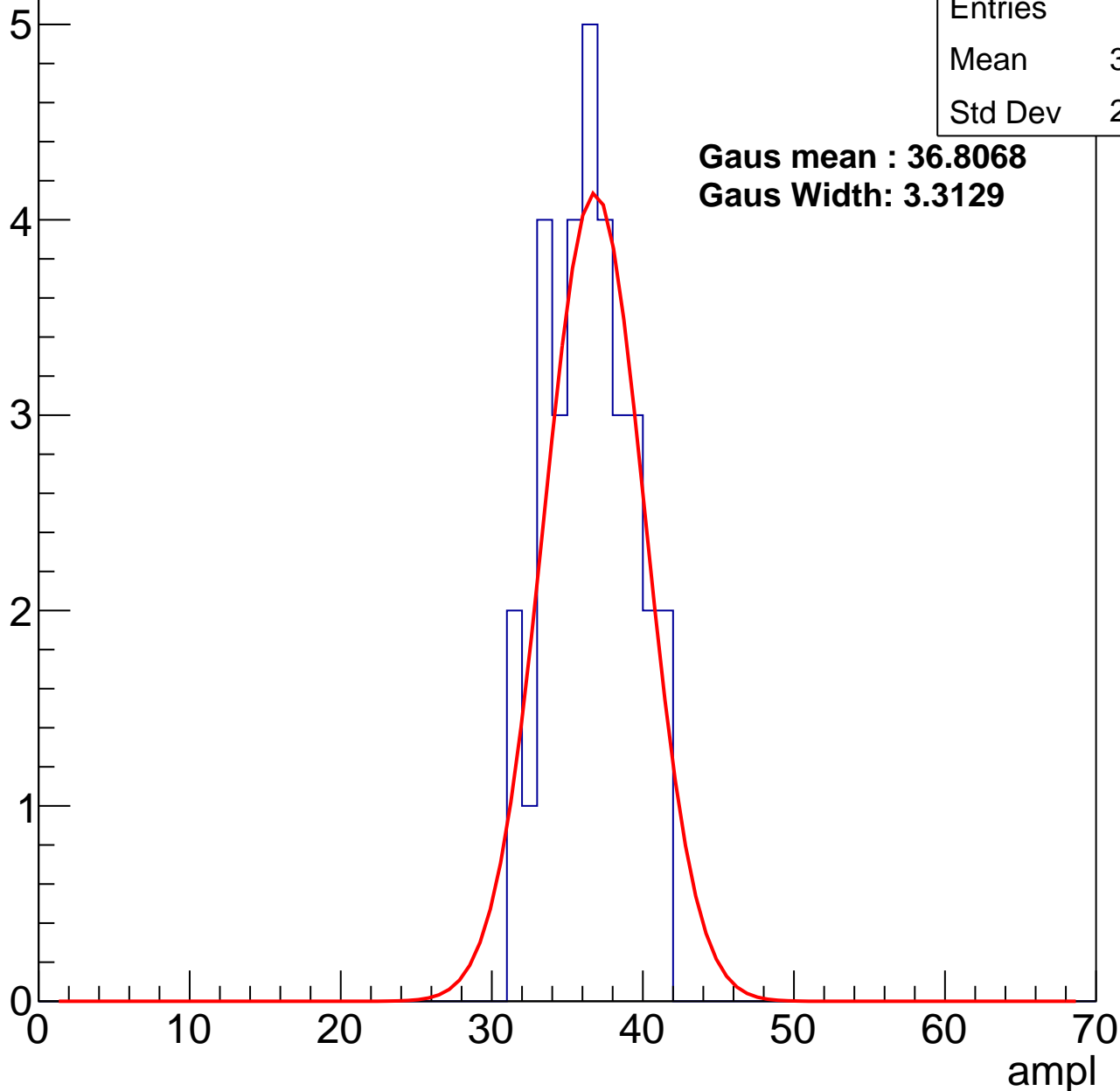
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	36.03
Std Dev	2.713

Gaus mean : 36.8068

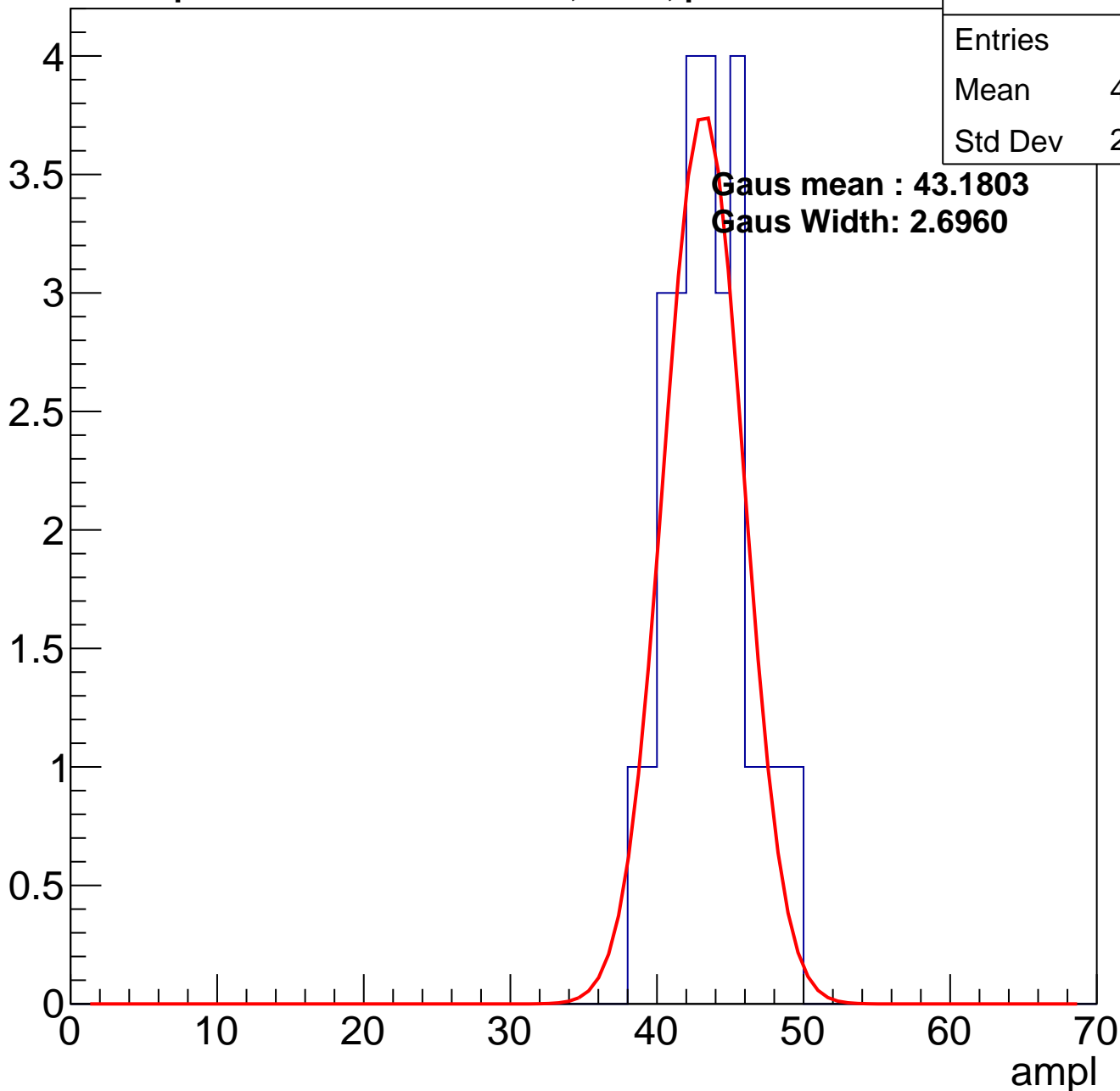
Gaus Width: 3.3129



B1L103S, U15-ch51, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	27
Mean	43.04
Std Dev	2.645

Gaus mean : 43.1803

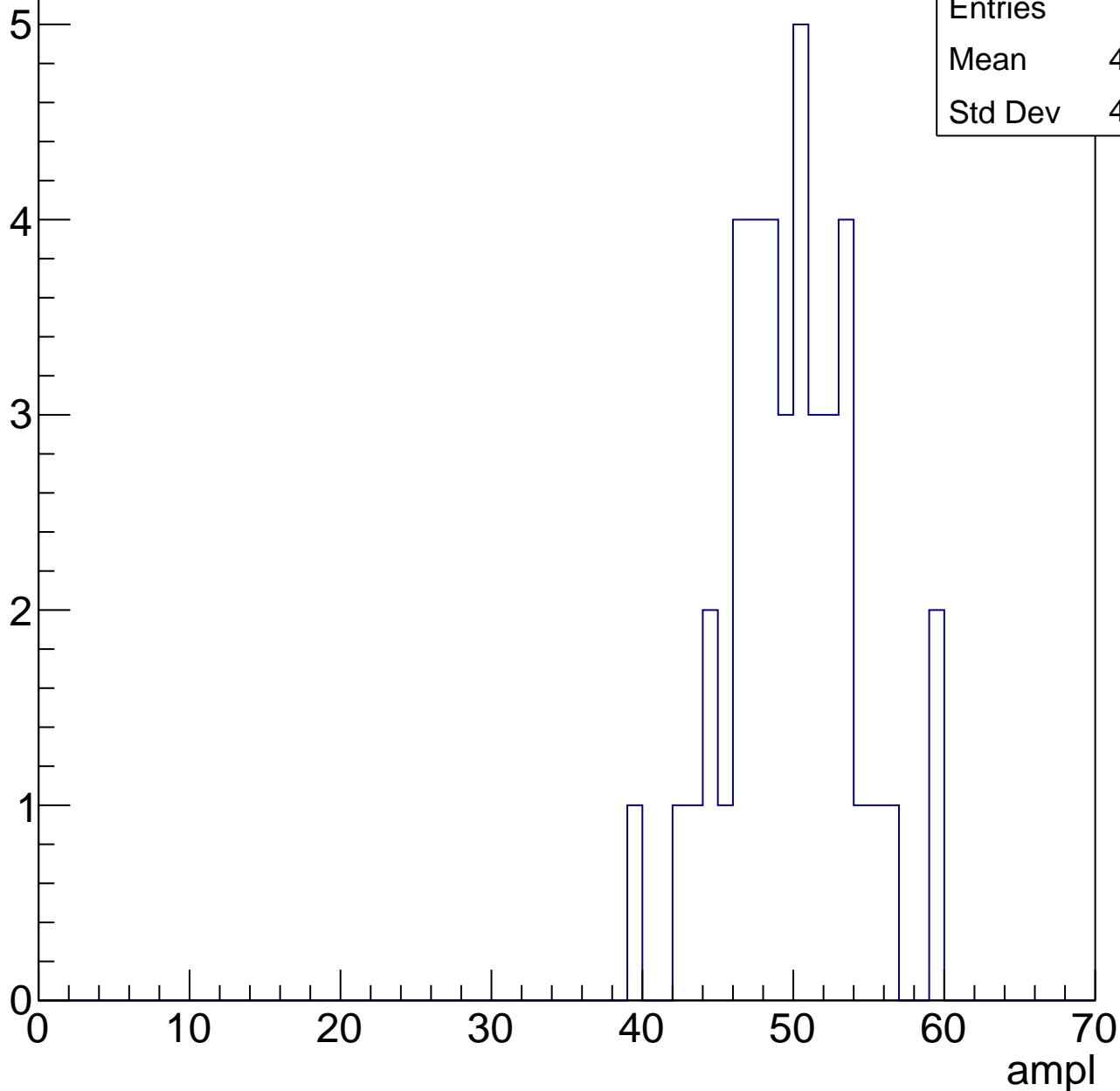
Gaus Width: 2.6960

B1L103S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

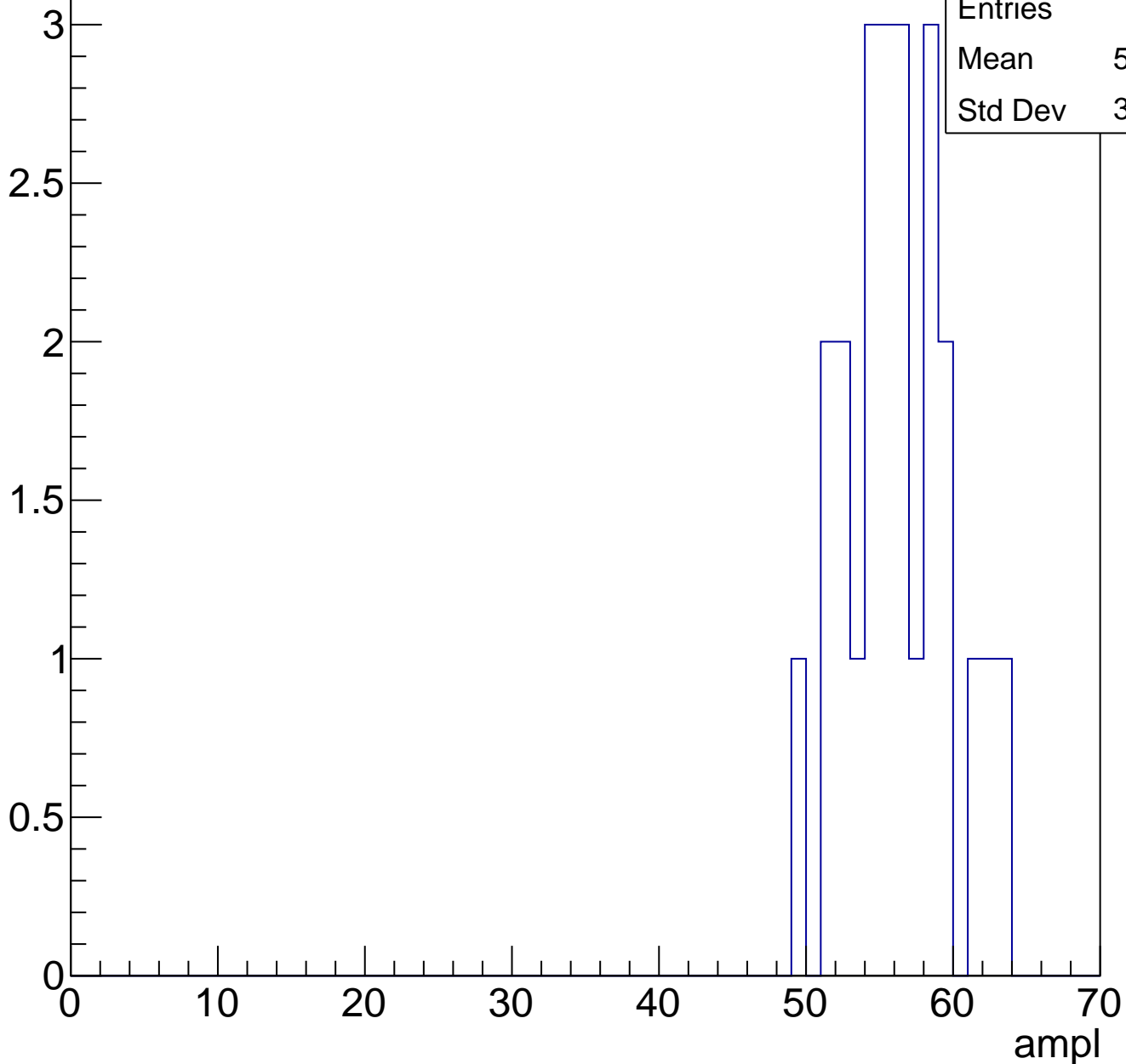
Entries	41
Mean	49.32
Std Dev	4.193



B1L103S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

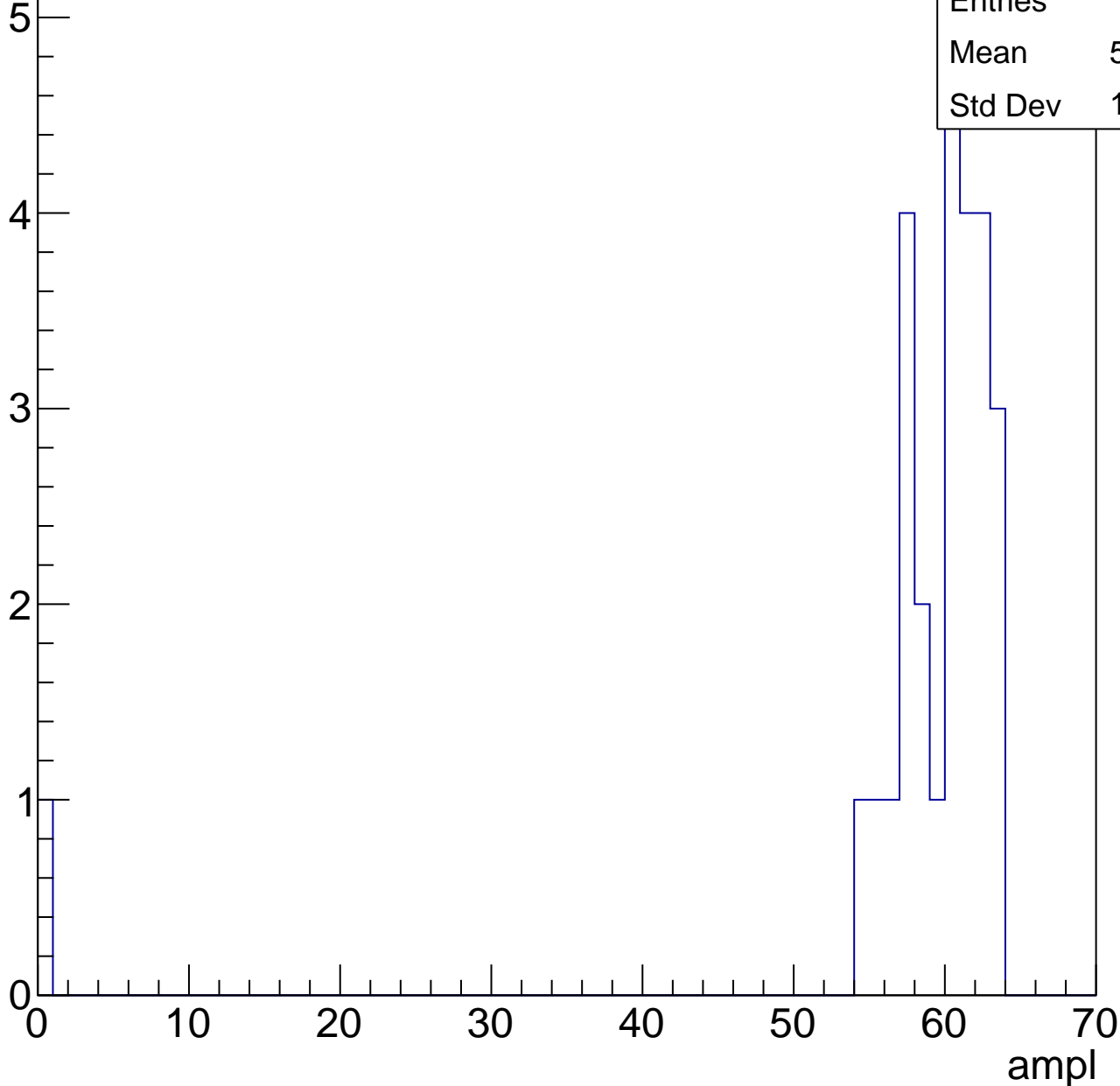


B1L103S, U15-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	57.37
Std Dev	11.52



B1L103S, U15-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch52, adc0

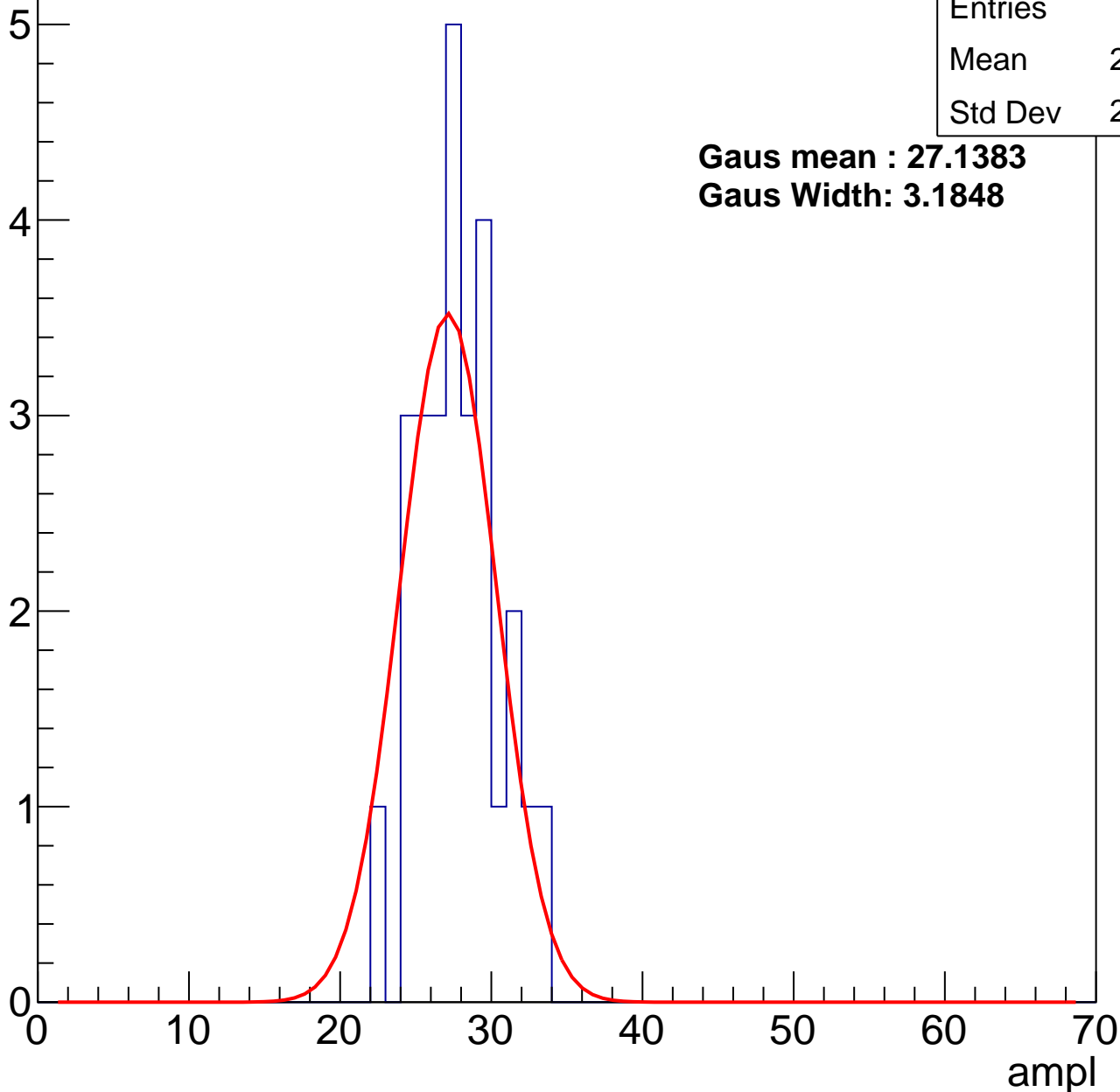
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	27.37
Std Dev	2.613

Gaus mean : 27.1383

Gaus Width: 3.1848



B1L103S, U15-ch52, adc1

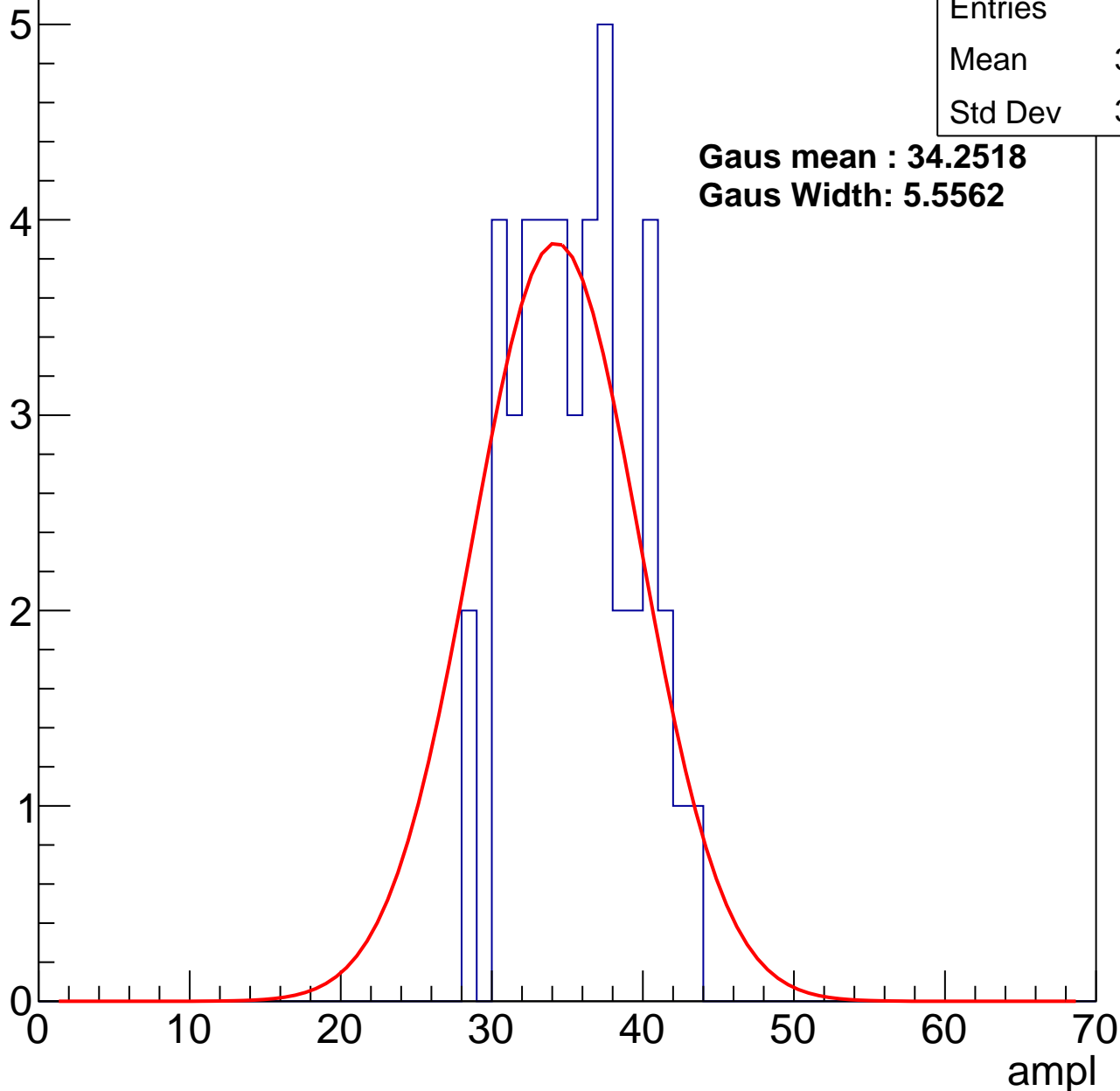
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	35.11
Std Dev	3.831

Gaus mean : 34.2518

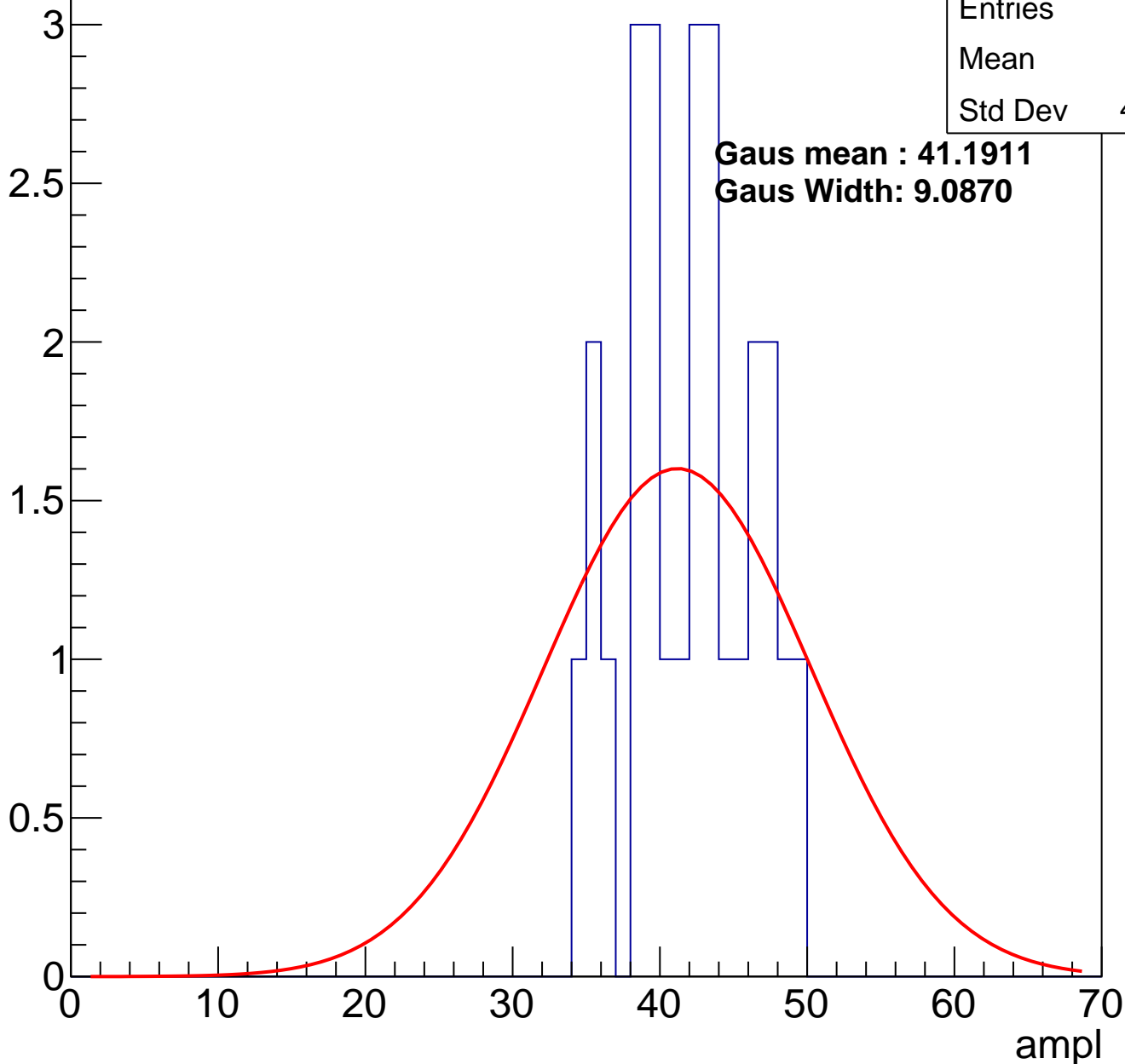
Gaus Width: 5.5562



B1L103S, U15-ch52, adc2

calib_packv5_042523_0143.root, FC#7, port C2

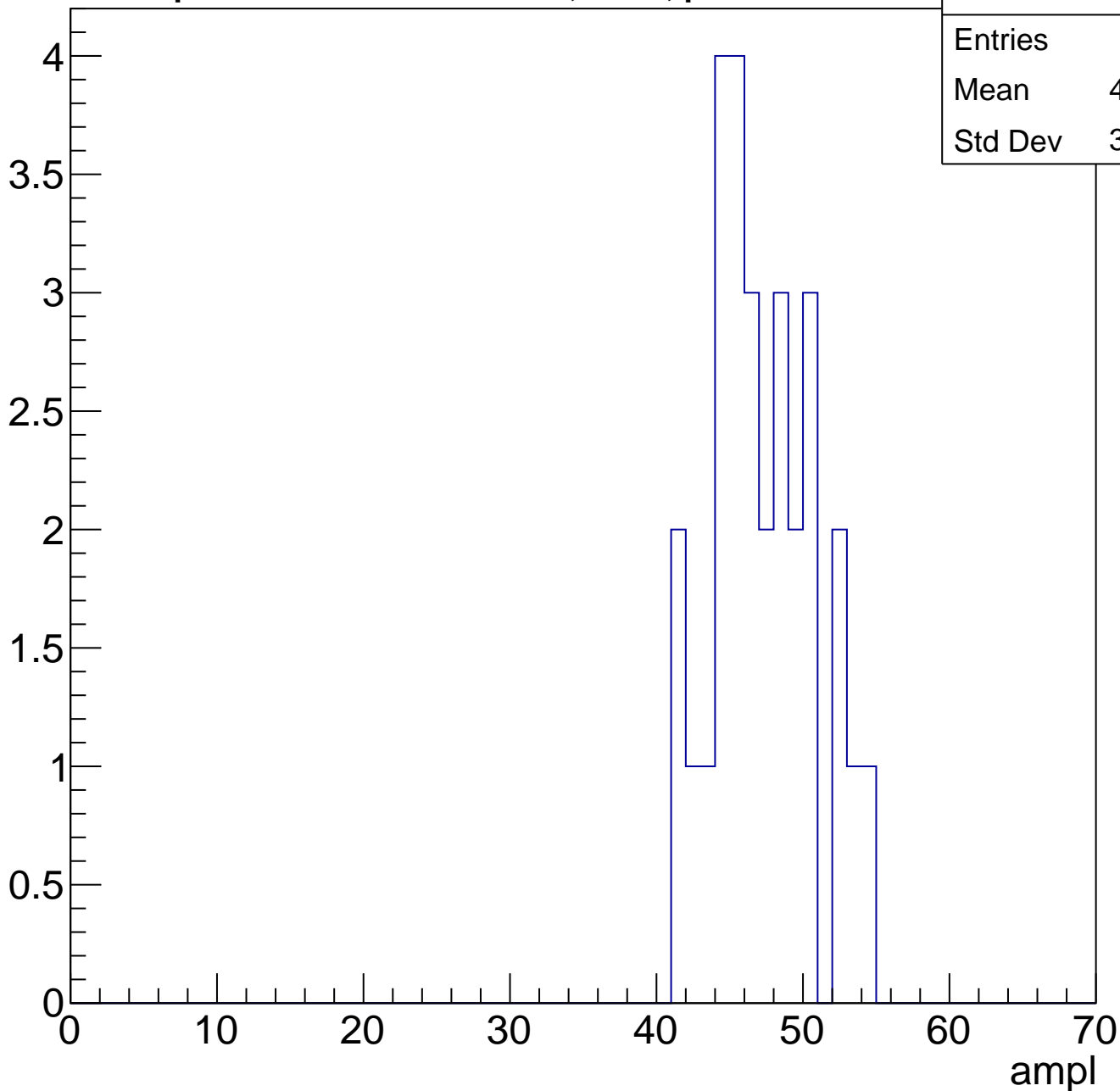
Entry



B1L103S, U15-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

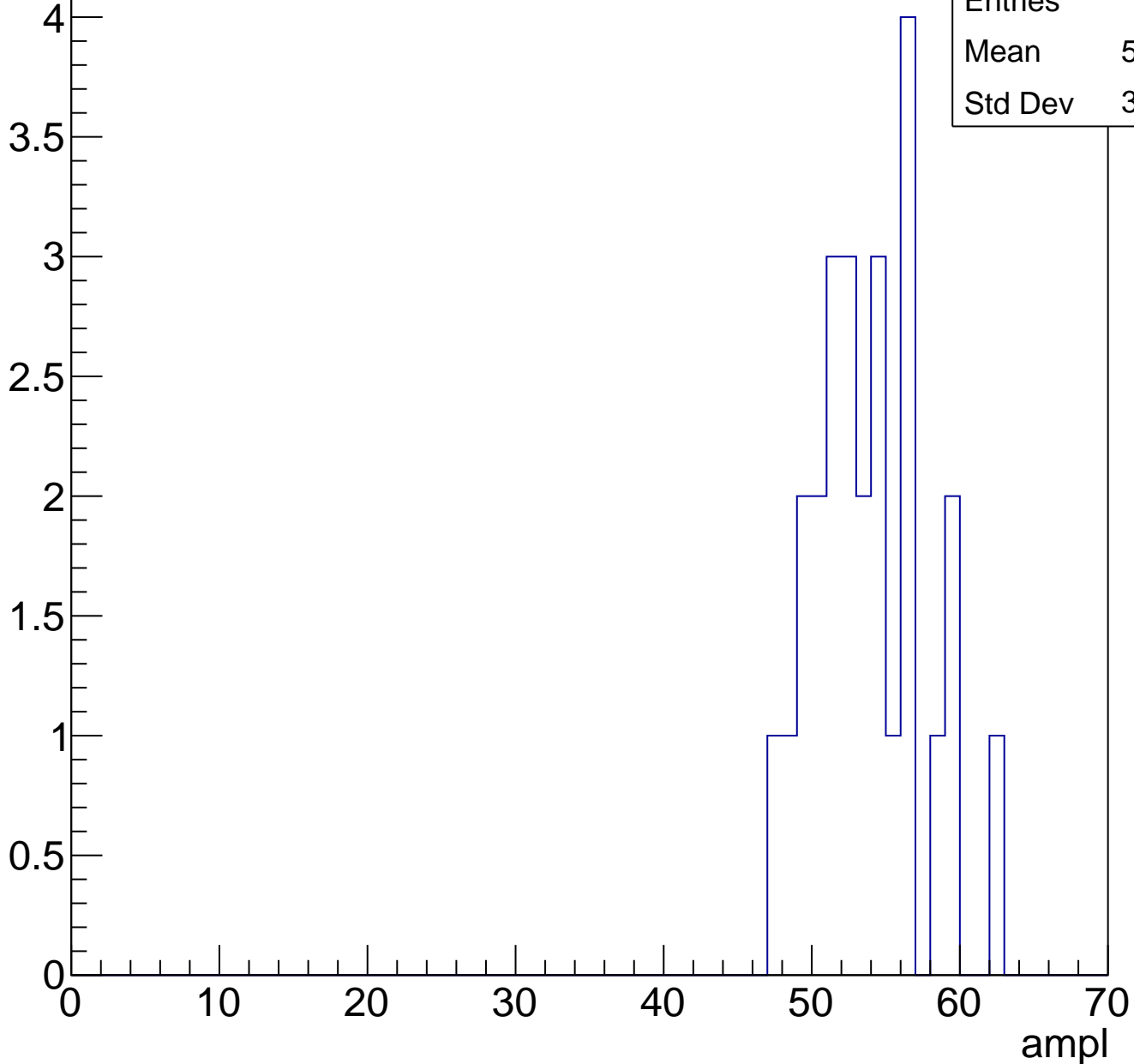
Entry



B1L103S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

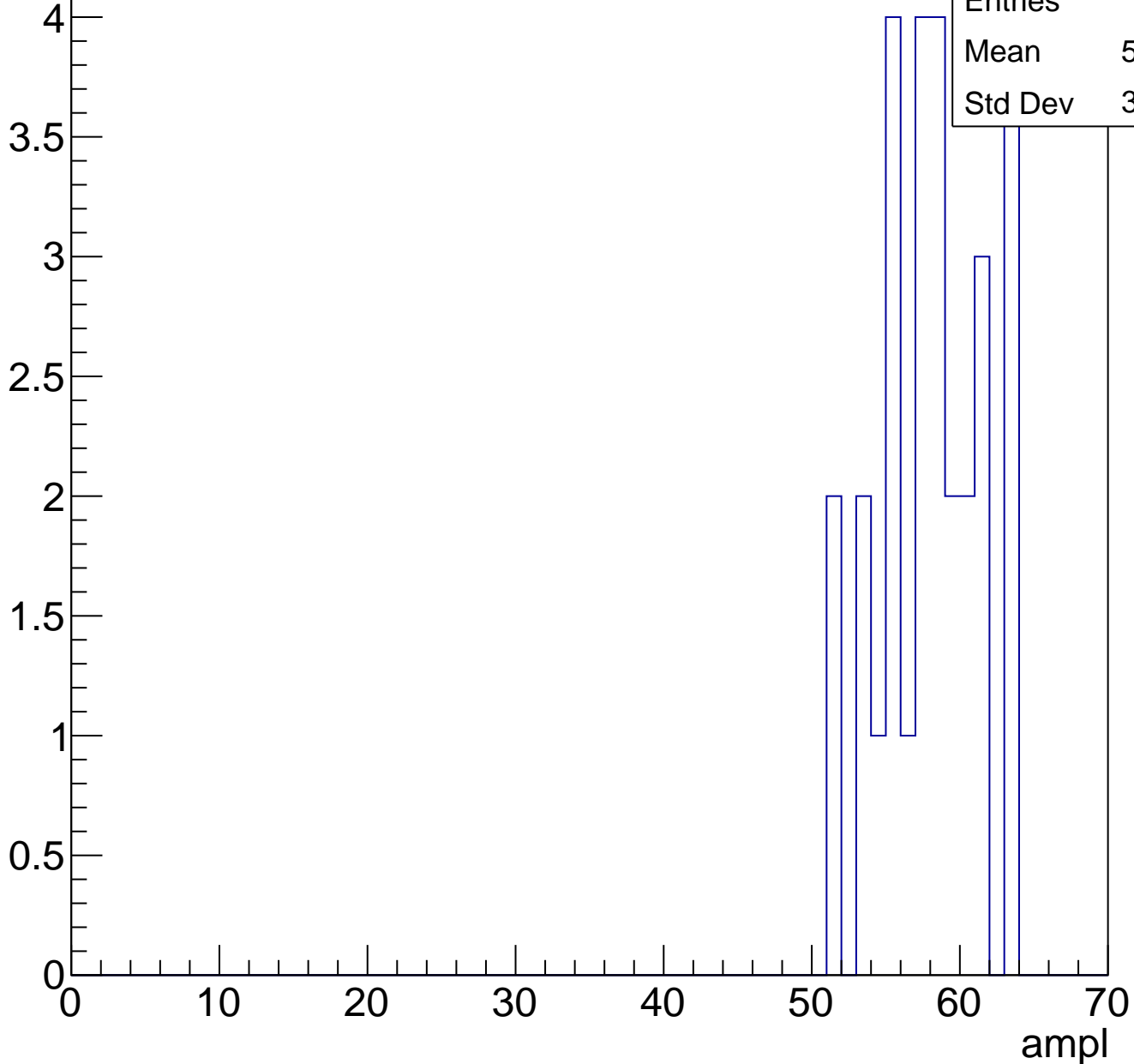
Entry



B1L103S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

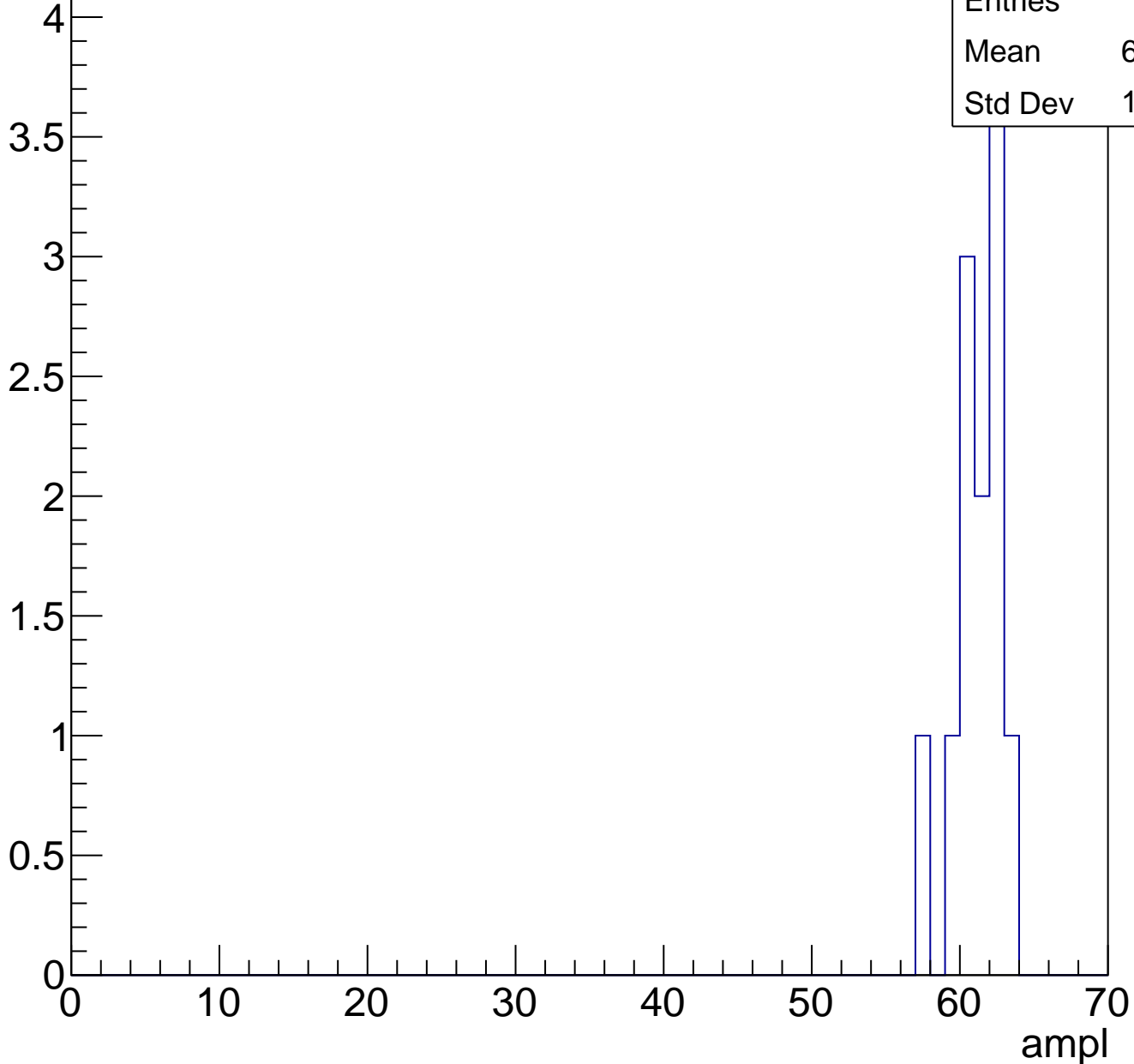


Entries	29
Mean	57.62
Std Dev	3.428

B1L103S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

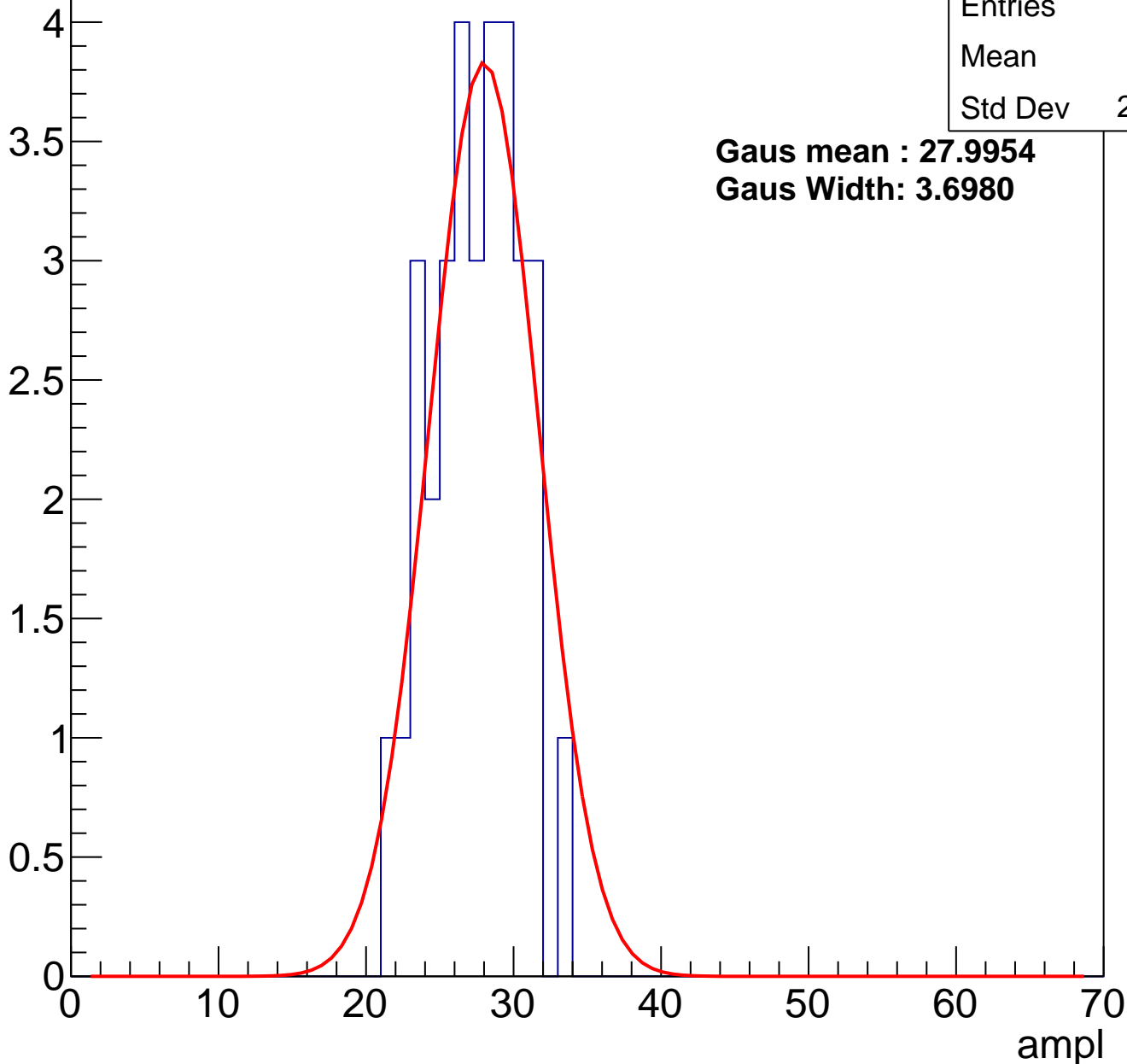


Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U15-ch53, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch53, adc1

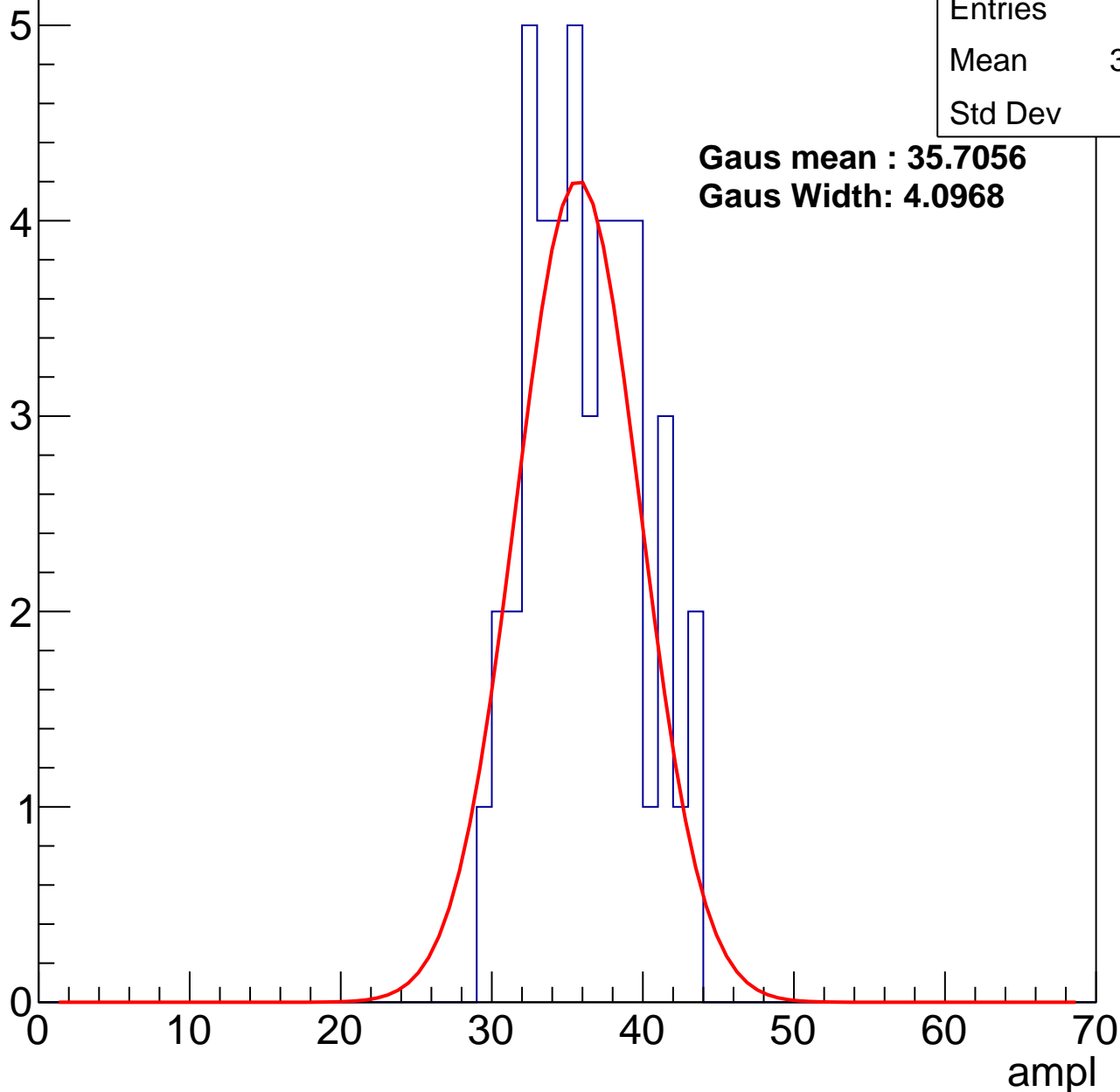
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	35.76
Std Dev	3.61

Gaus mean : 35.7056

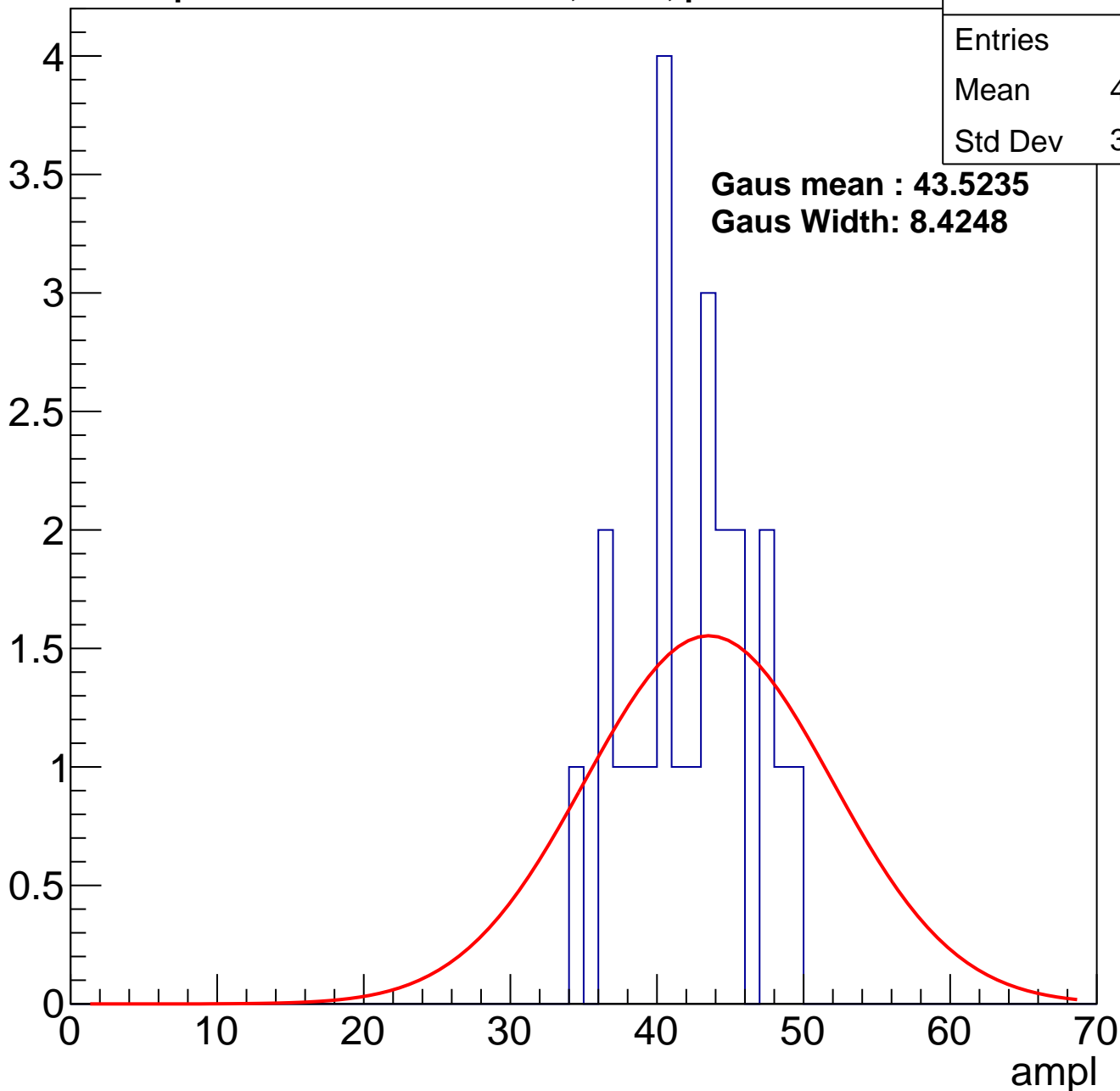
Gaus Width: 4.0968



B1L103S, U15-ch53, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

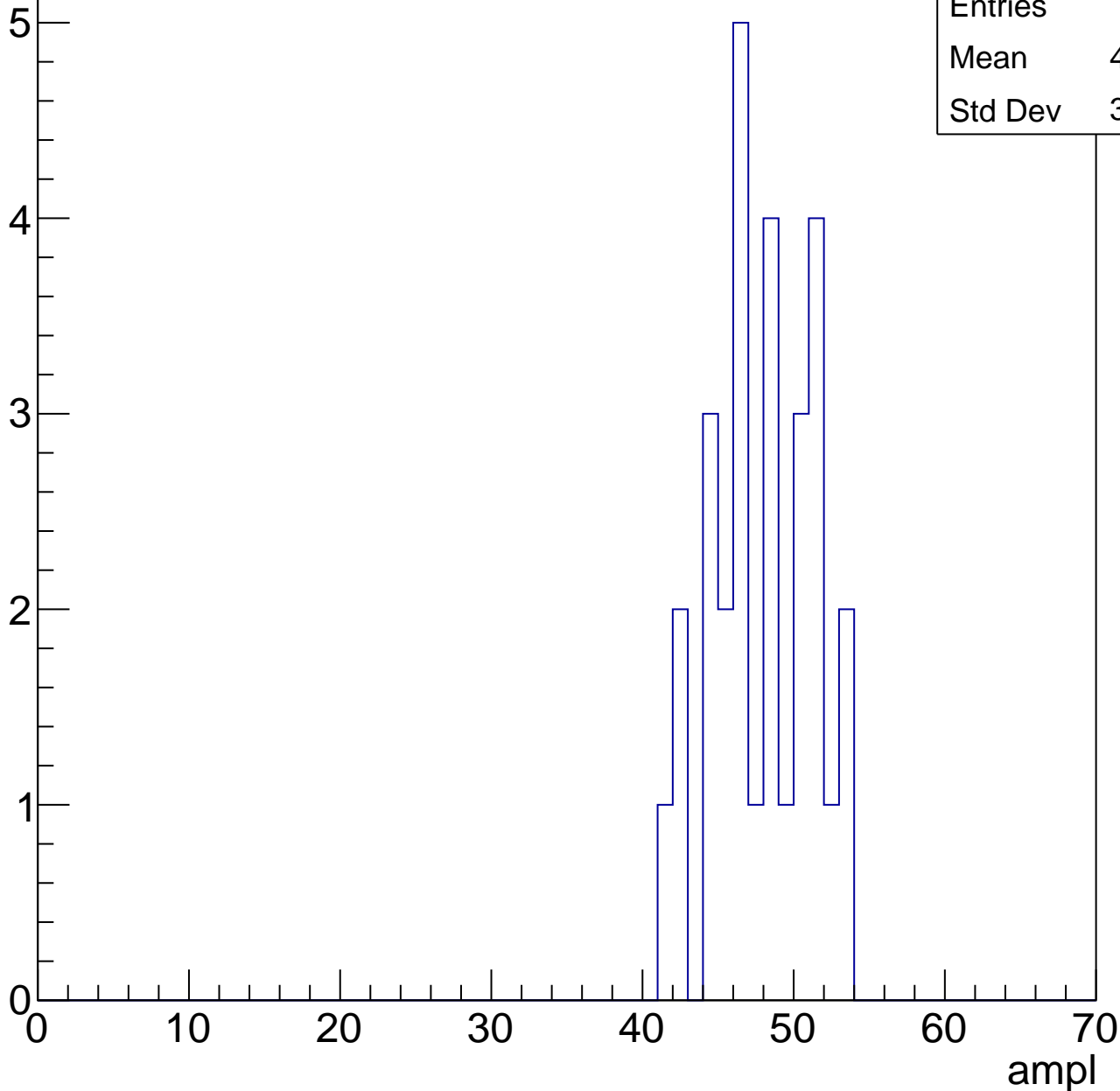


B1L103S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	47.48
Std Dev	3.297

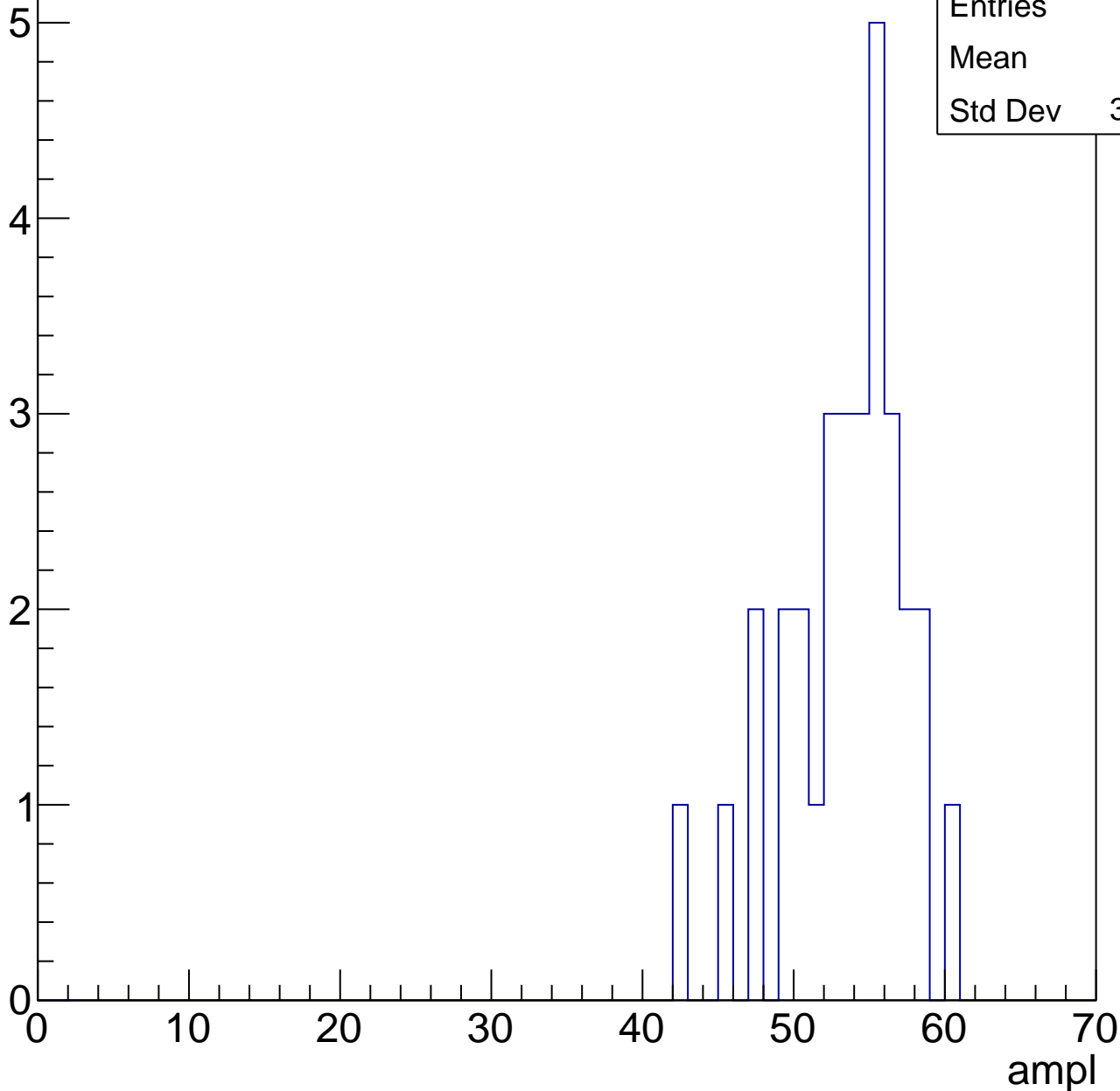


B1L103S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

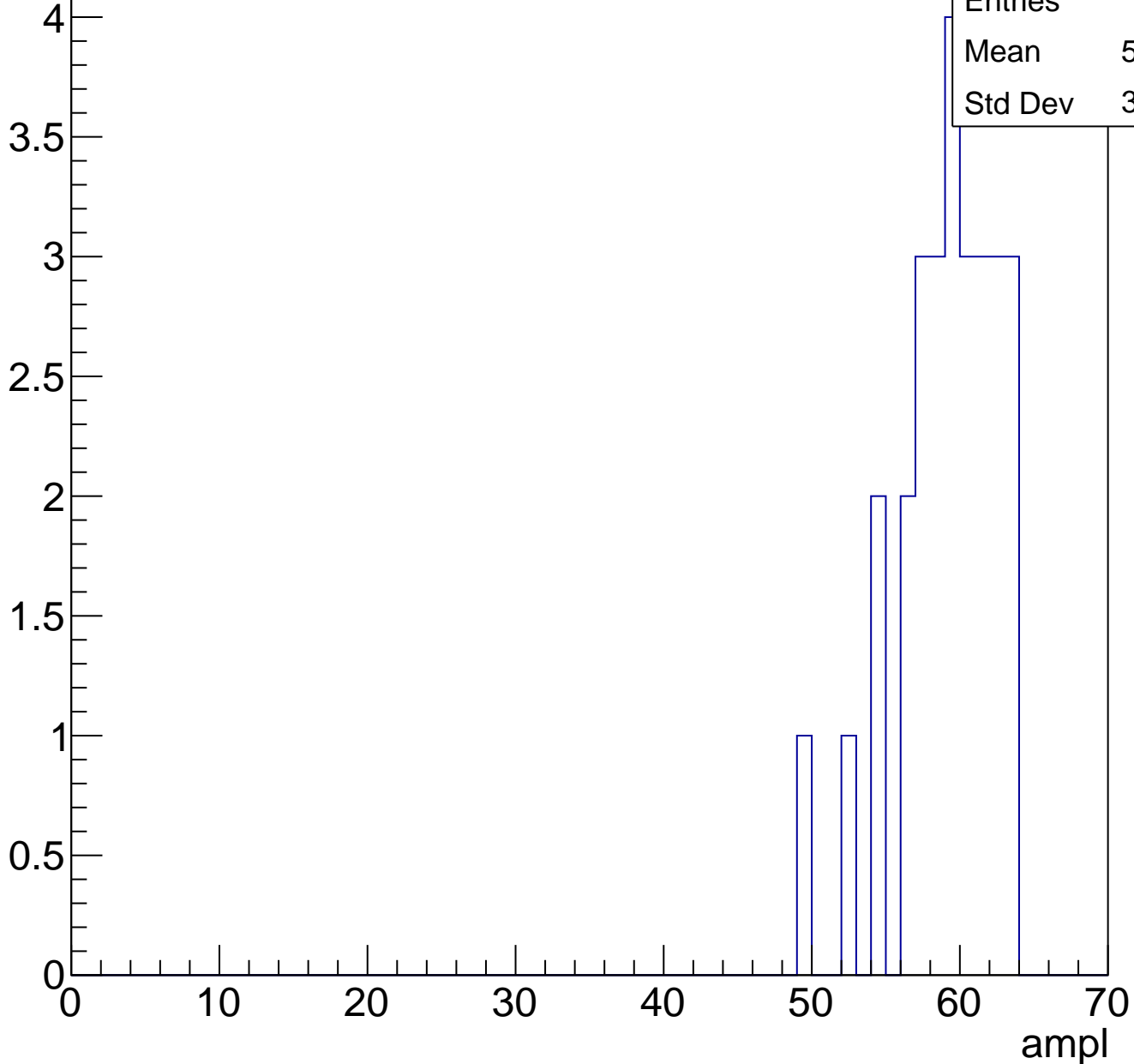
Entries	31
Mean	52.9
Std Dev	3.995



B1L103S, U15-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

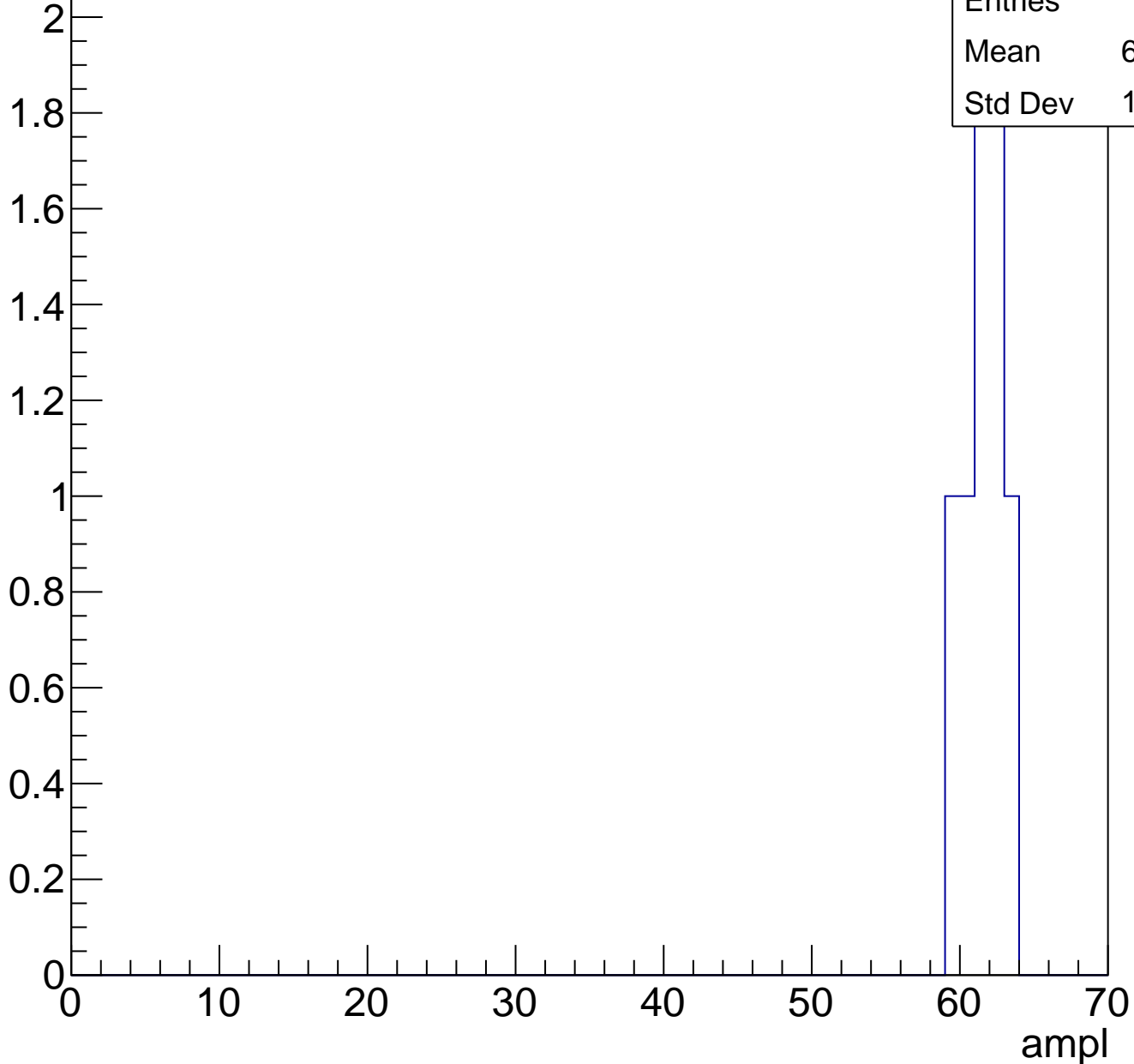
Entry



B1L103S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

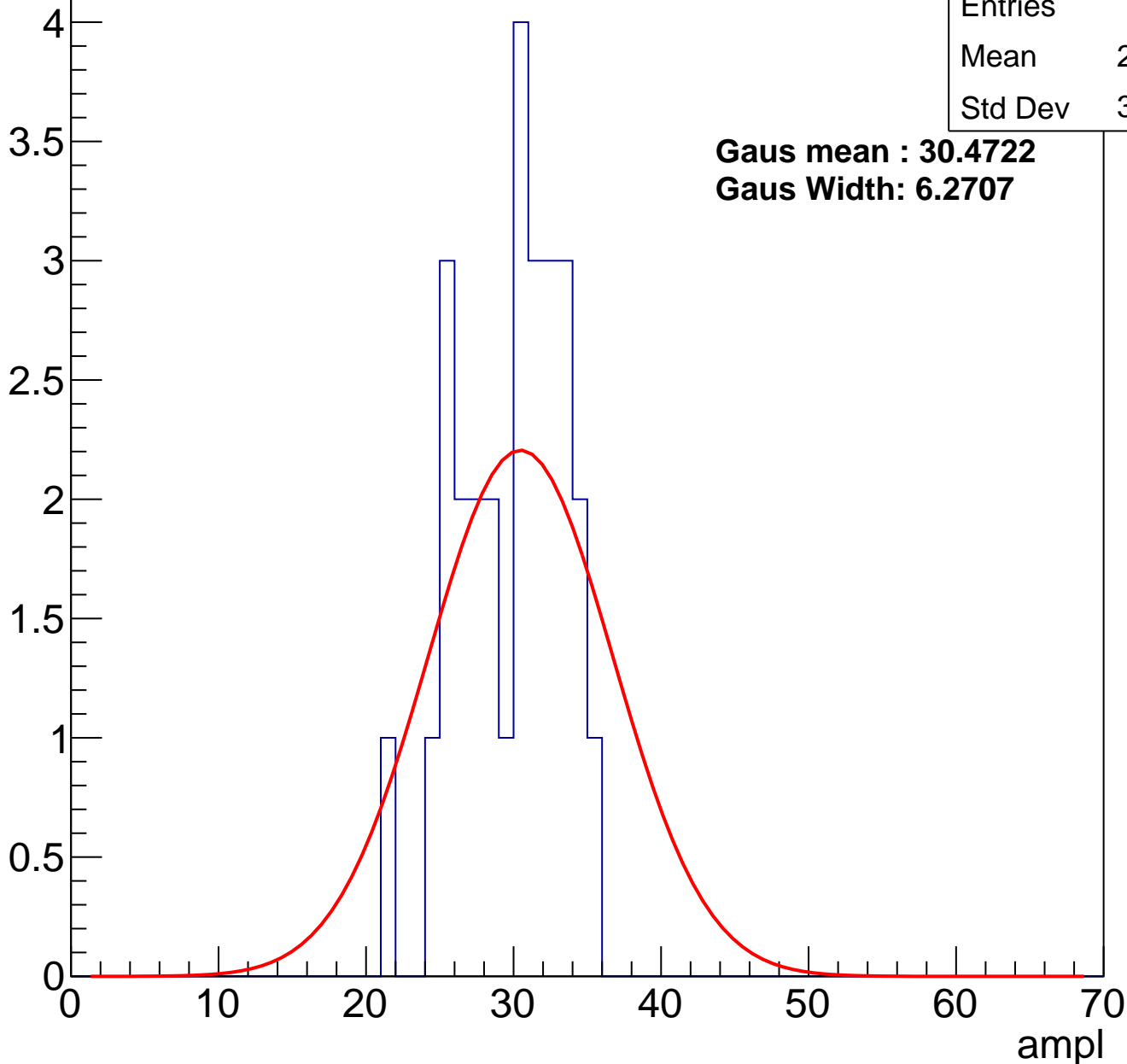
Entry



B1L103S, U15-ch54, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch54, adc1

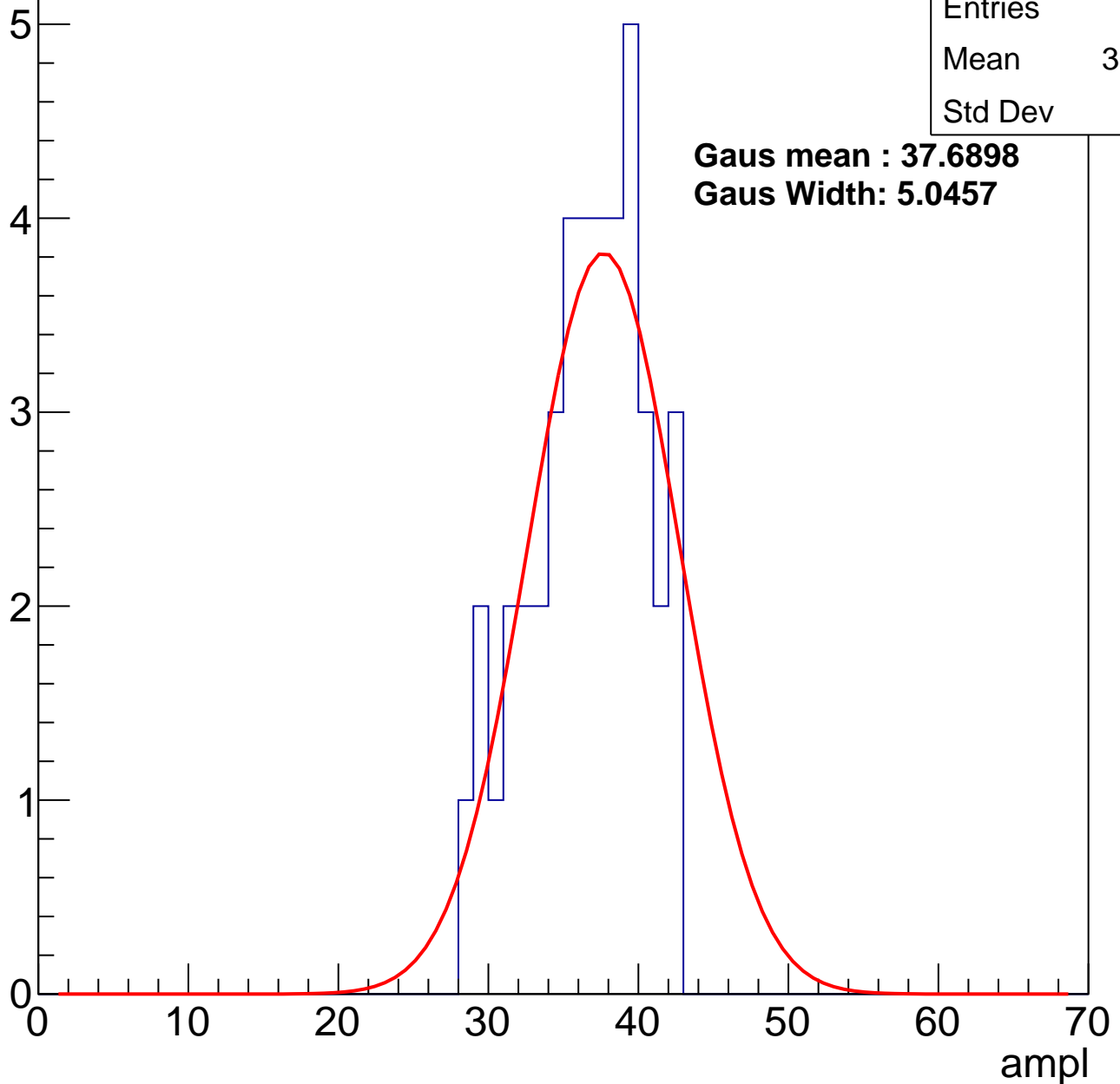
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	36.12
Std Dev	3.73

Gaus mean : 37.6898

Gaus Width: 5.0457



B1L103S, U15-ch54, adc2

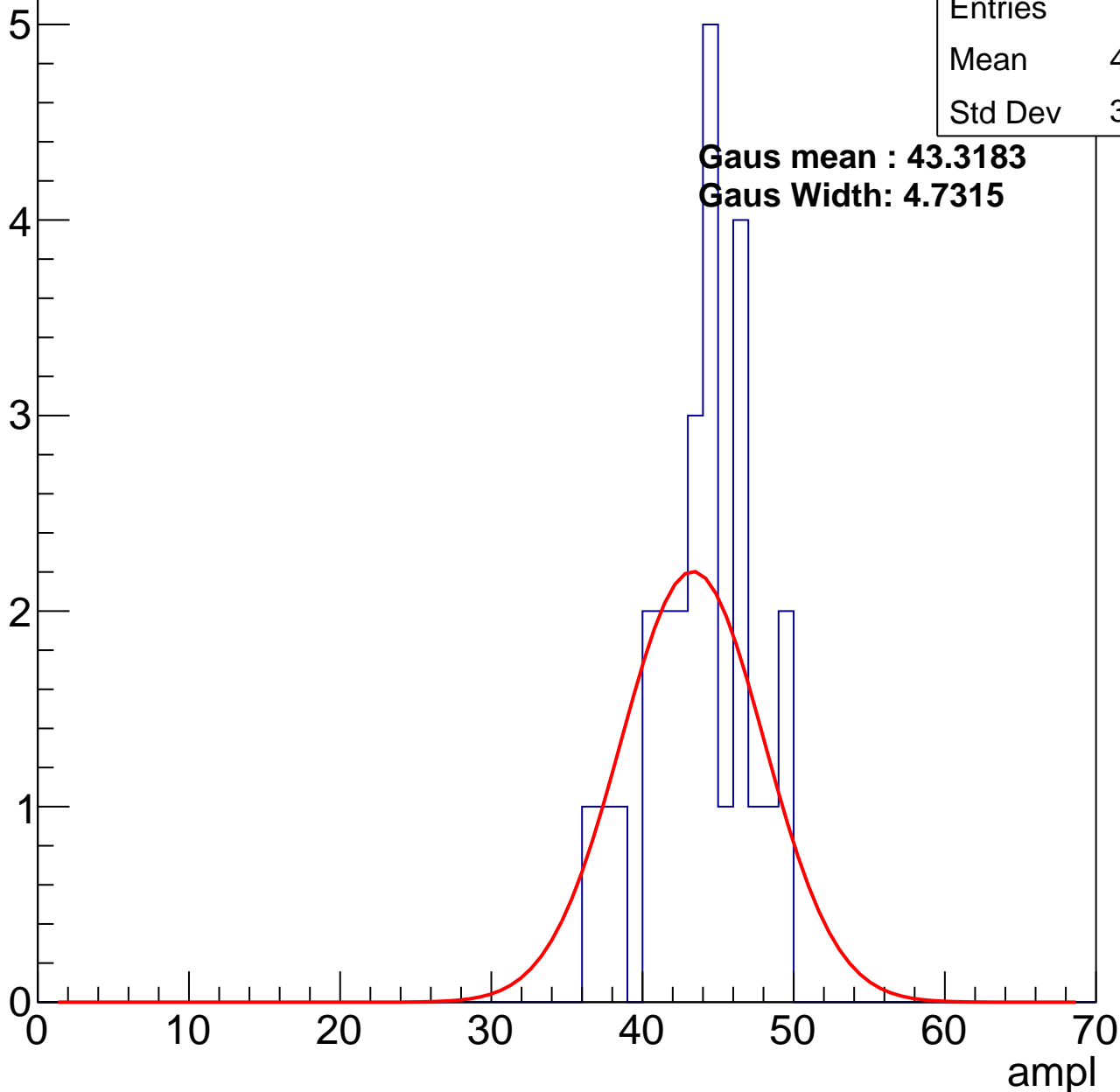
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	43.38
Std Dev	3.352

Gaus mean : 43.3183

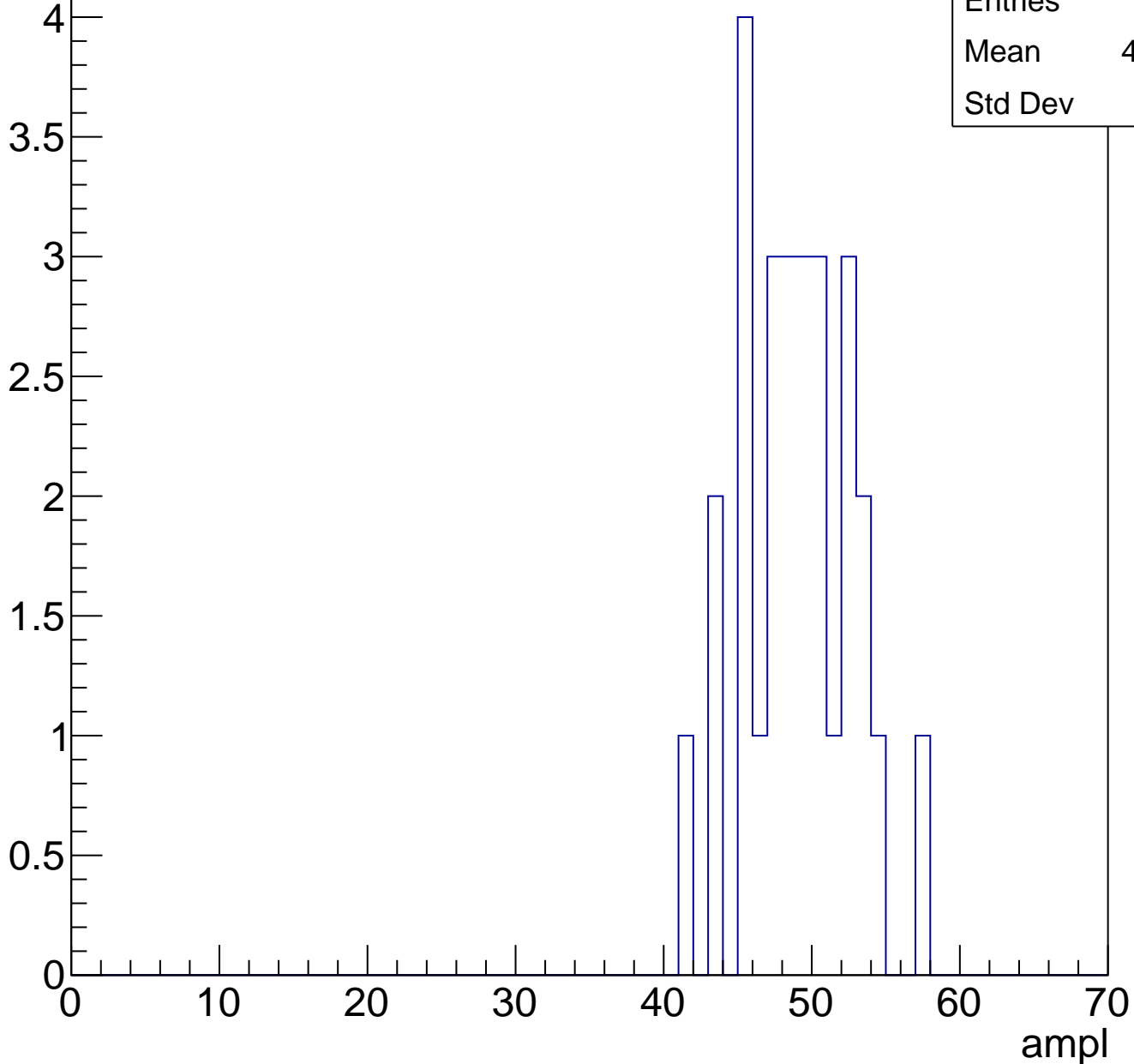
Gaus Width: 4.7315



B1L103S, U15-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

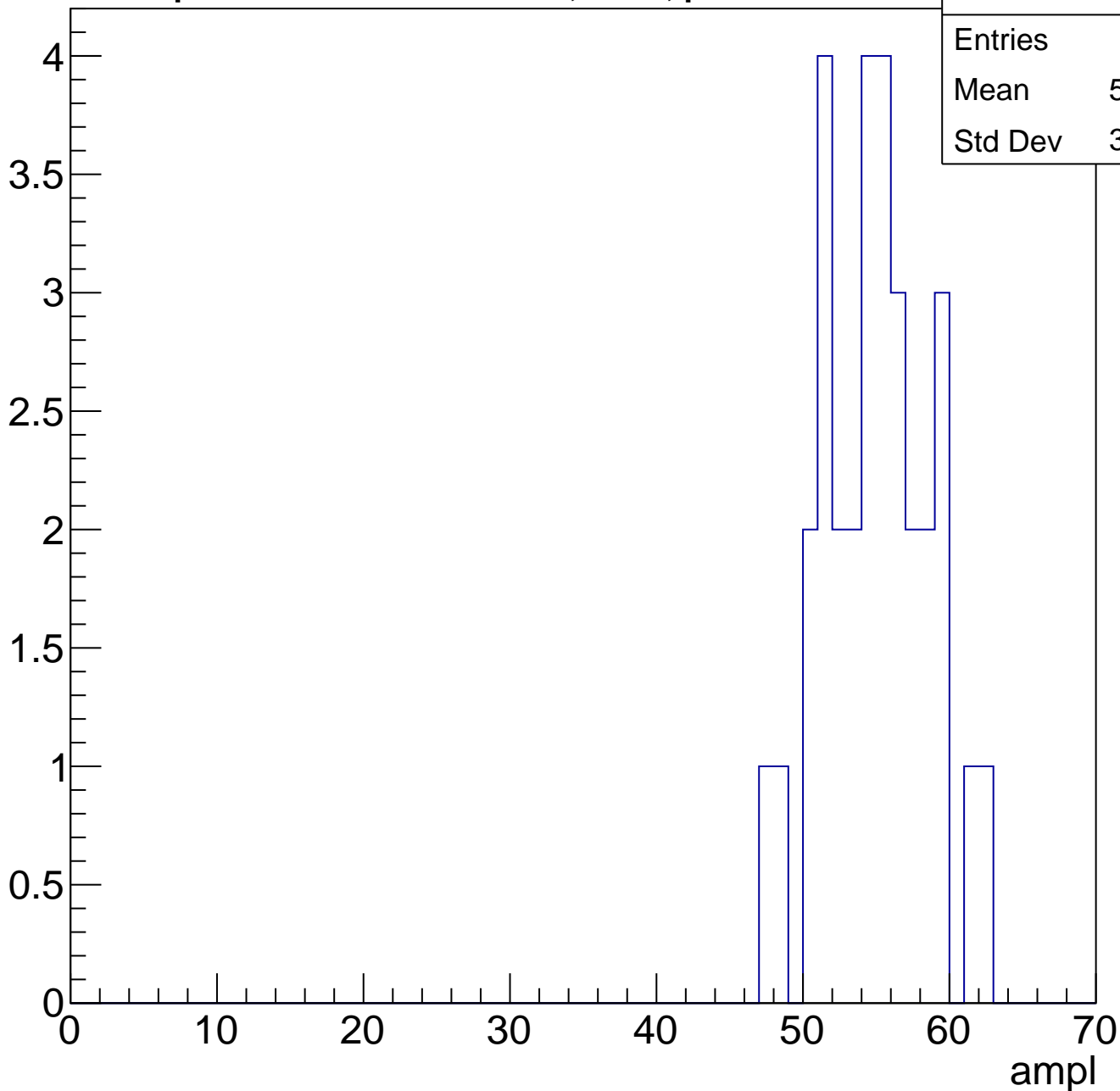


Entries	28
Mean	48.54
Std Dev	3.65

B1L103S, U15-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

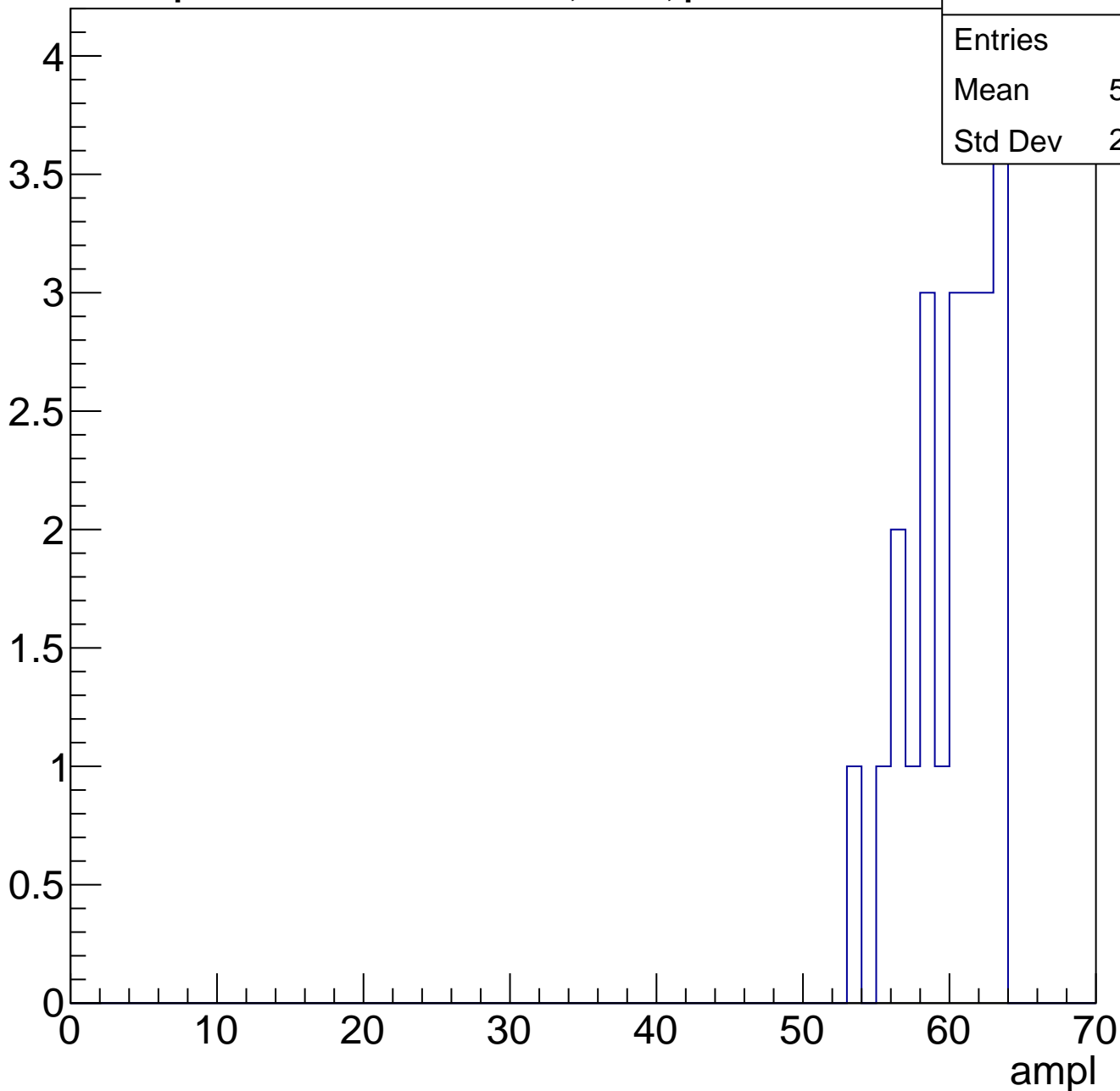
Entry



B1L103S, U15-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	22
Mean	59.59
Std Dev	2.823

B1L103S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

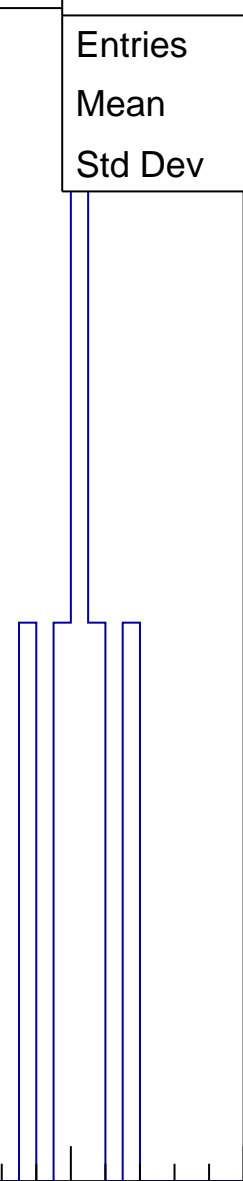
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60
Std Dev	1.826

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch55, adc0

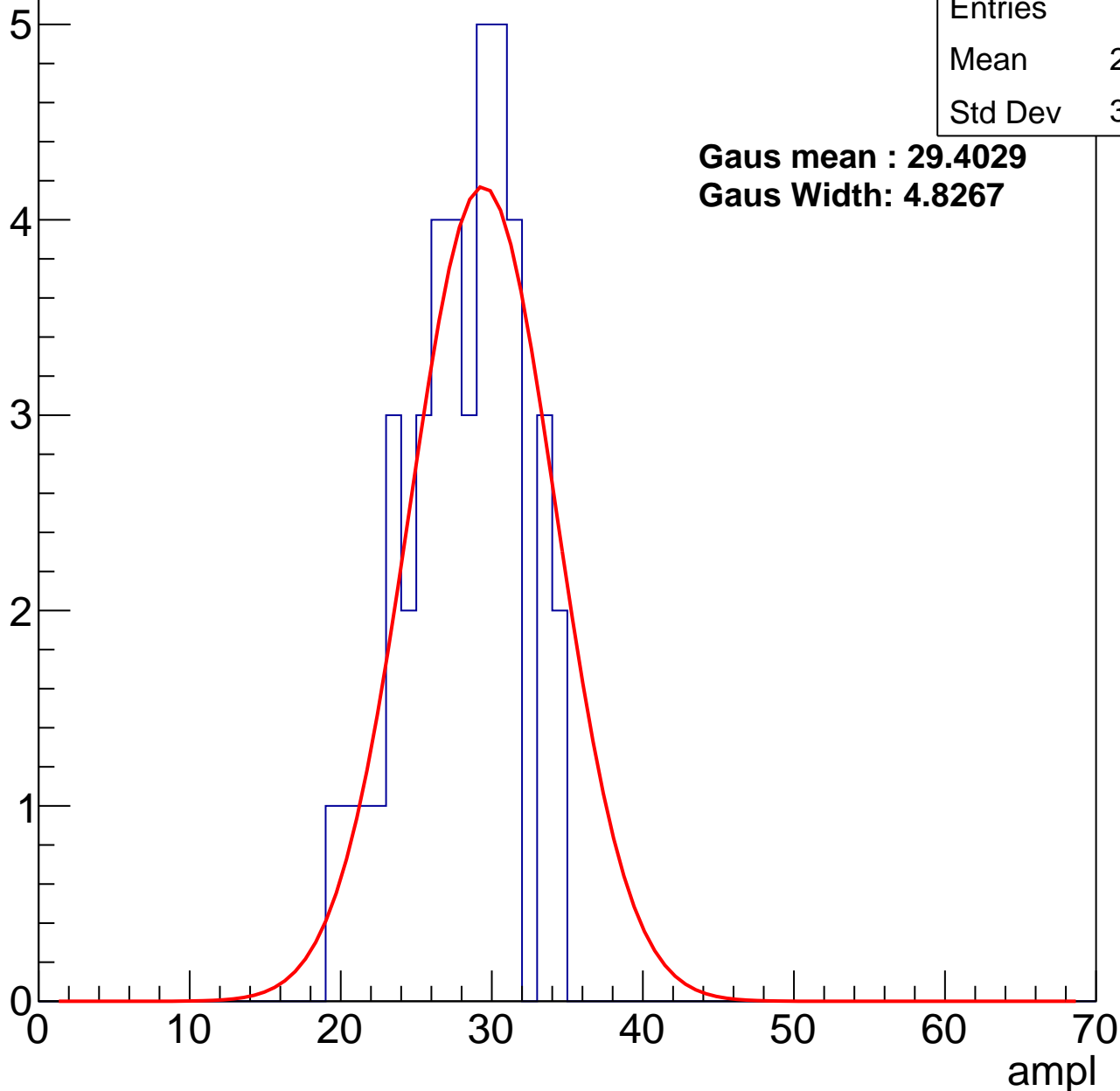
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	27.52
Std Dev	3.724

Gaus mean : 29.4029

Gaus Width: 4.8267



B1L103S, U15-ch55, adc1

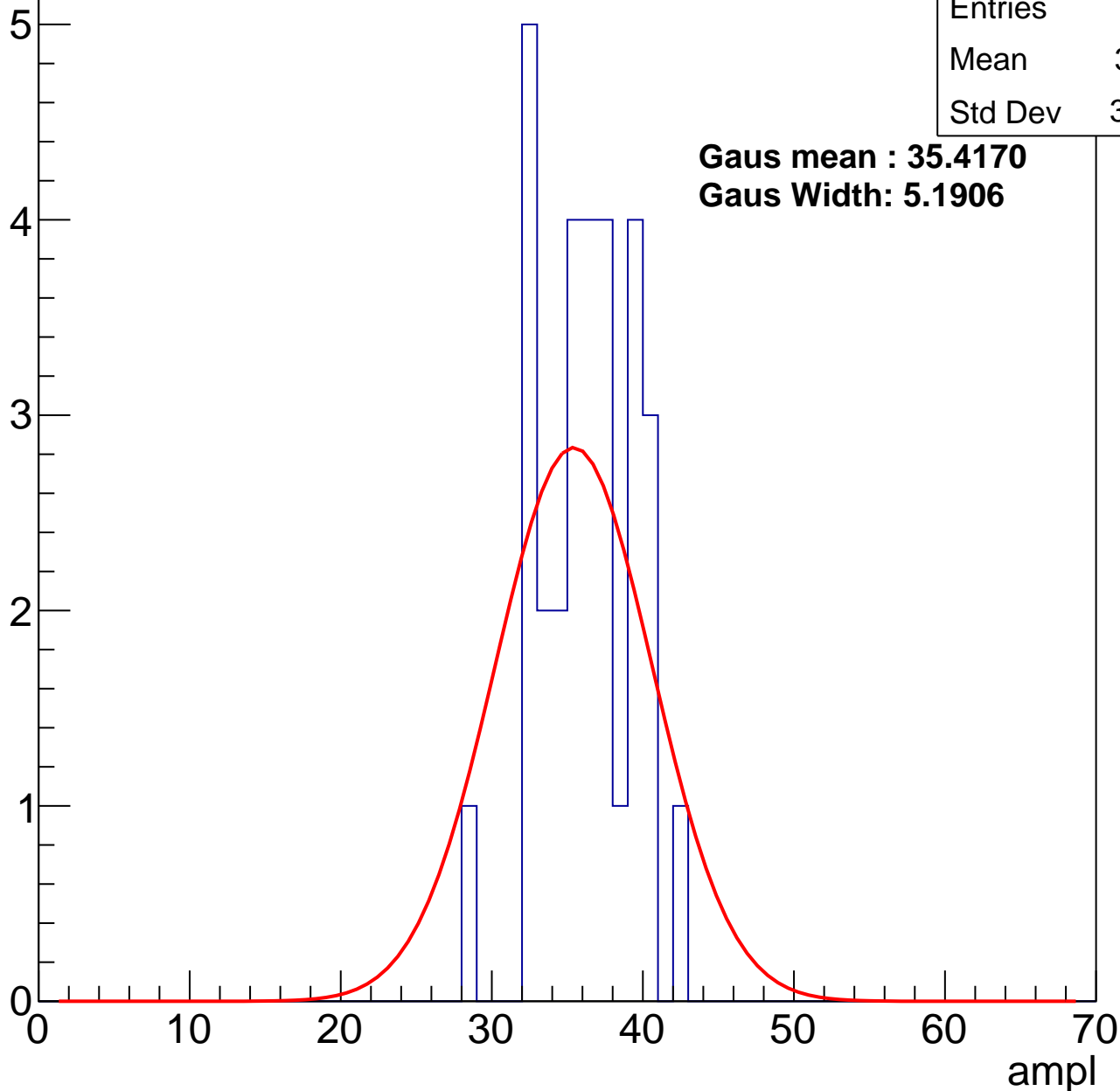
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	35.81
Std Dev	3.115

Gaus mean : 35.4170

Gaus Width: 5.1906



B1L103S, U15-ch55, adc2

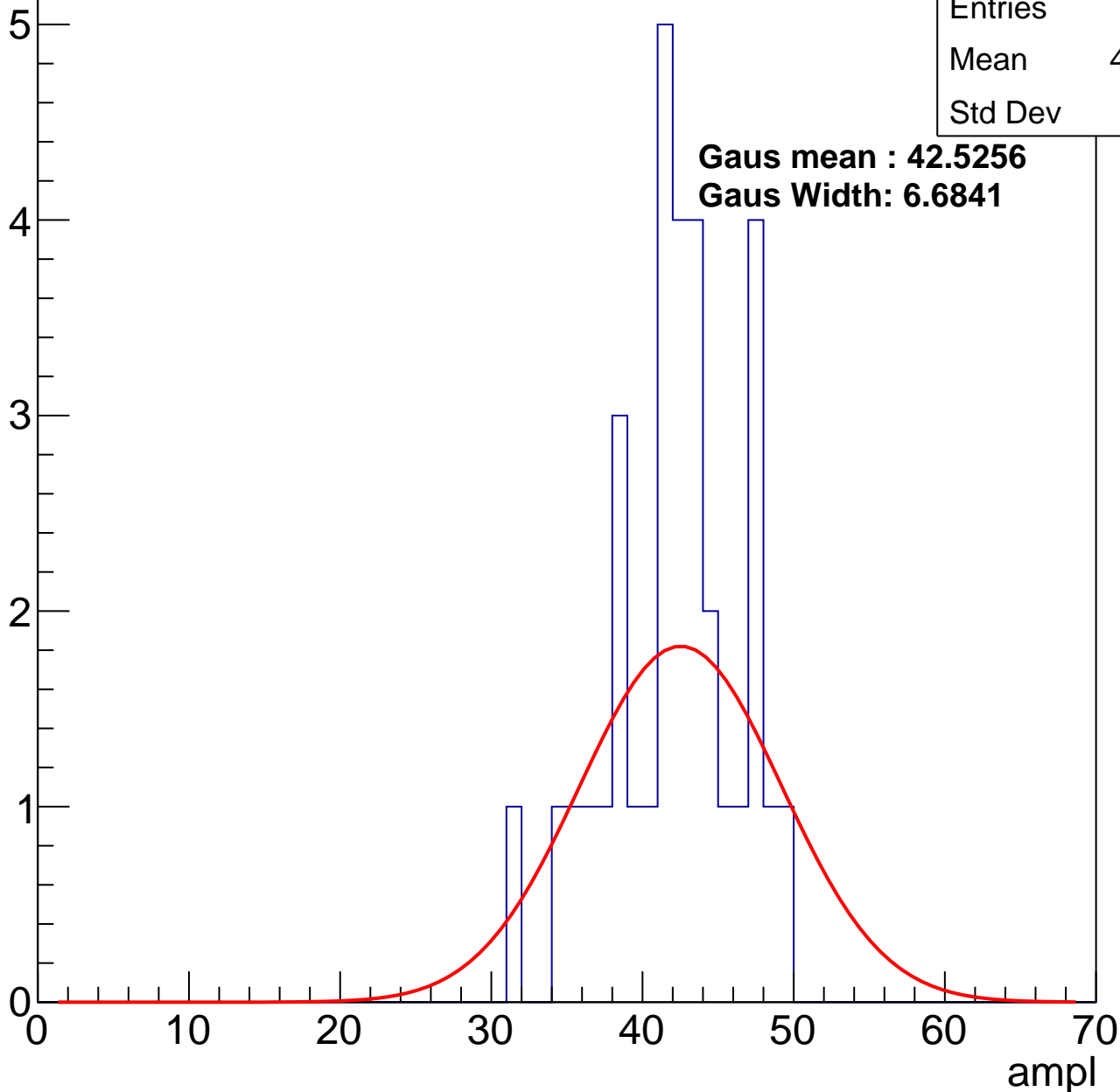
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	41.67
Std Dev	4.19

Gaus mean : 42.5256

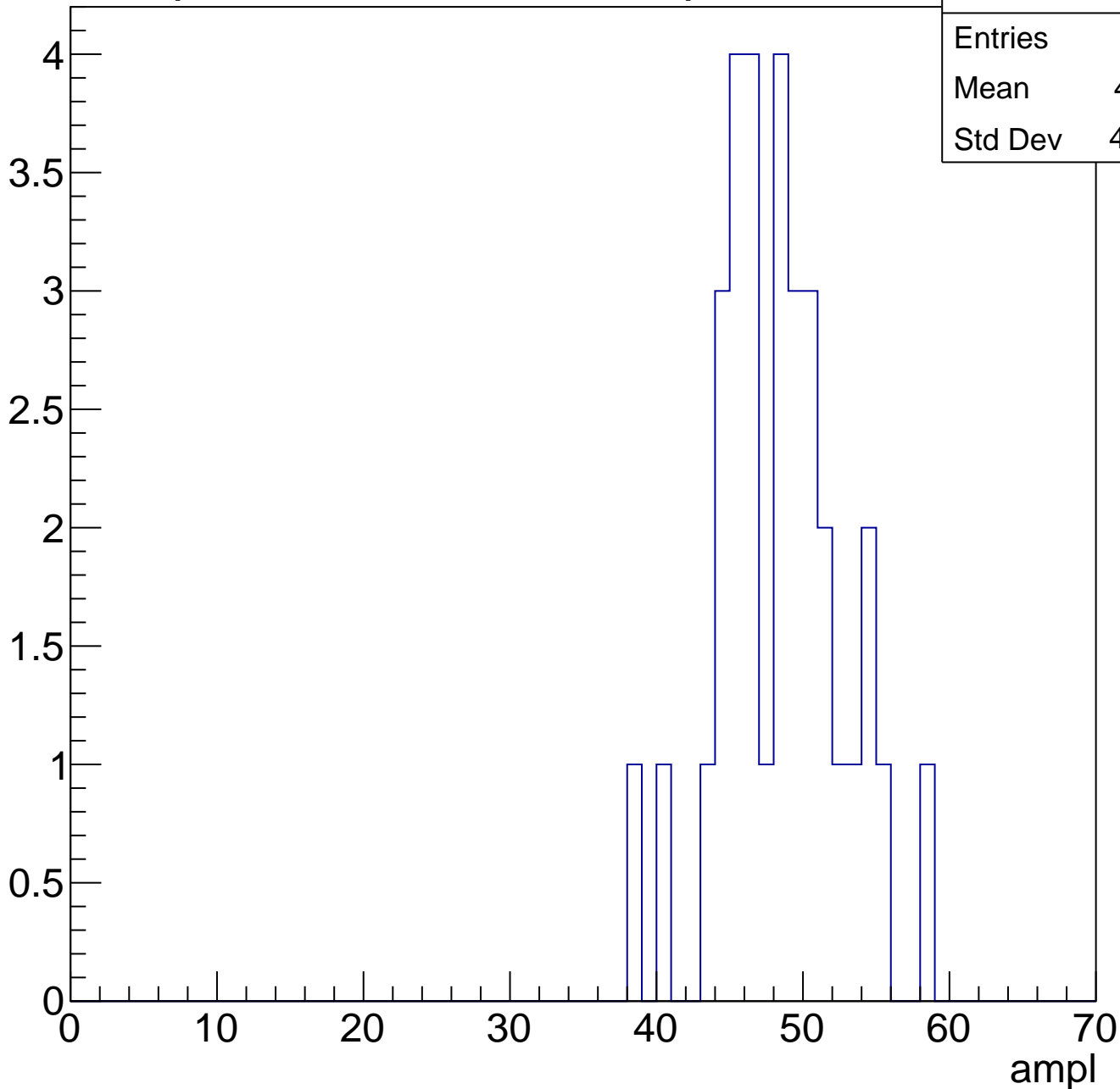
Gaus Width: 6.6841



B1L103S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

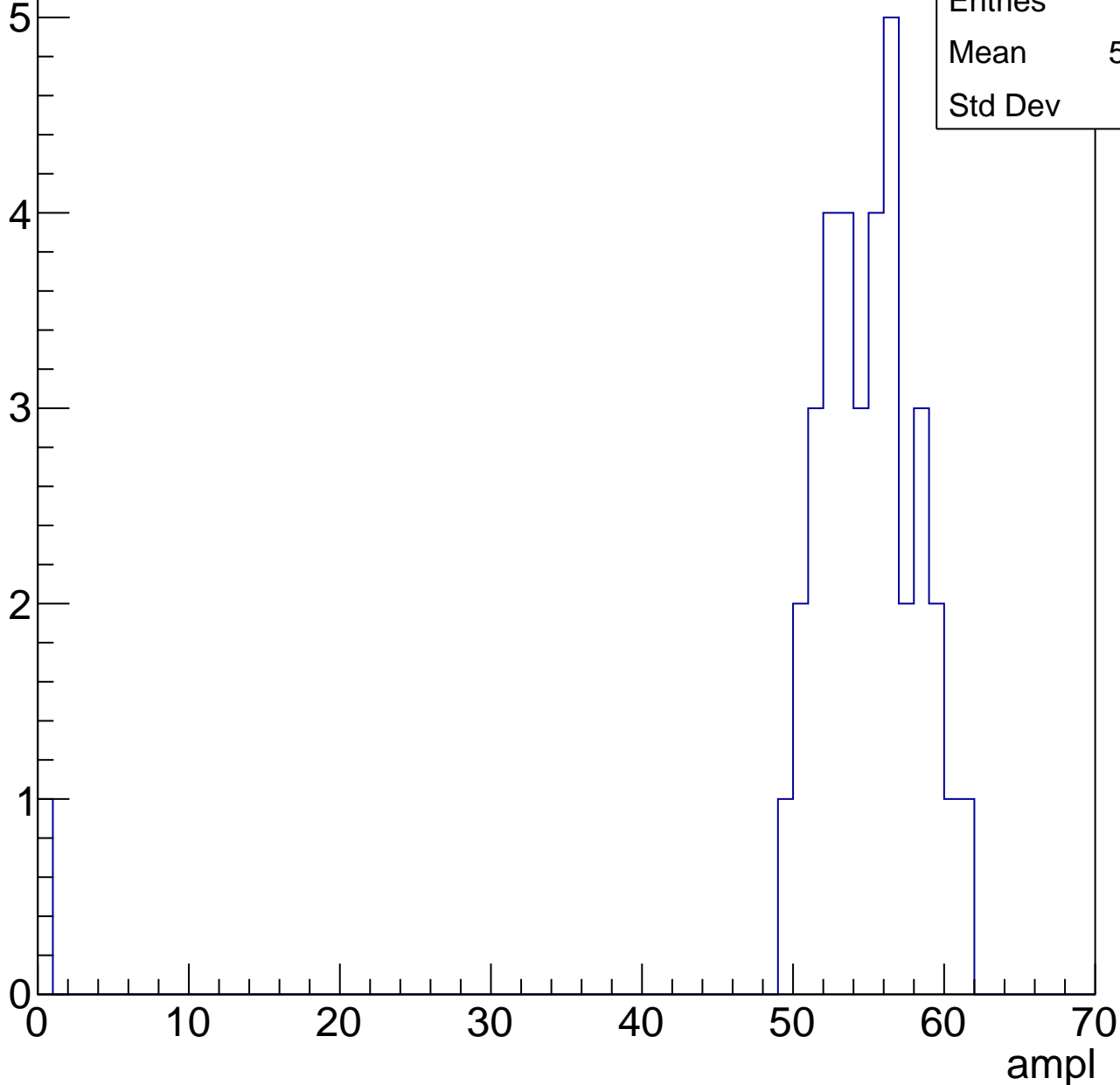


B1L103S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

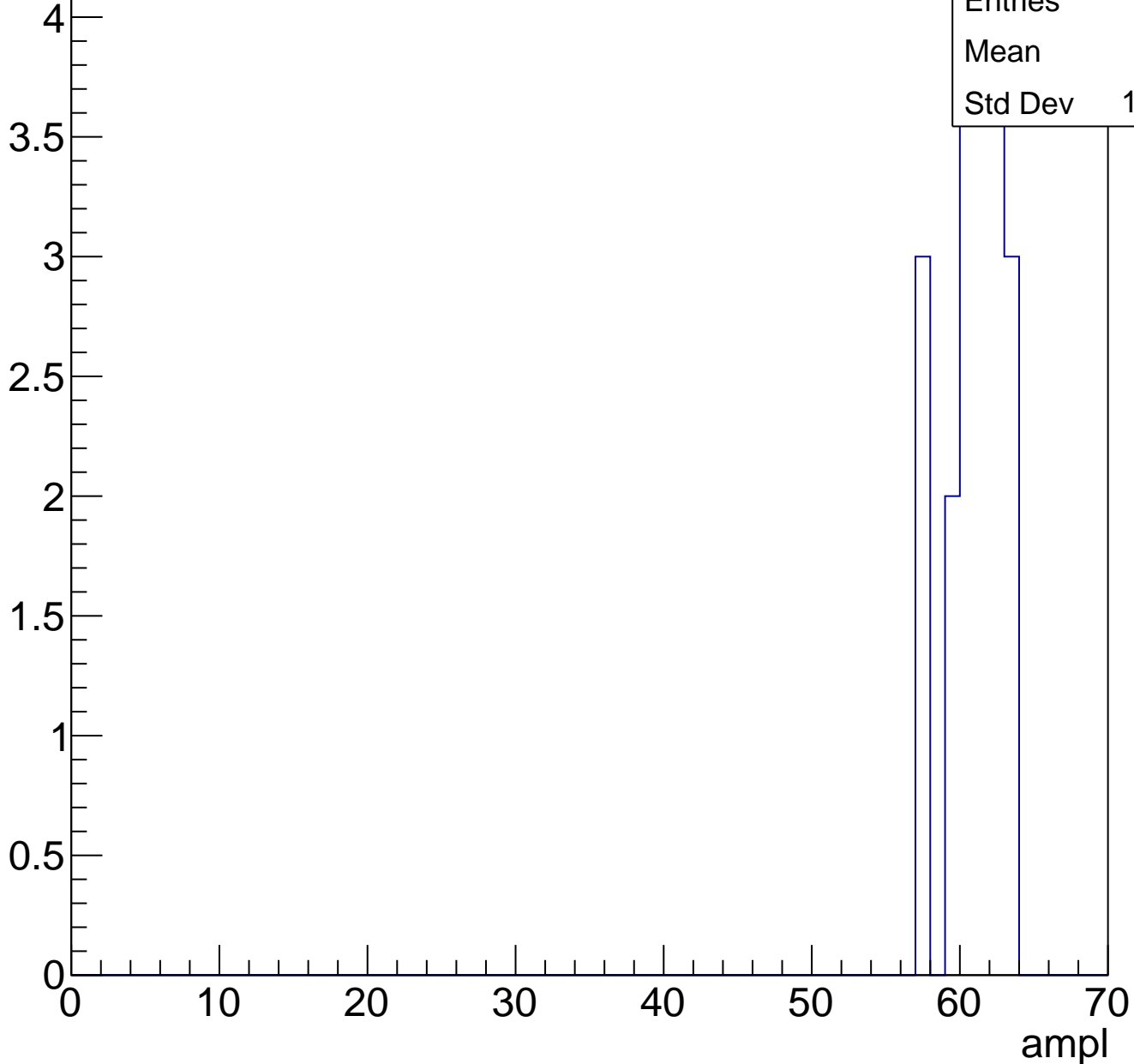
Entries	36
Mean	53.08
Std Dev	9.45



B1L103S, U15-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61
Std Dev	2.098

ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

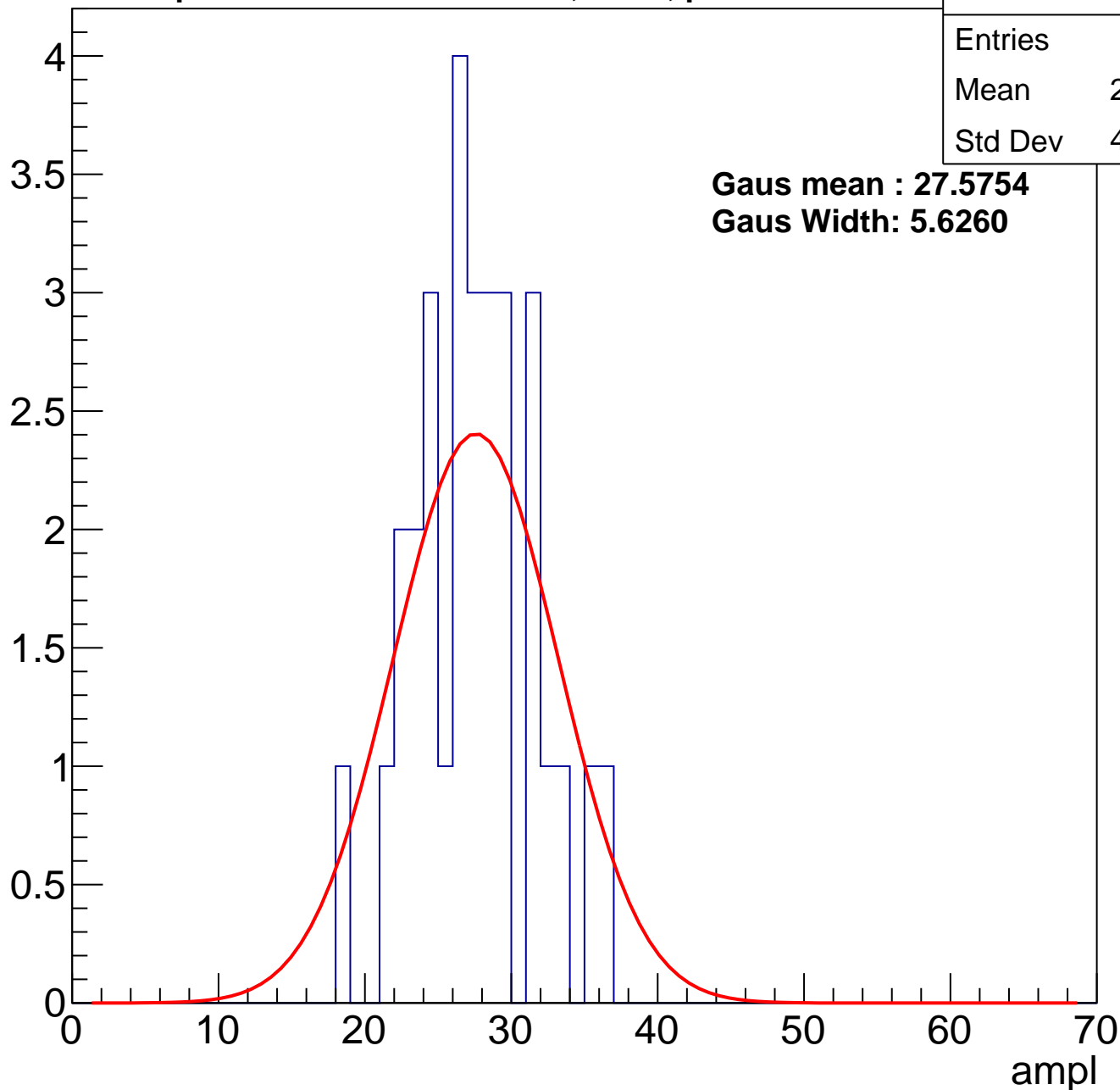


Entries	3
Mean	0
Std Dev	0

B1L103S, U15-ch56, adc0

calib_packv5_042523_0143.root, FC#7, port C2

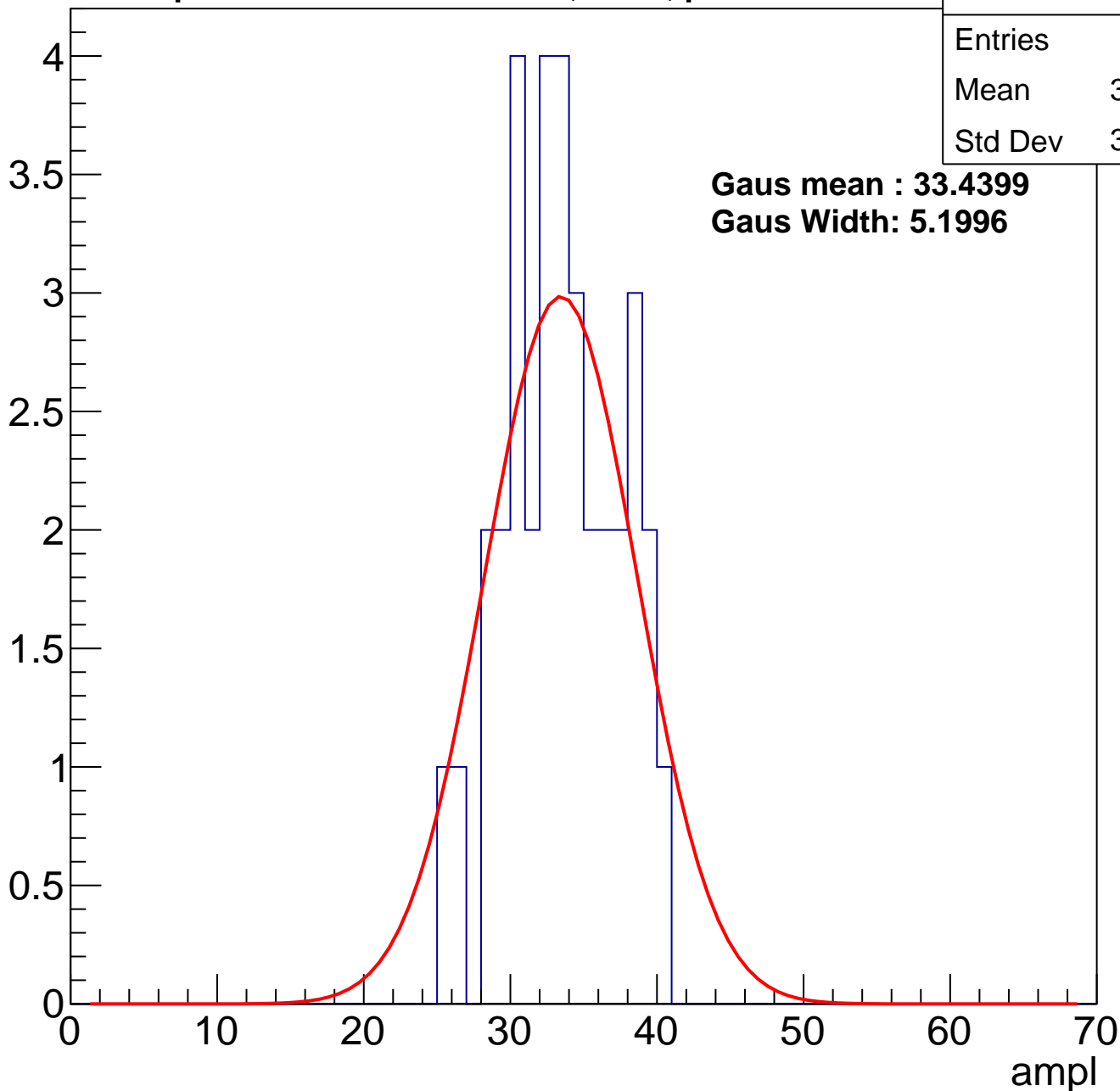
Entry



B1L103S, U15-ch56, adc1

calib_packv5_042523_0143.root, FC#7, port C2

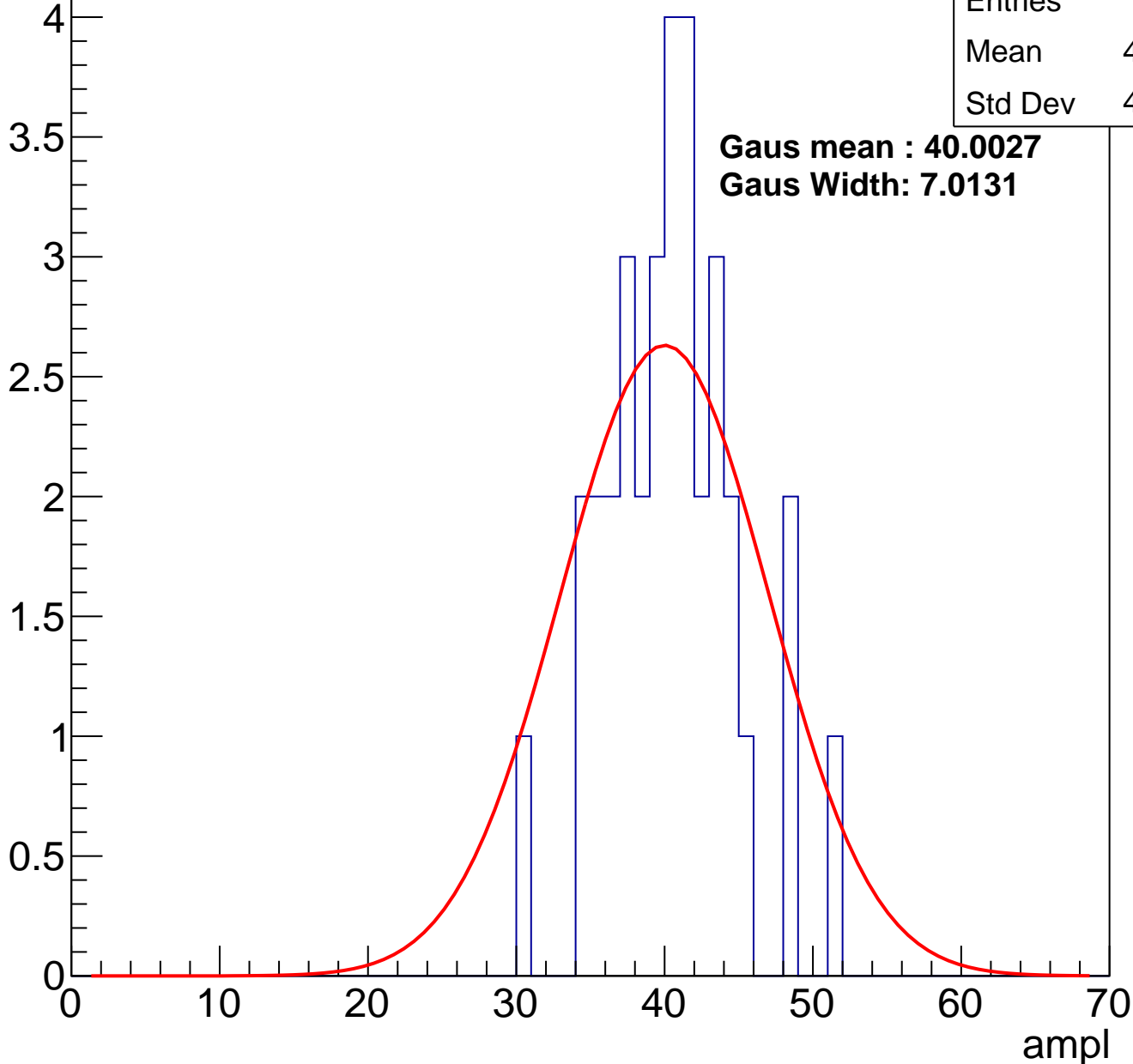
Entry



B1L103S, U15-ch56, adc2

calib_packv5_042523_0143.root, FC#7, port C2

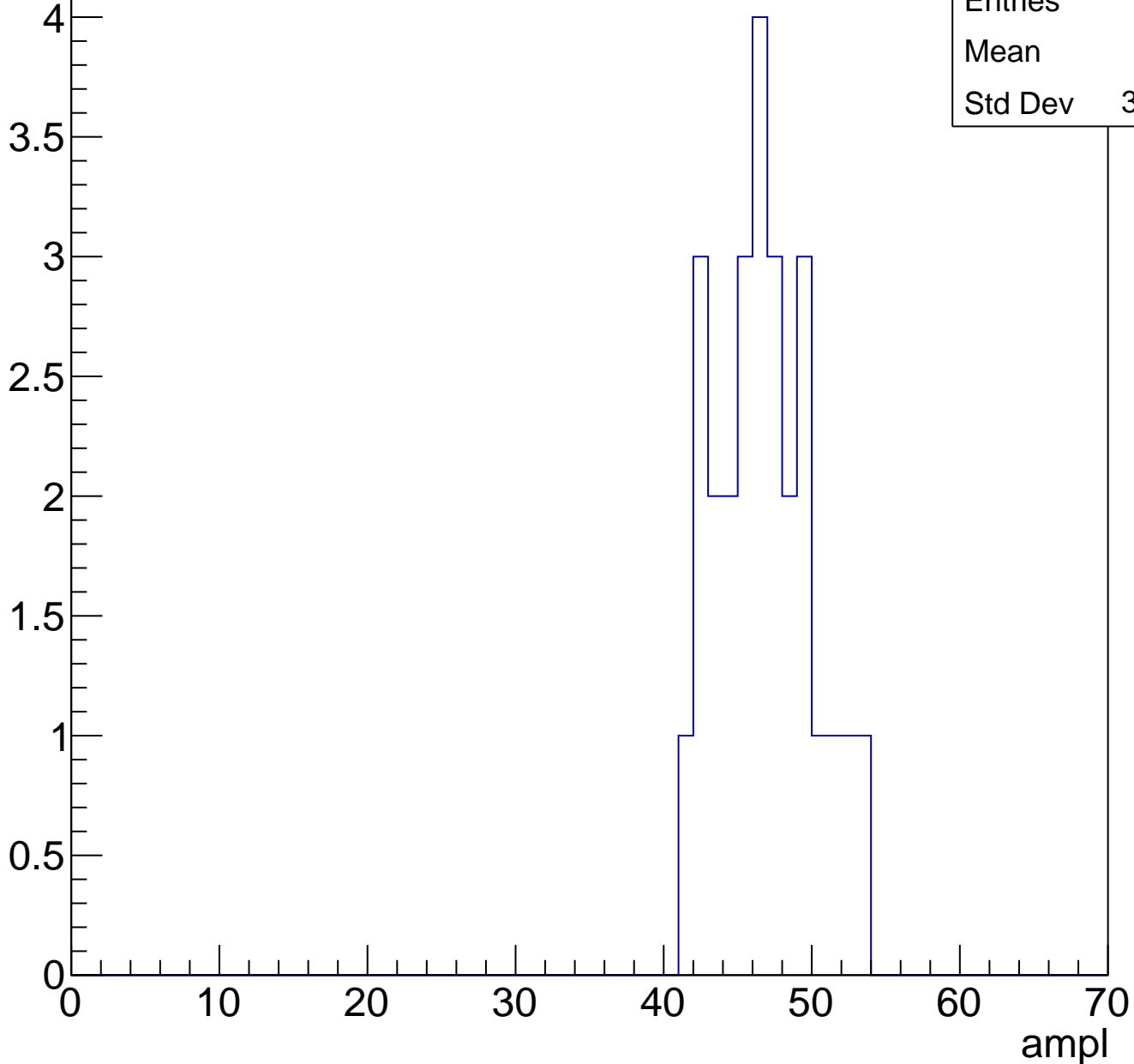
Entry



B1L103S, U15-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

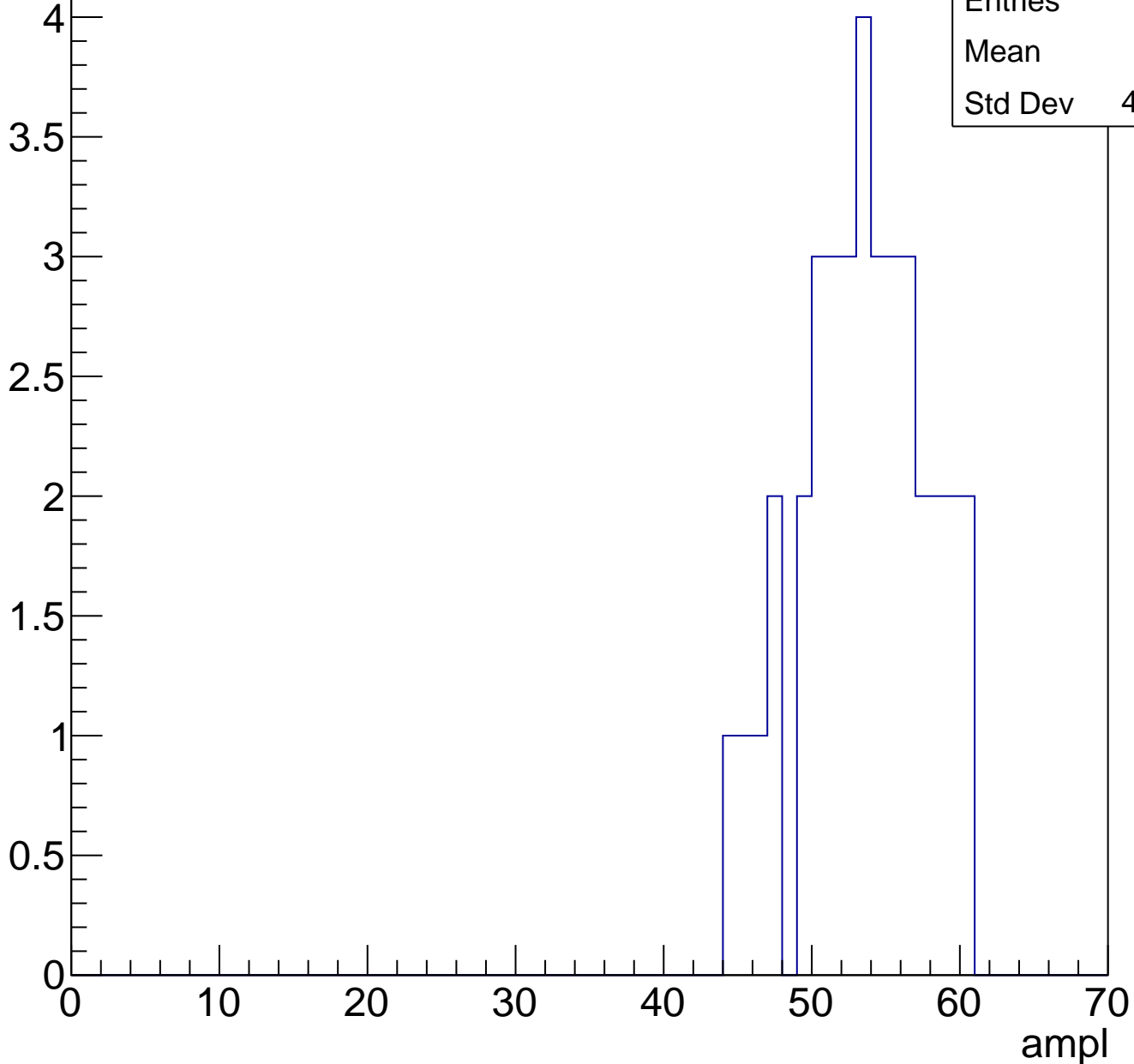
Entry



B1L103S, U15-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

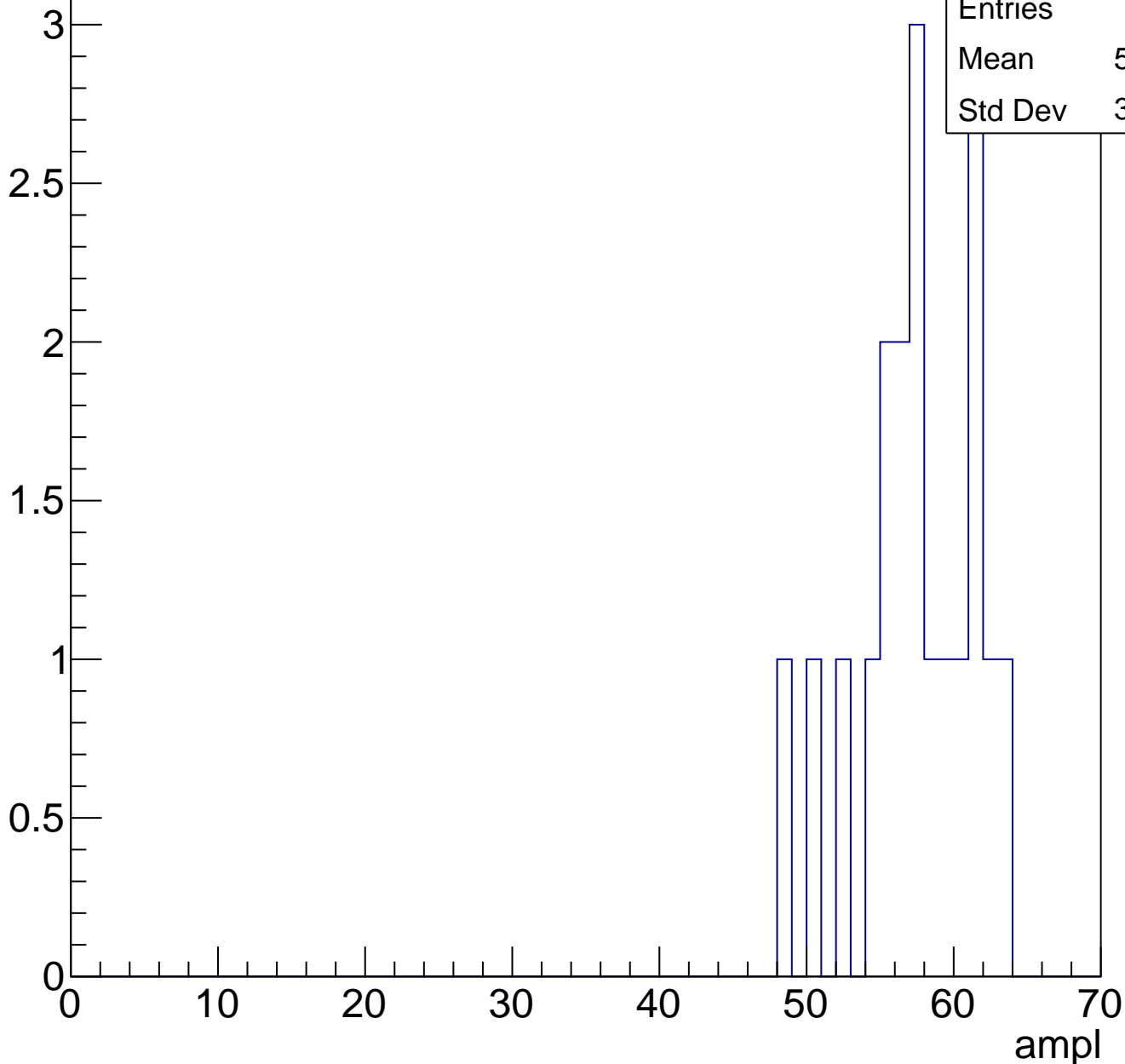


Entries	37
Mean	53
Std Dev	4.139

B1L103S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

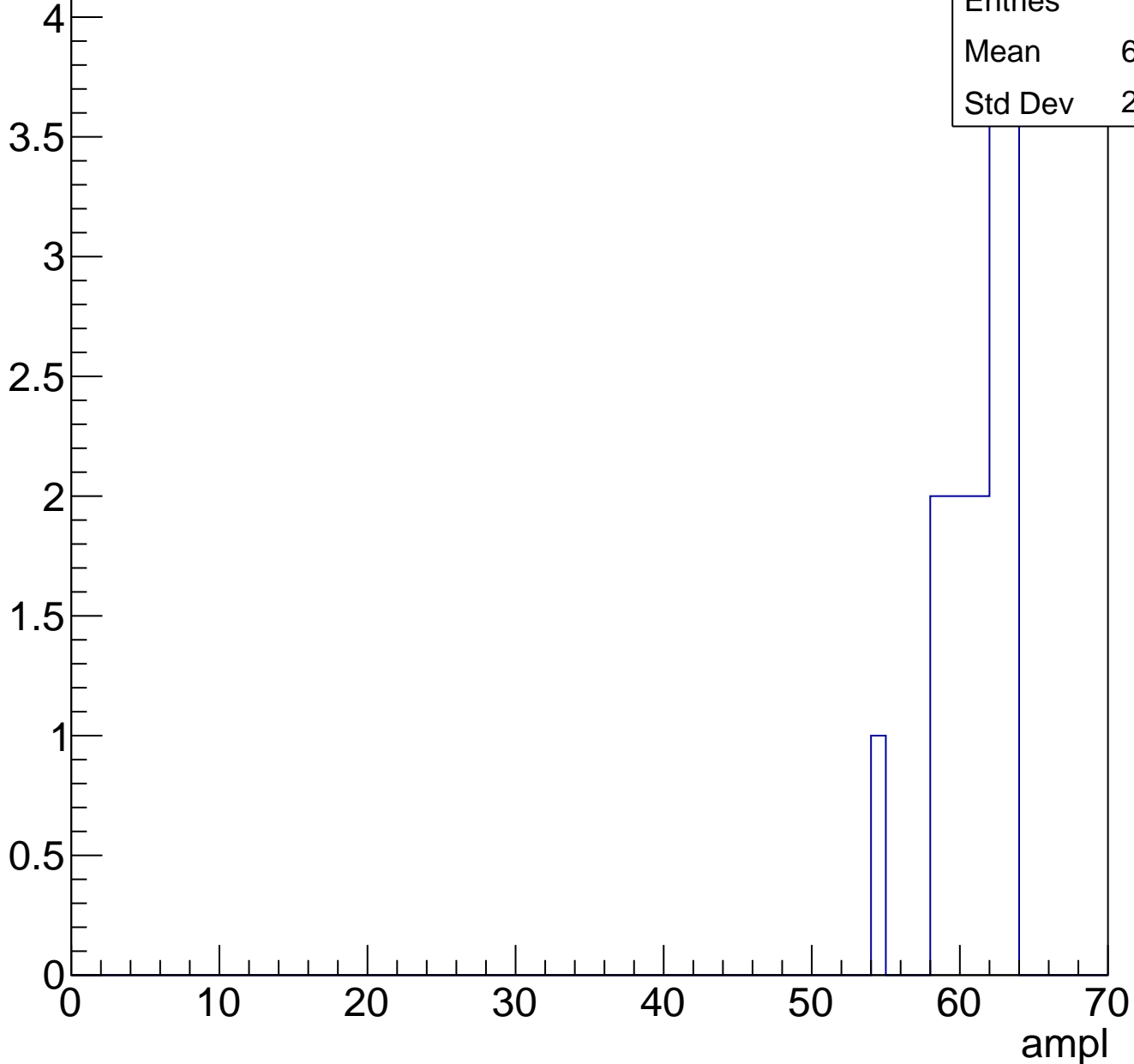
Entry



B1L103S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

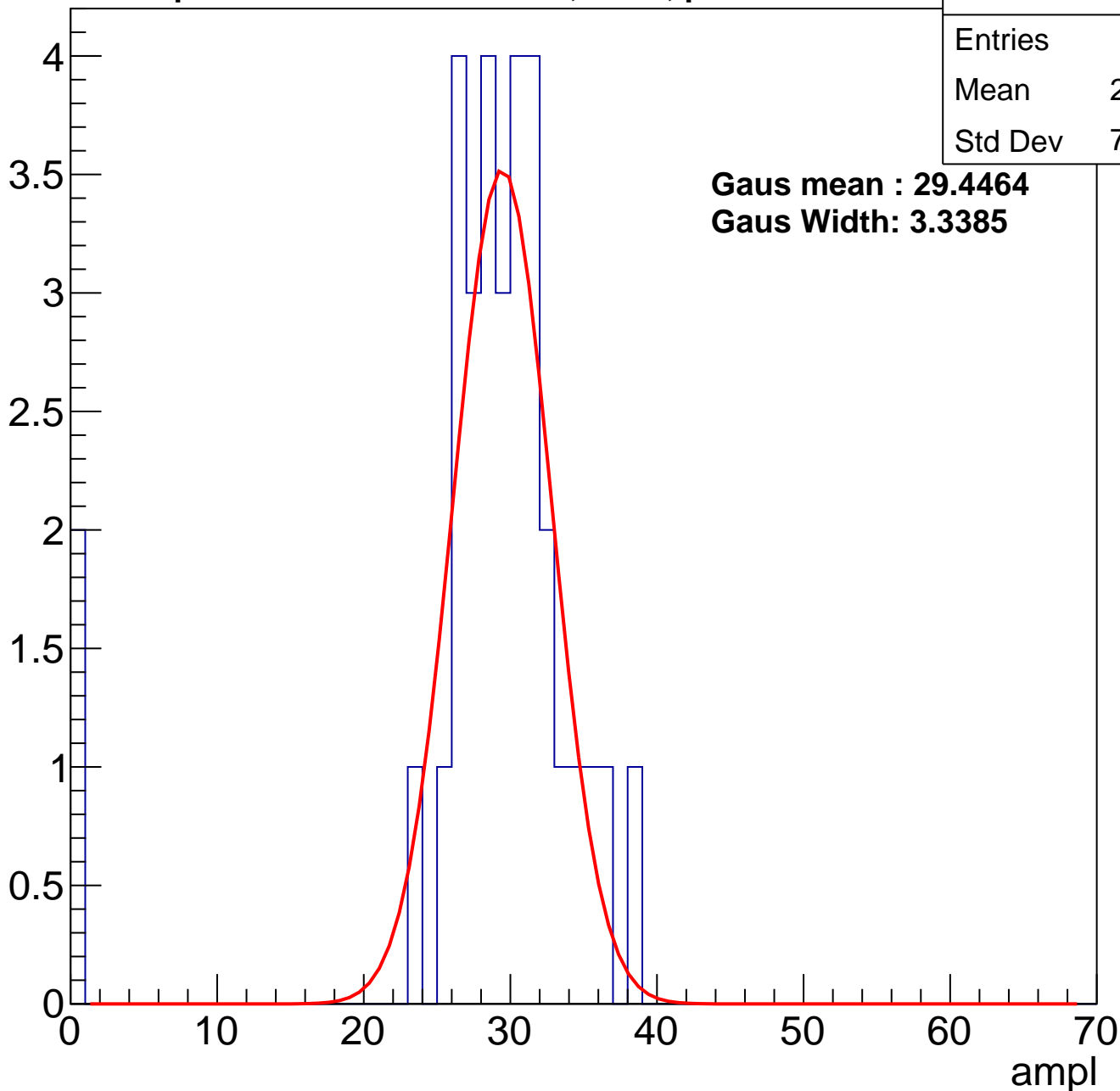


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch57, adc0

calib_packv5_042523_0143.root, FC#7, port C2

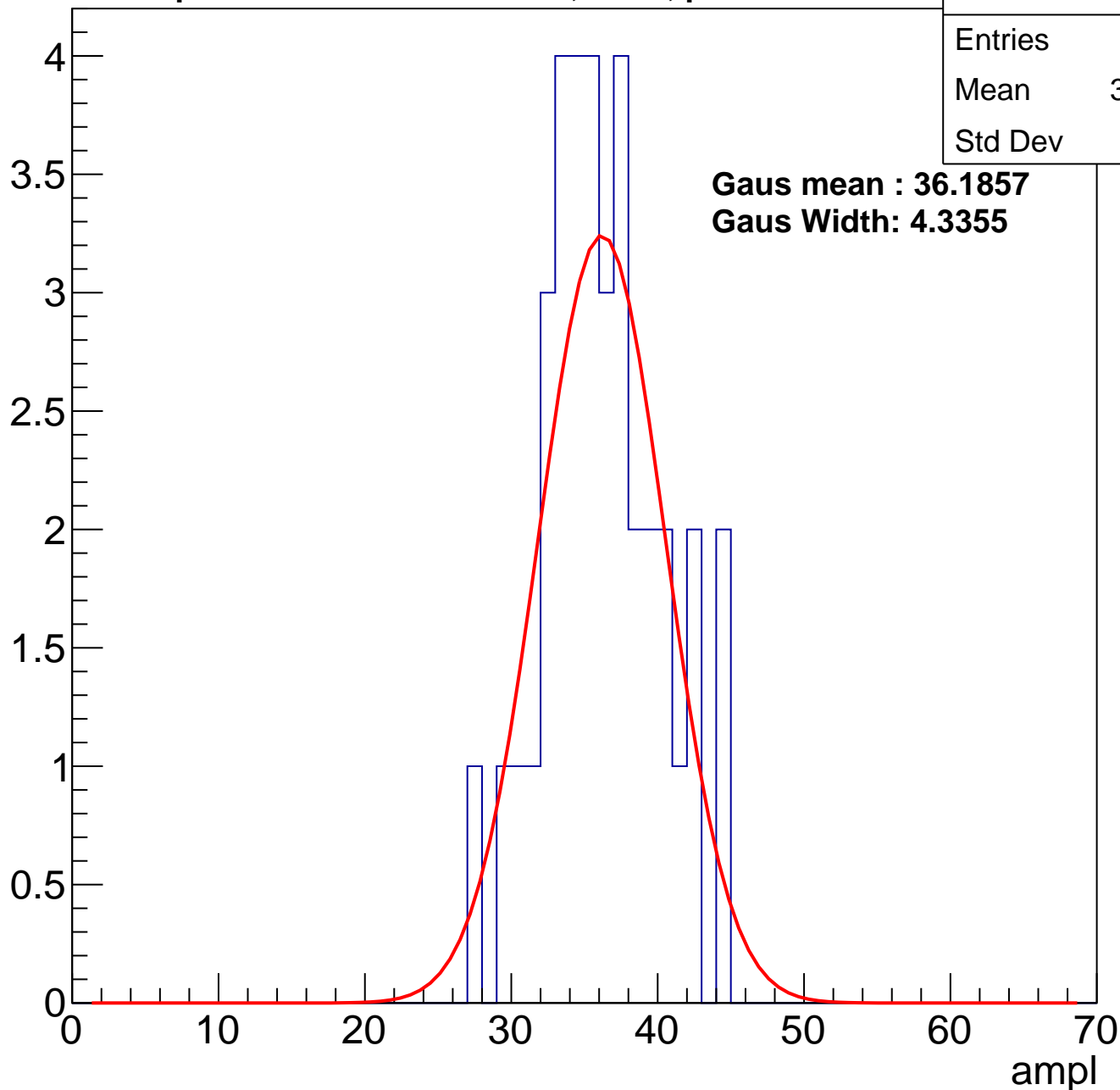
Entry



B1L103S, U15-ch57, adc1

calib_packv5_042523_0143.root, FC#7, port C2

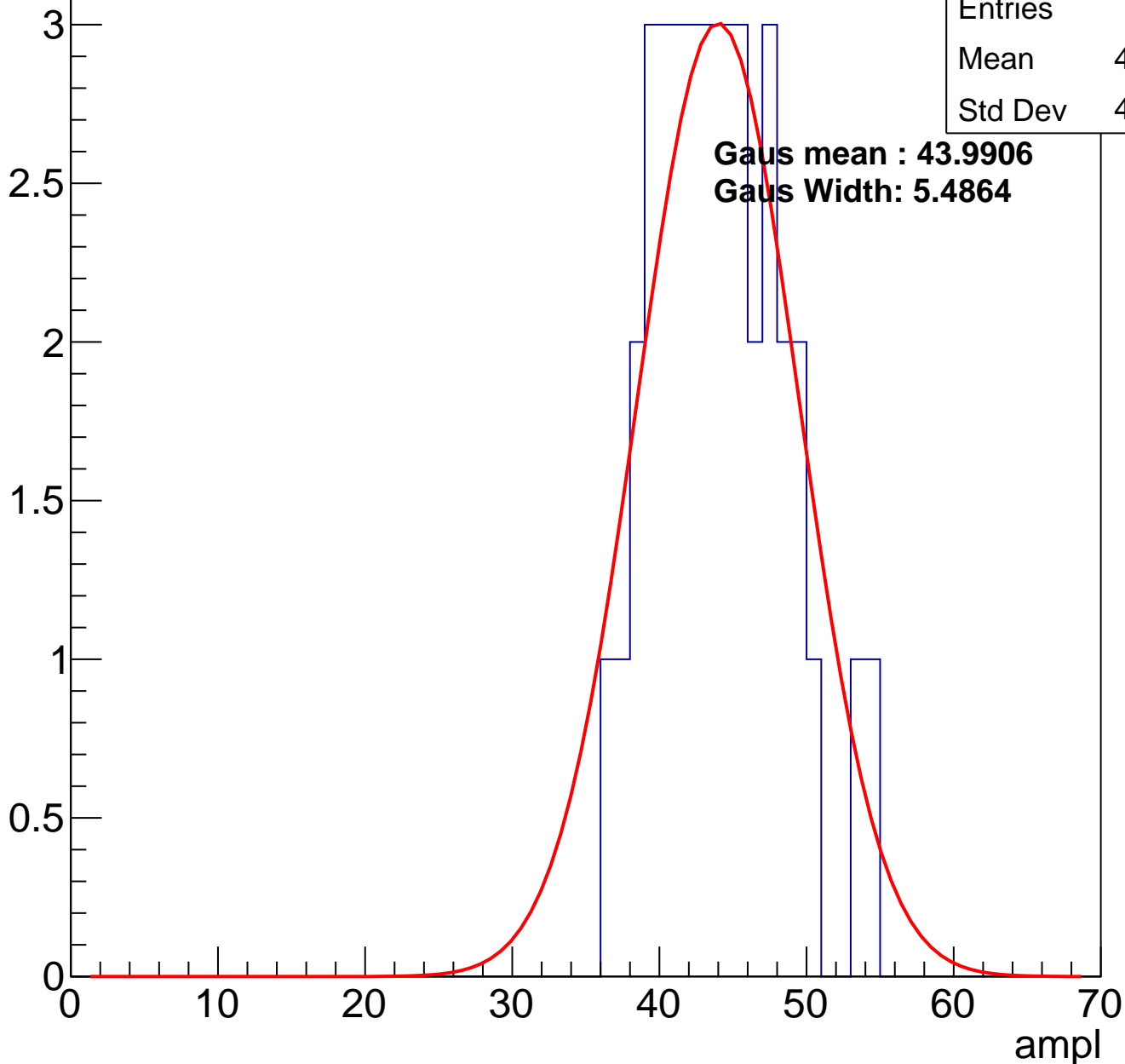
Entry



B1L103S, U15-ch57, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

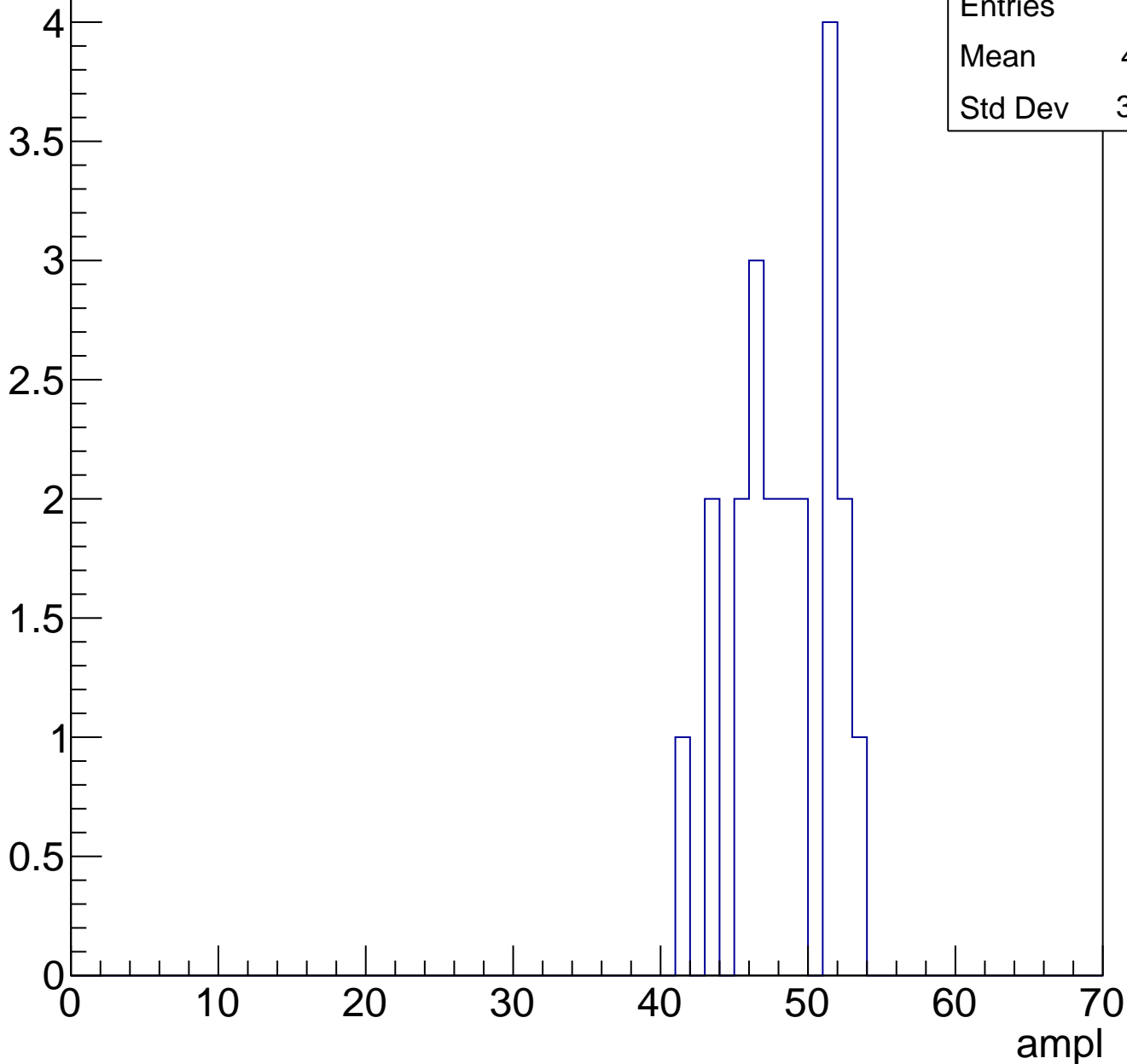


Entries	37
Mean	43.65
Std Dev	4.295

B1L103S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

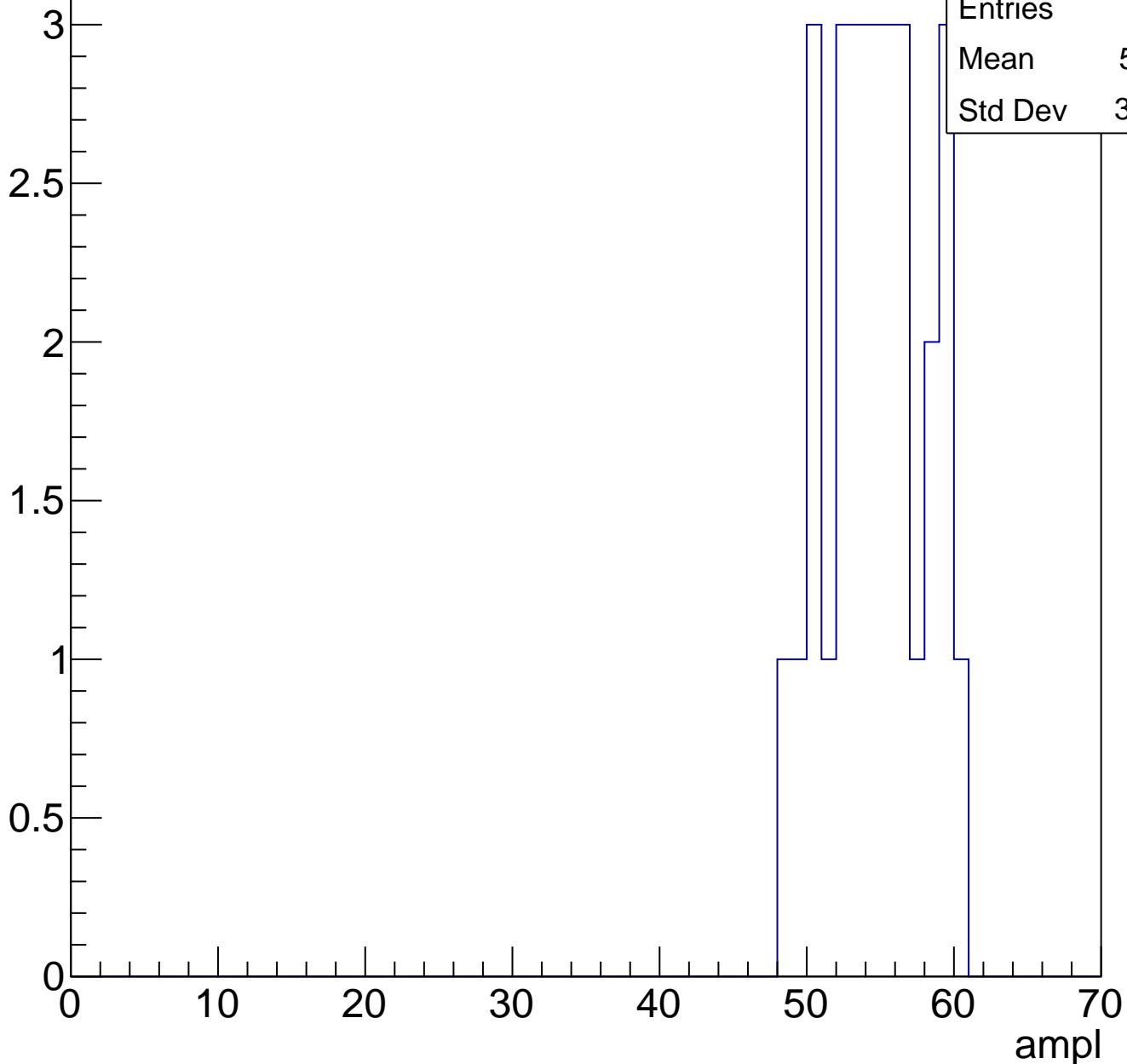
Entry



B1L103S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



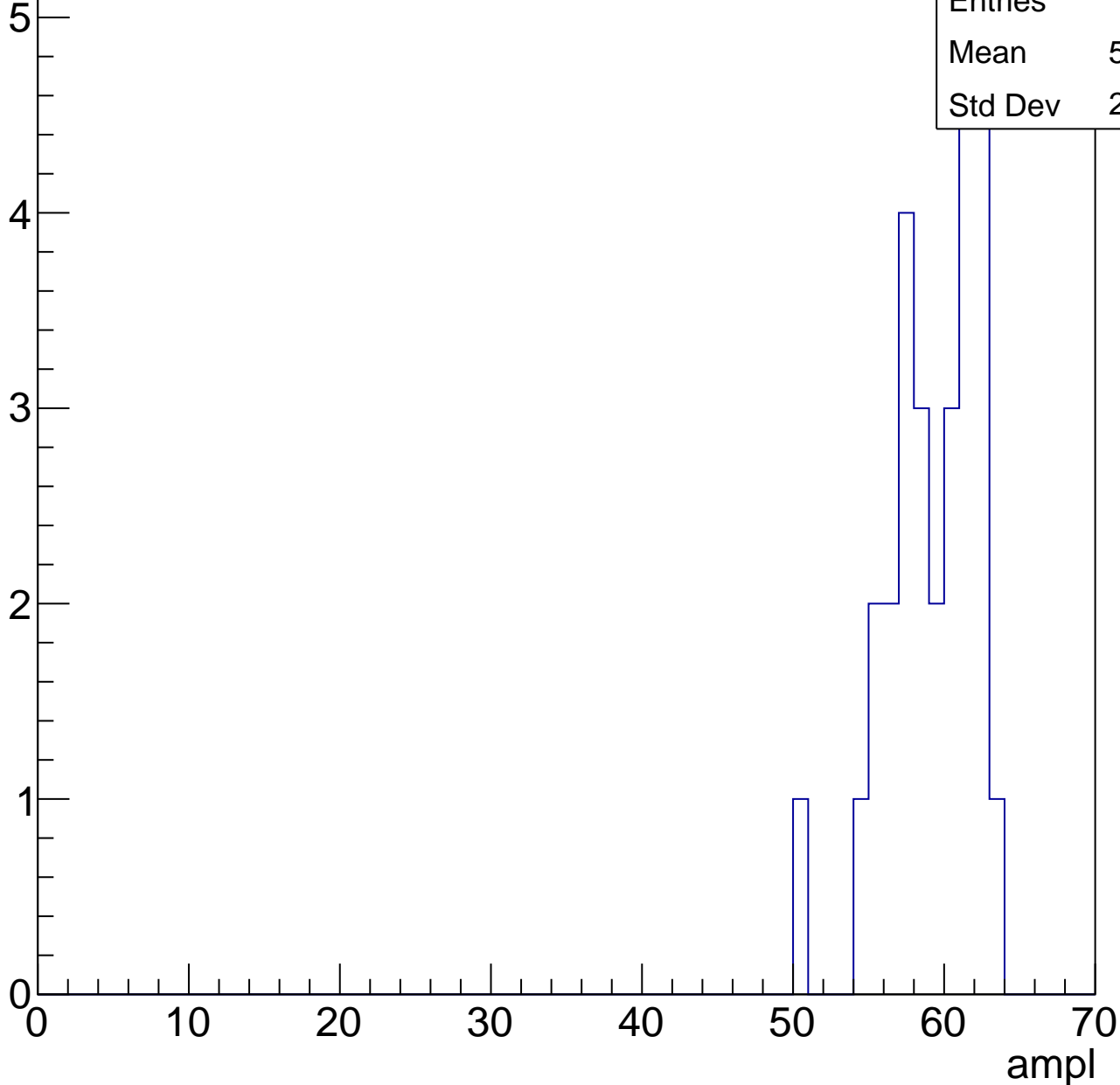
Entries	28
Mean	54.21
Std Dev	3.266

B1L103S, U15-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

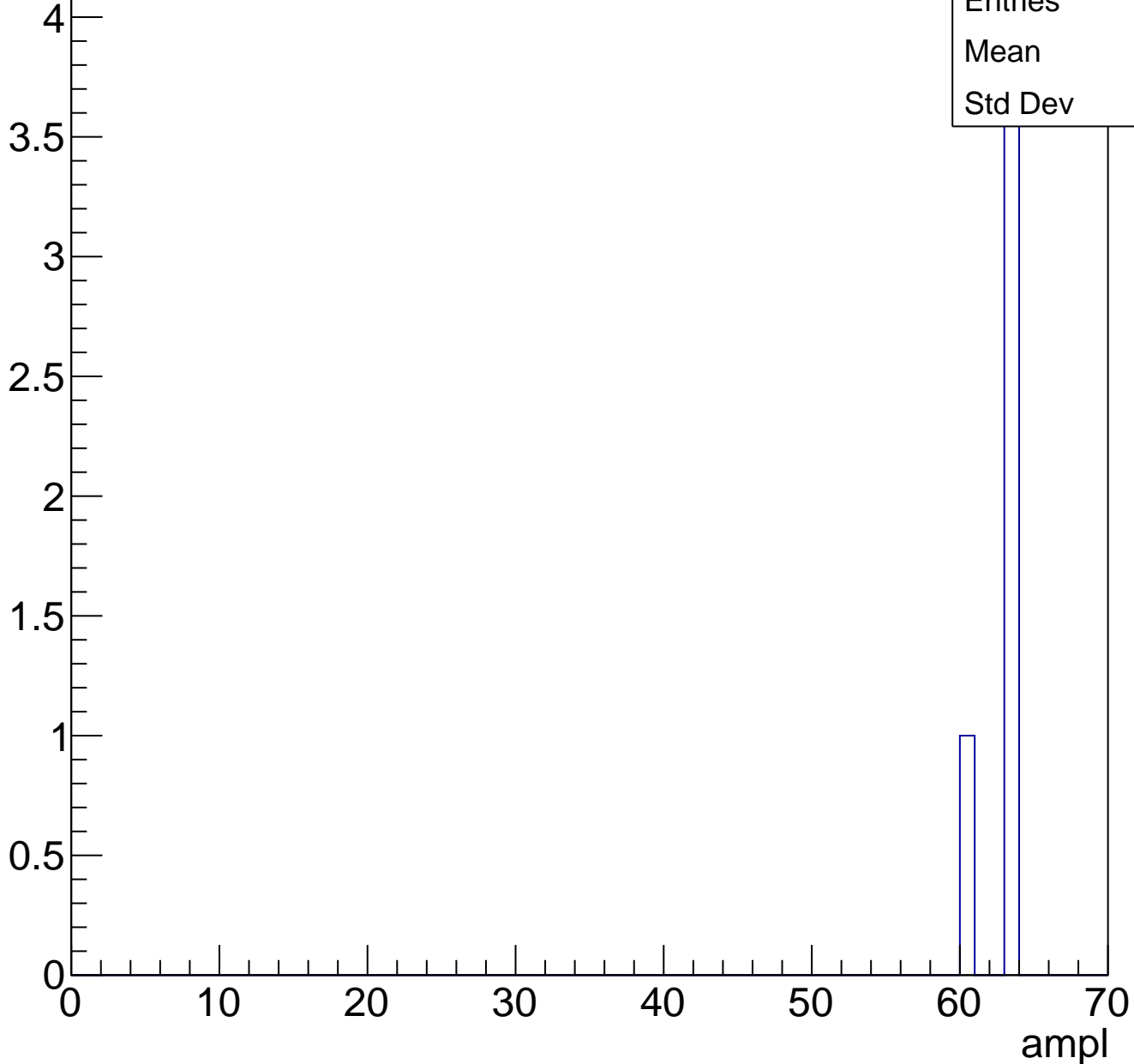
Entries	29
Mean	58.76
Std Dev	2.979



B1L103S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch58, adc0

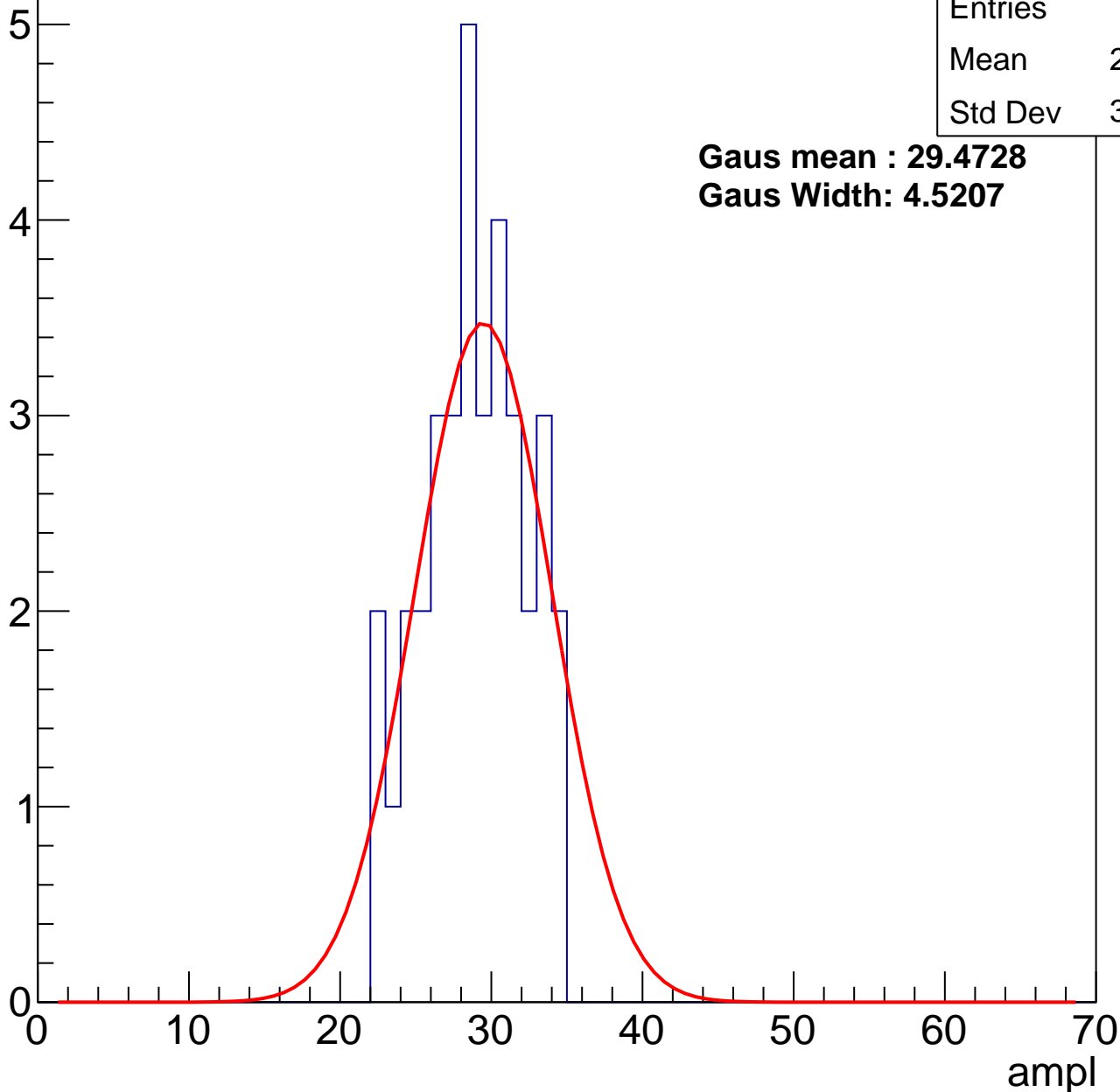
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	28.43
Std Dev	3.297

Gaus mean : 29.4728

Gaus Width: 4.5207



B1L103S, U15-ch58, adc1

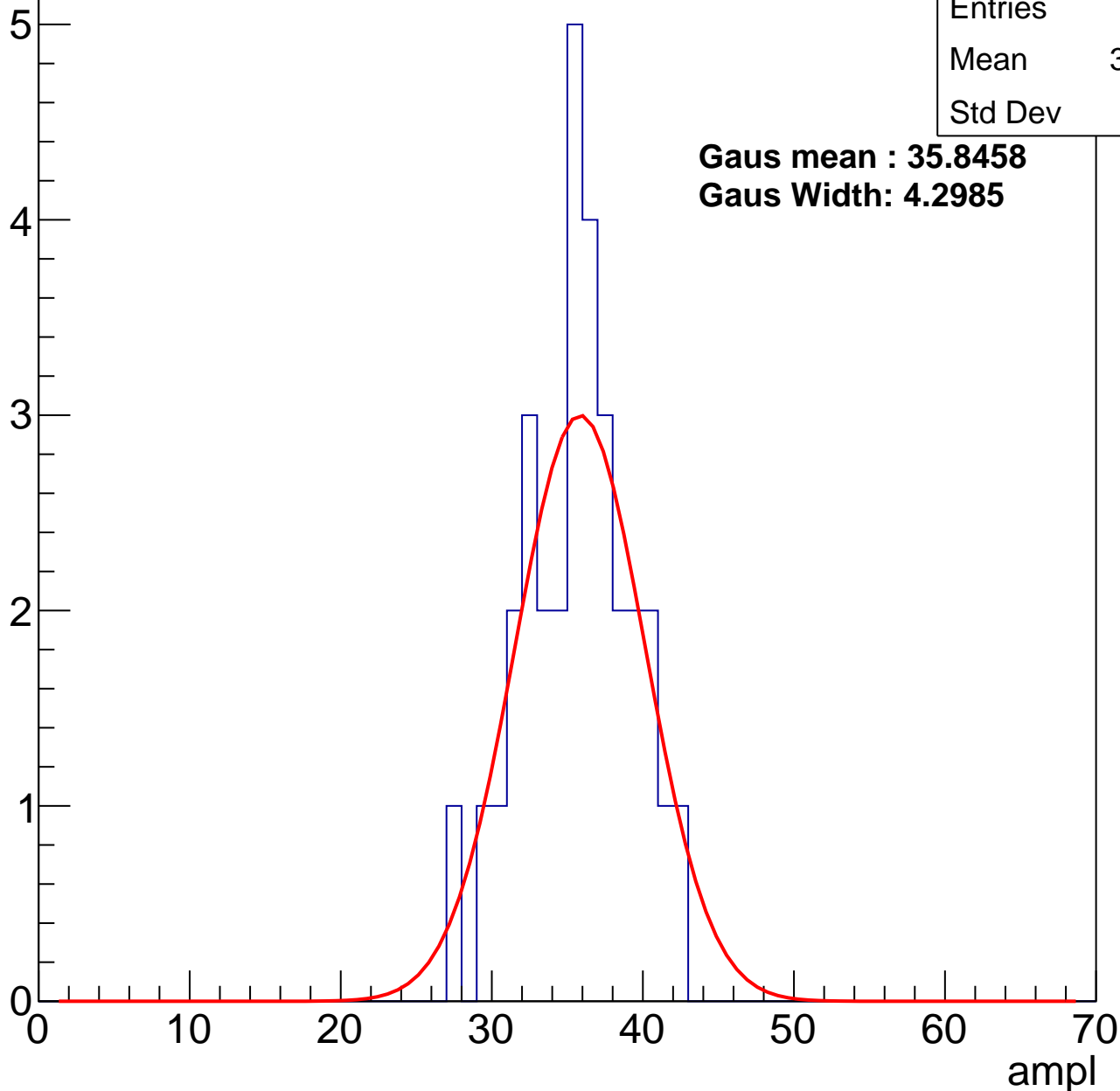
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	35.16
Std Dev	3.51

Gaus mean : 35.8458

Gaus Width: 4.2985



B1L103S, U15-ch58, adc2

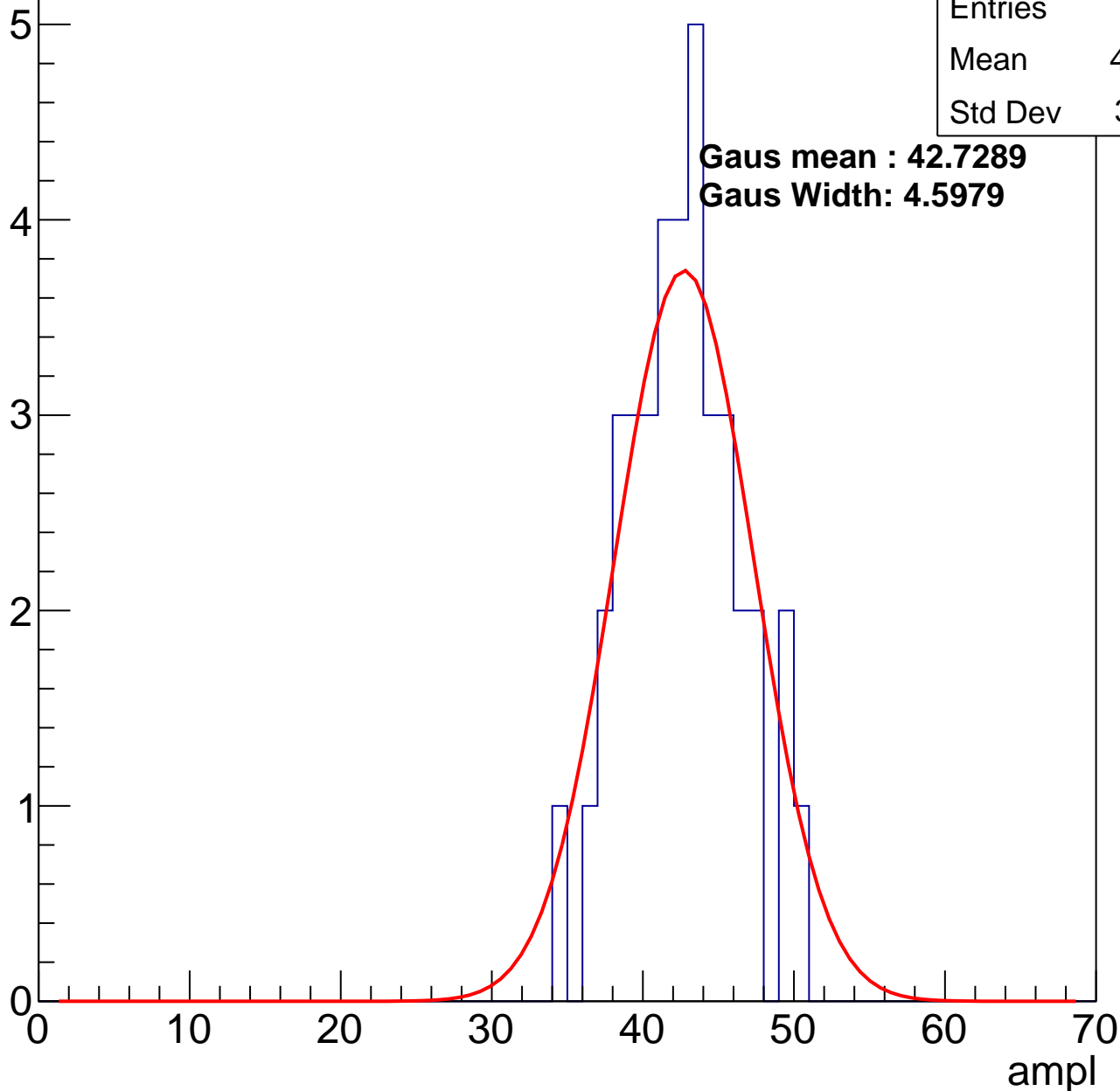
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	42.13
Std Dev	3.681

Gaus mean : 42.7289

Gaus Width: 4.5979



B1L103S, U15-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

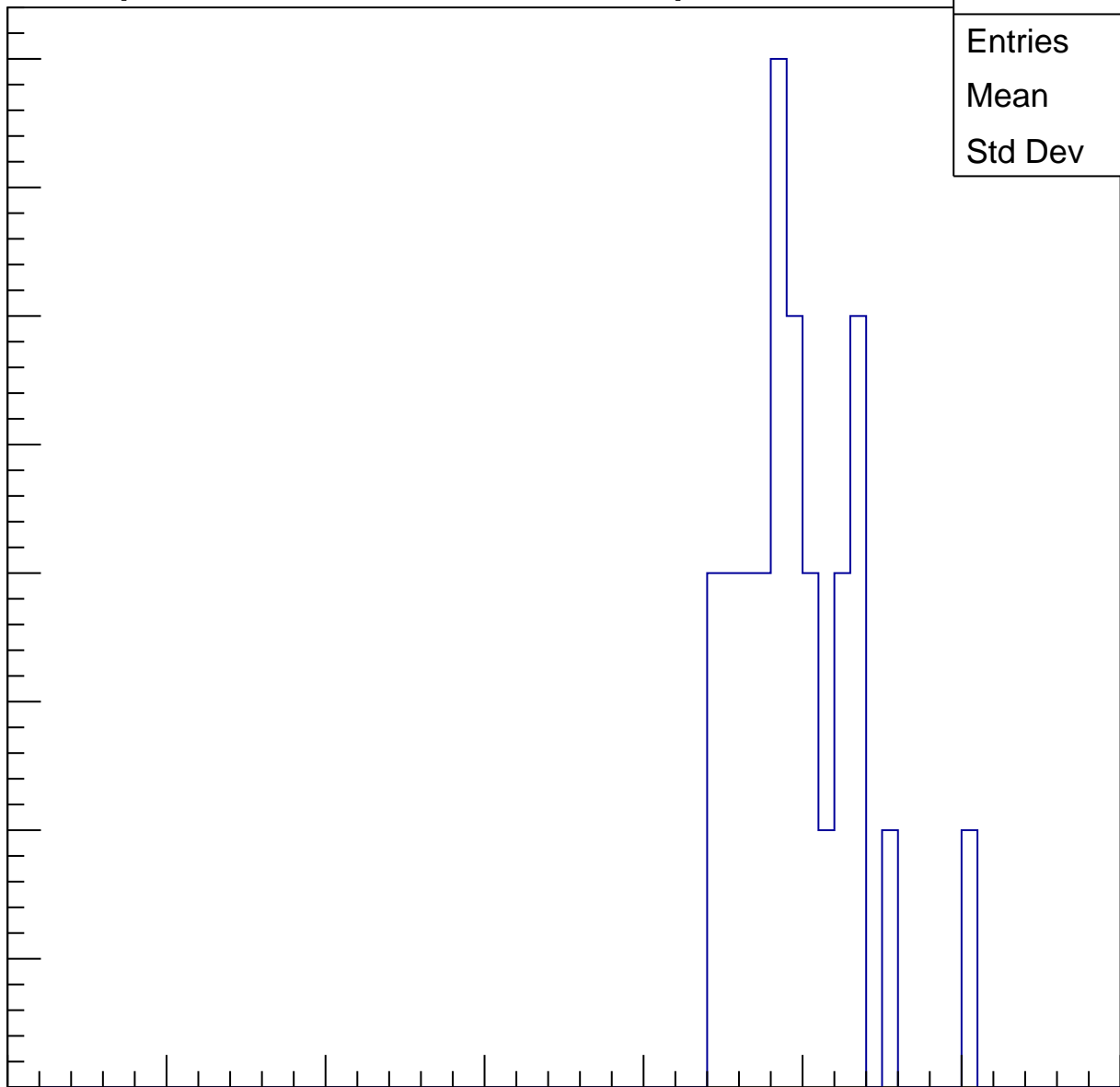
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	25
Mean	49.28
Std Dev	3.683

ampl

0 10 20 30 40 50 60 70

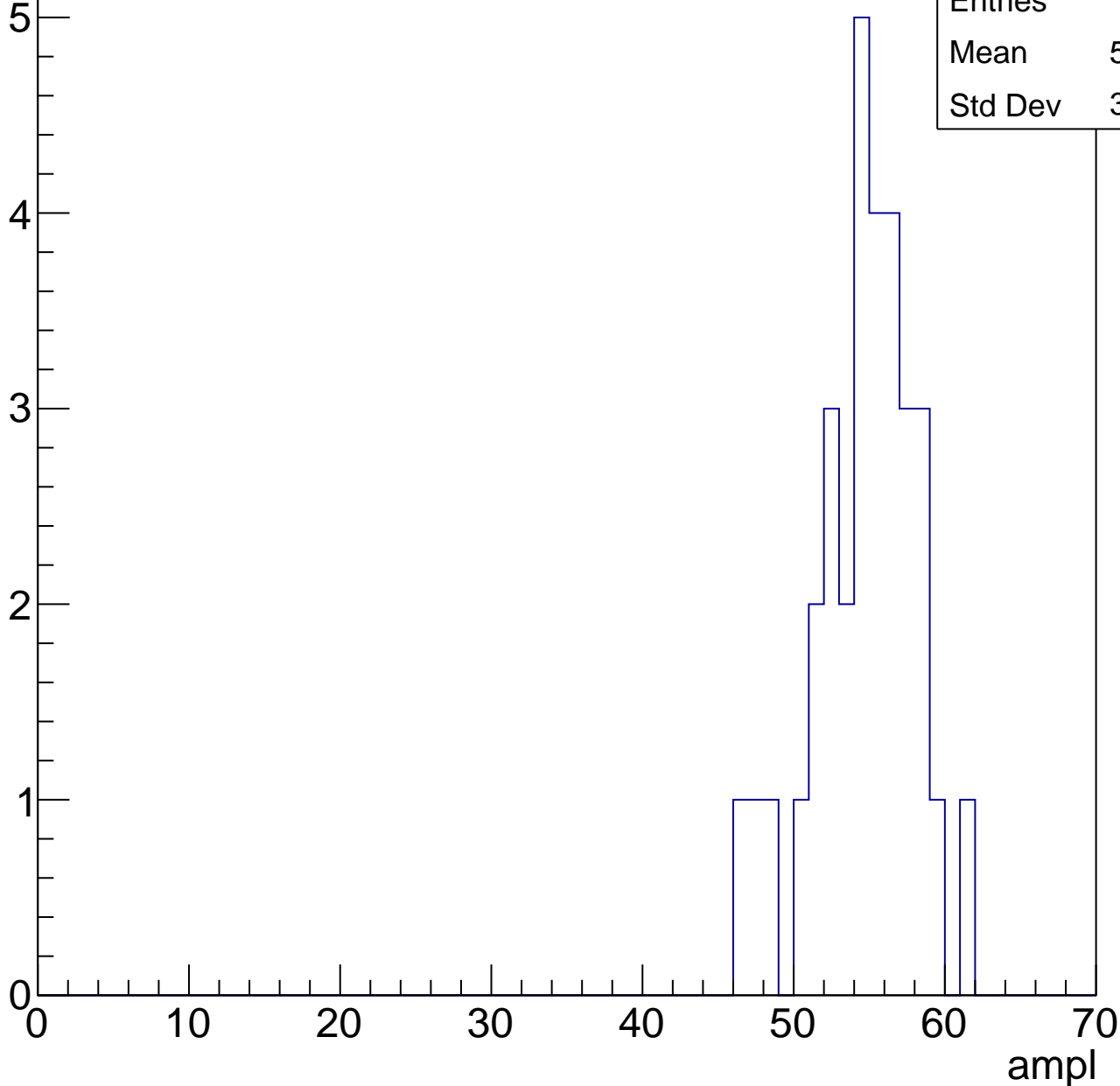


B1L103S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	54.19
Std Dev	3.377

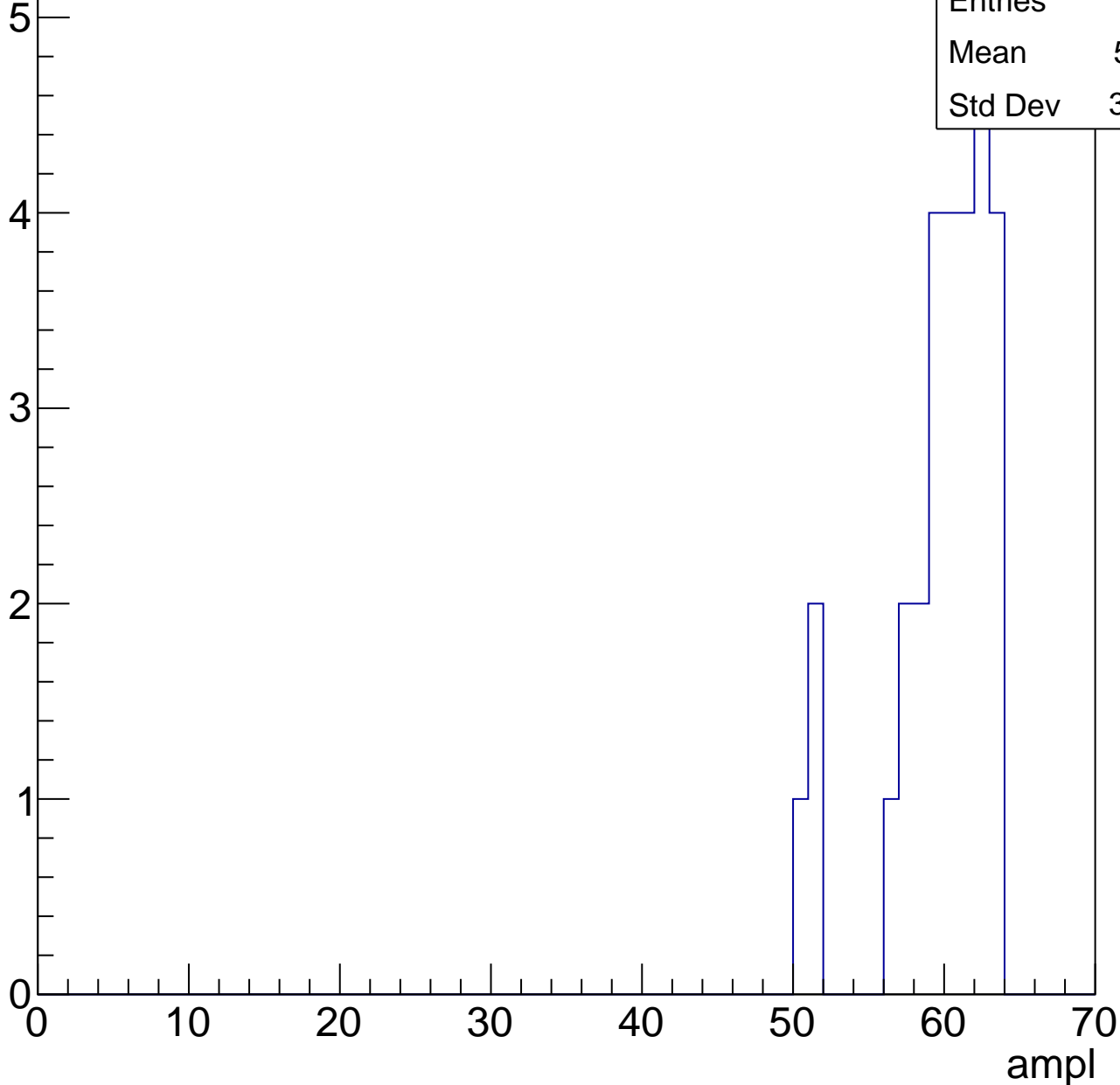


B1L103S, U15-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	59.31
Std Dev	3.495



B1L103S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch59, adc0

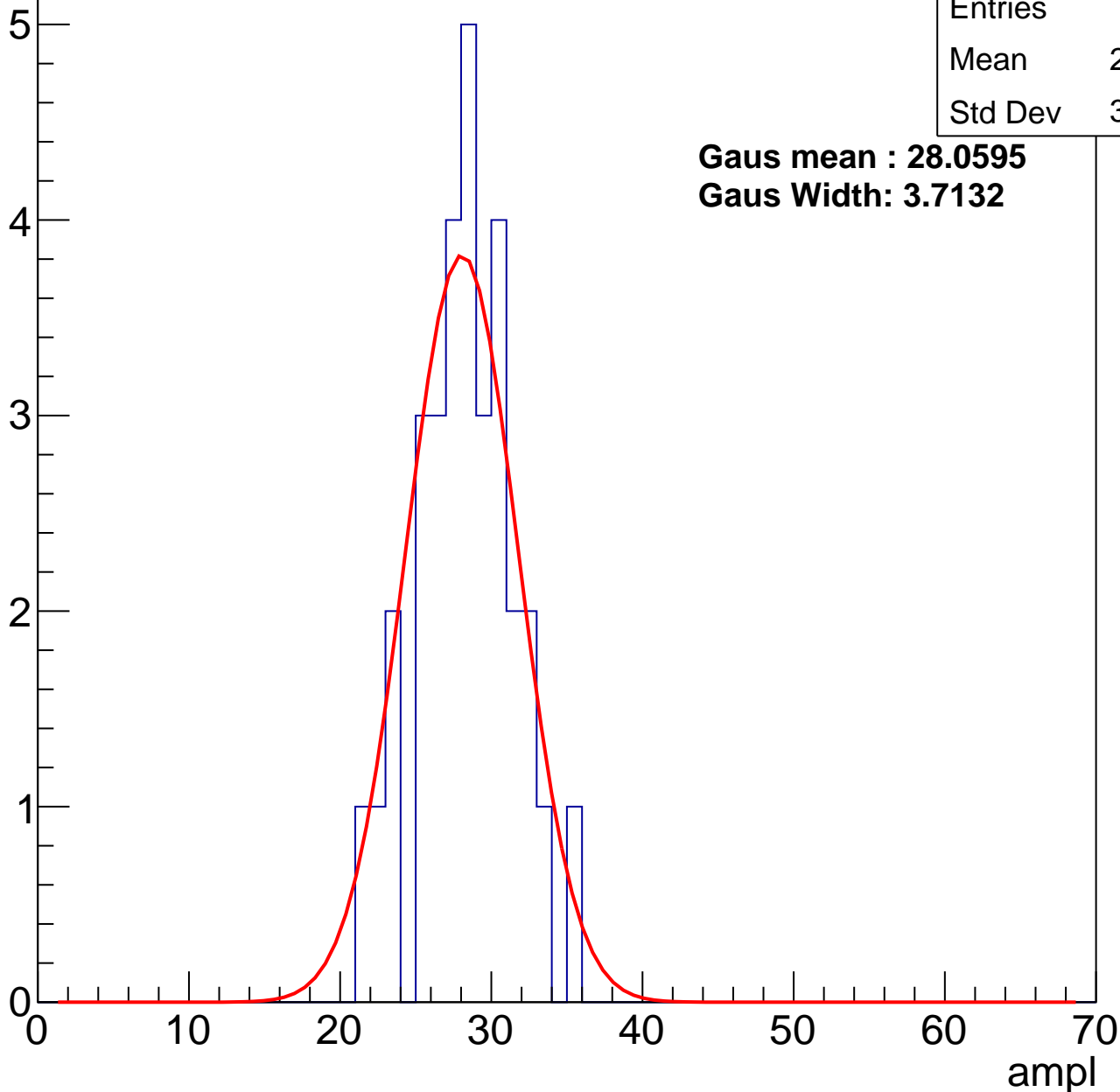
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	27.84
Std Dev	3.163

Gaus mean : 28.0595

Gaus Width: 3.7132



B1L103S, U15-ch59, adc1

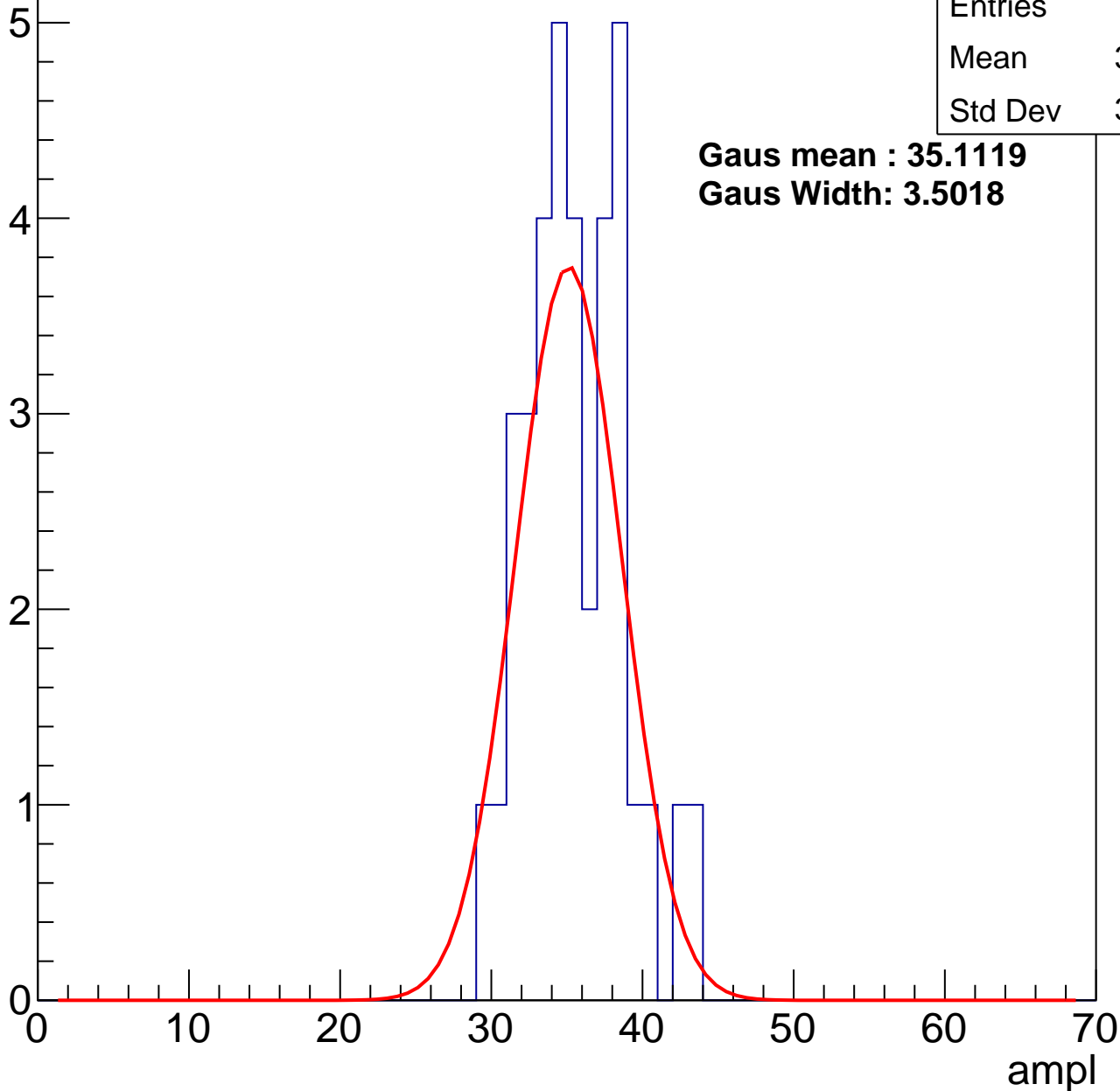
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	35.11
Std Dev	3.221

Gaus mean : 35.1119

Gaus Width: 3.5018



B1L103S, U15-ch59, adc2

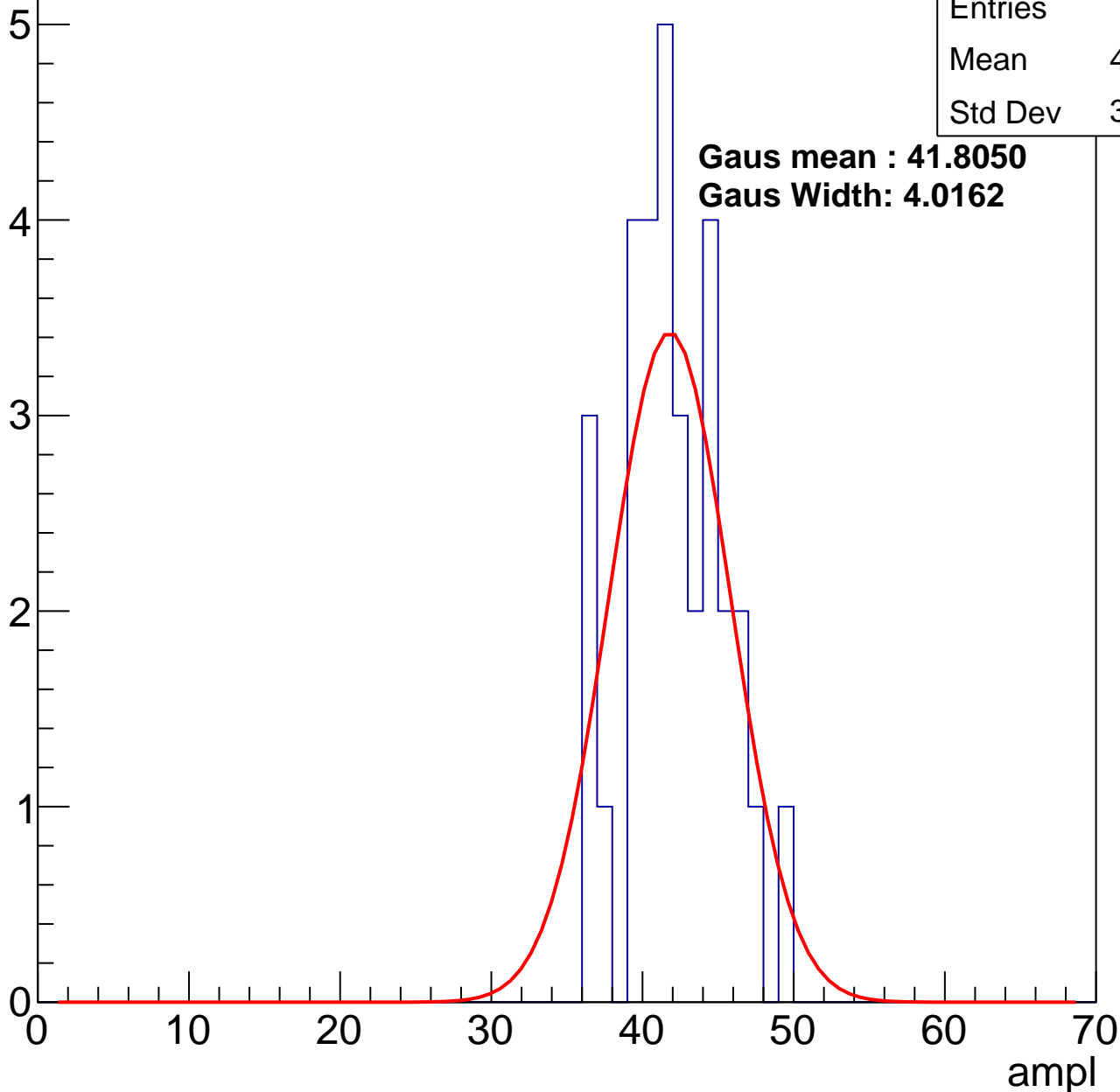
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	41.62
Std Dev	3.199

Gaus mean : 41.8050

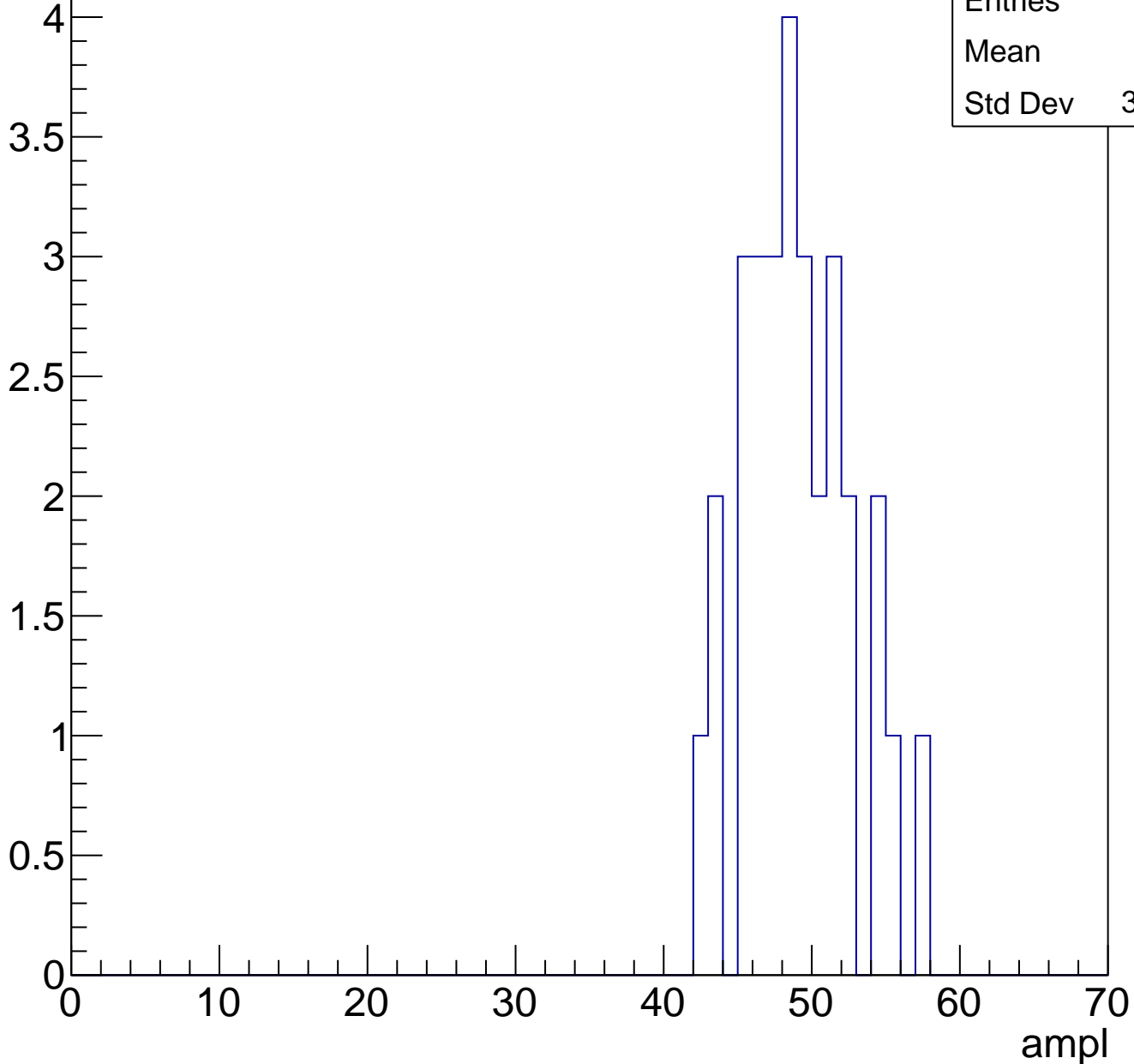
Gaus Width: 4.0162



B1L103S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

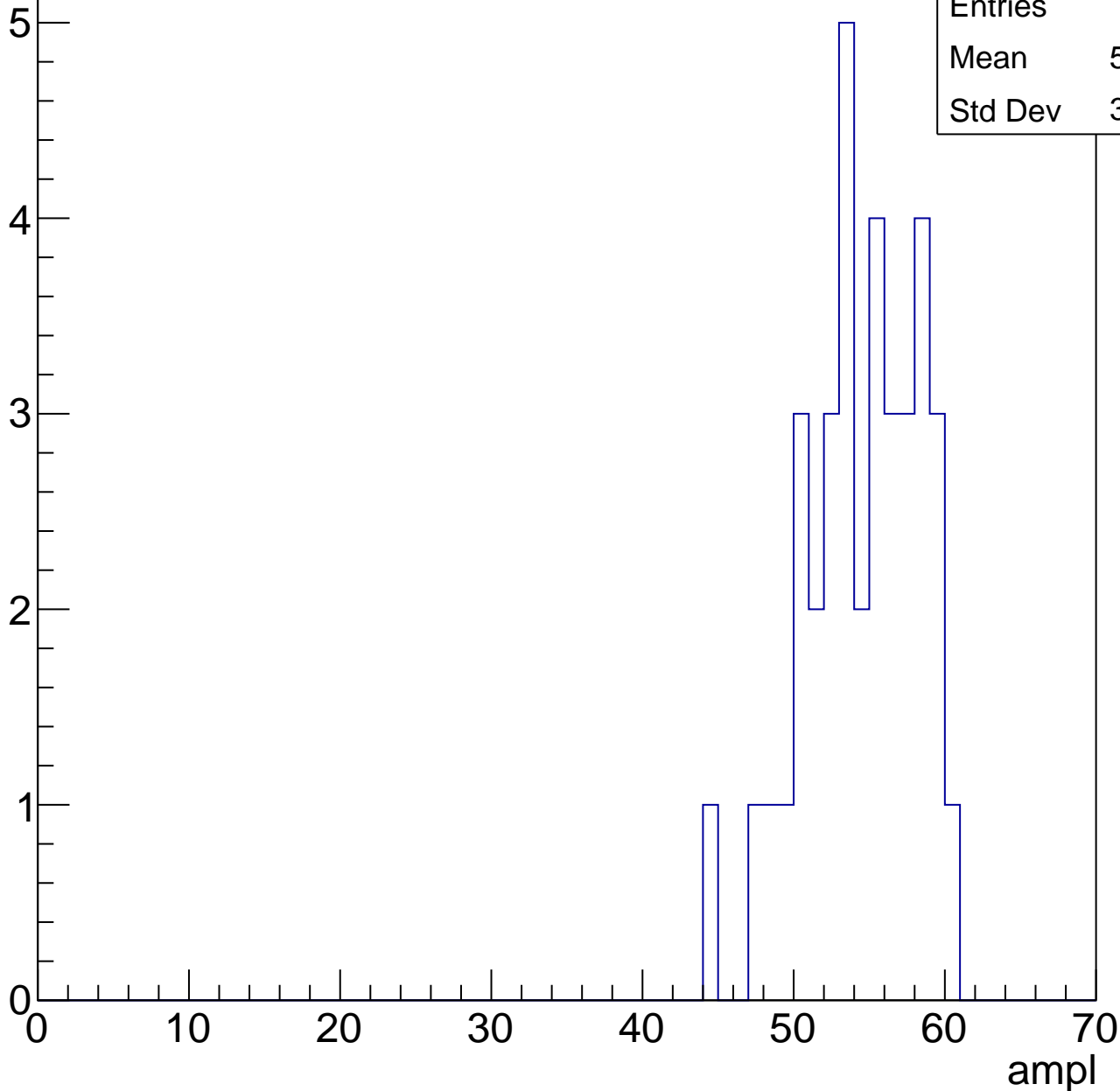


B1L103S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	53.97
Std Dev	3.716



B1L103S, U15-ch59, adc5

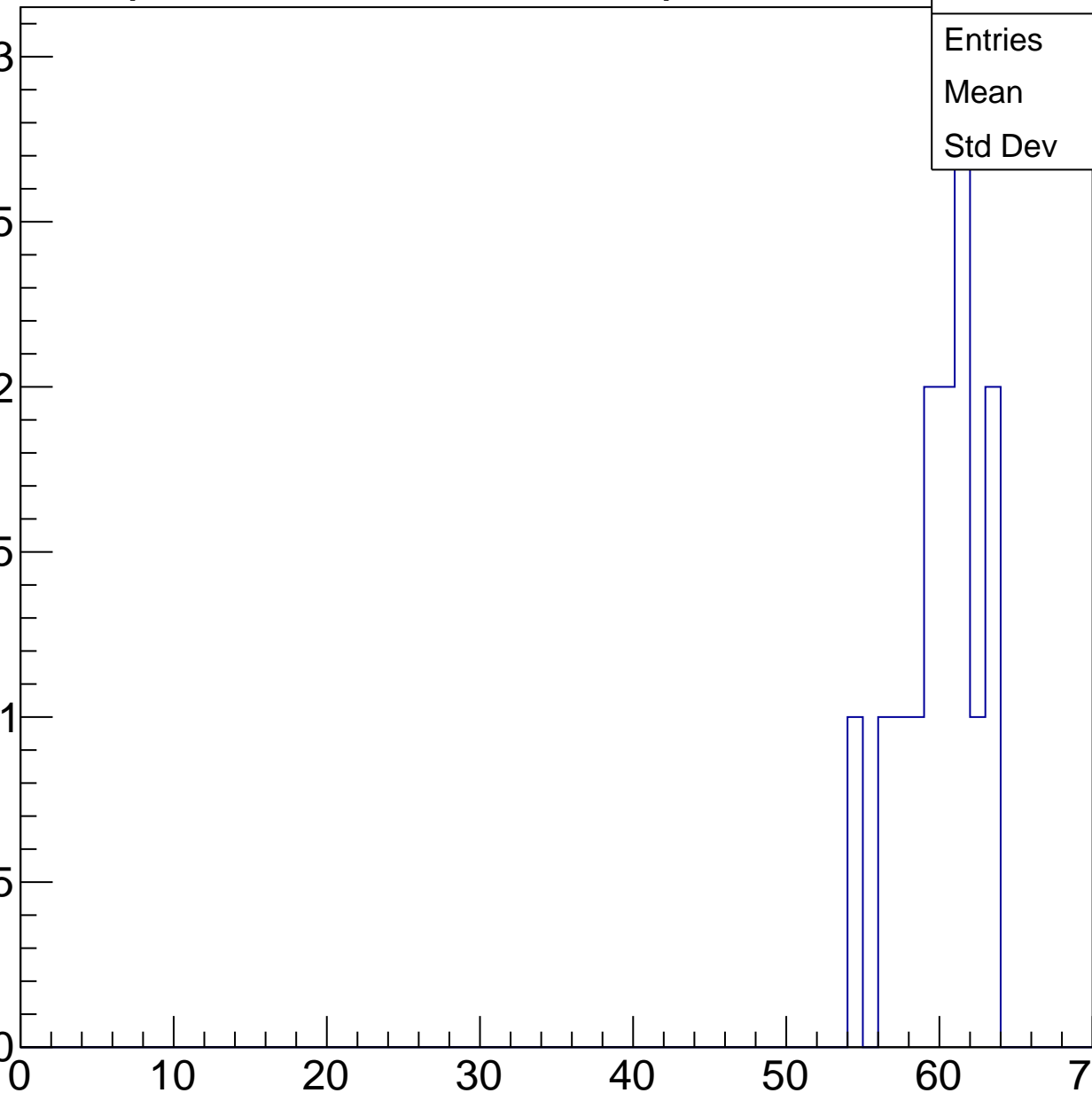
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	14
Mean	59.57
Std Dev	2.527

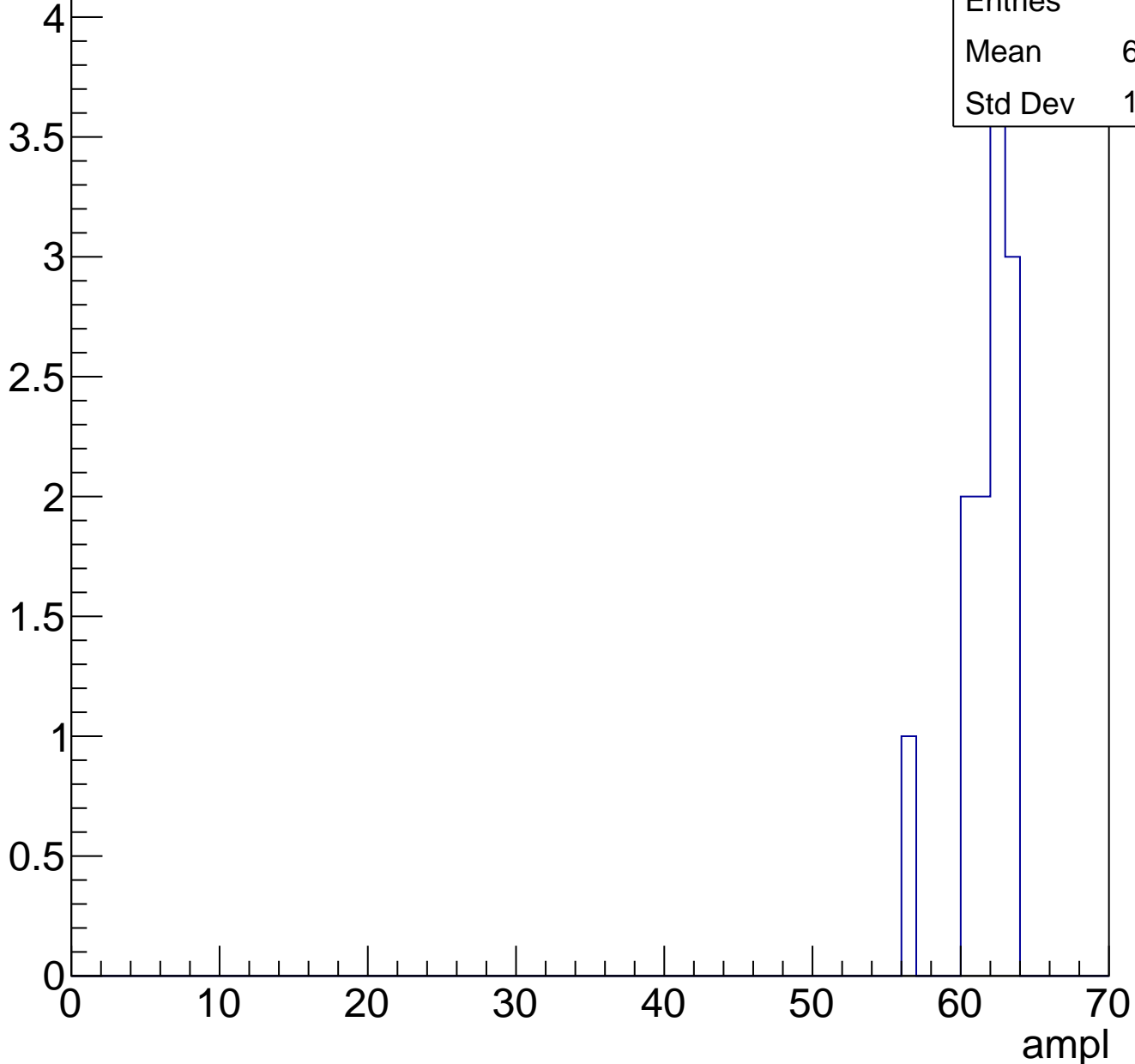
ampl



B1L103S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

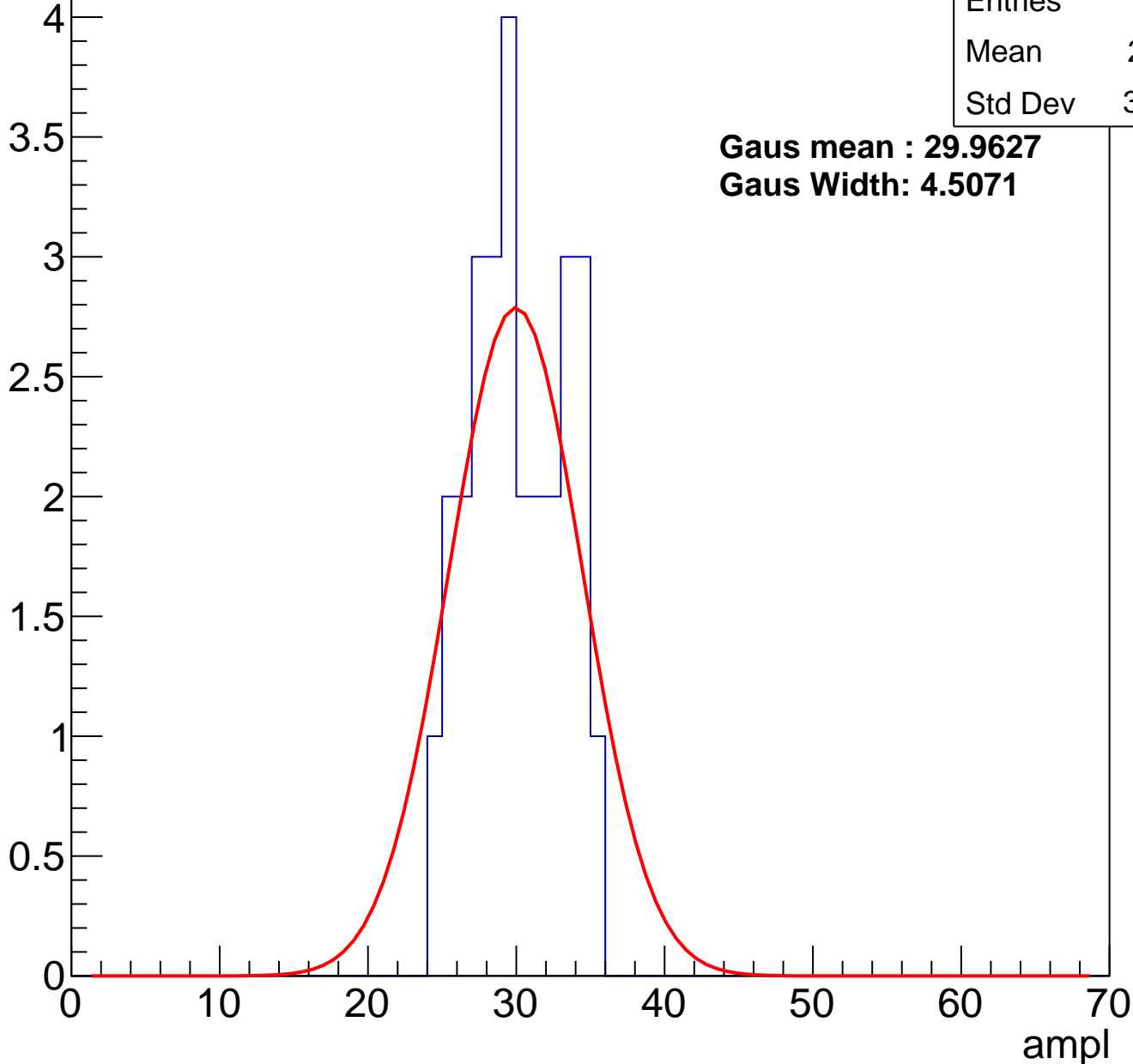


Entries	2
Mean	0
Std Dev	0

B1L103S, U15-ch60, adc0

calib_packv5_042523_0143.root, FC#7, port C2

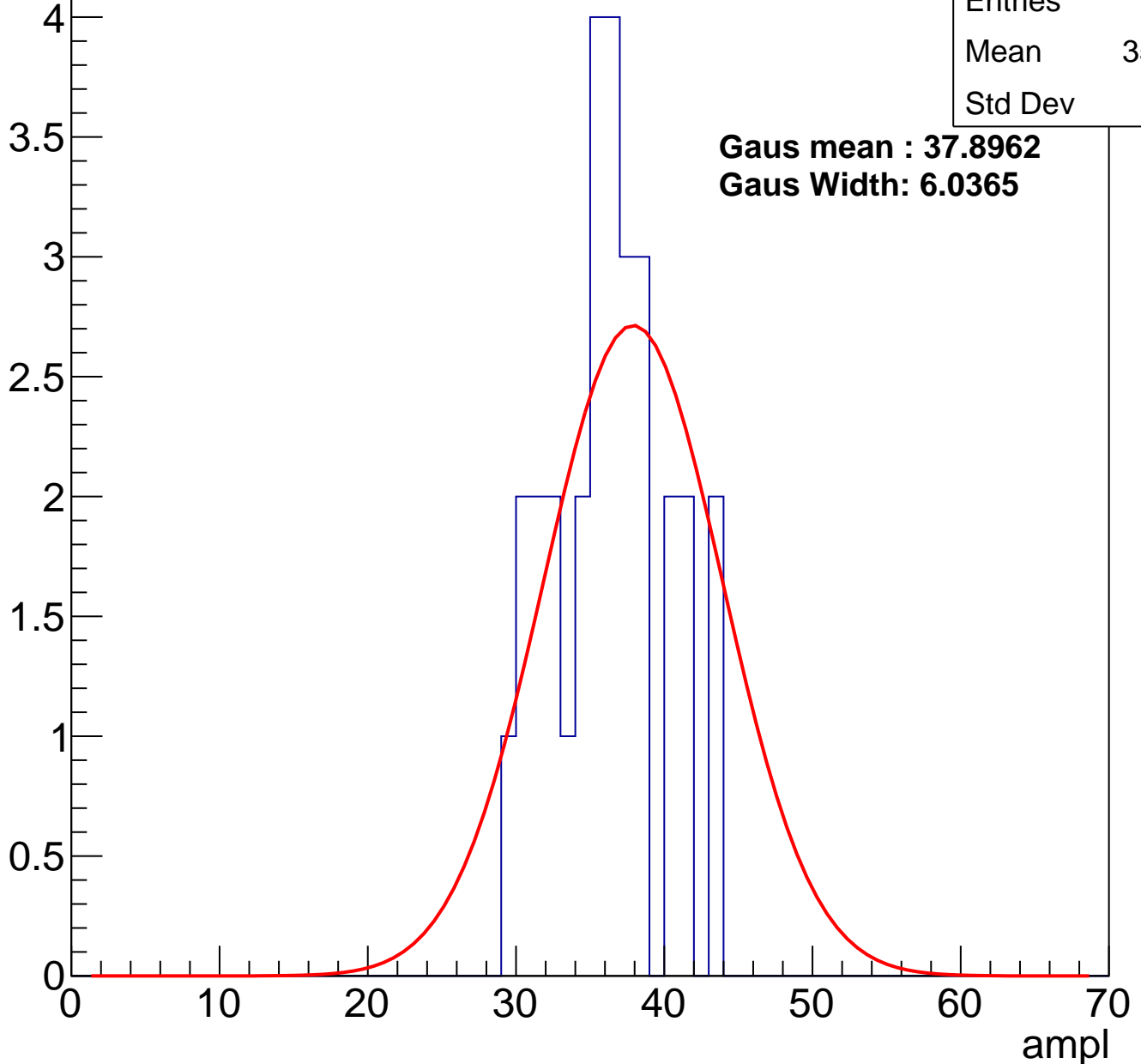
Entry



B1L103S, U15-ch60, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch60, adc2

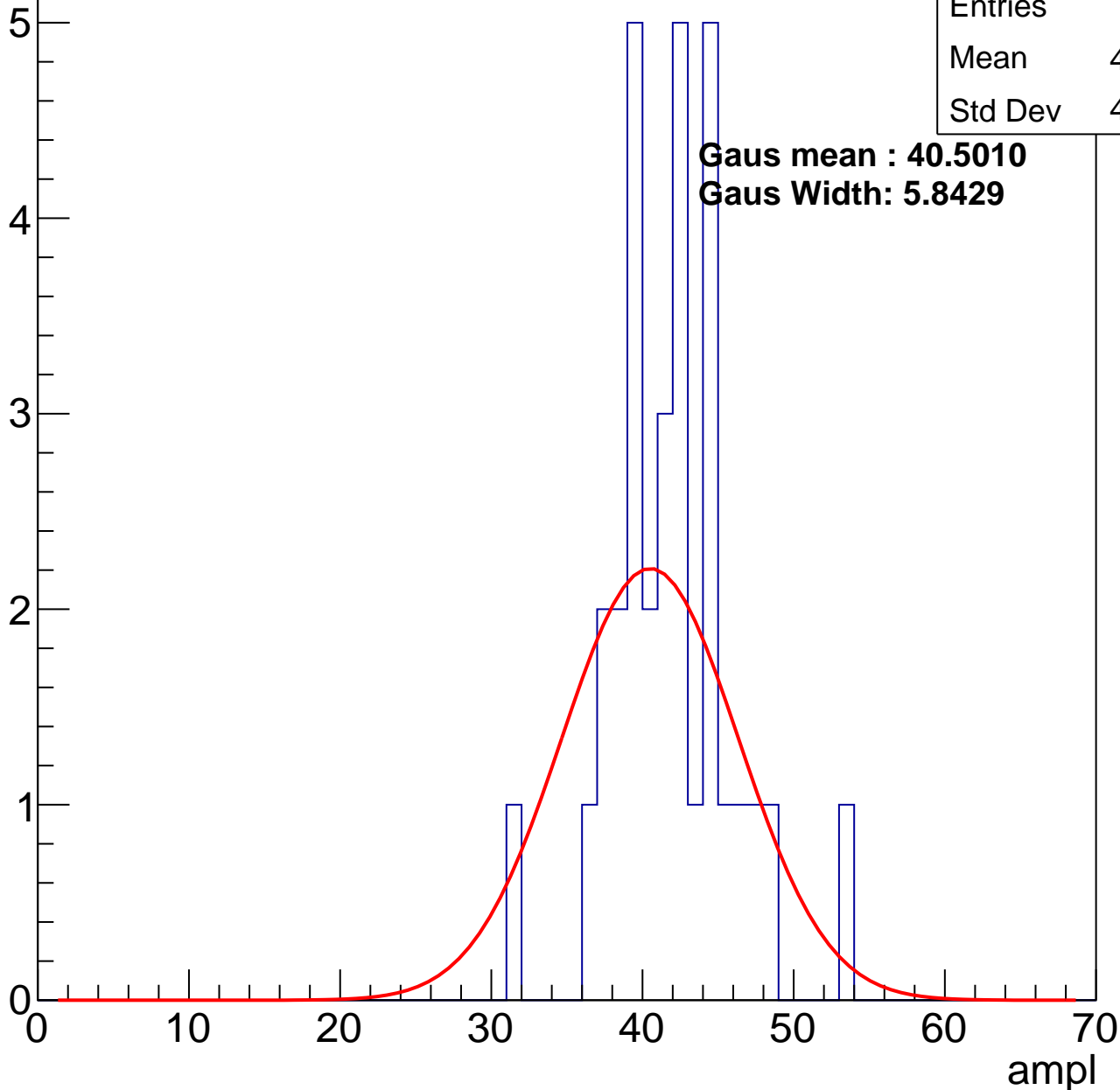
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	41.47
Std Dev	4.008

Gaus mean : 40.5010

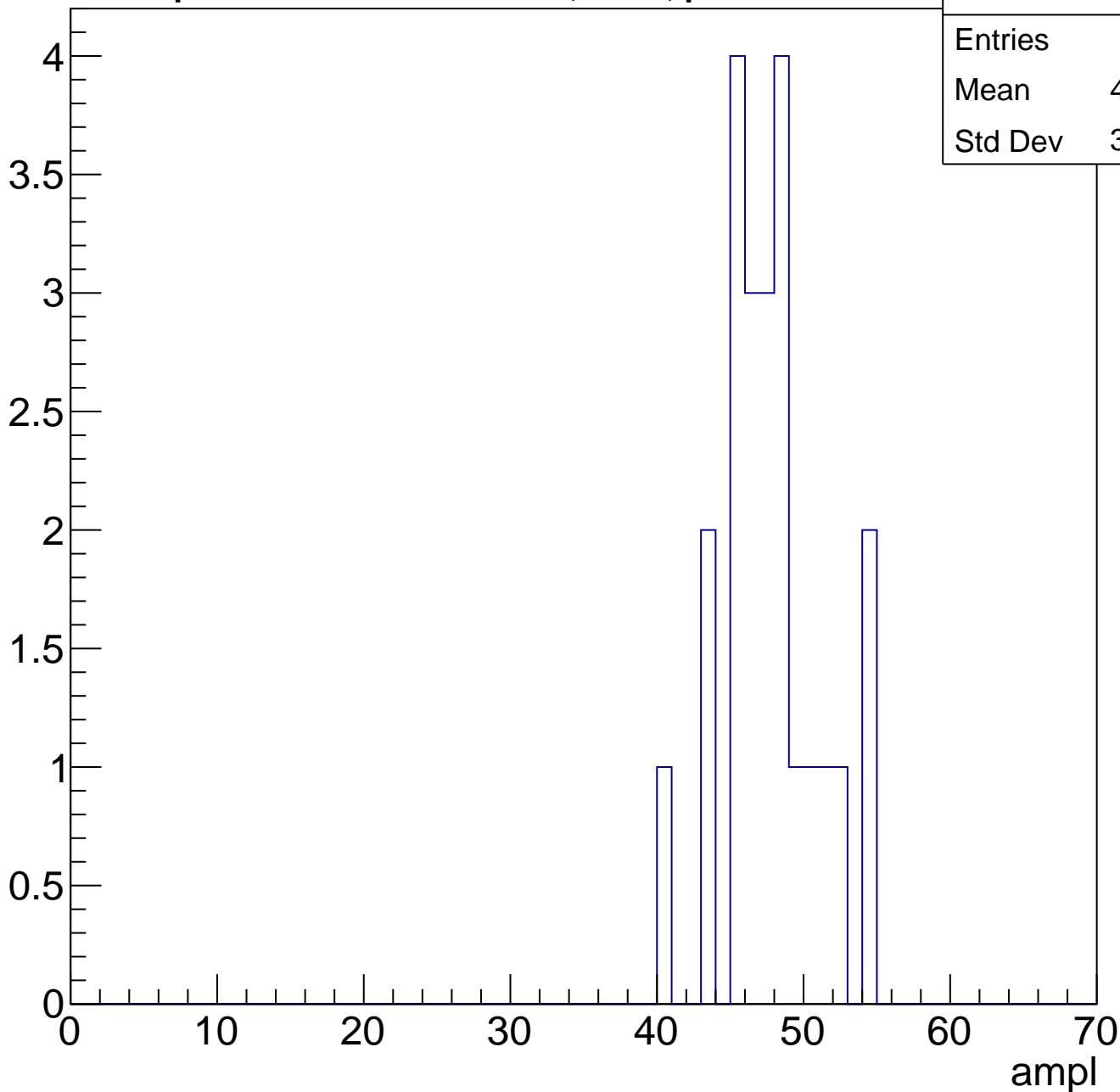
Gaus Width: 5.8429



B1L103S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

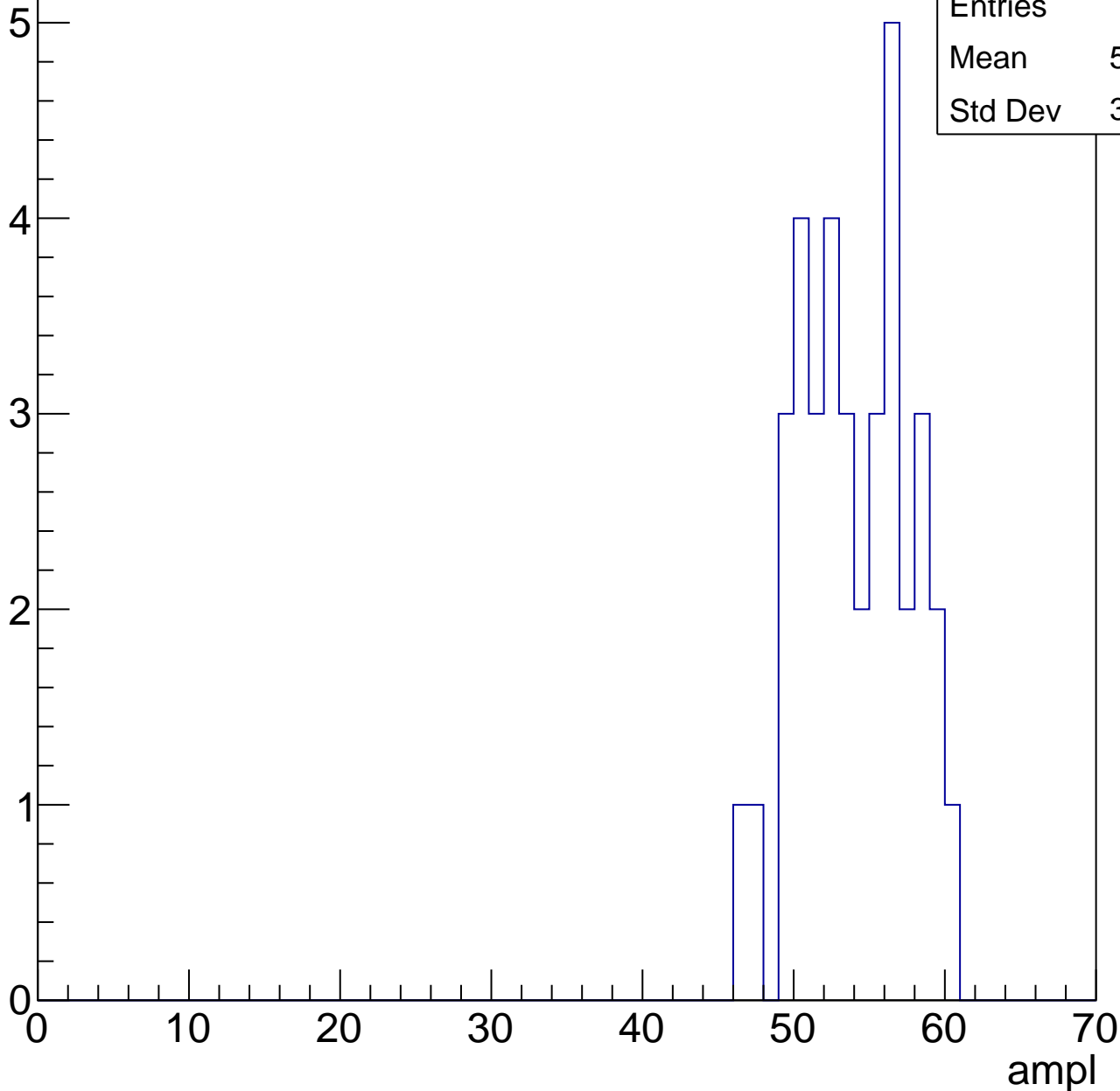


B1L103S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

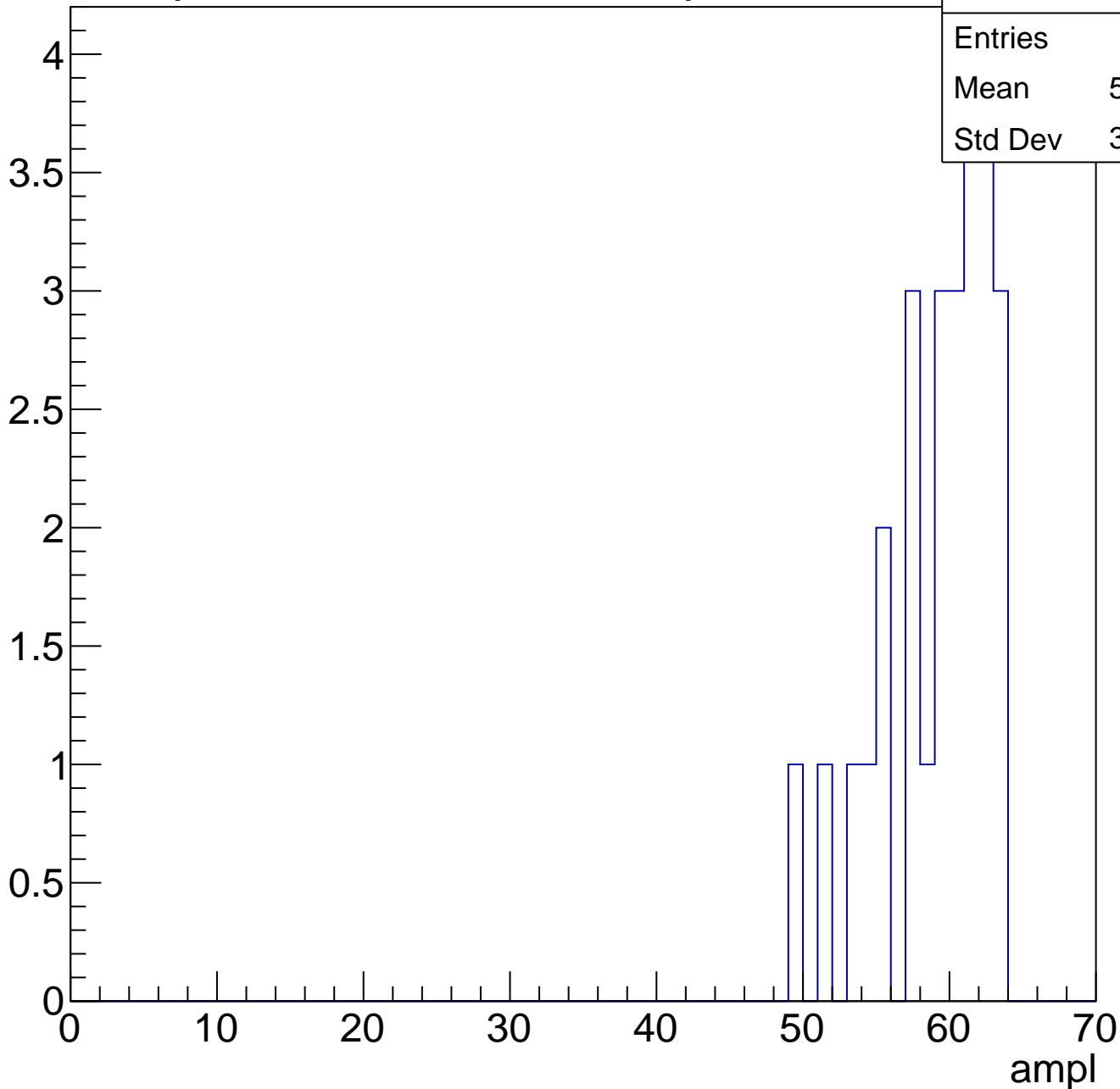
Entries	37
Mean	53.49
Std Dev	3.538



B1L103S, U15-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

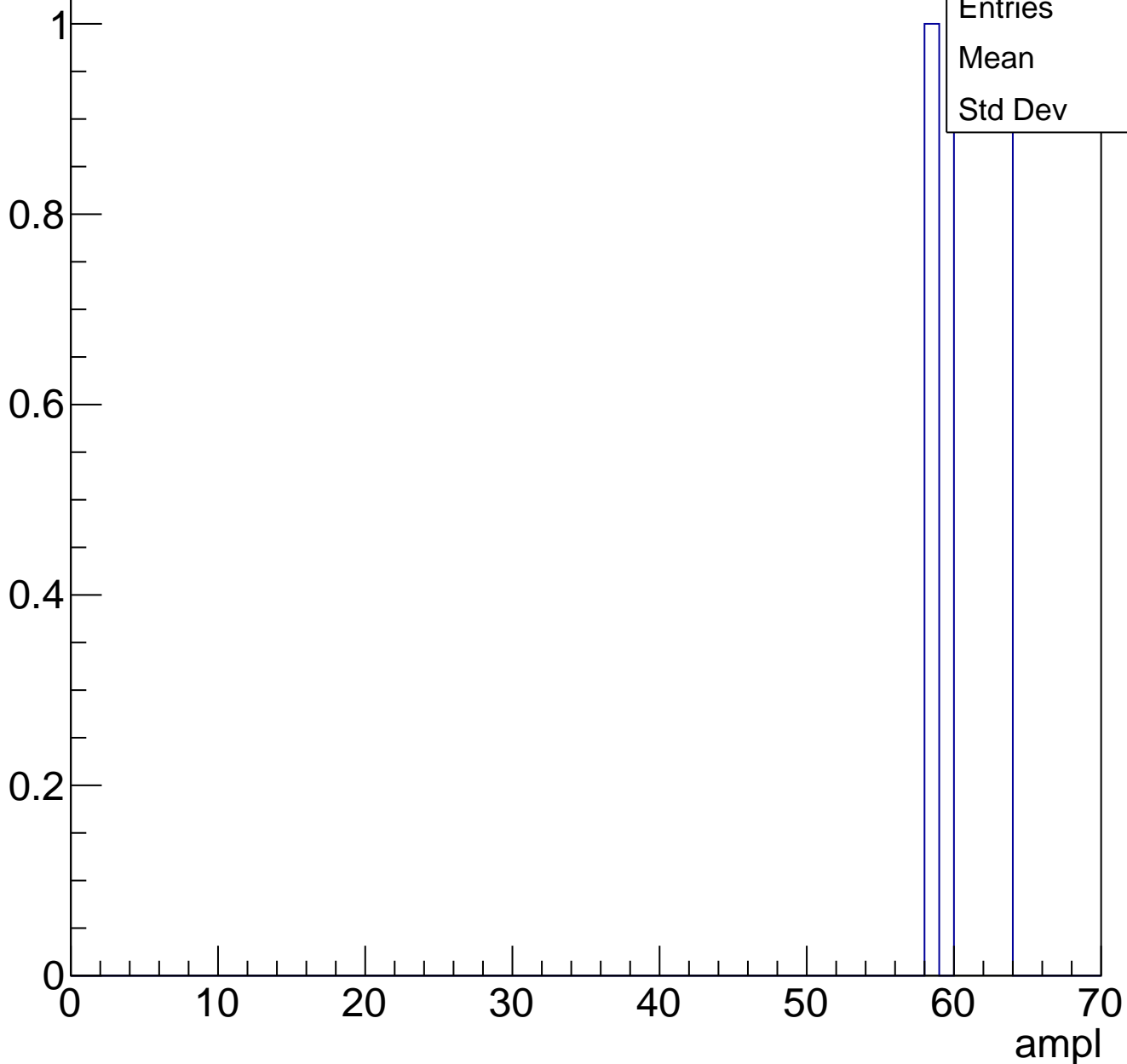
Entry



B1L103S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch61, adc0

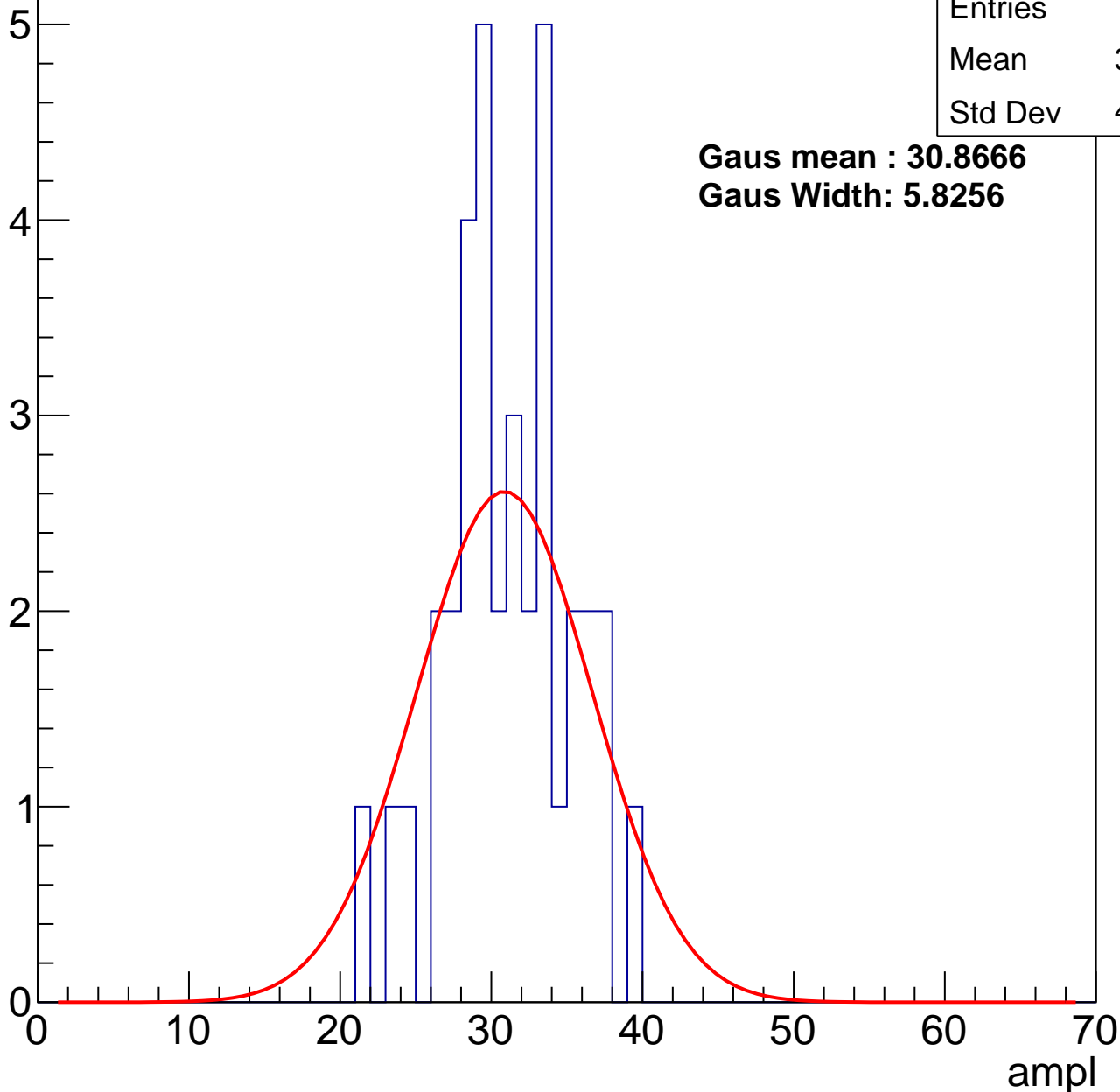
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	30.61
Std Dev	4.071

Gaus mean : 30.8666

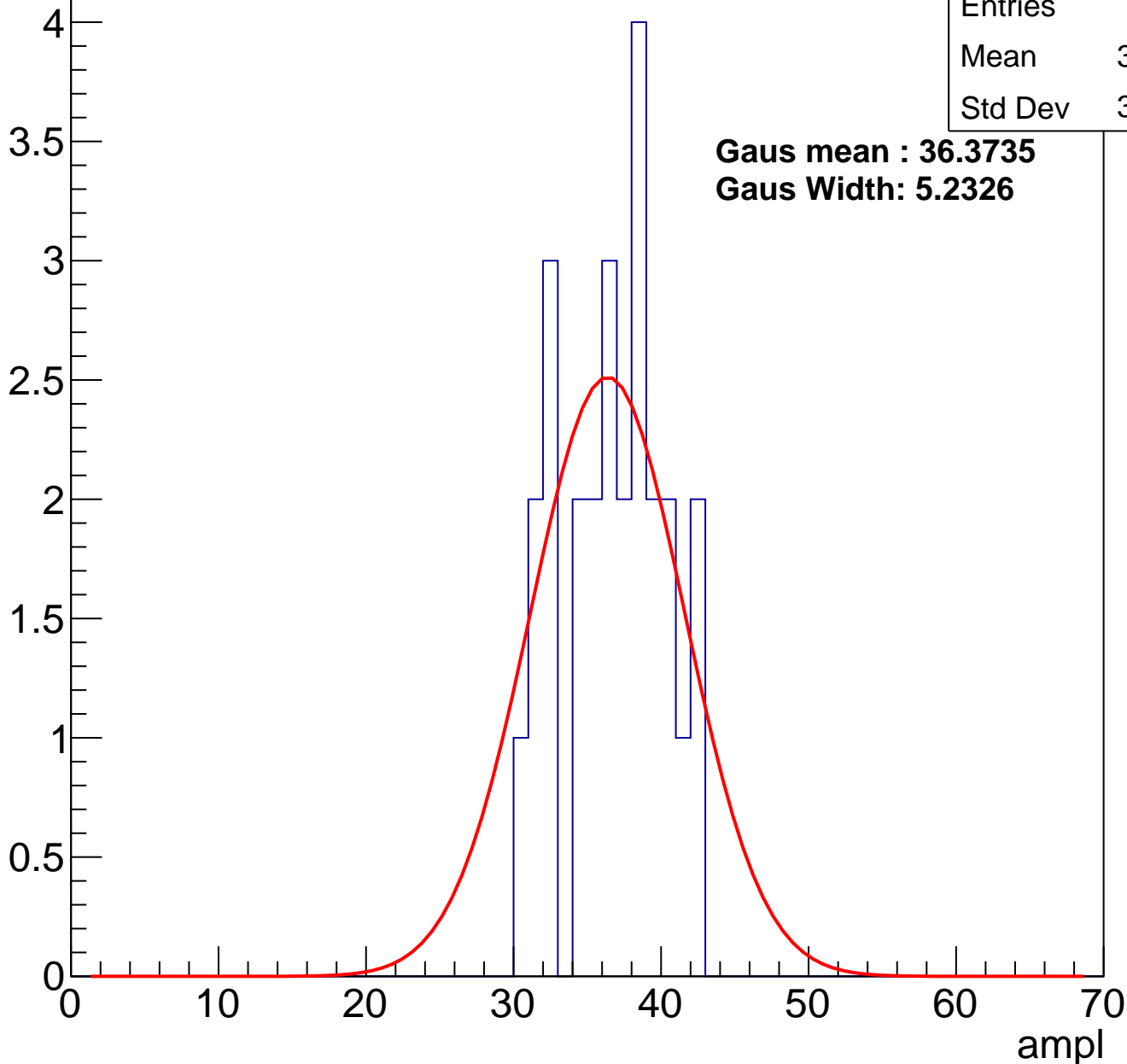
Gaus Width: 5.8256



B1L103S, U15-ch61, adc1

calib_packv5_042523_0143.root, FC#7, port C2

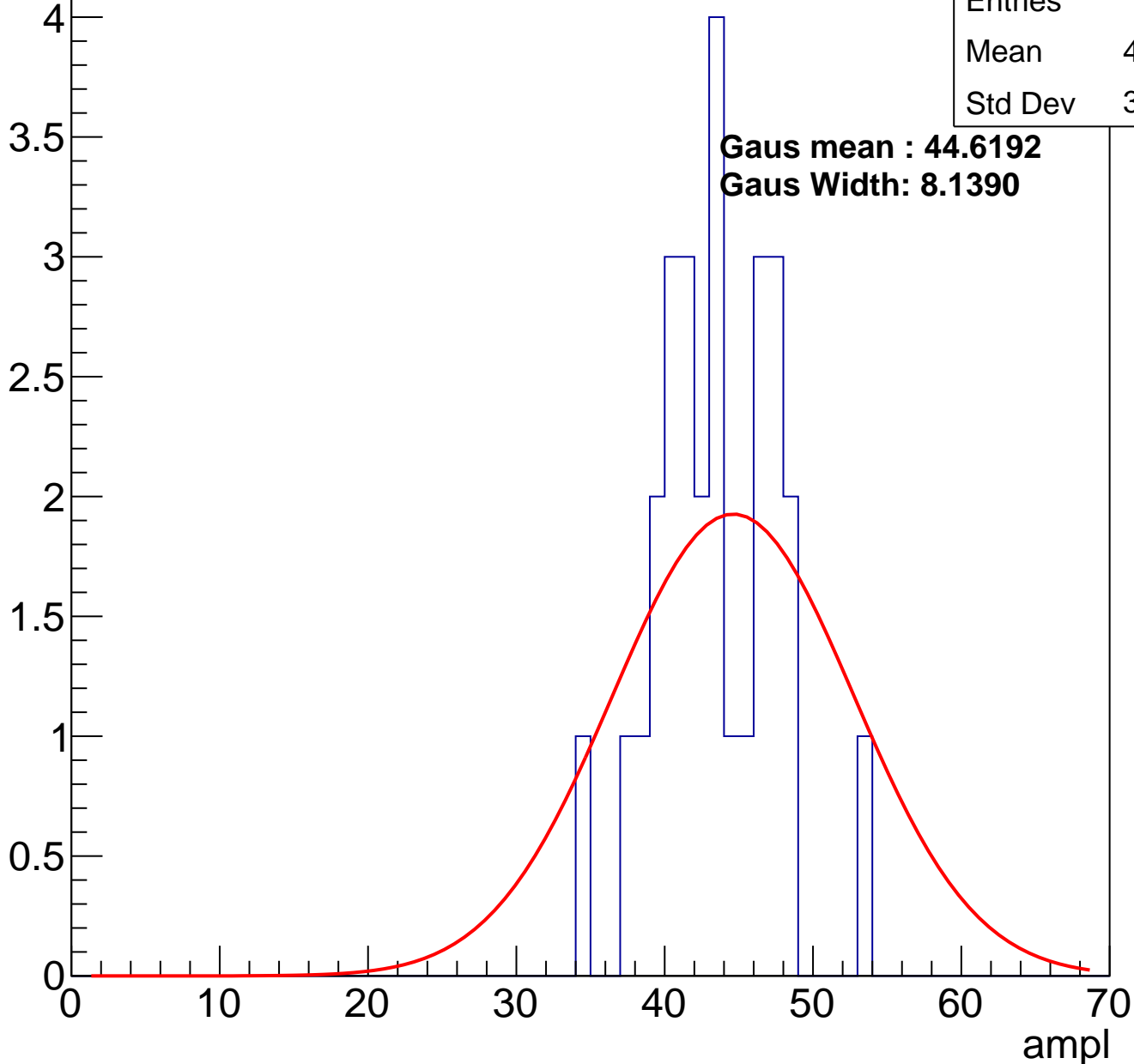
Entry



B1L103S, U15-ch61, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

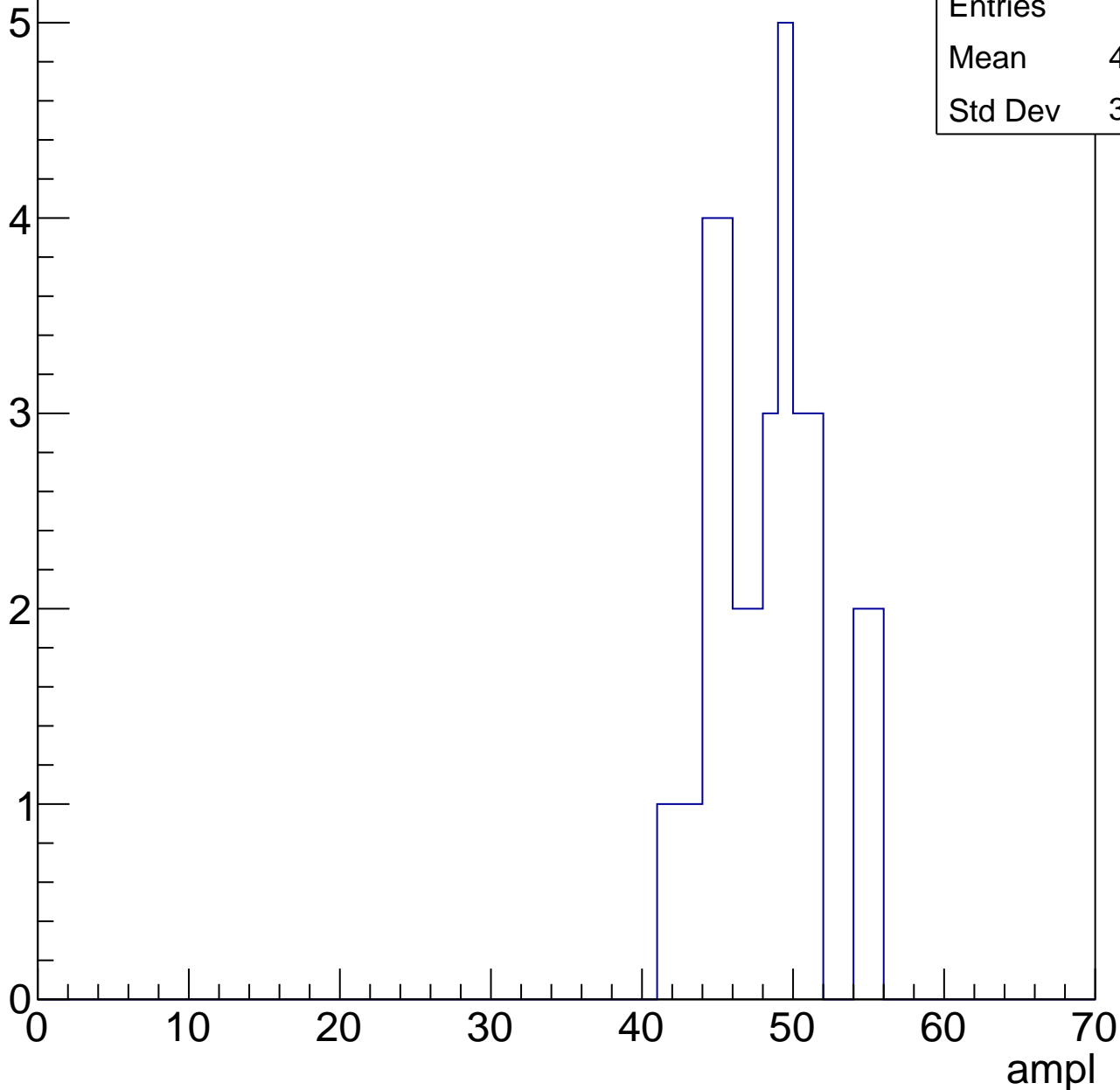


B1L103S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

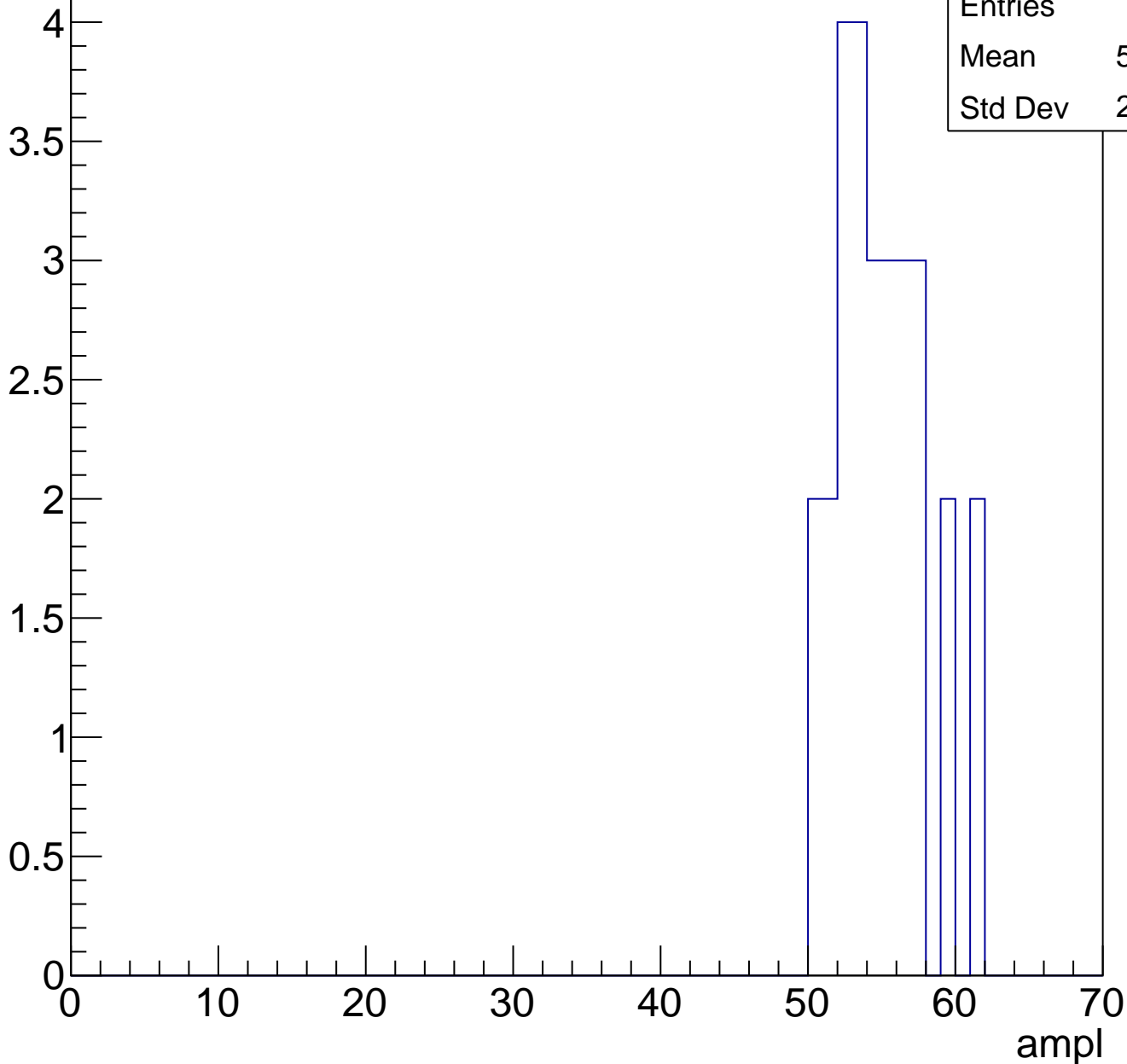
Entries	33
Mean	47.82
Std Dev	3.622



B1L103S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



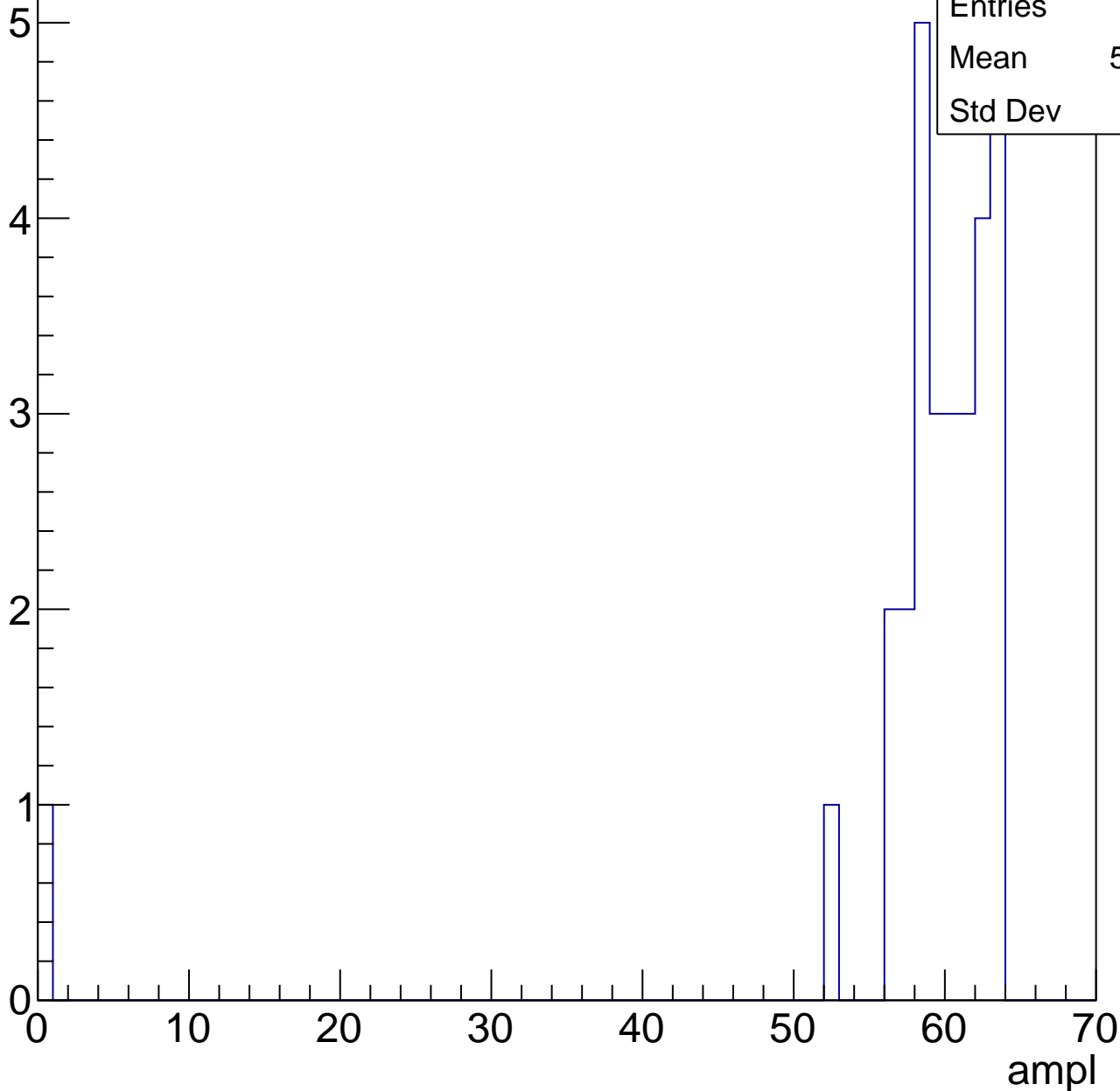
Entries	28
Mean	54.57
Std Dev	2.993

B1L103S, U15-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

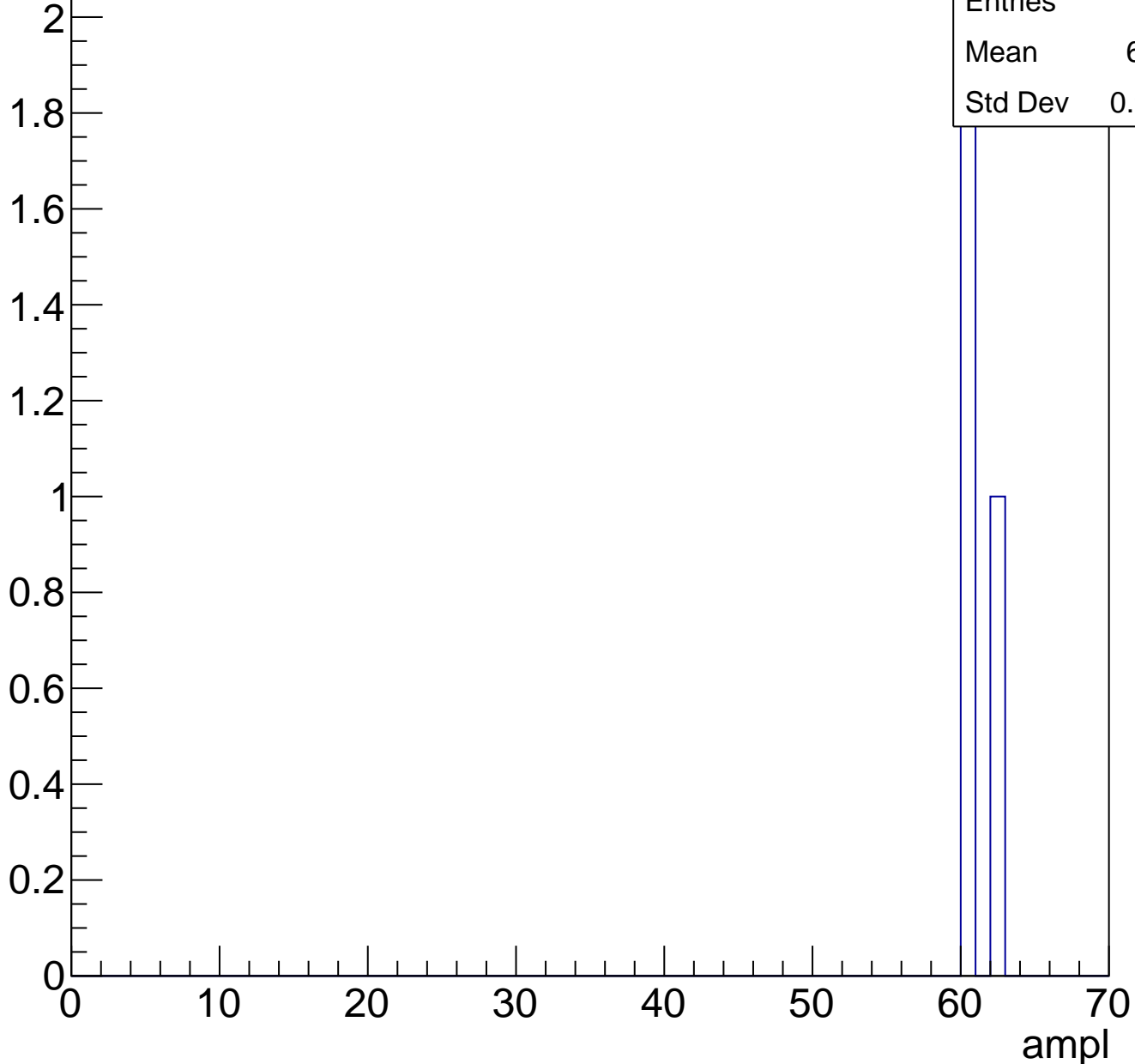
Entries	29
Mean	57.62
Std Dev	11.2



B1L103S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	60.67
Std Dev	0.9428

B1L103S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

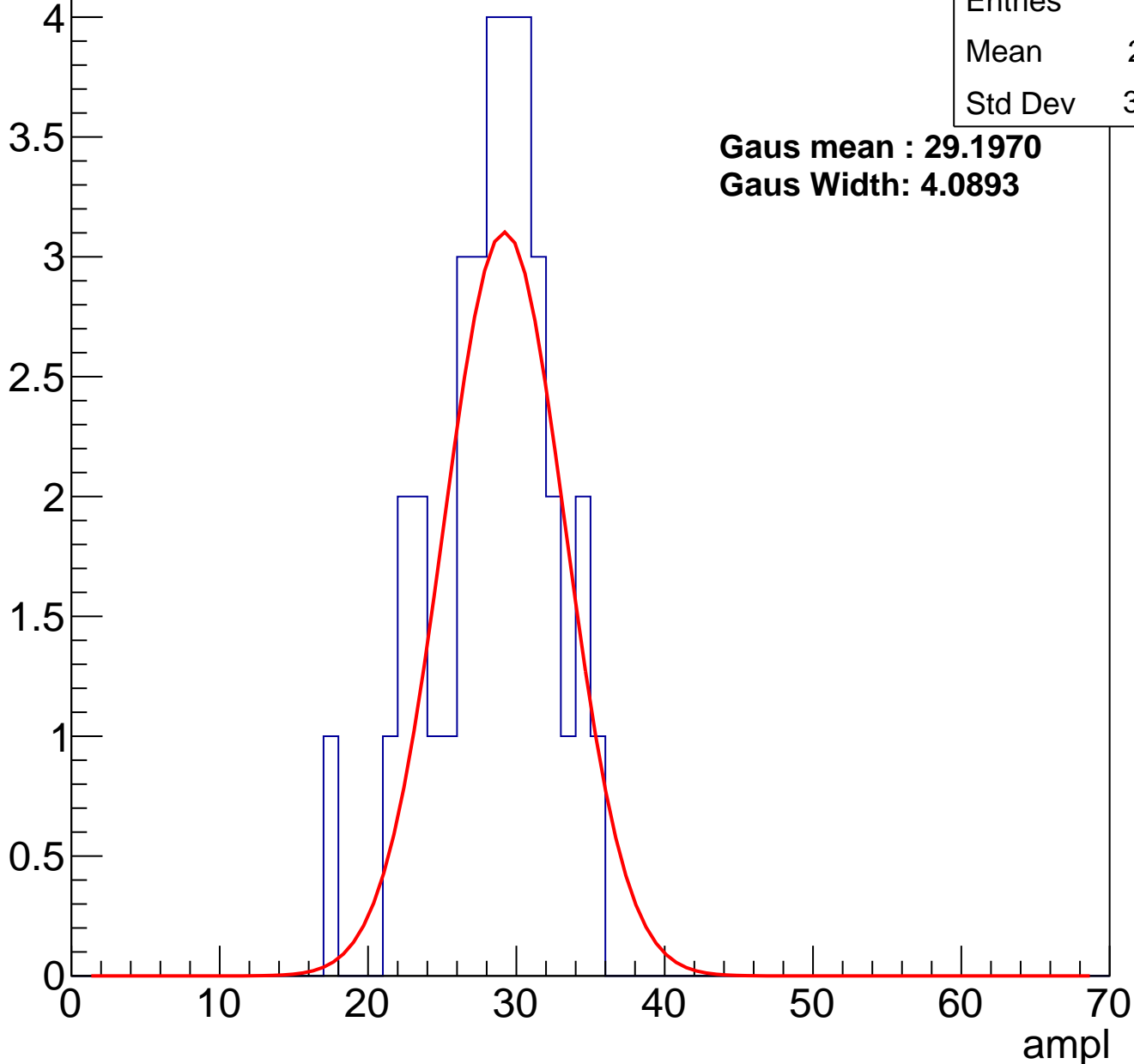
Entry



B1L103S, U15-ch62, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch62, adc1

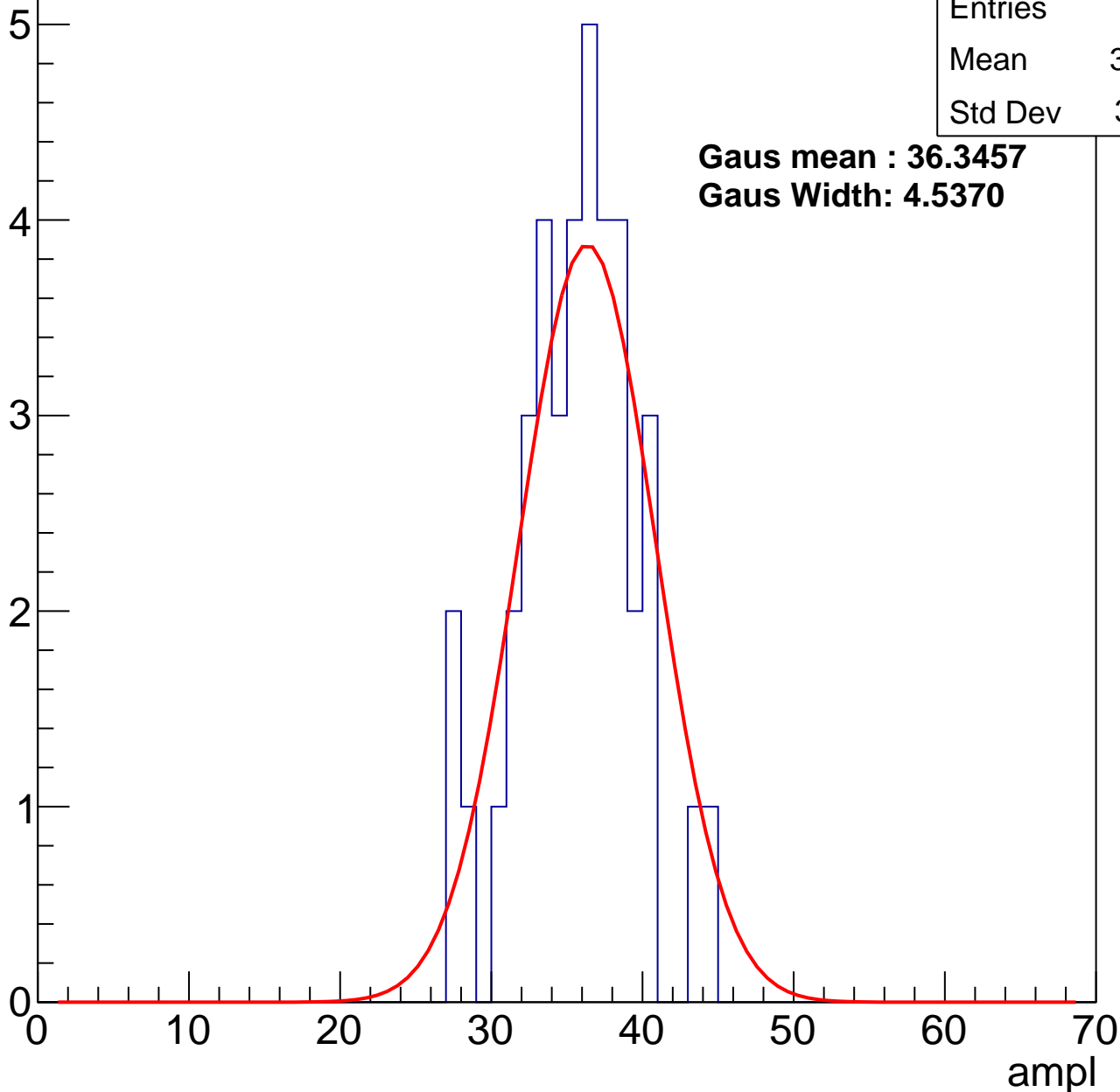
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	35.23
Std Dev	3.831

Gaus mean : 36.3457

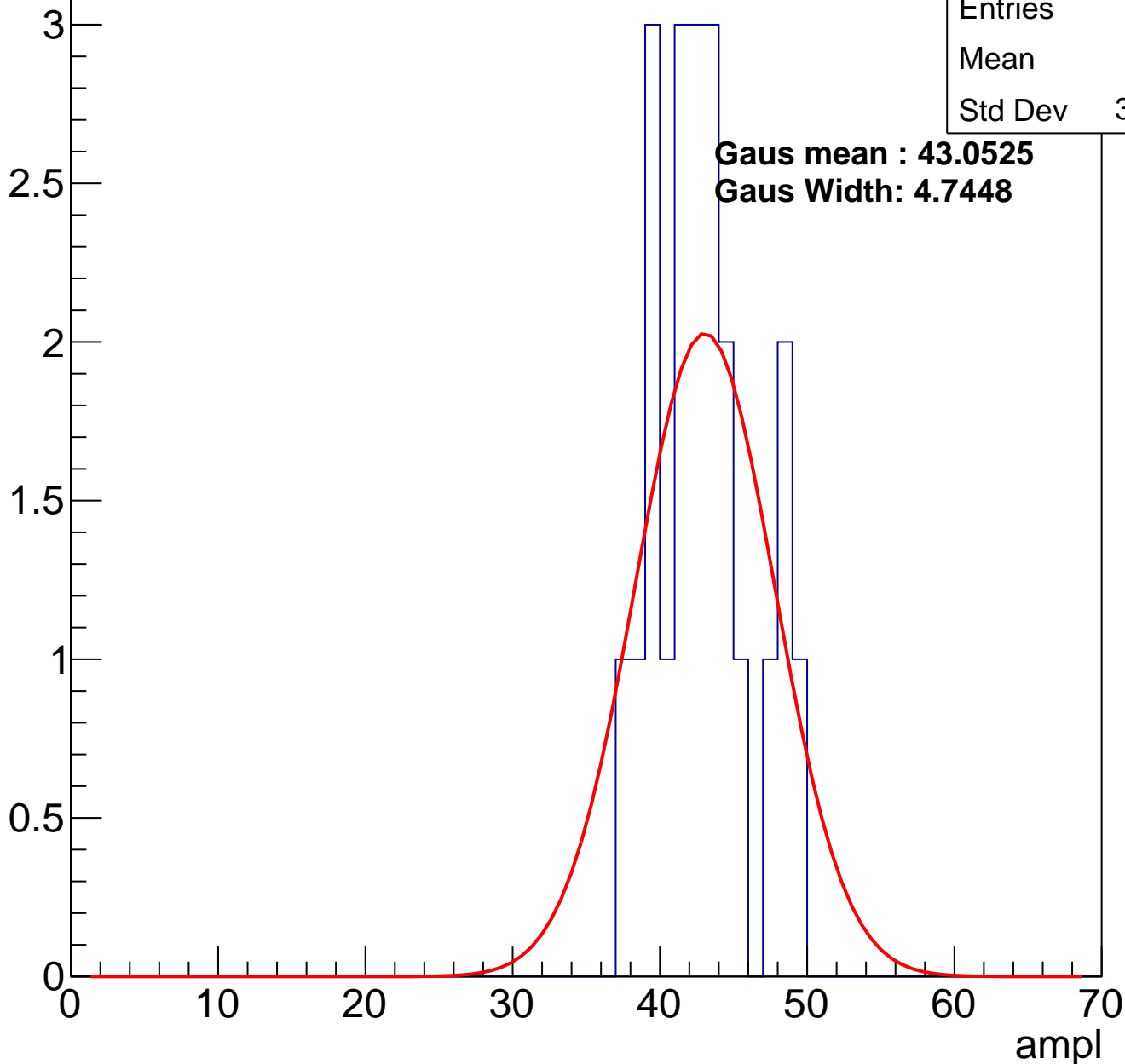
Gaus Width: 4.5370



B1L103S, U15-ch62, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

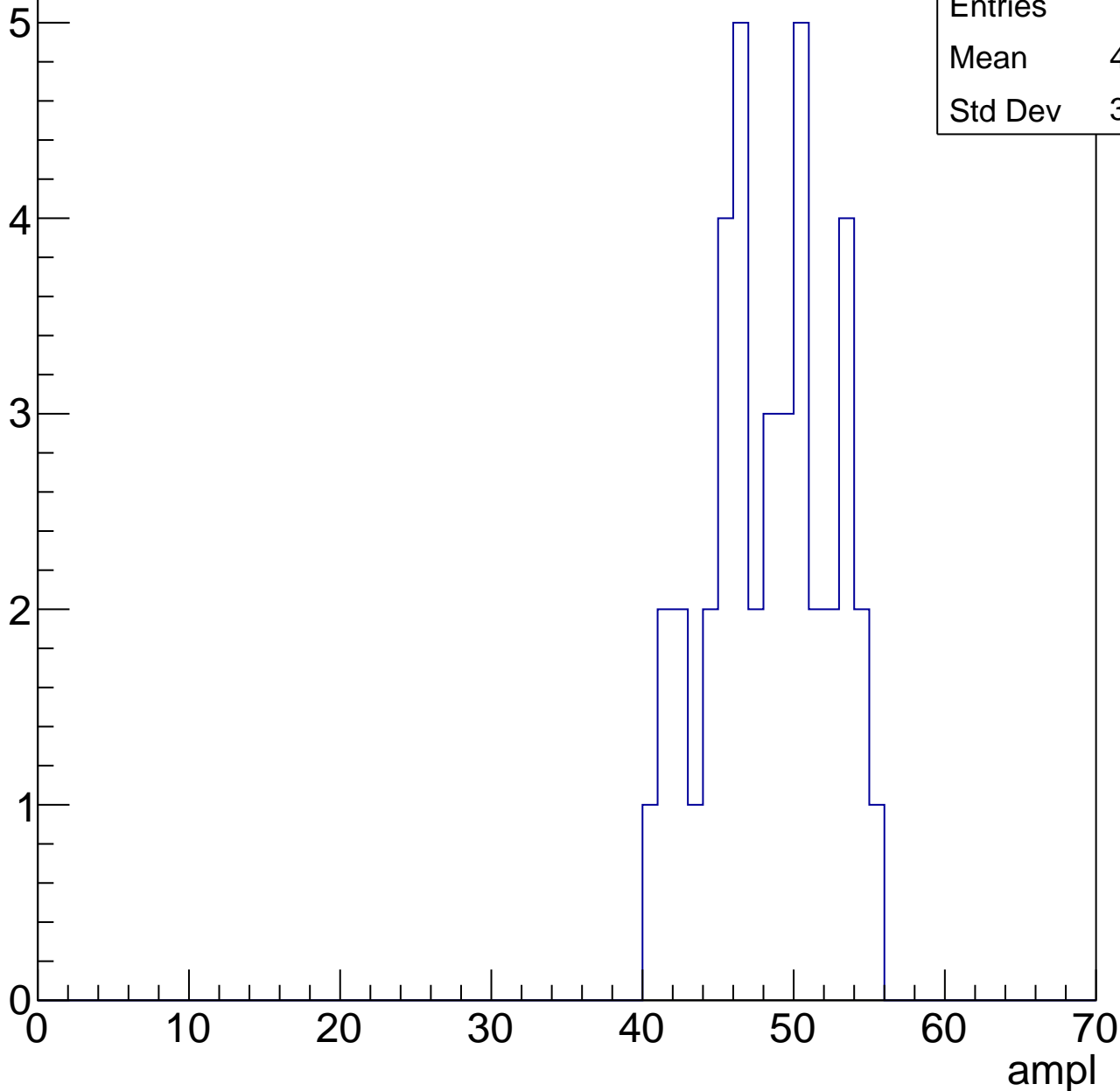


B1L103S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	47.88
Std Dev	3.958

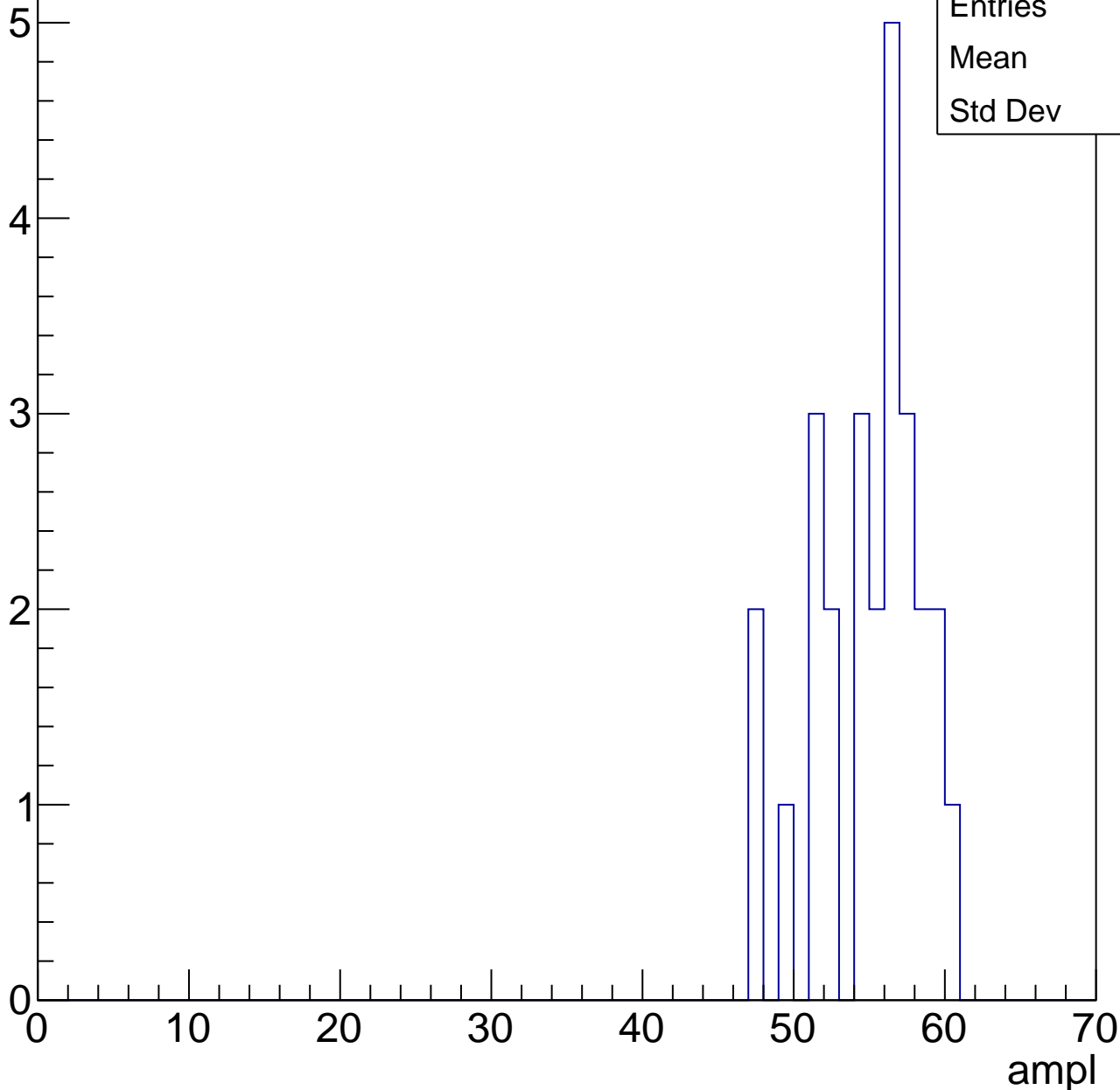


B1L103S, U15-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

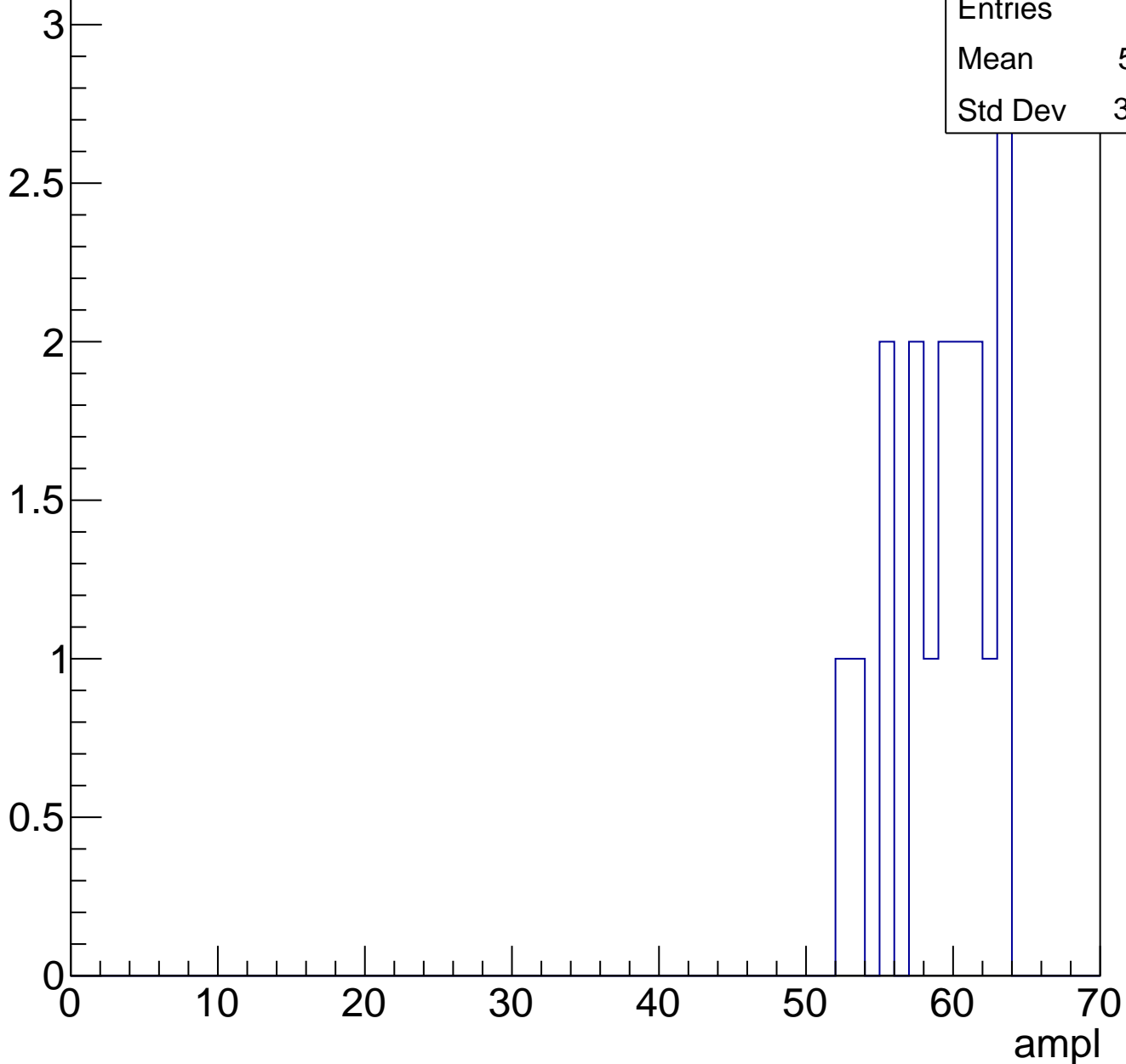
Entries	26
Mean	54.5
Std Dev	3.5



B1L103S, U15-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

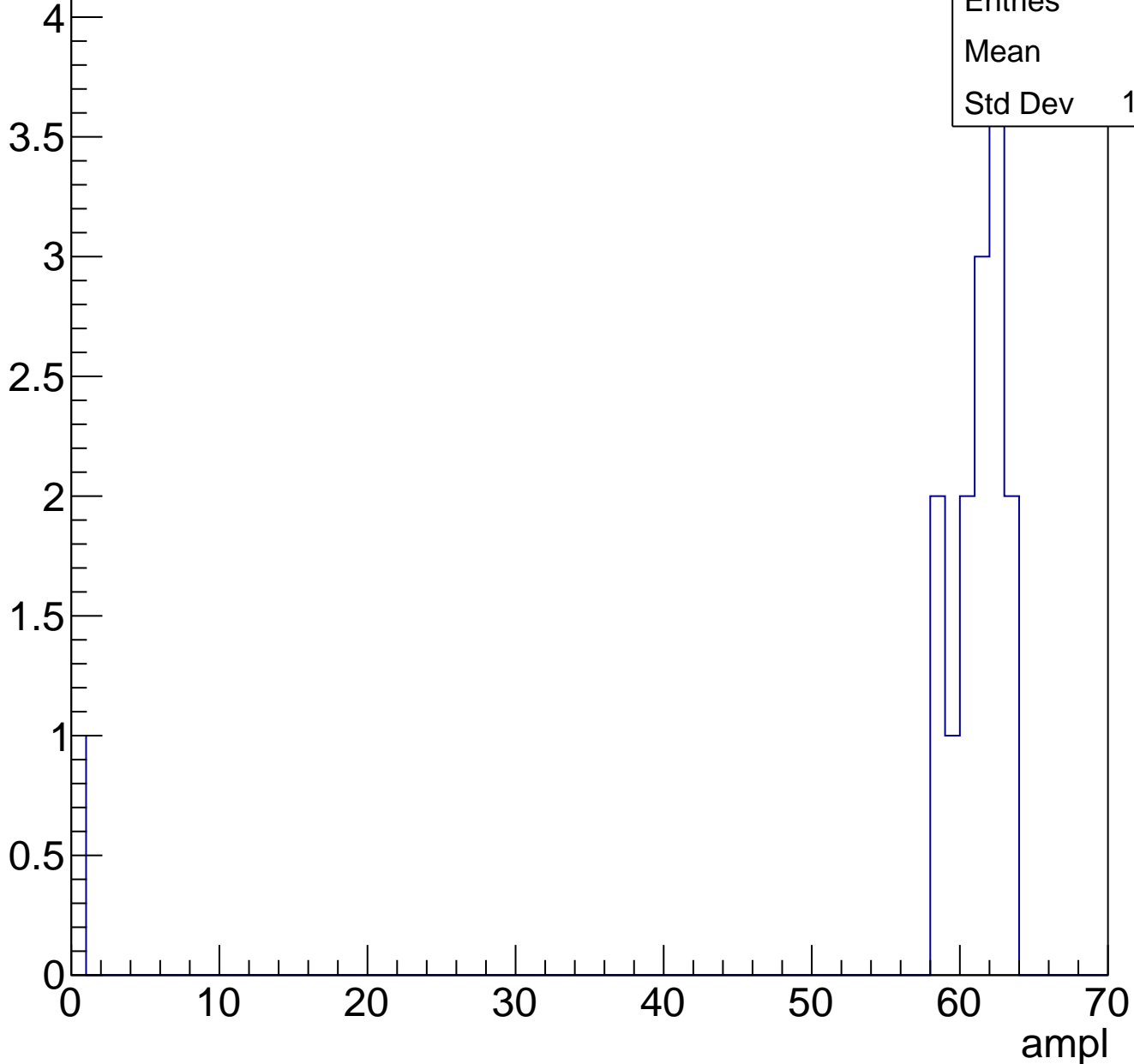
Entry



B1L103S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch63, adc0

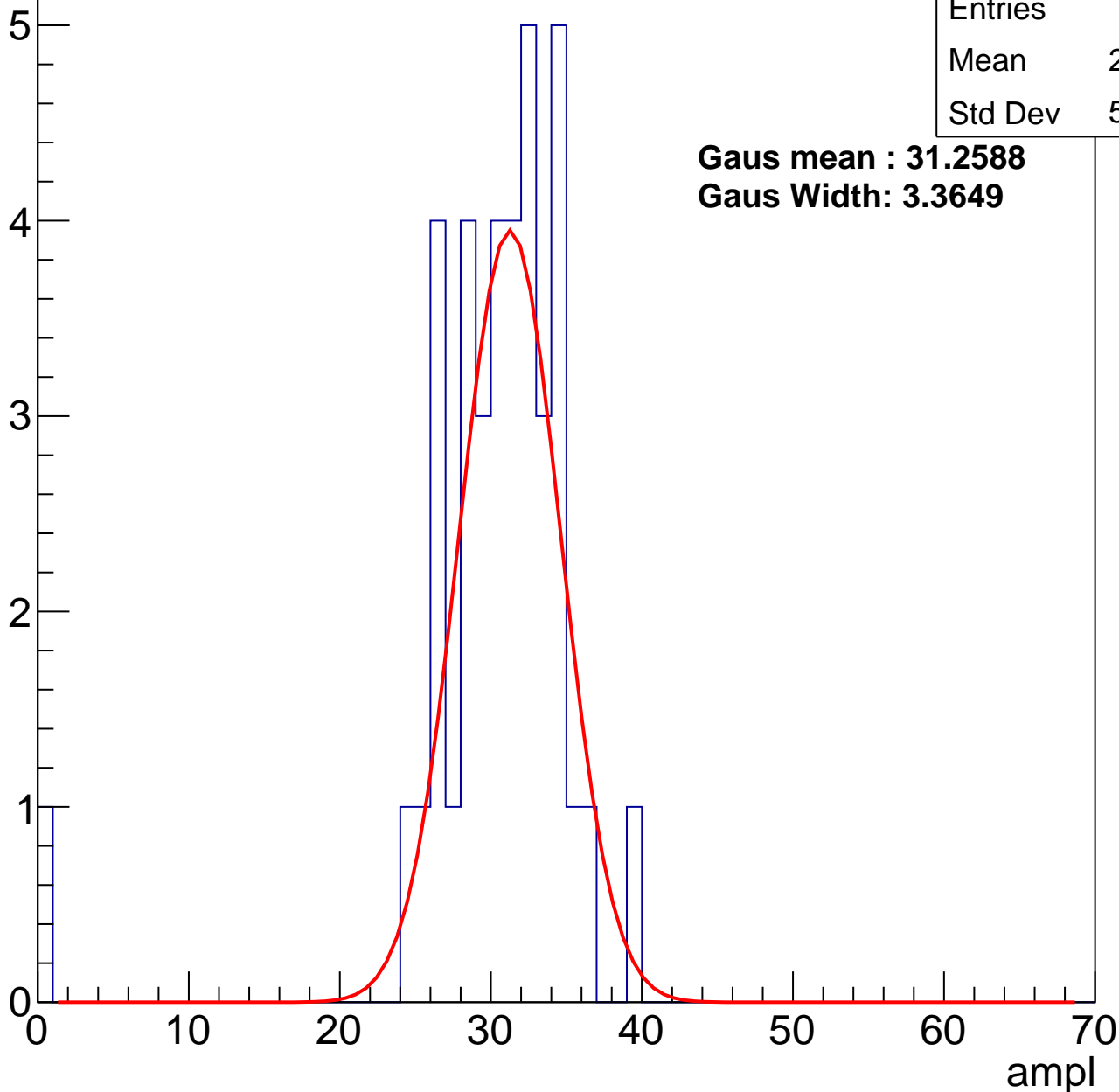
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	29.79
Std Dev	5.827

Gaus mean : 31.2588

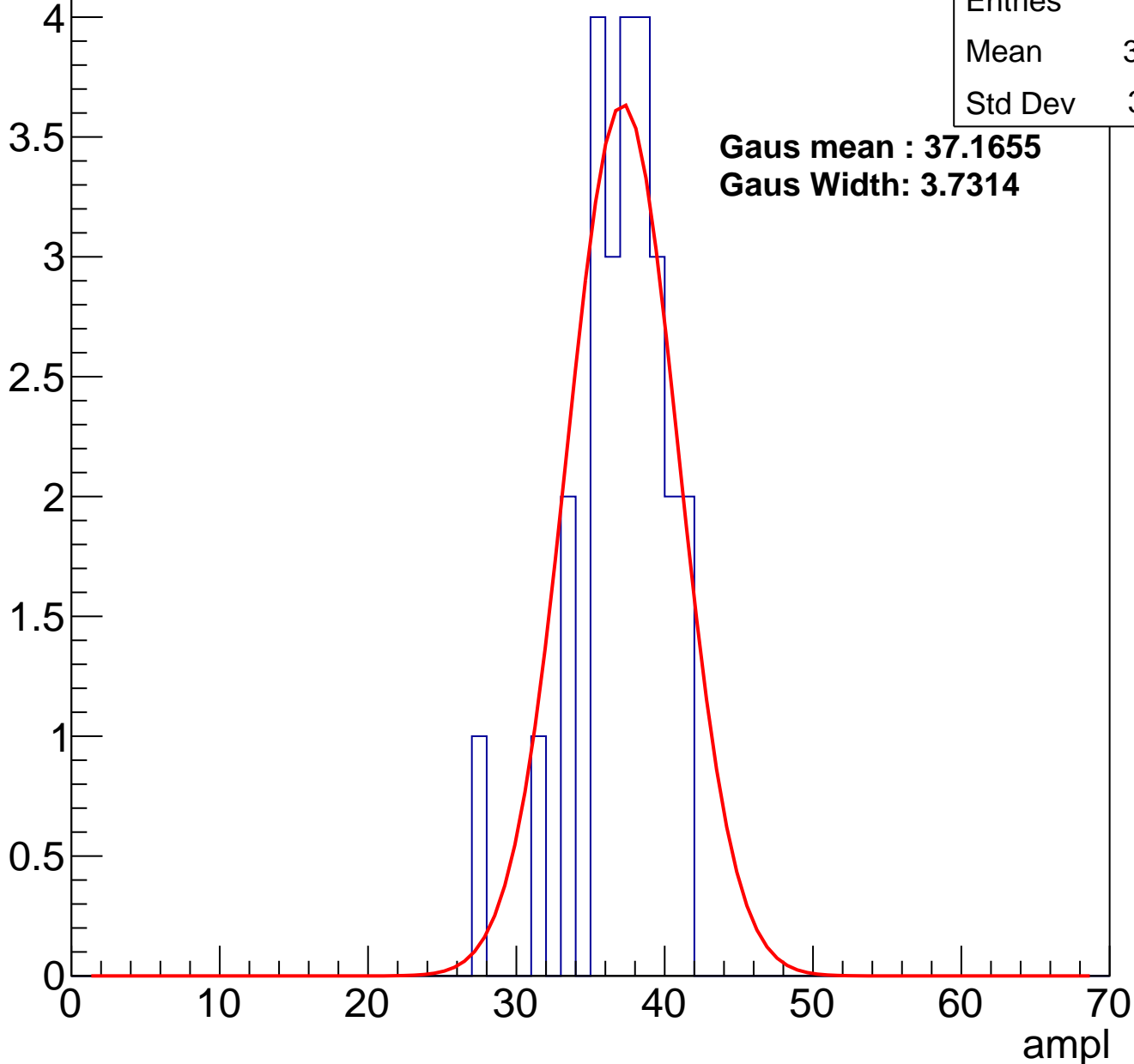
Gaus Width: 3.3649



B1L103S, U15-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch63, adc2

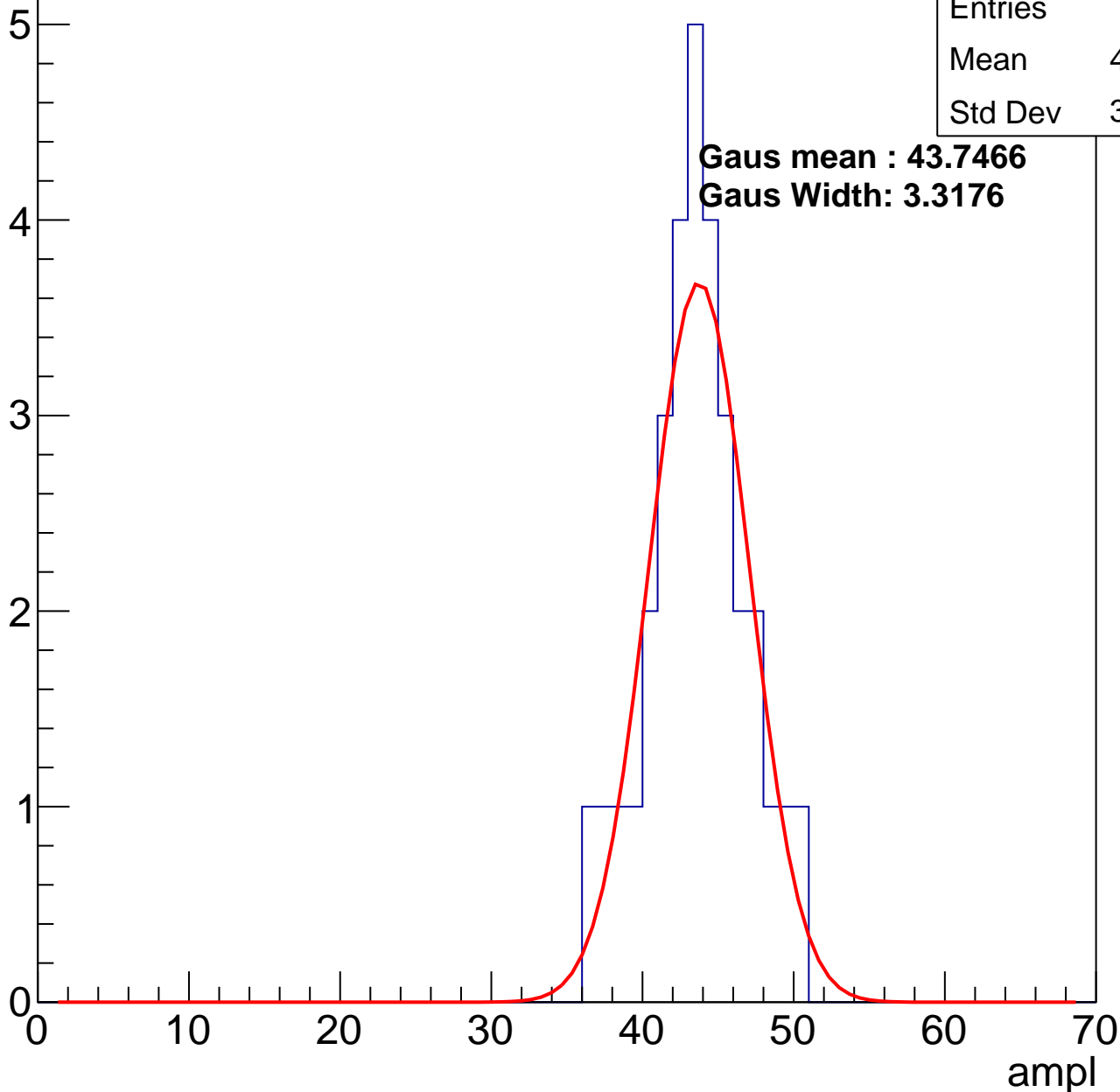
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	43.12
Std Dev	3.238

Gaus mean : 43.7466

Gaus Width: 3.3176

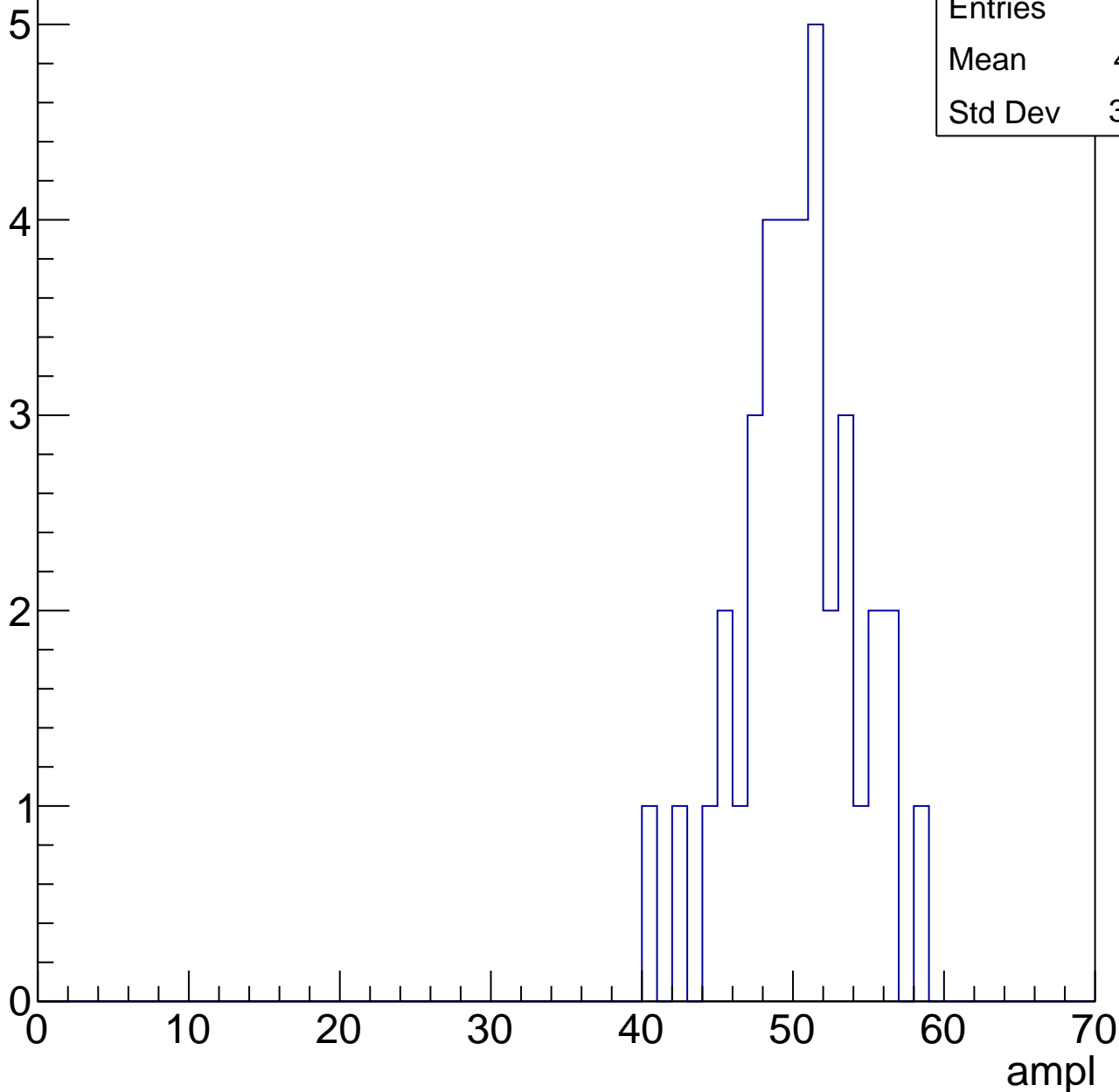


B1L103S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

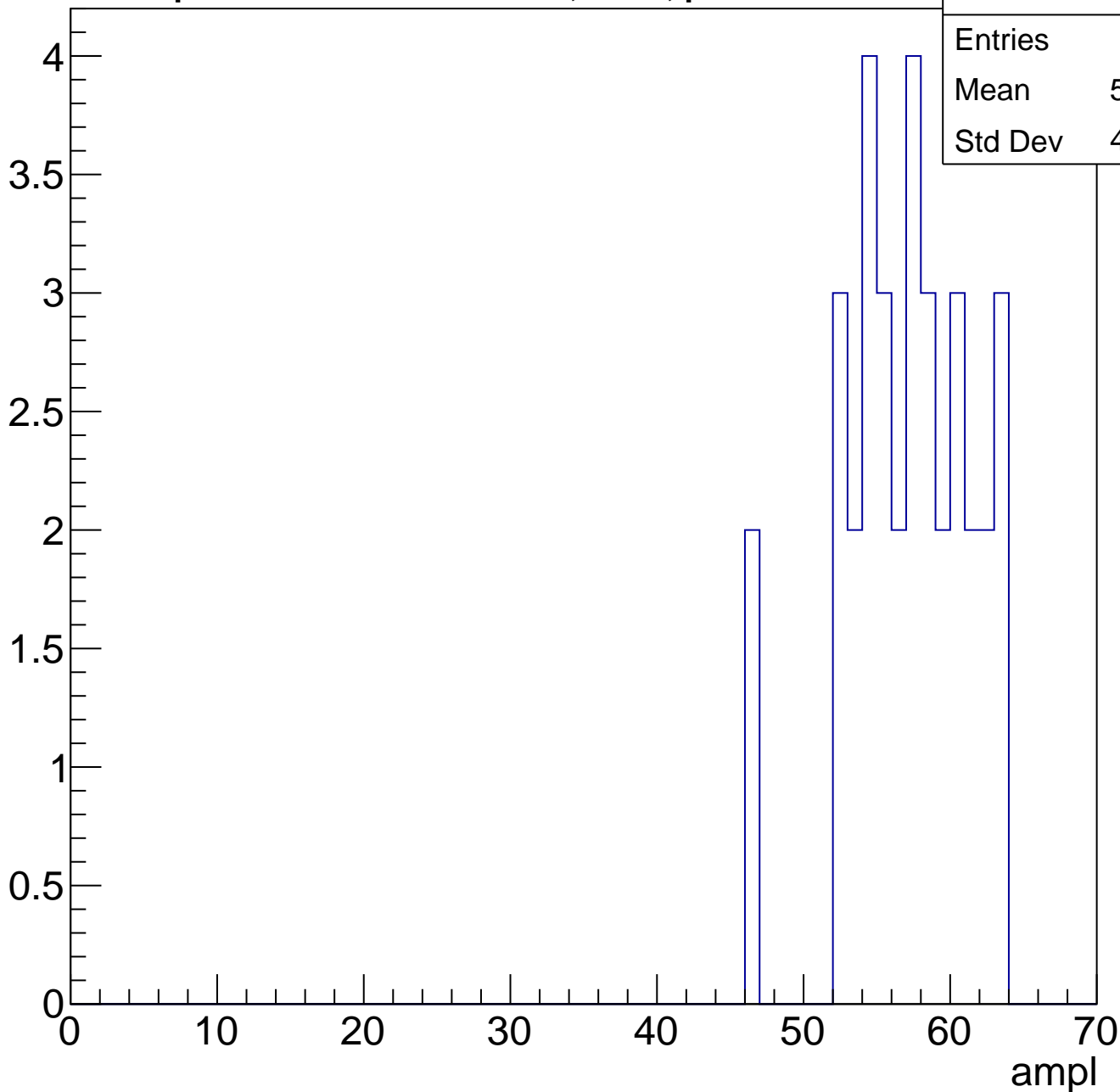
Entries	37
Mean	49.81
Std Dev	3.868



B1L103S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	14
Mean	59.86
Std Dev	1.922

B1L103S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch64, adc0

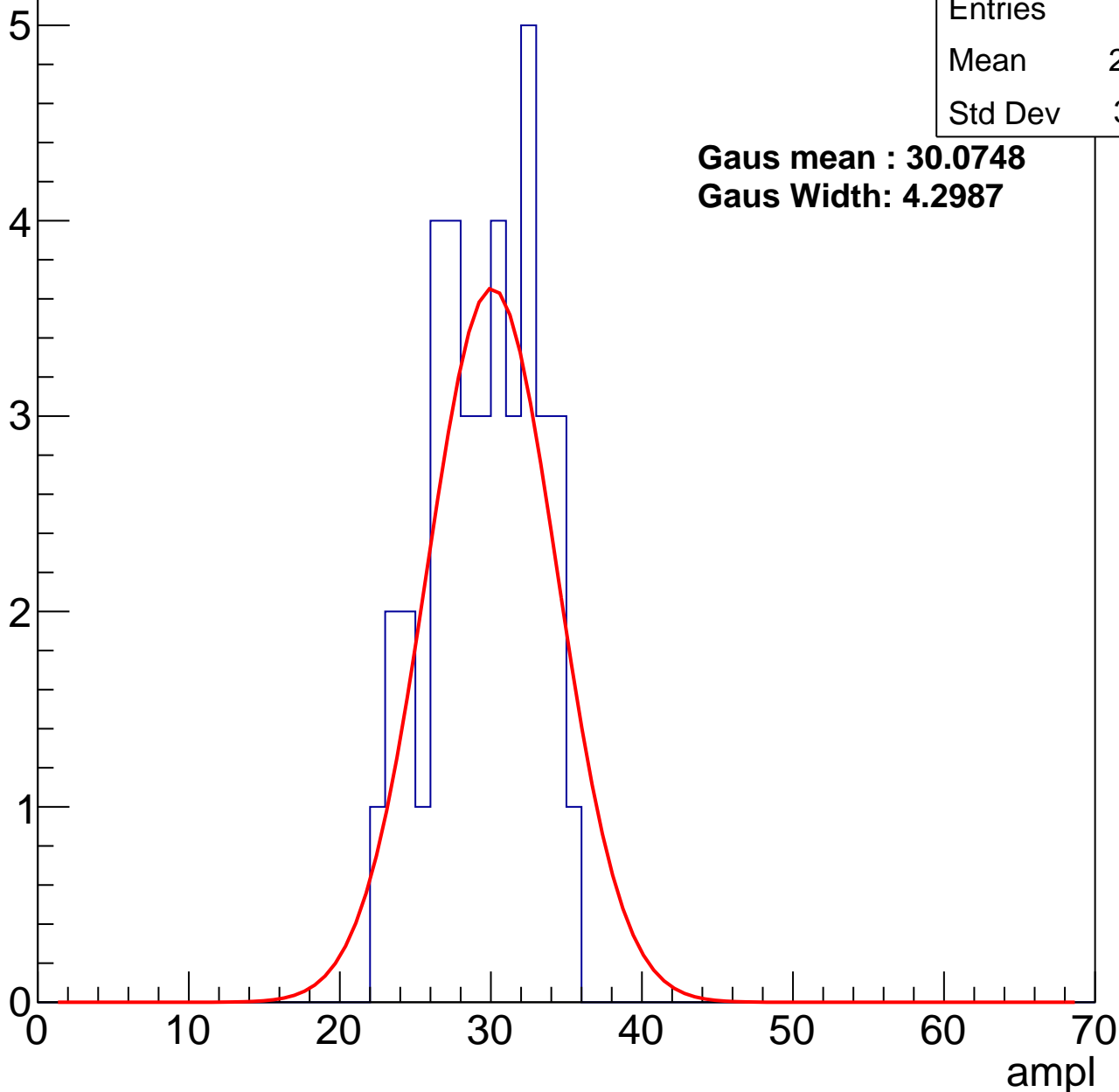
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	29.05
Std Dev	3.441

Gaus mean : 30.0748

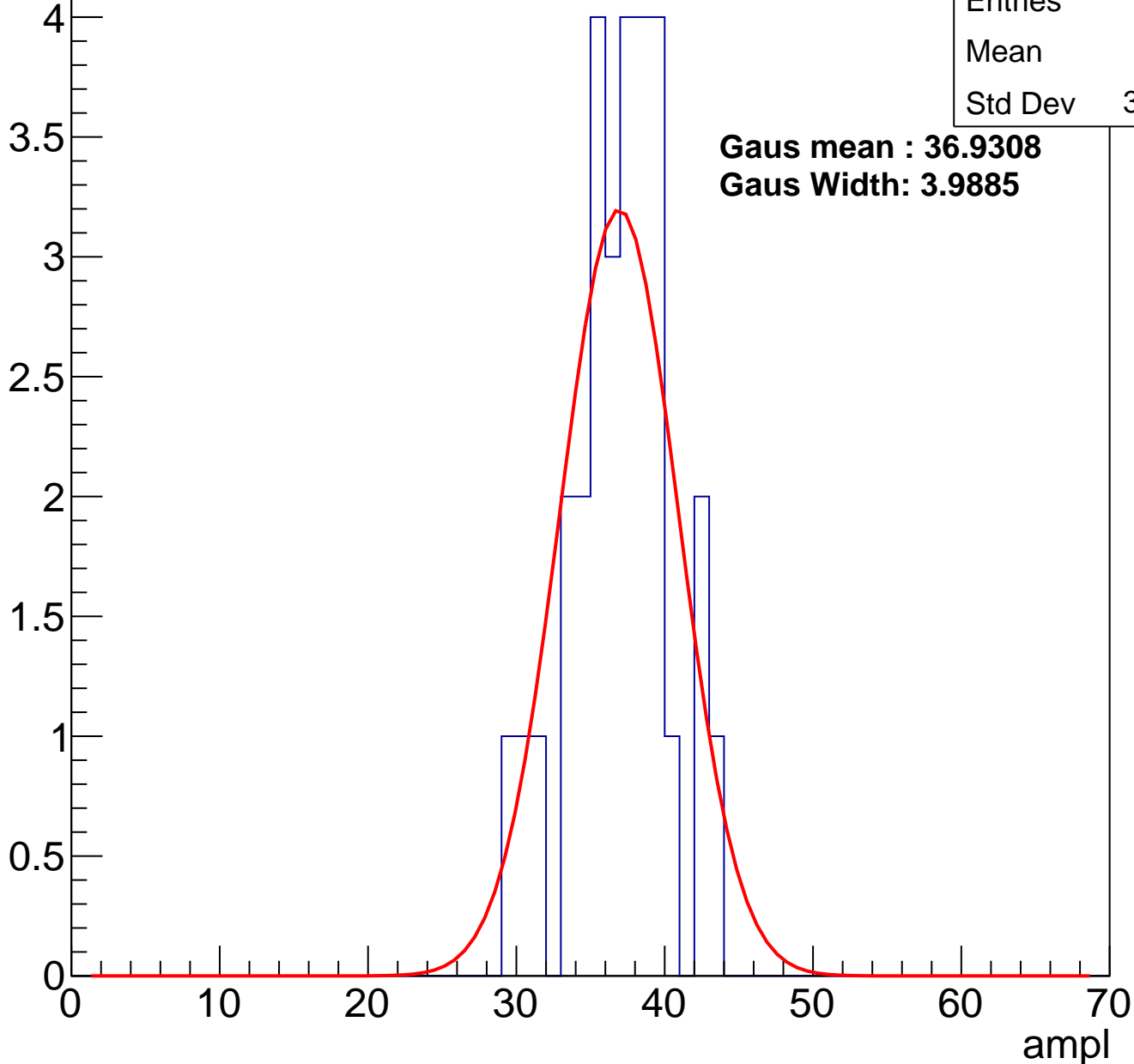
Gaus Width: 4.2987



B1L103S, U15-ch64, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch64, adc2

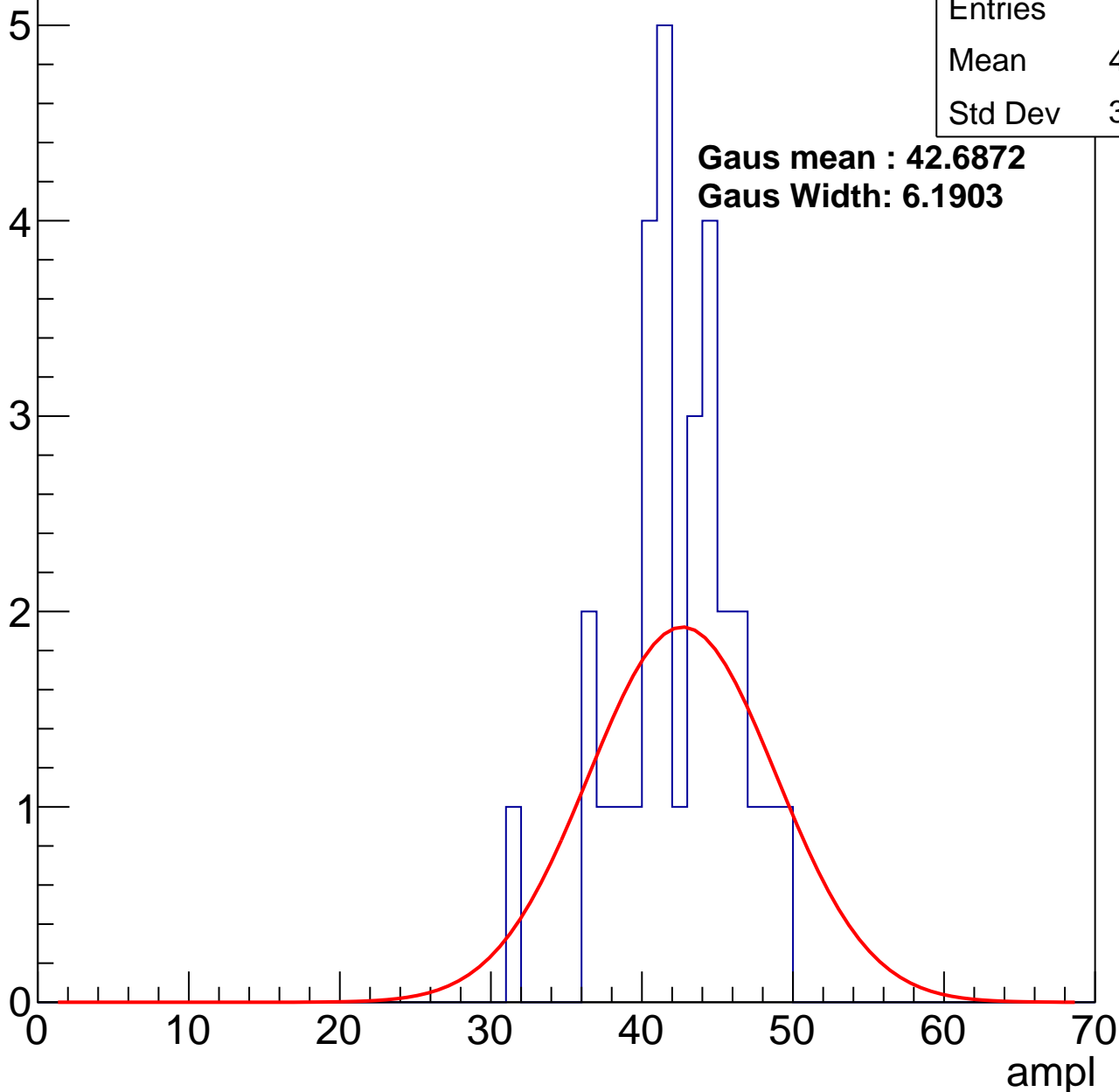
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	41.83
Std Dev	3.839

Gaus mean : 42.6872

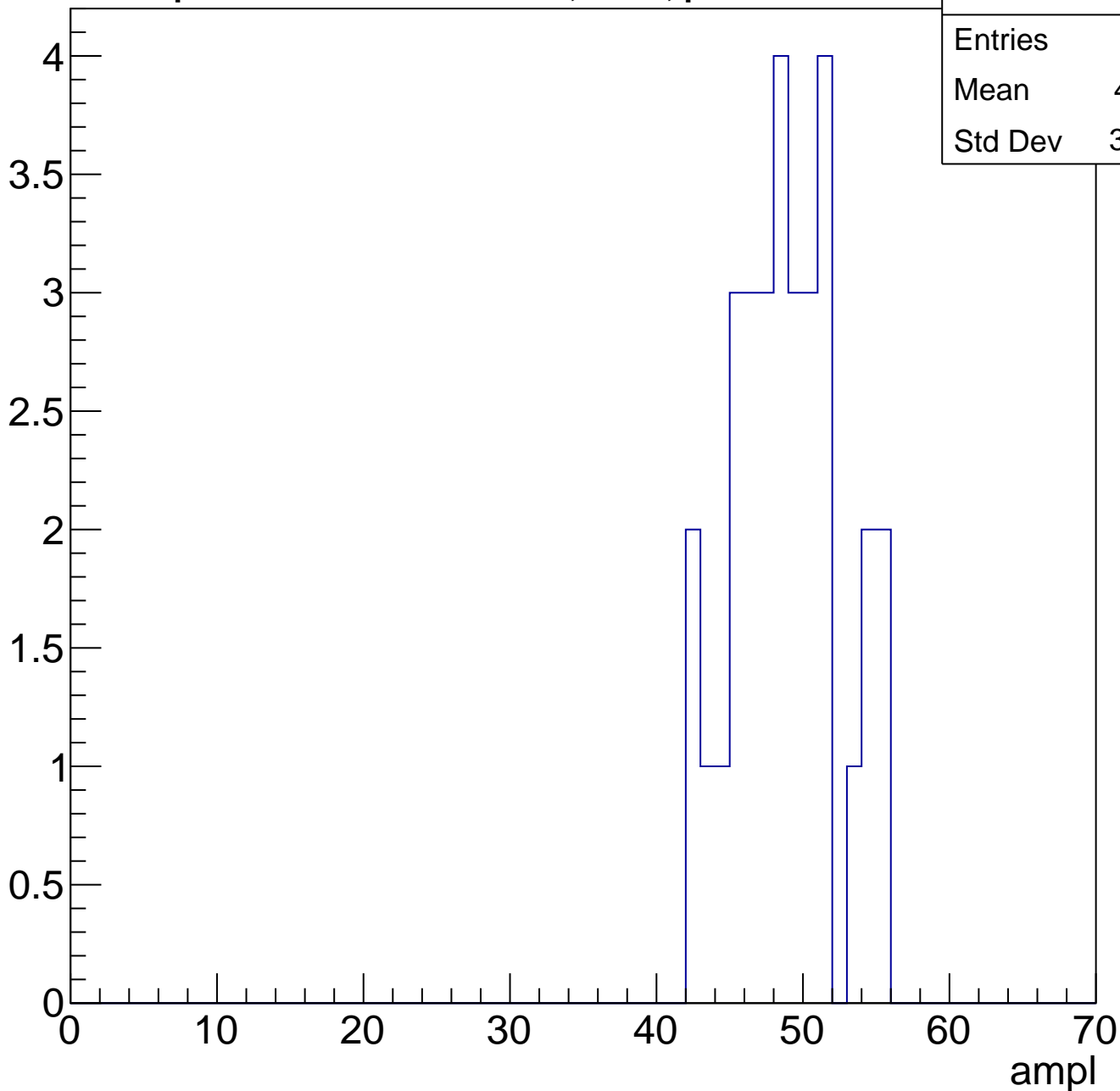
Gaus Width: 6.1903



B1L103S, U15-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



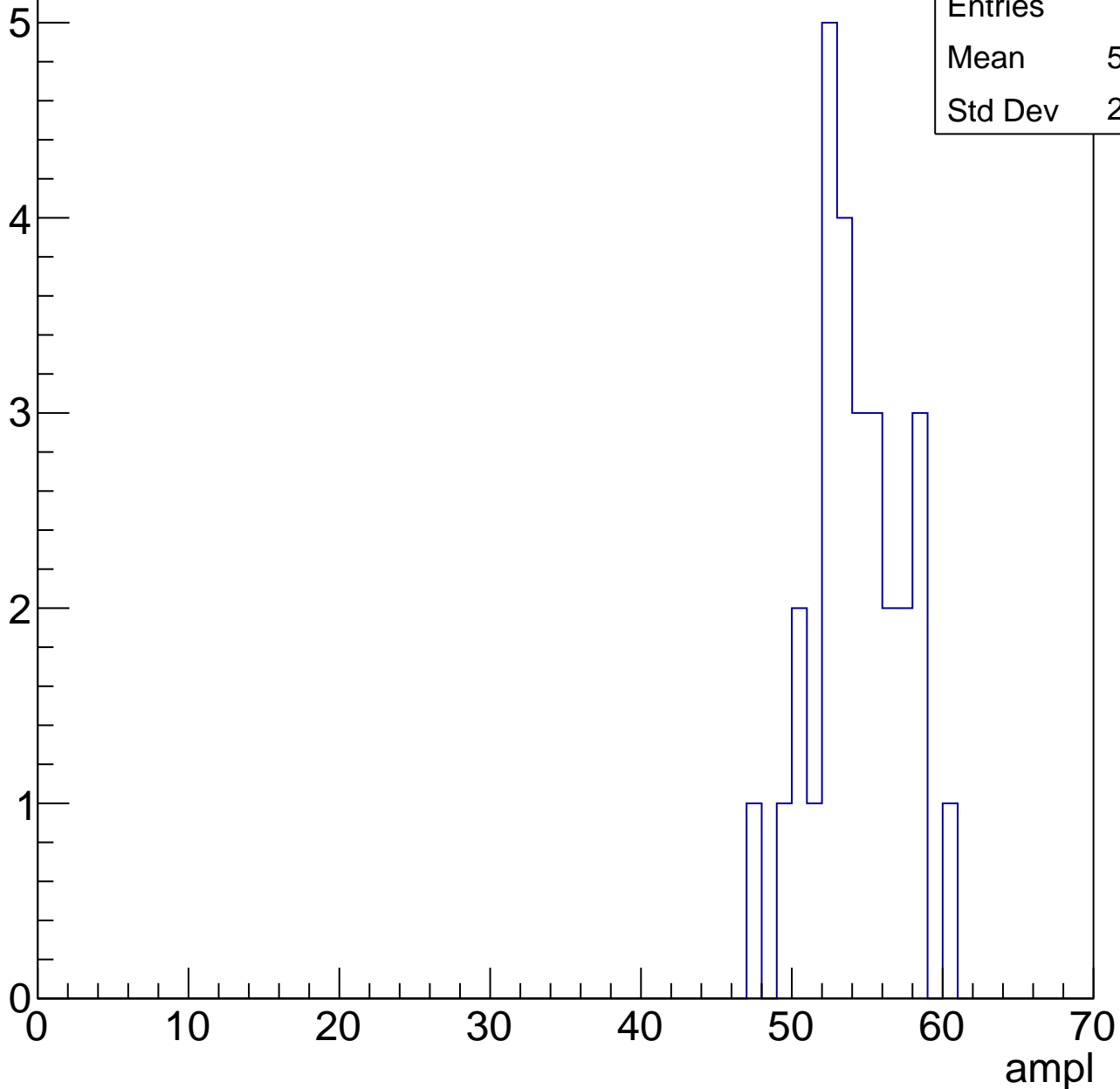
Entries	32
Mean	48.41
Std Dev	3.517

B1L103S, U15-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

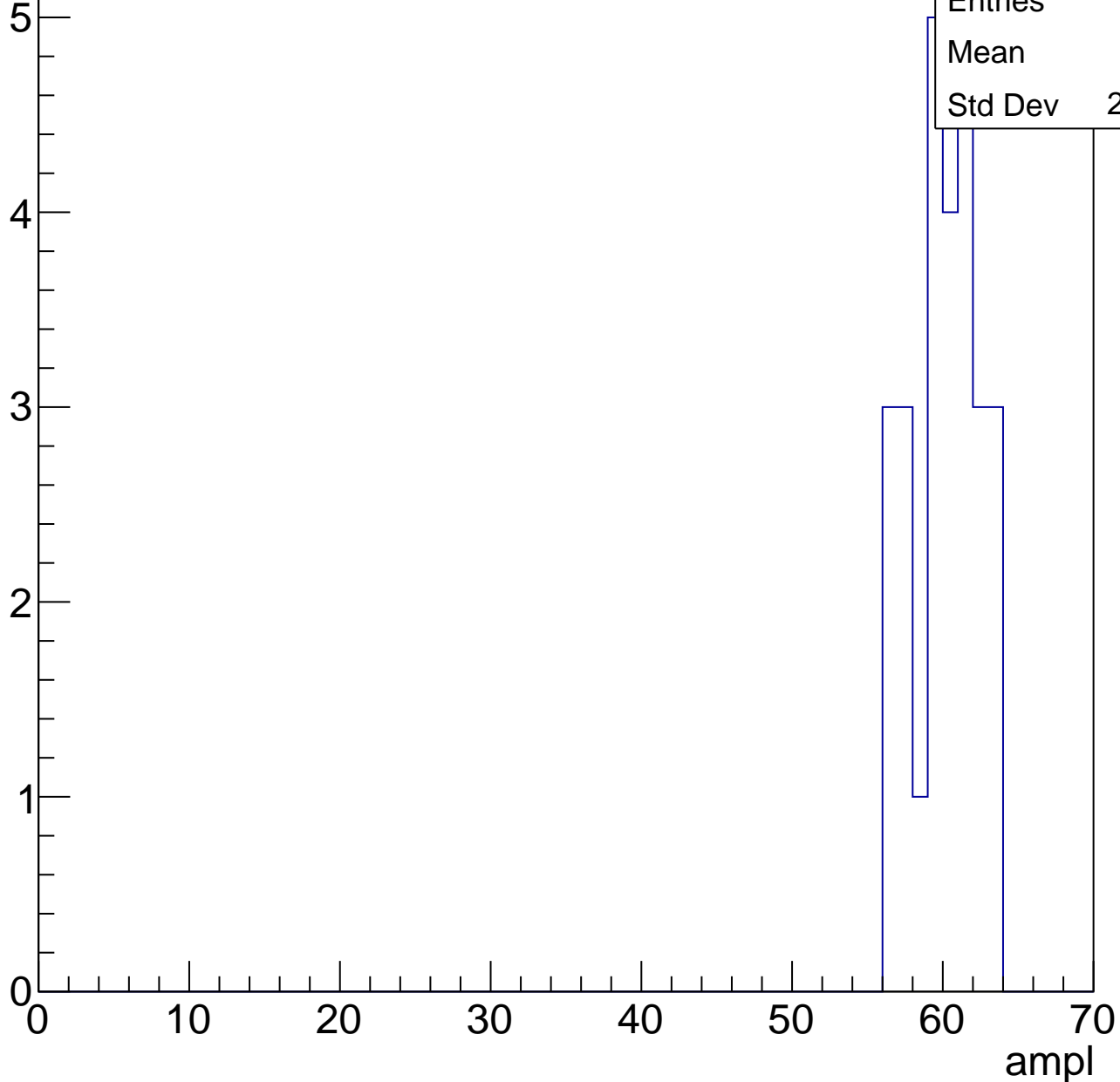
Entries	28
Mean	53.79
Std Dev	2.992



B1L103S, U15-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

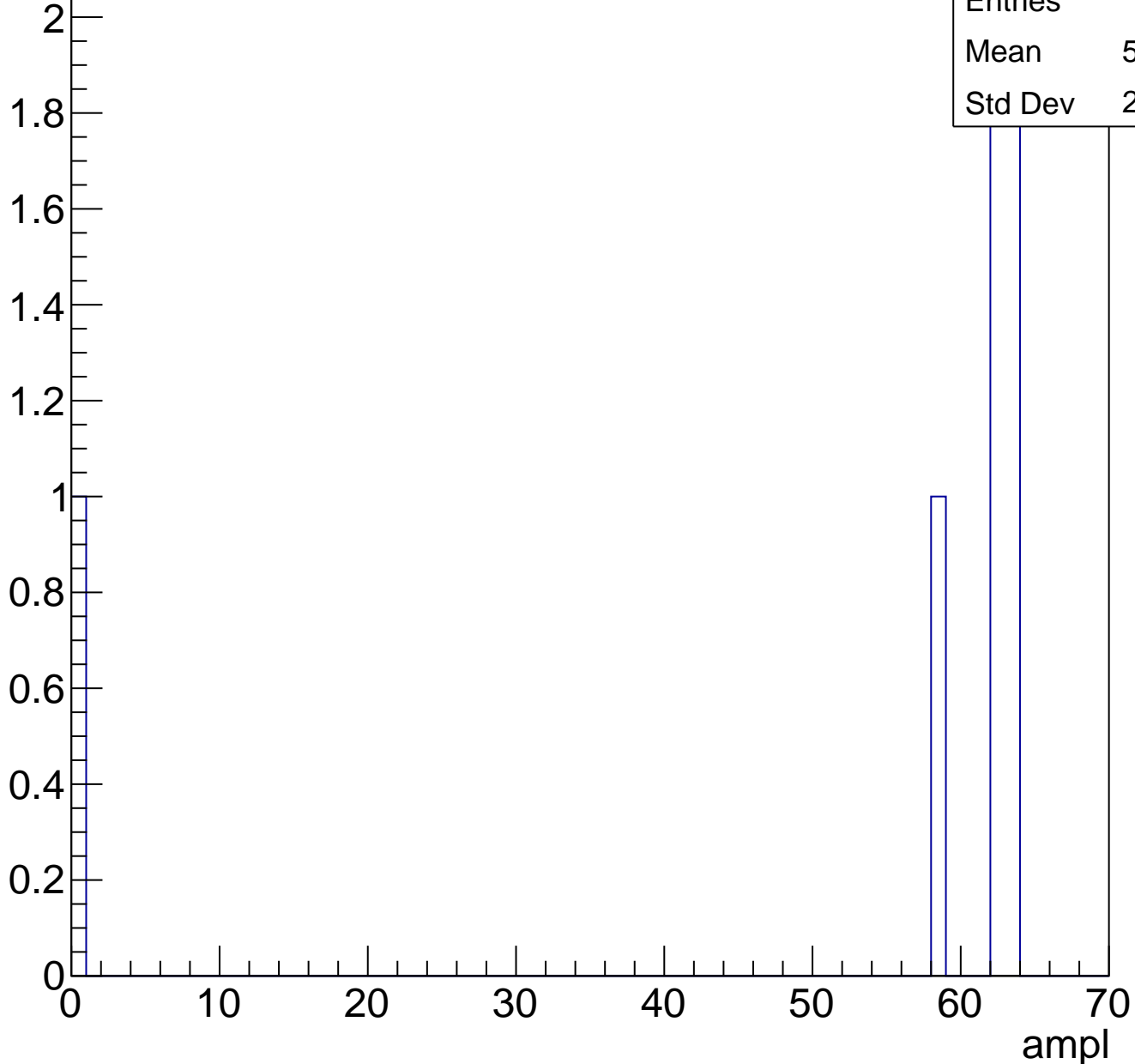
Entry



B1L103S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	51.33
Std Dev	23.02

B1L103S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch65, adc0

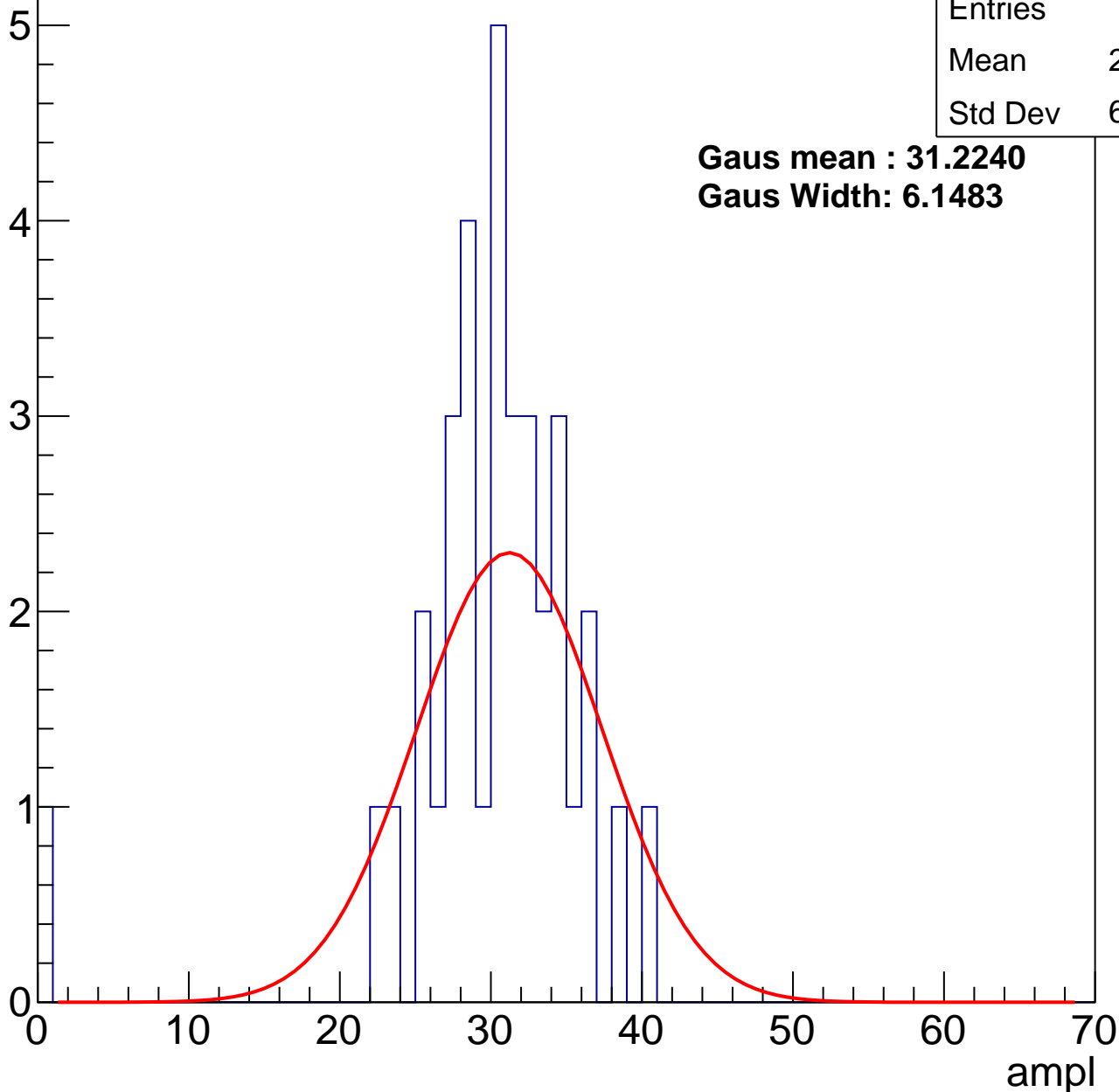
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	29.57
Std Dev	6.455

Gaus mean : 31.2240

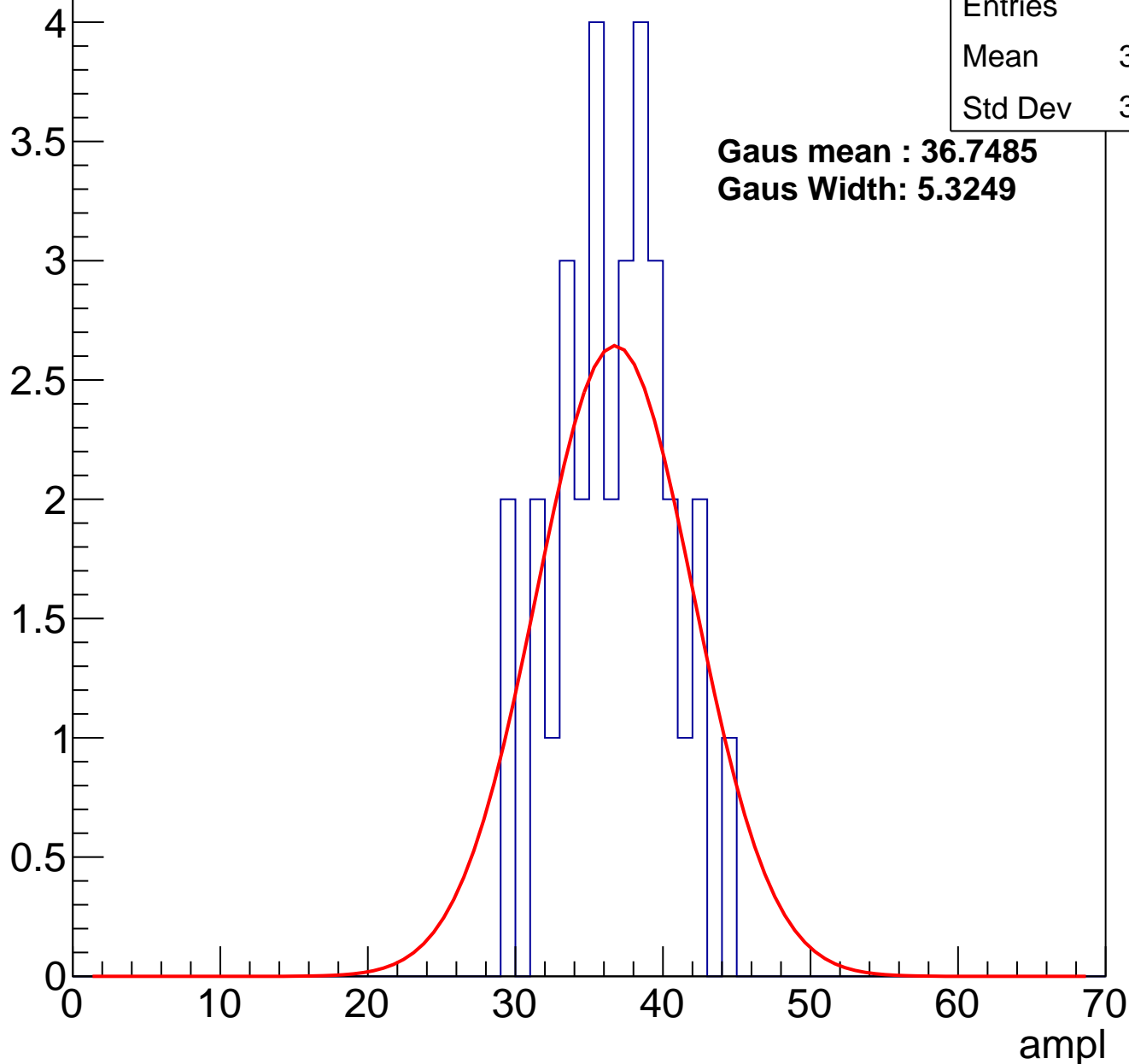
Gaus Width: 6.1483



B1L103S, U15-ch65, adc1

calib_packv5_042523_0143.root, FC#7, port C2

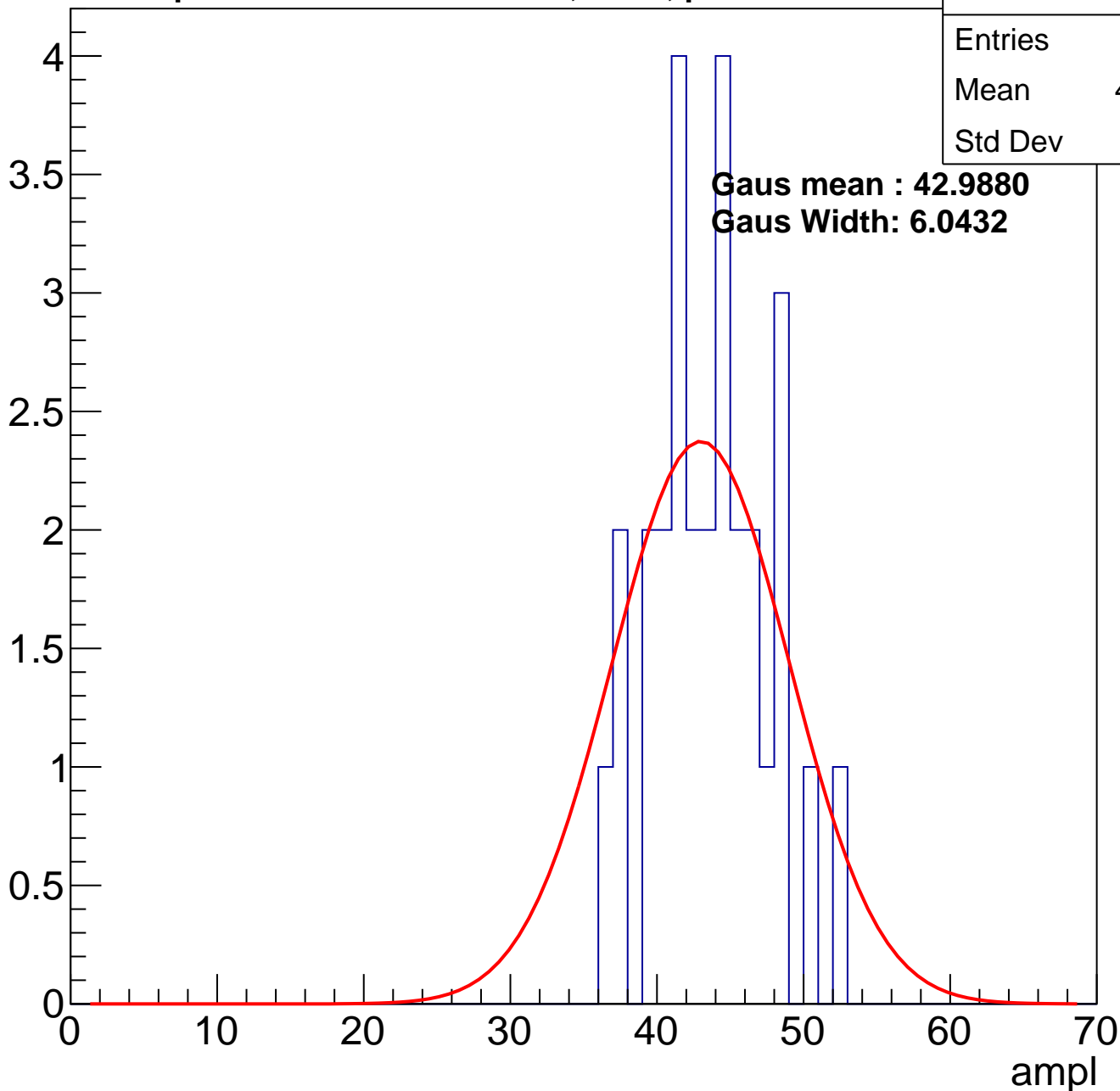
Entry



B1L103S, U15-ch65, adc2

calib_packv5_042523_0143.root, FC#7, port C2

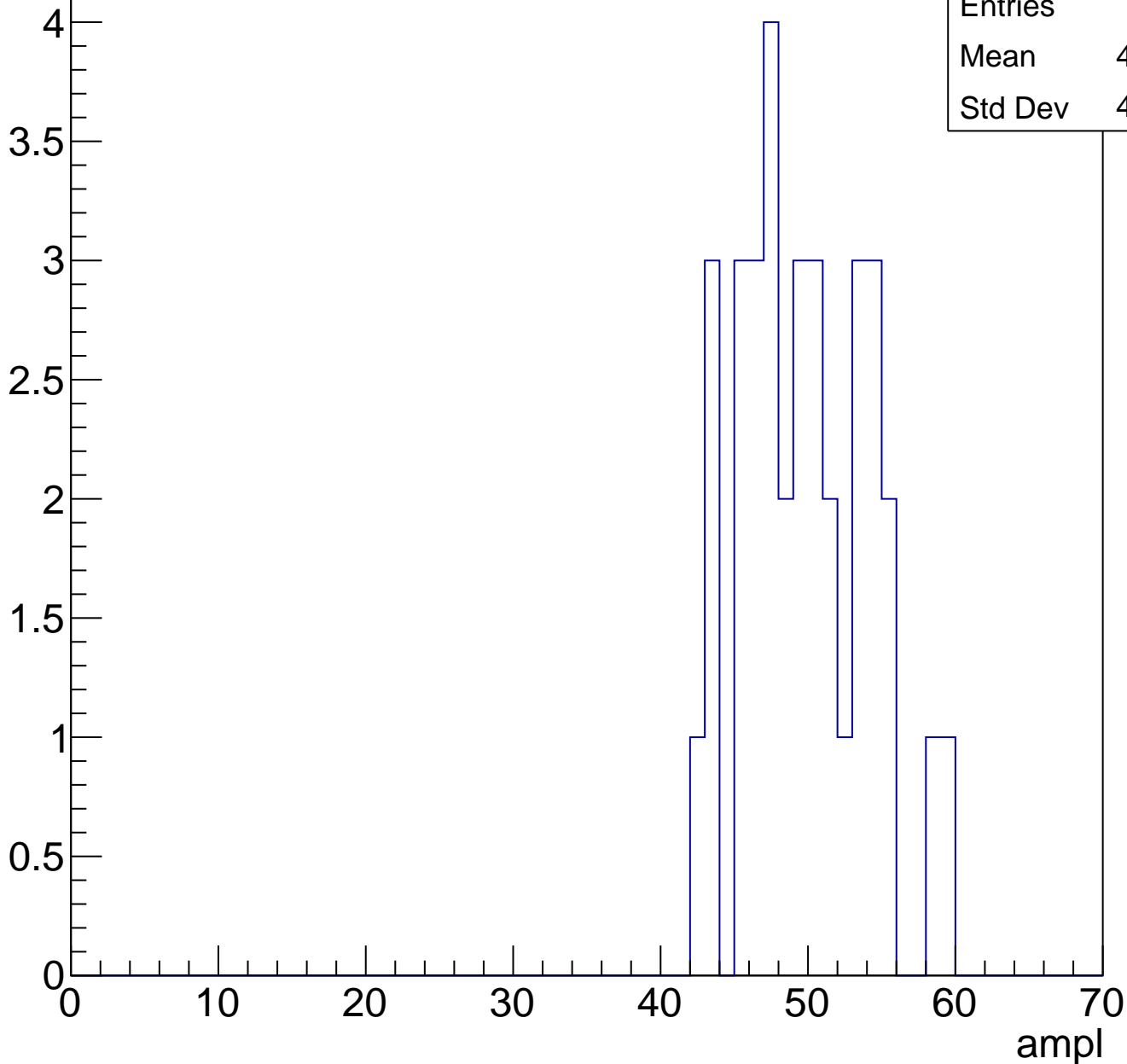
Entry



B1L103S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

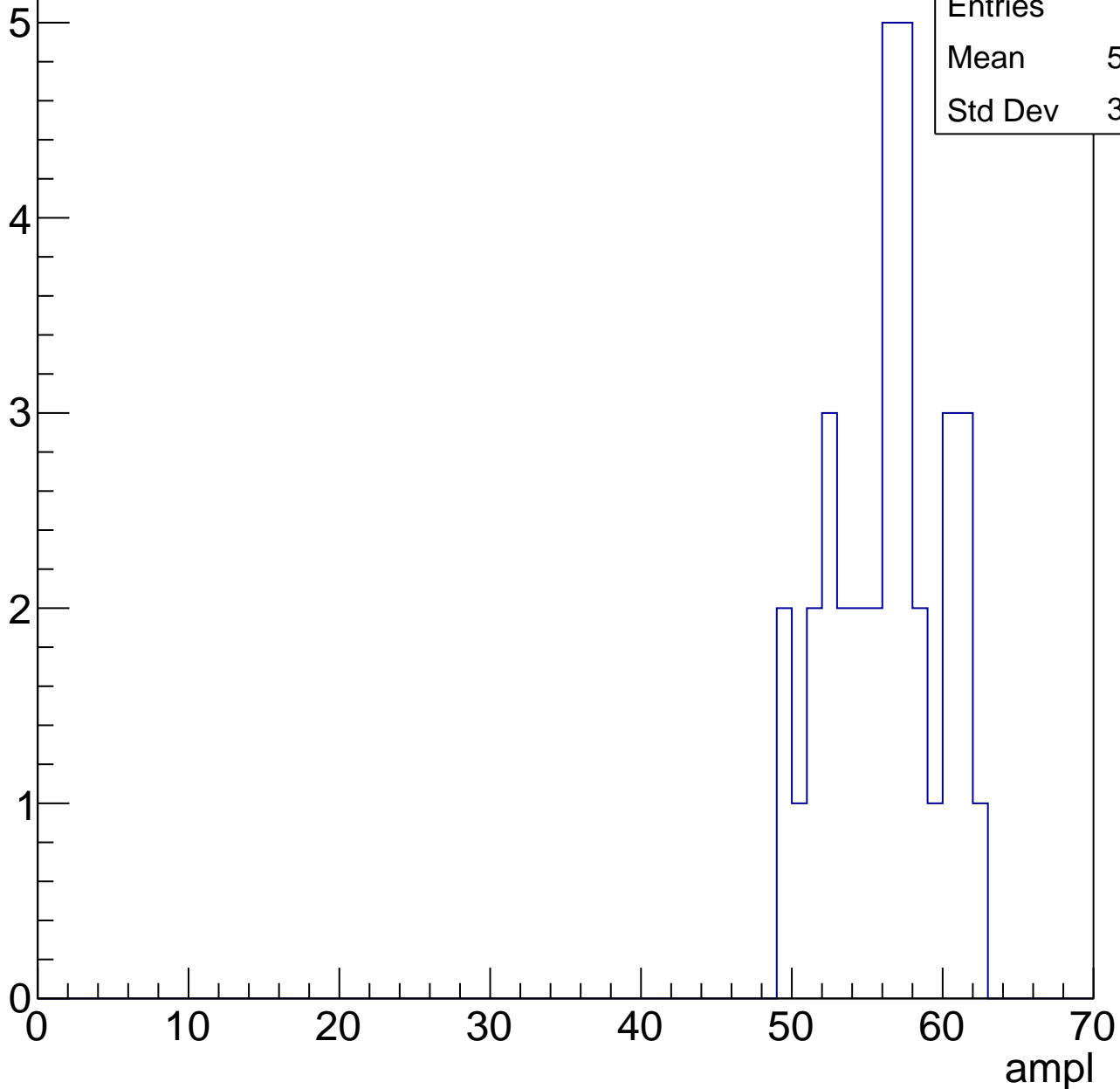


B1L103S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

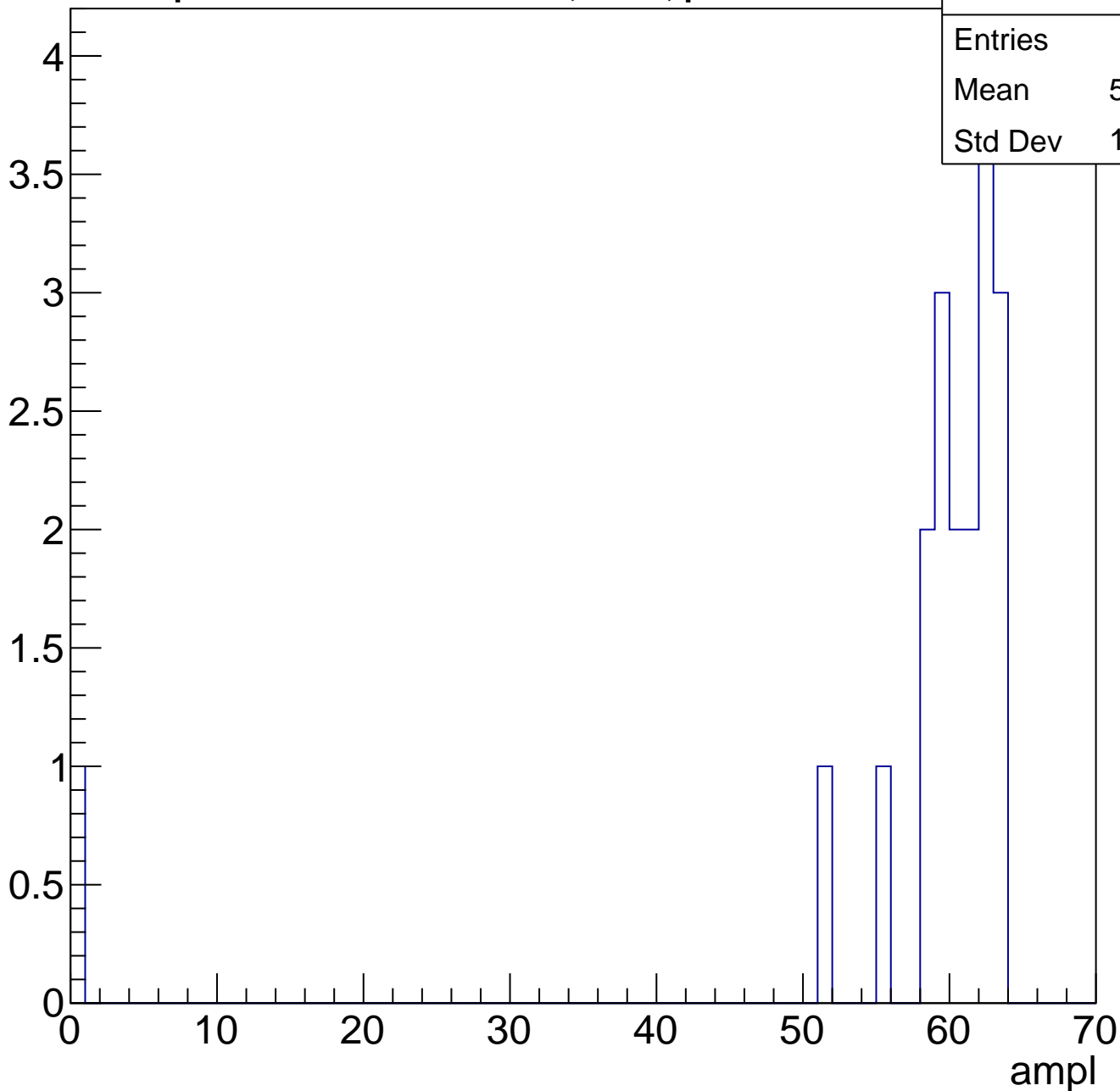
Entries	34
Mean	55.74
Std Dev	3.592



B1L103S, U15-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch66, adc0

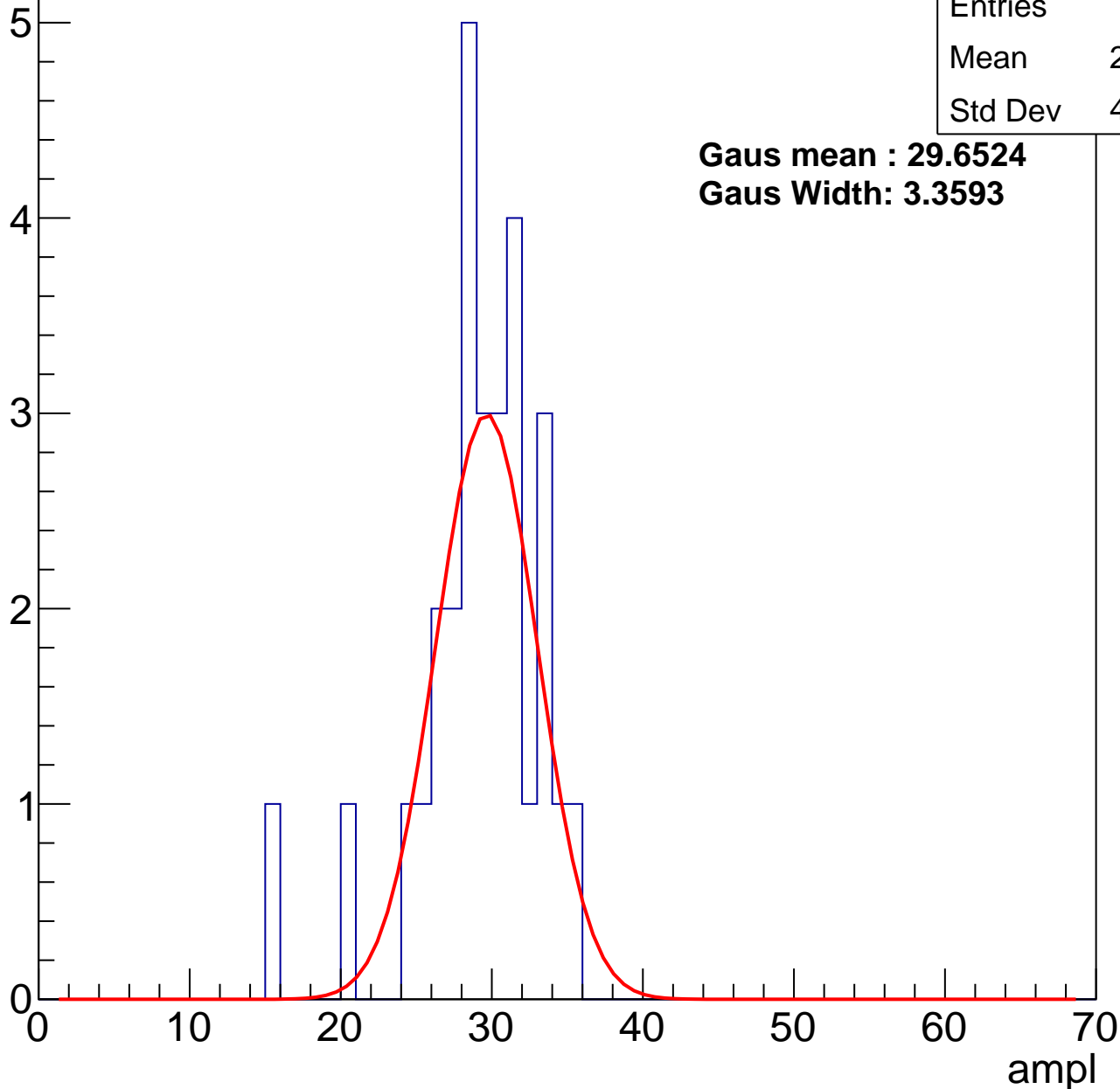
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	28.66
Std Dev	4.079

Gaus mean : 29.6524

Gaus Width: 3.3593



B1L103S, U15-ch66, adc1

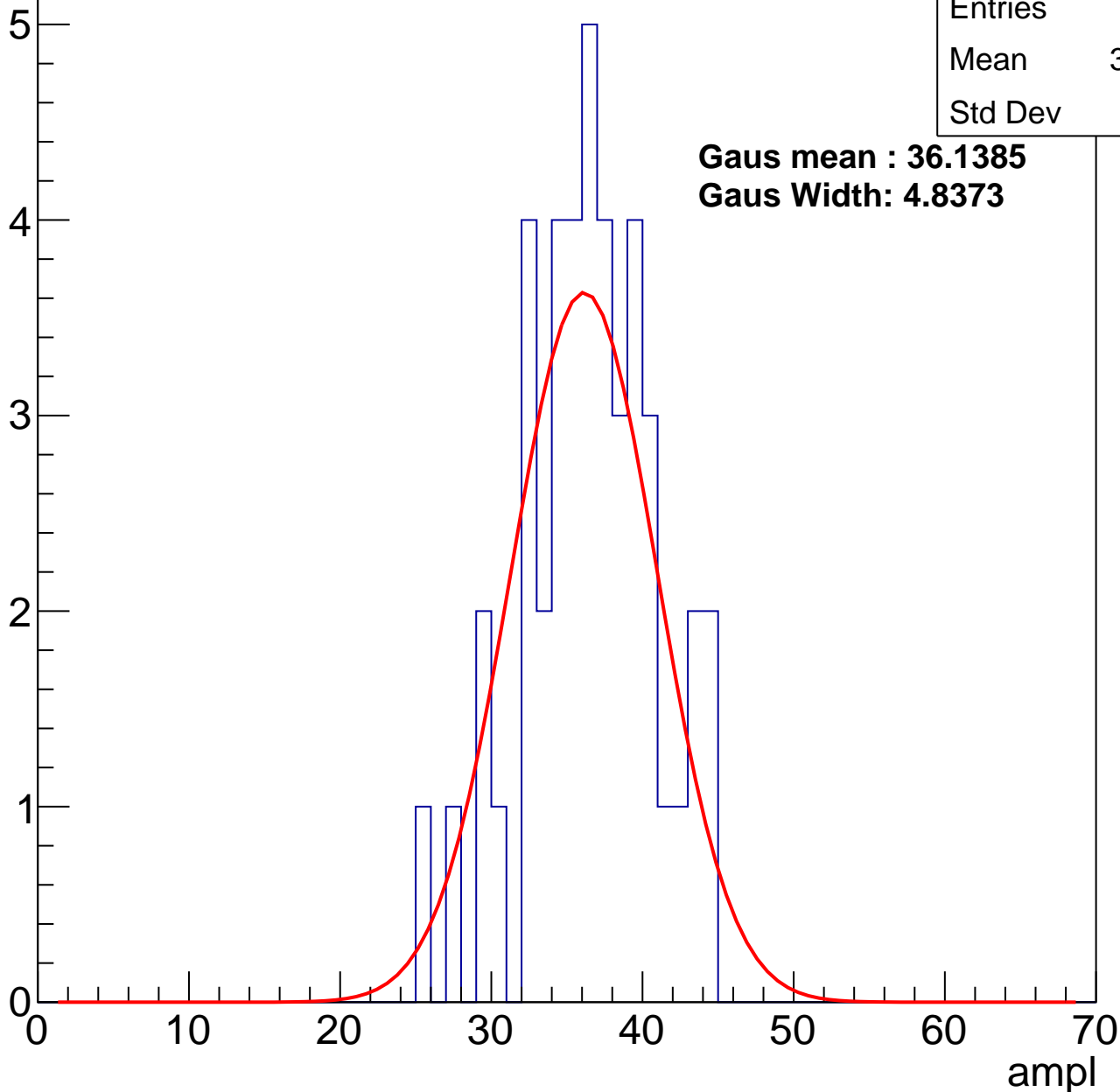
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	36.02
Std Dev	4.33

Gaus mean : 36.1385

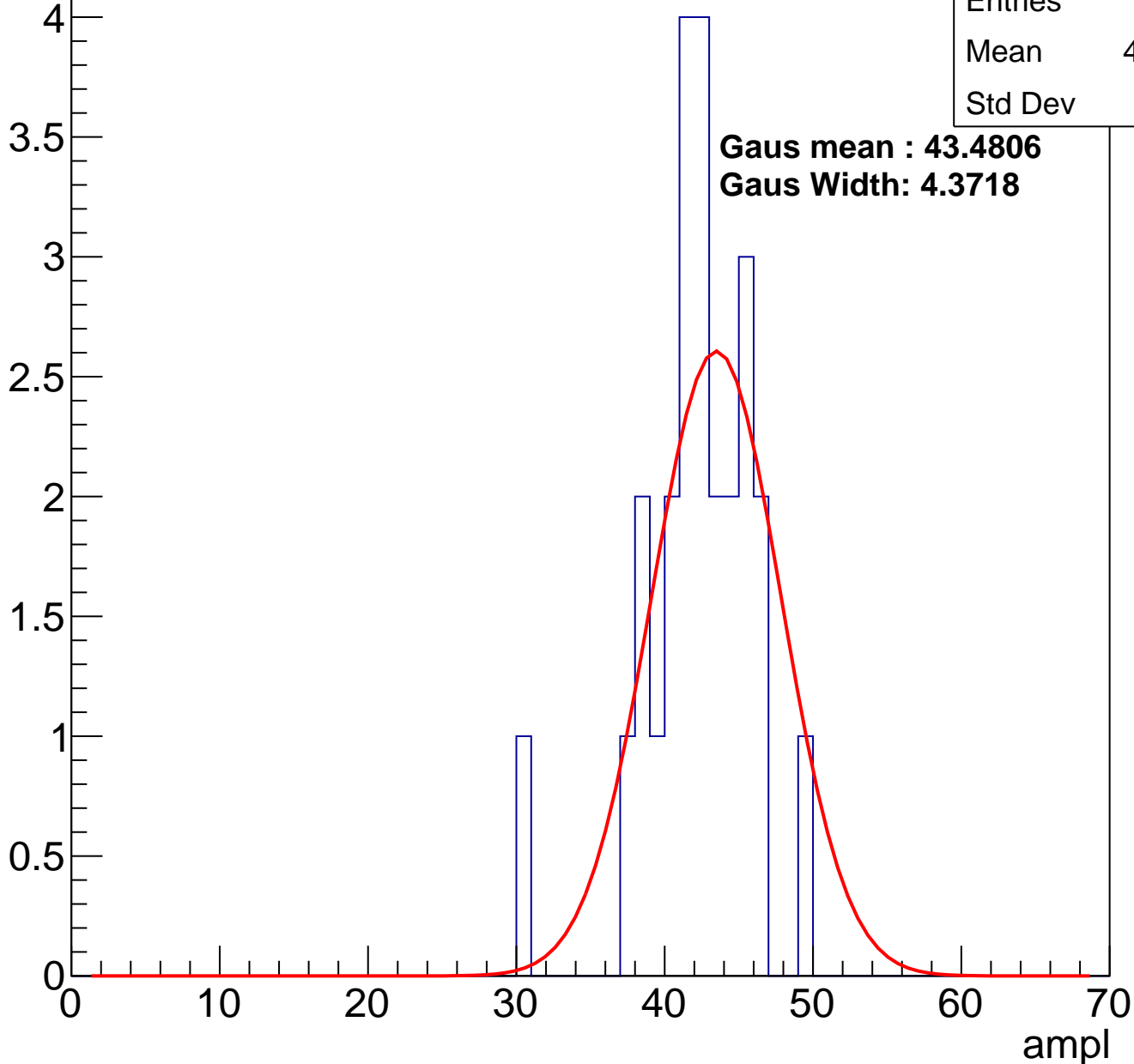
Gaus Width: 4.8373



B1L103S, U15-ch66, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



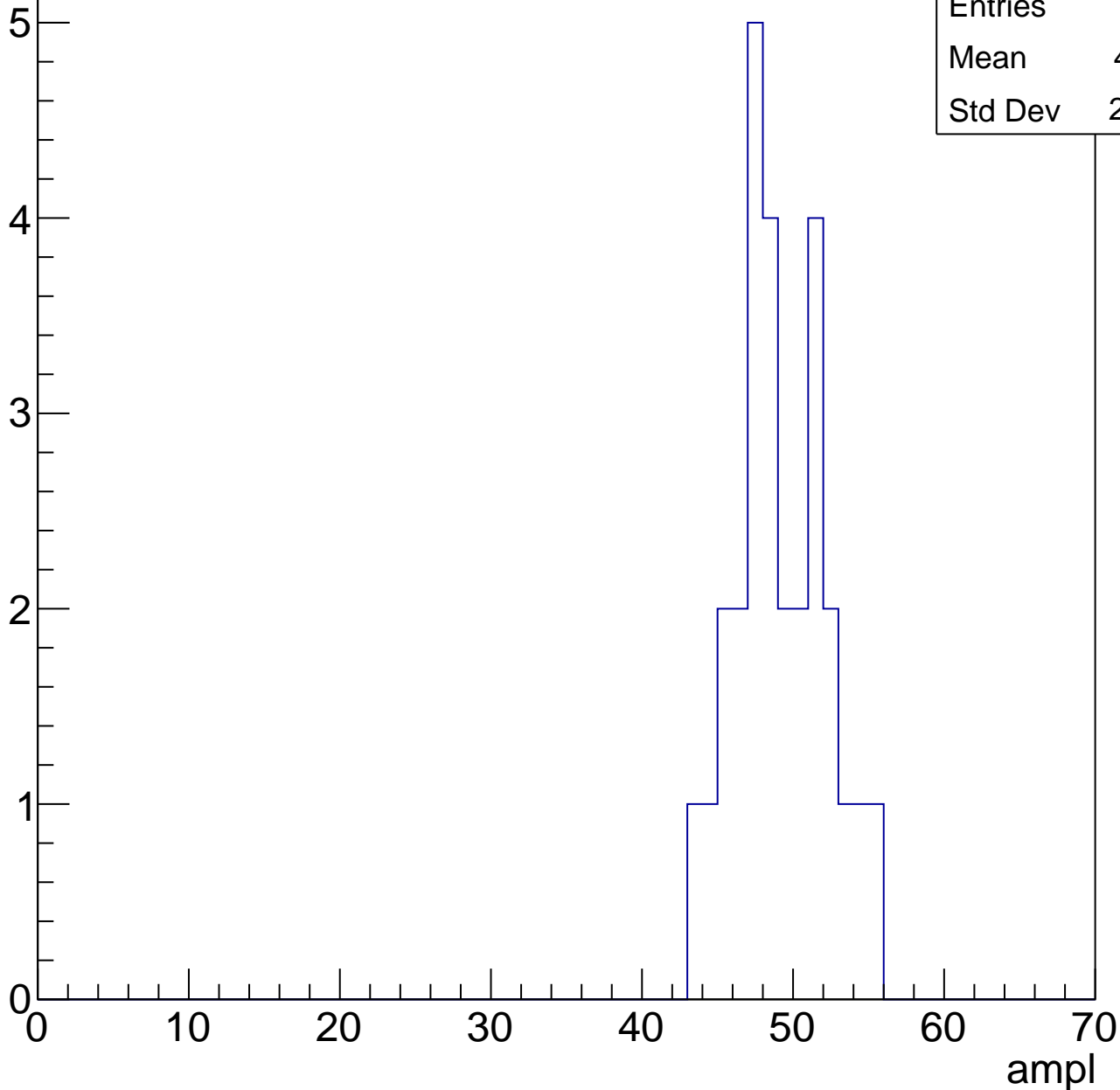
Entries	25
Mean	41.76
Std Dev	3.68

B1L103S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

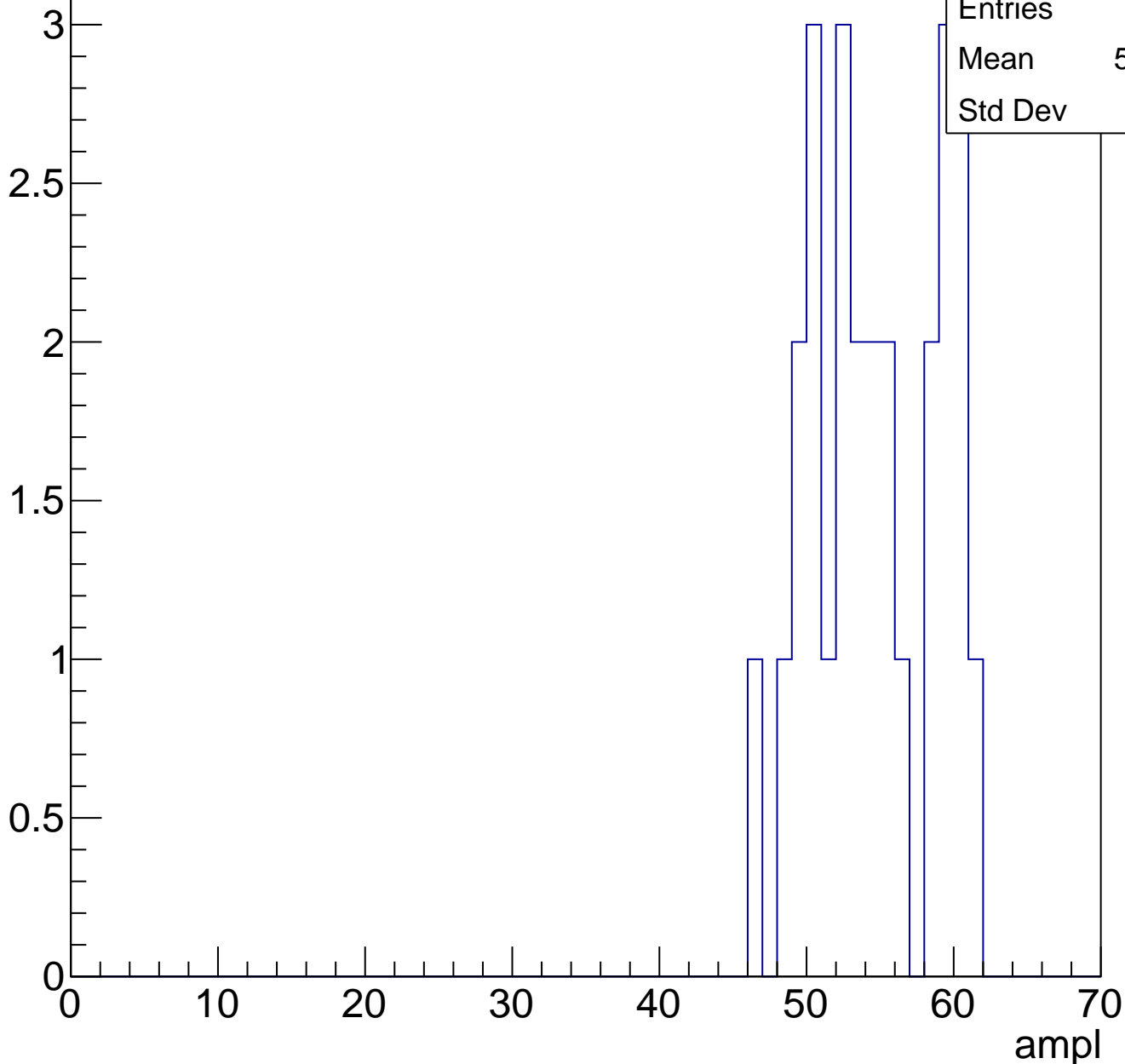
Entries	28
Mean	48.71
Std Dev	2.962



B1L103S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch66, adc5

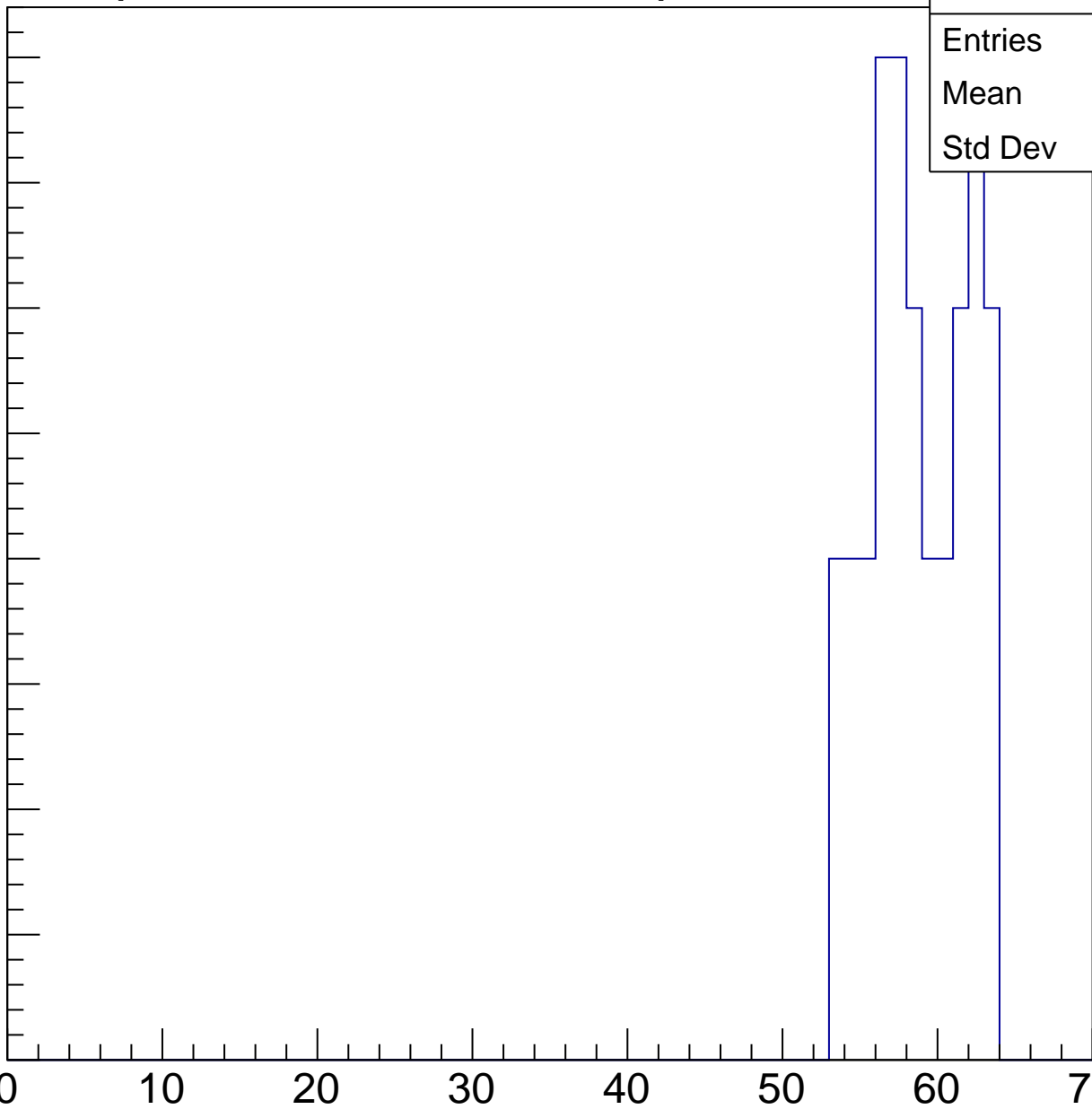
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	31
Mean	58.32
Std Dev	3.073

ampl



B1L103S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

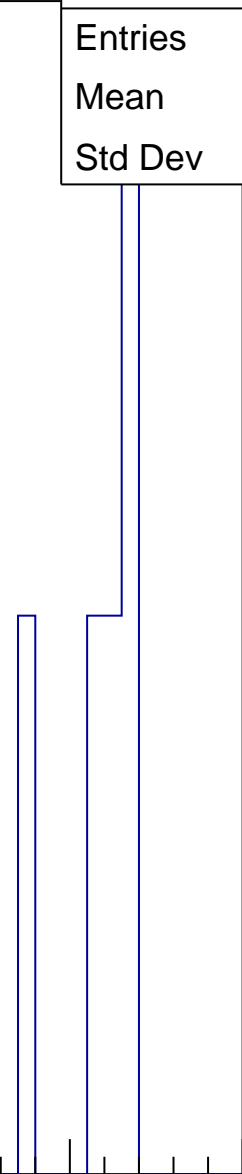
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.2
Std Dev	2.227

ampl

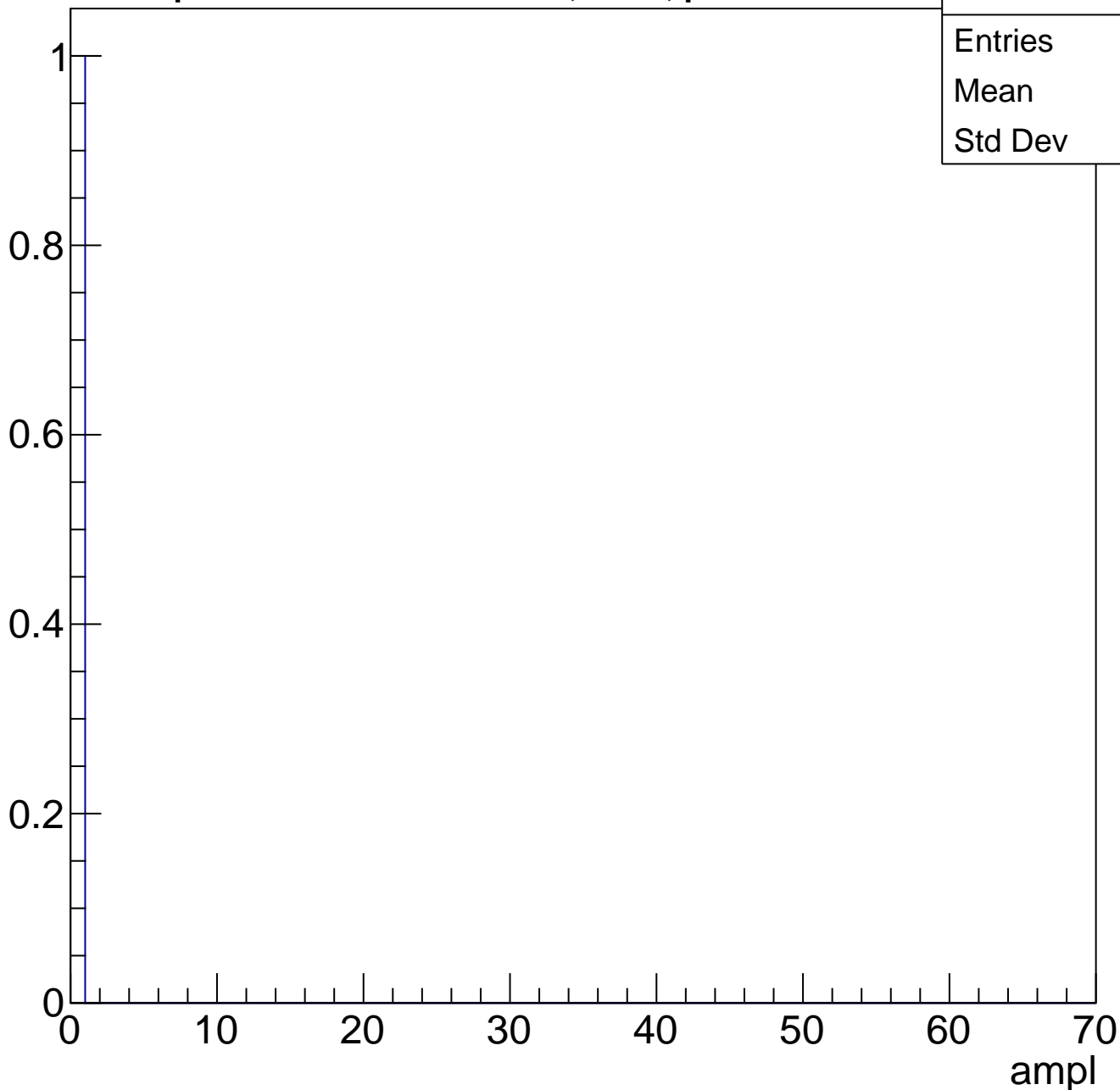
0 10 20 30 40 50 60 70



B1L103S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

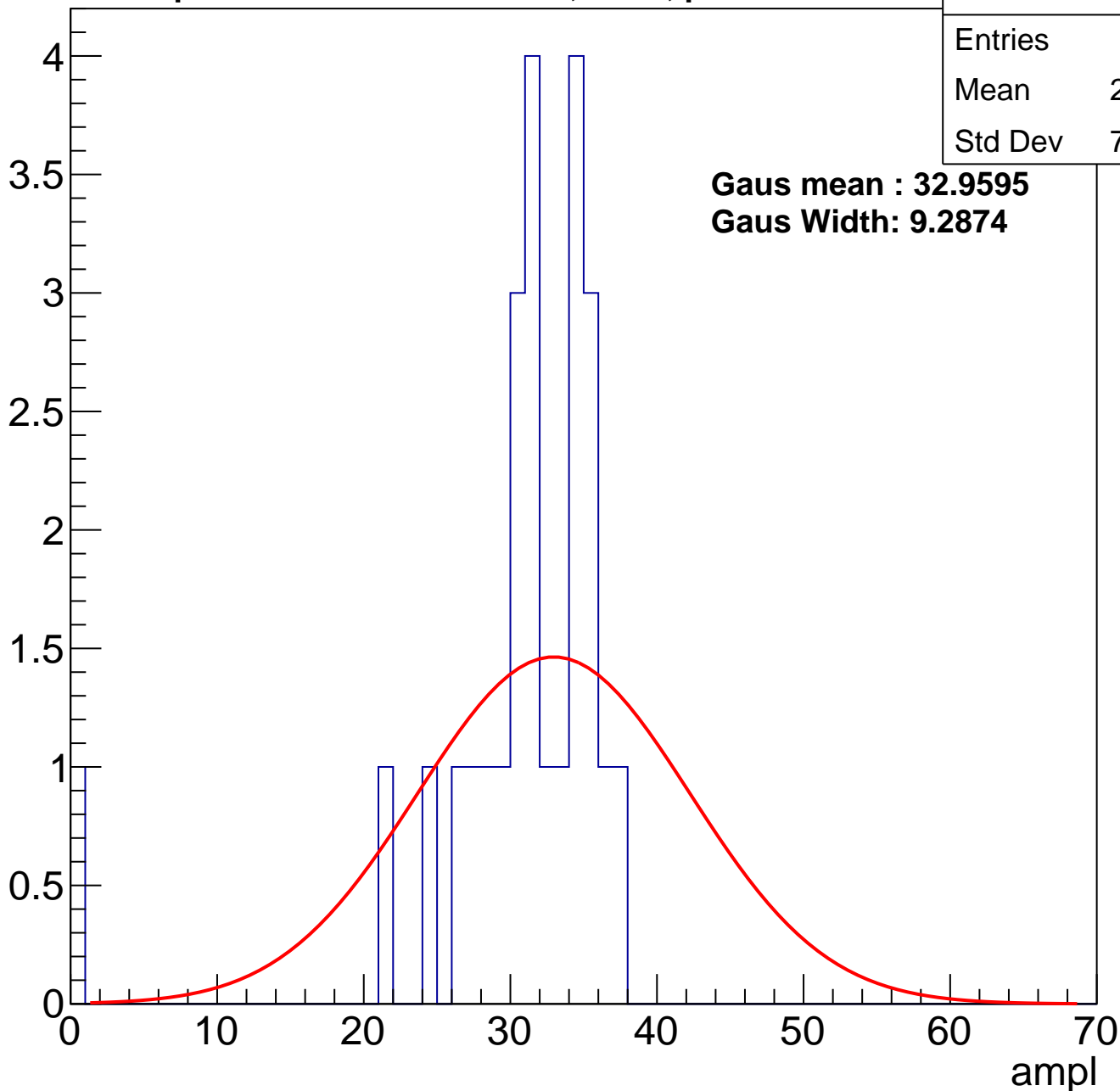
Entry



B1L103S, U15-ch67, adc0

calib_packv5_042523_0143.root, FC#7, port C2

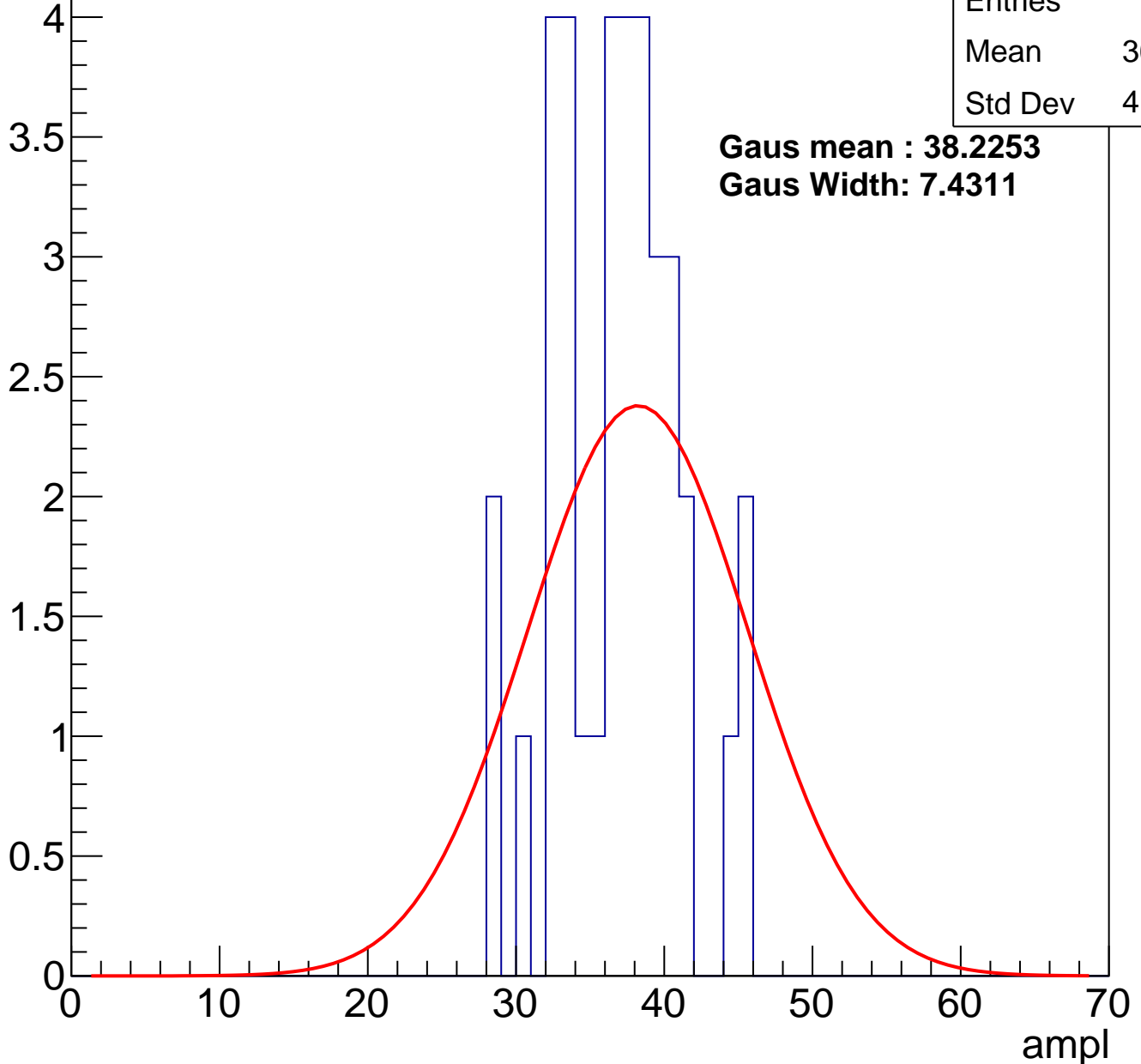
Entry



B1L103S, U15-ch67, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	36
Mean	36.44
Std Dev	4.193

B1L103S, U15-ch67, adc2

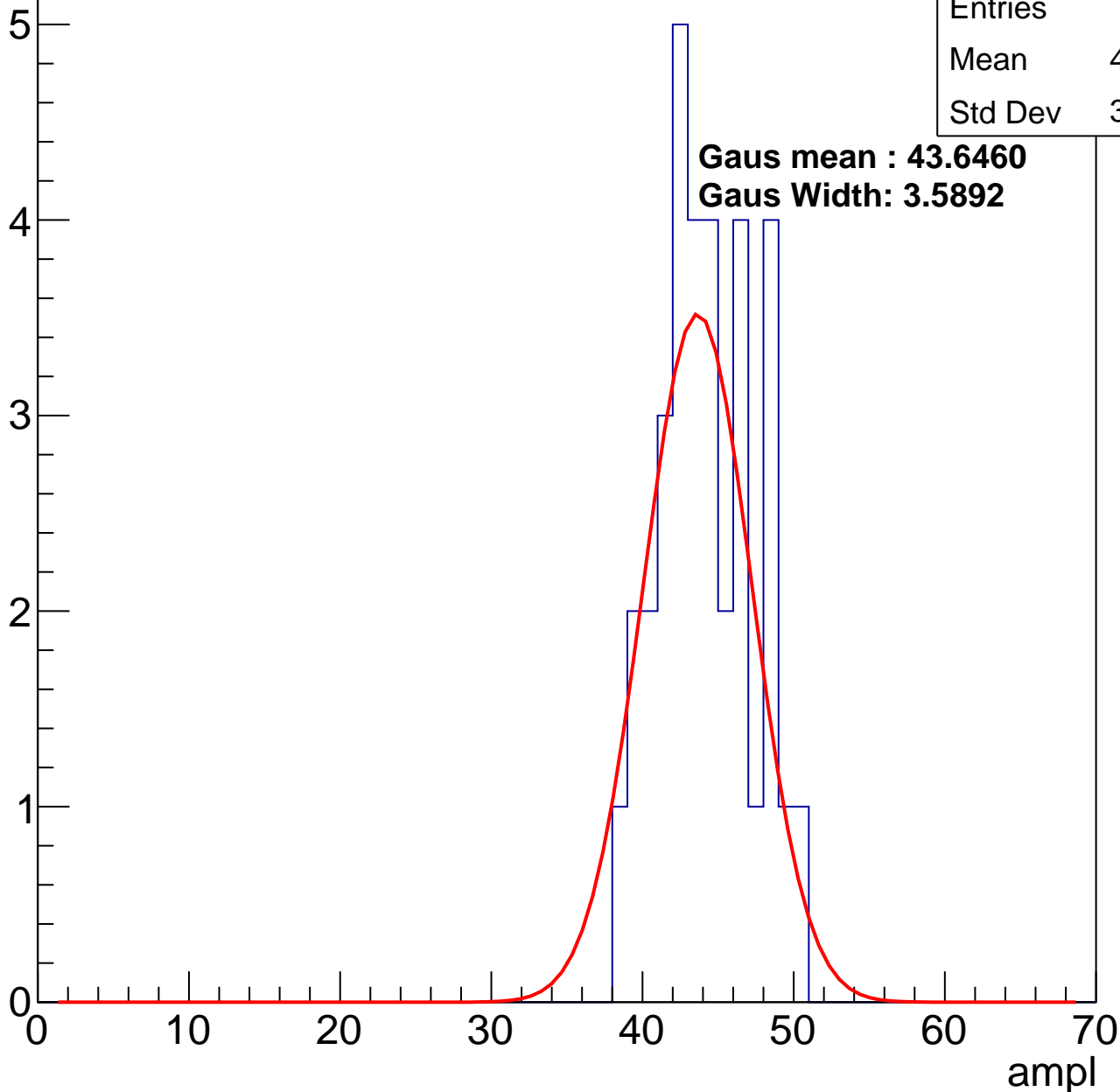
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	43.79
Std Dev	3.066

Gaus mean : 43.6460

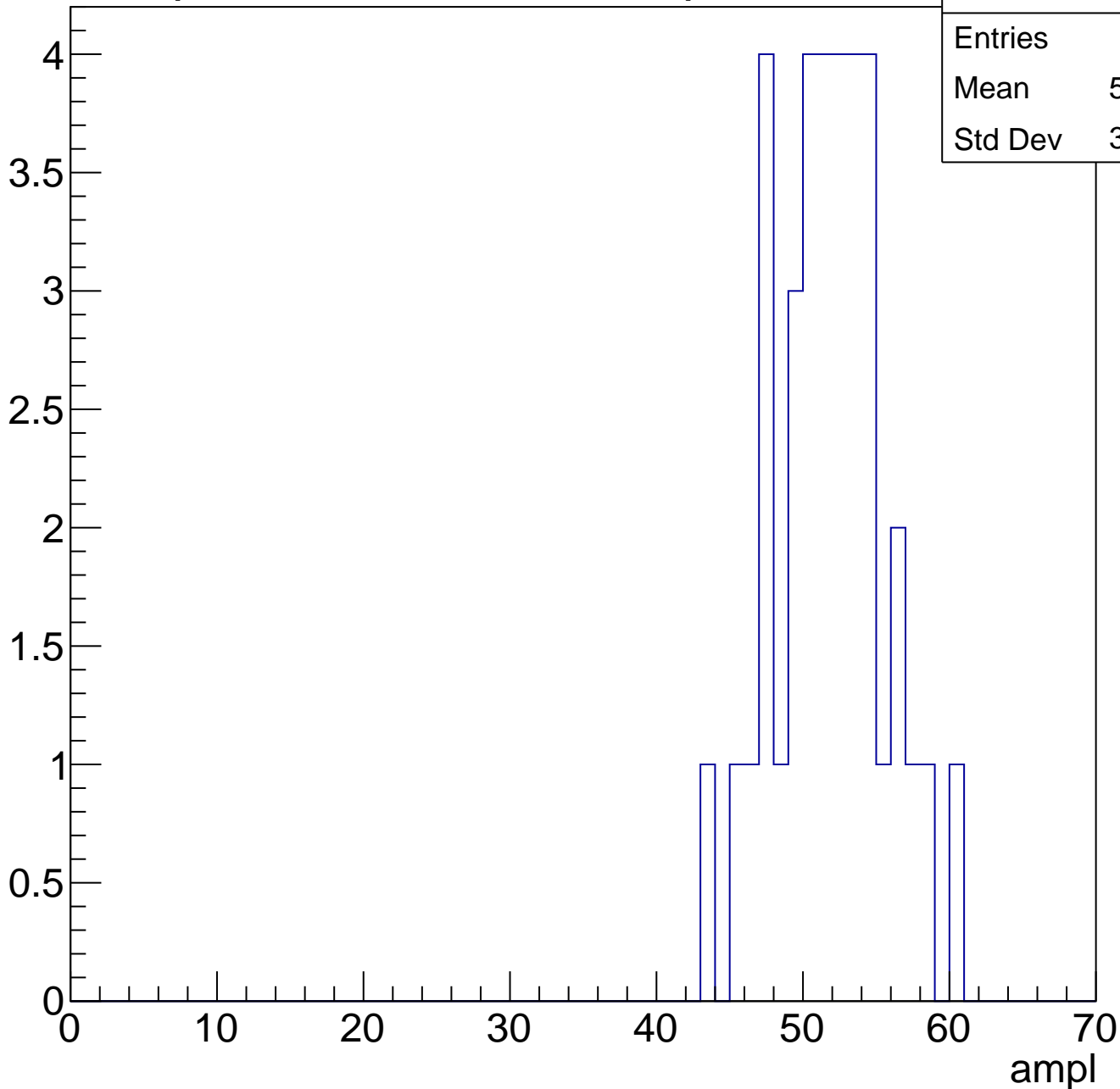
Gaus Width: 3.5892



B1L103S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

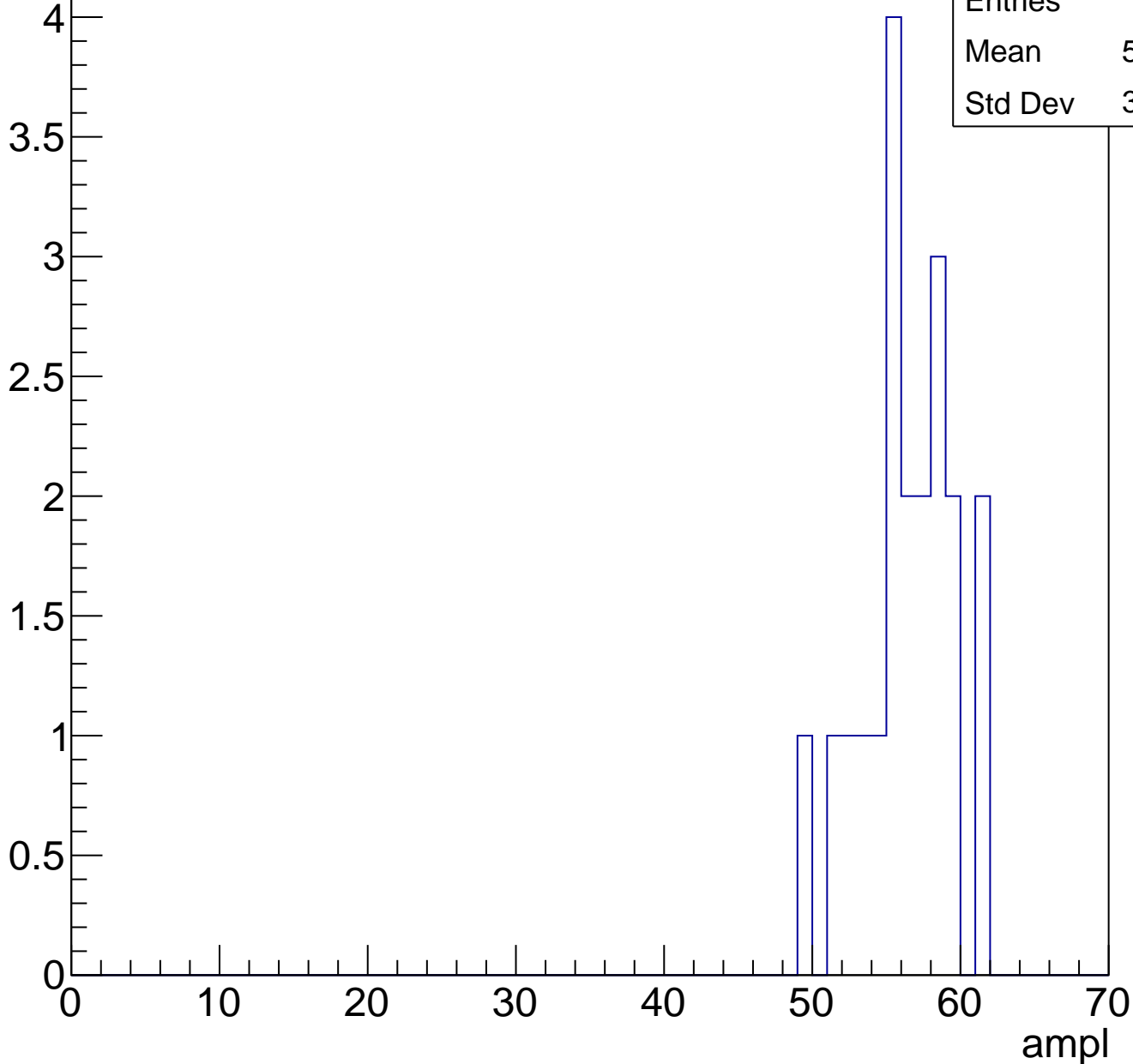


Entries	37
Mean	51.32
Std Dev	3.669

B1L103S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

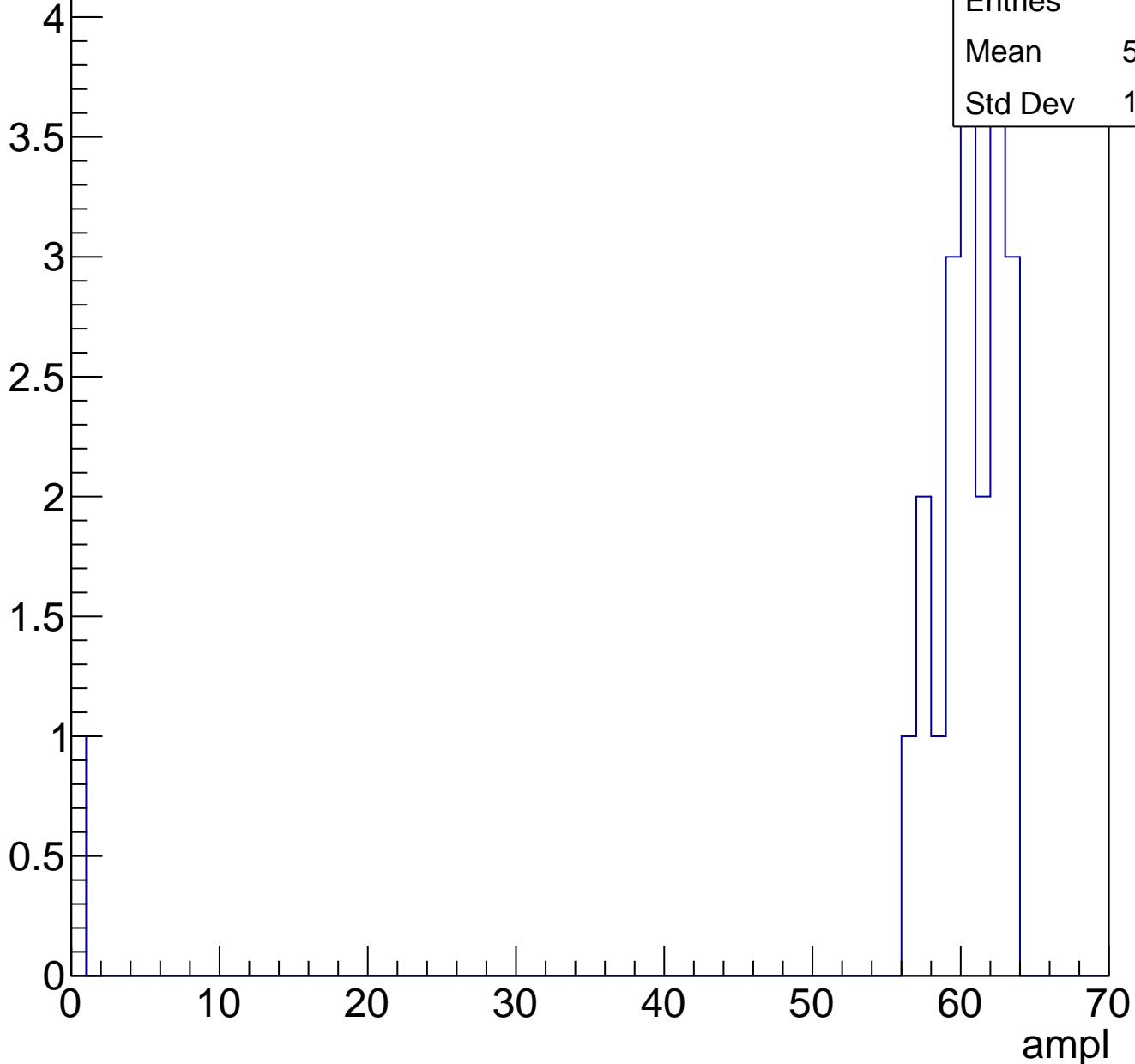
Entry



B1L103S, U15-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	21
Mean	57.33
Std Dev	12.98

B1L103S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B1L103S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

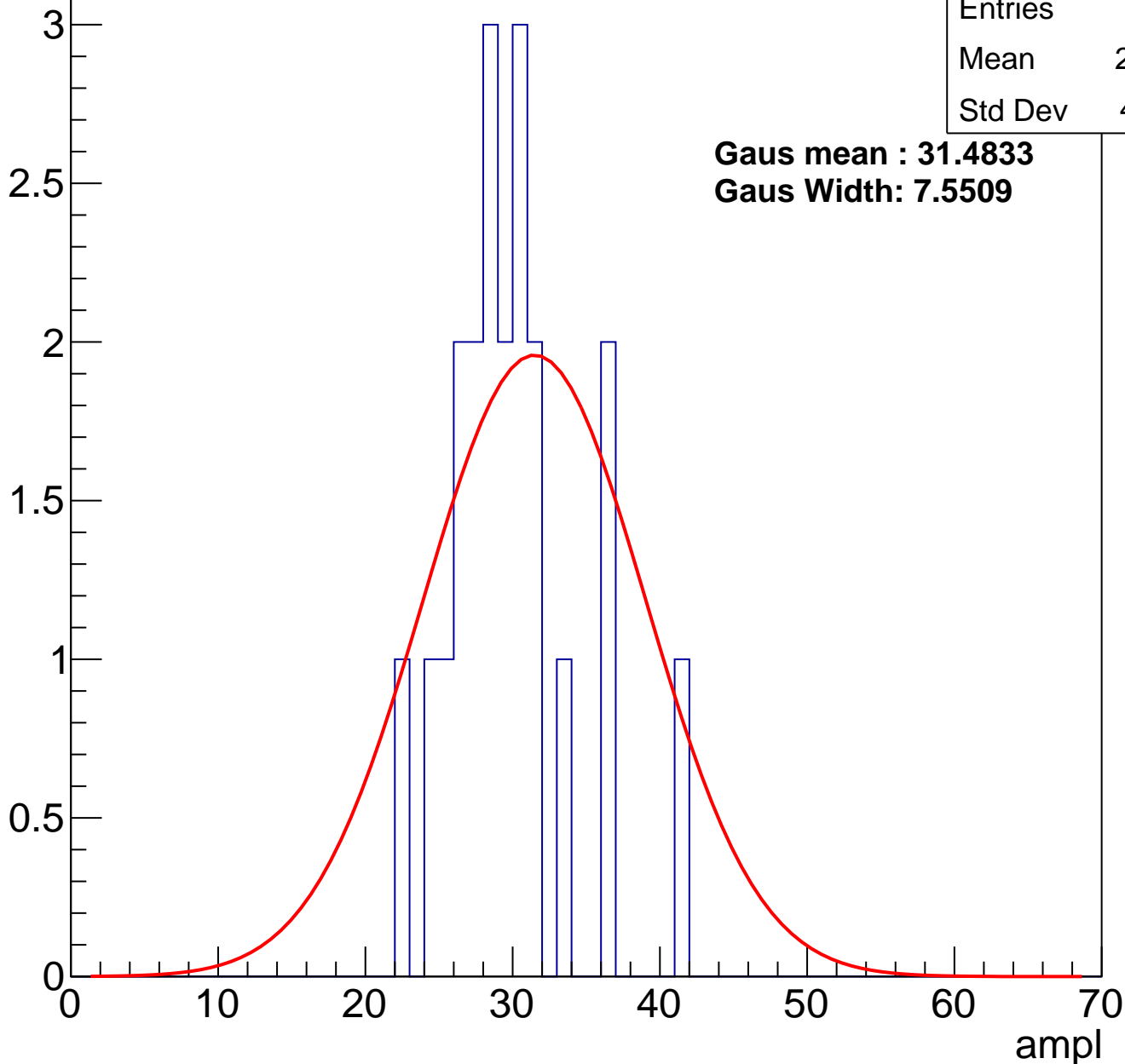


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch68, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch68, adc1

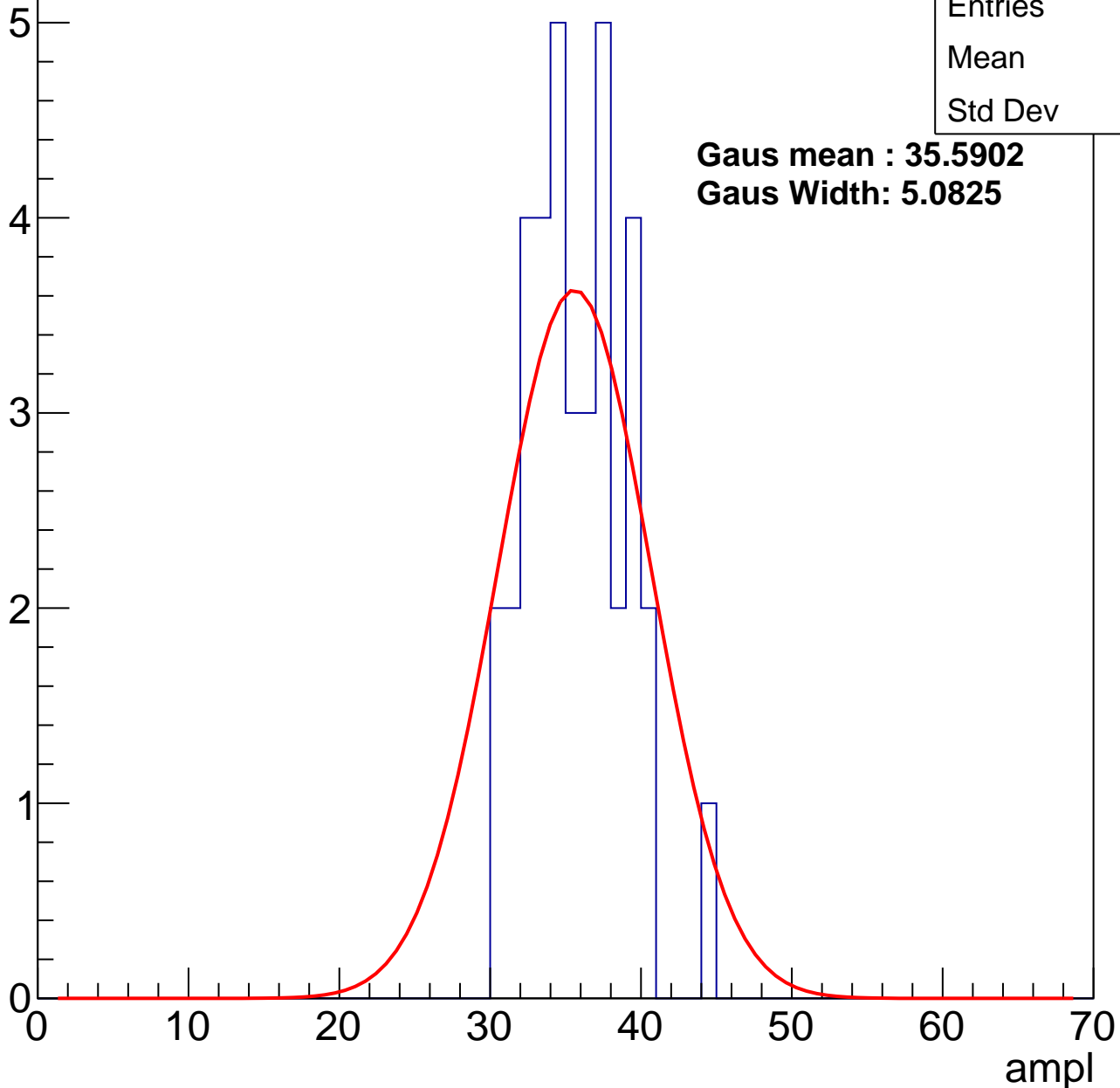
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	35.3
Std Dev	3.17

Gaus mean : 35.5902

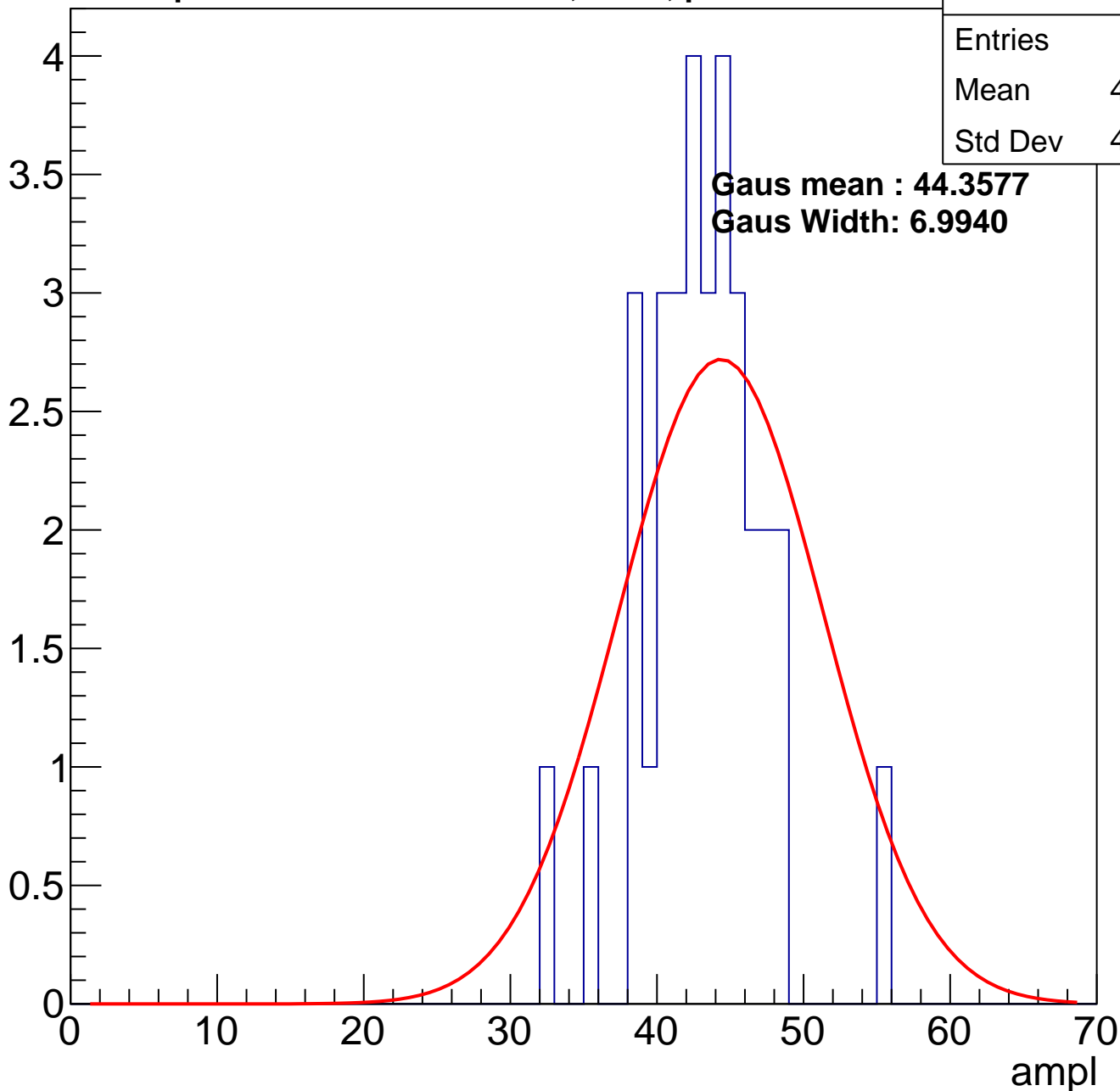
Gaus Width: 5.0825



B1L103S, U15-ch68, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

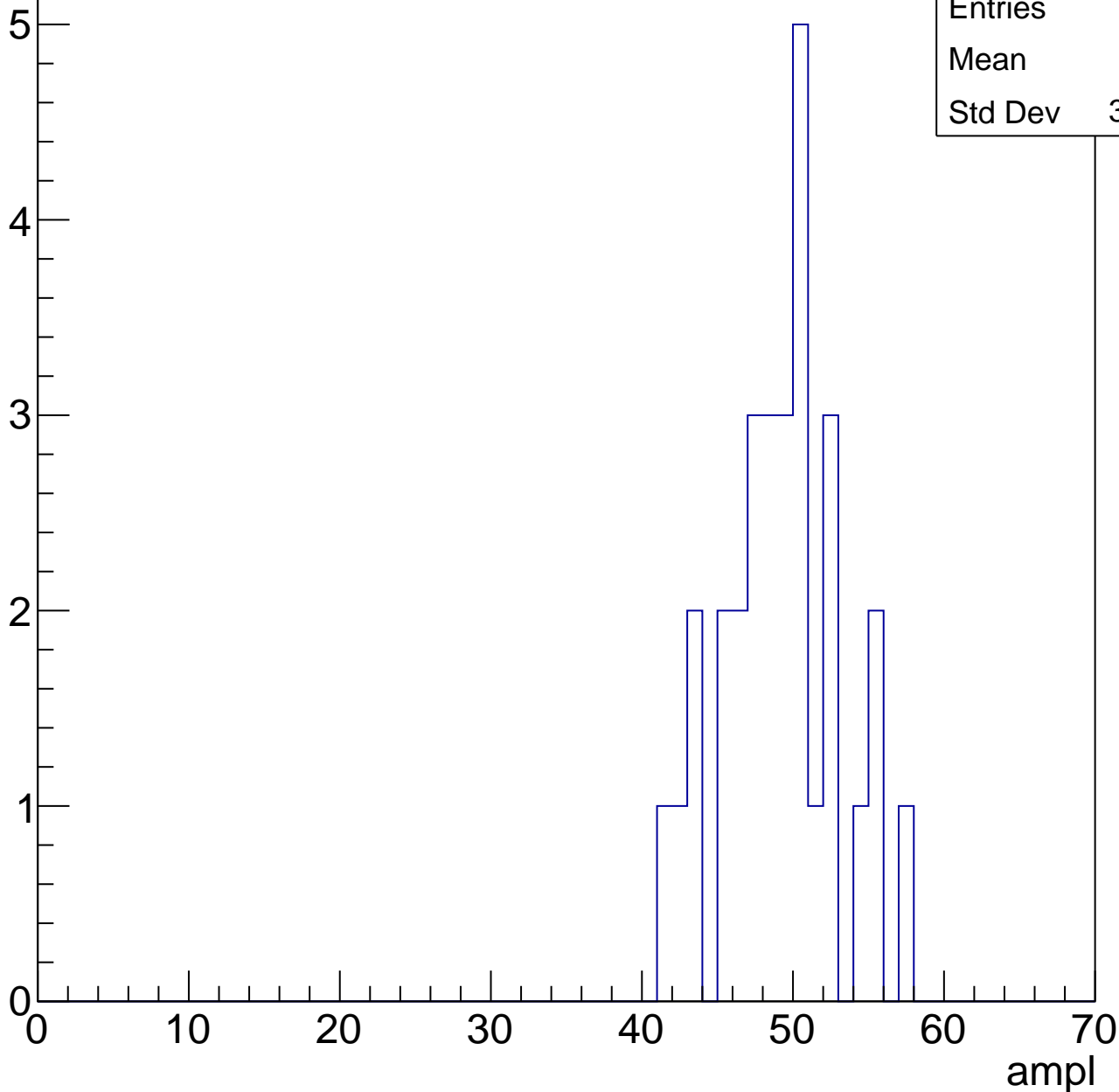


B1L103S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	48.7
Std Dev	3.848

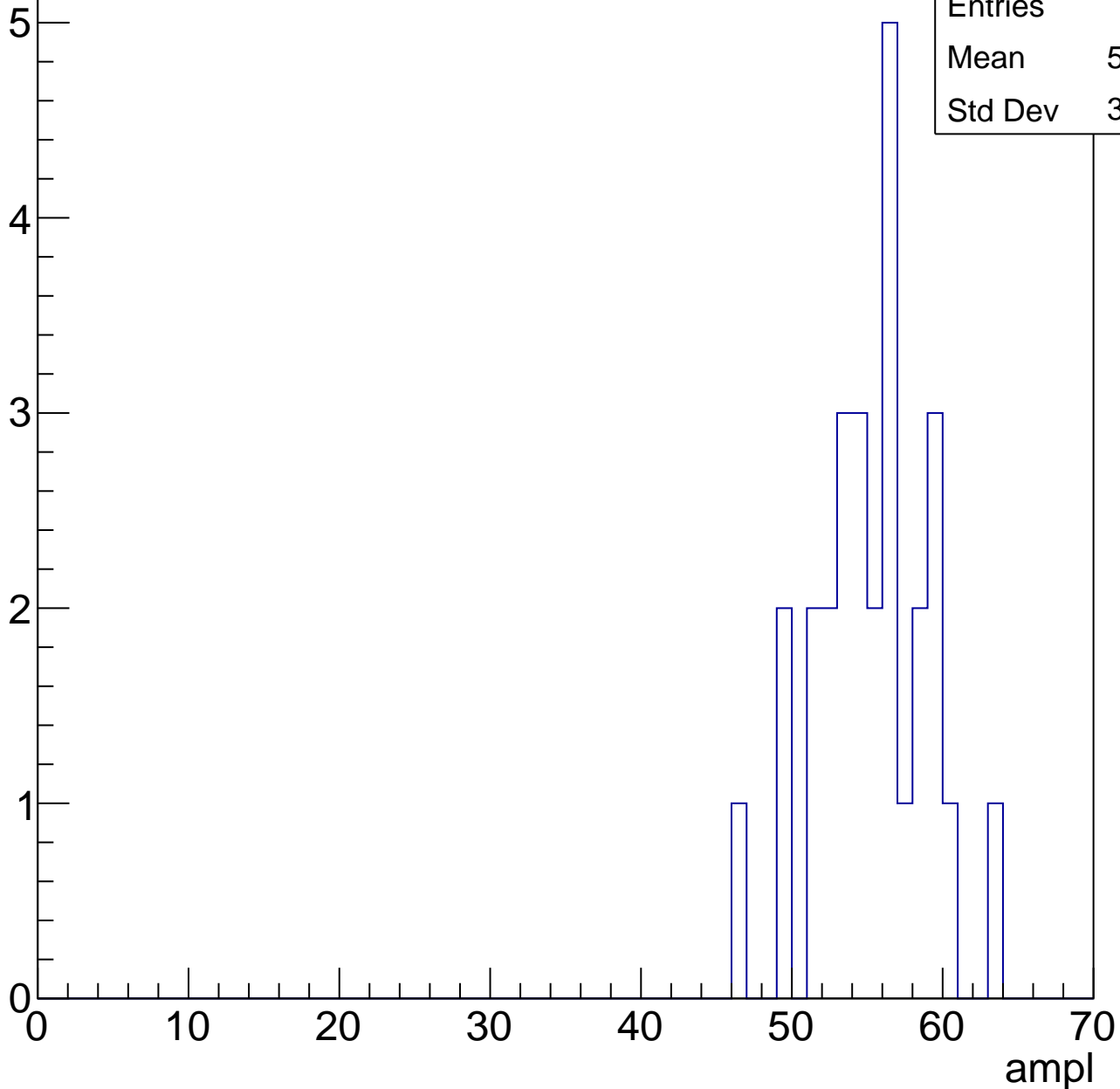


B1L103S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

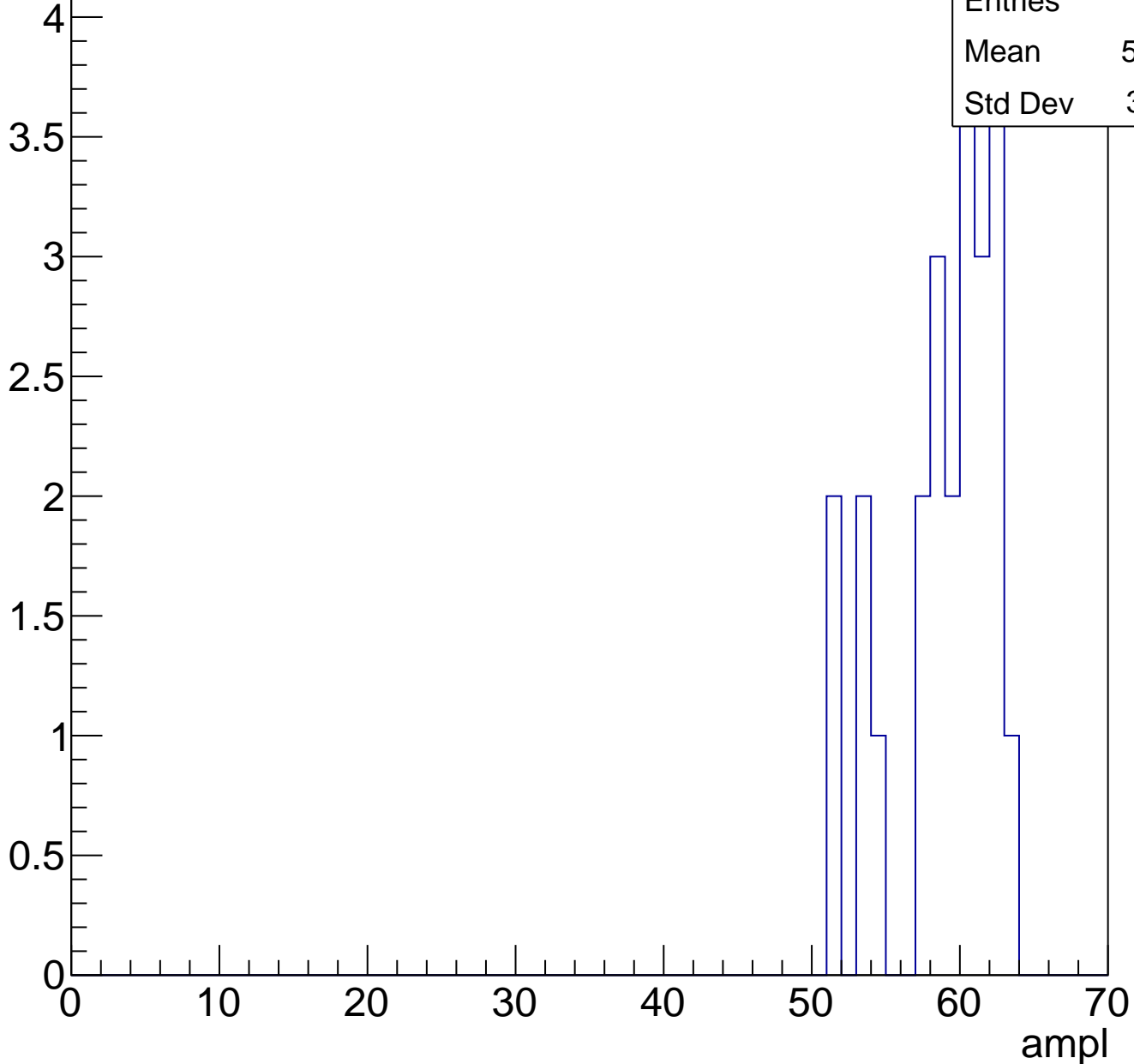
Entries	28
Mean	54.79
Std Dev	3.687



B1L103S, U15-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

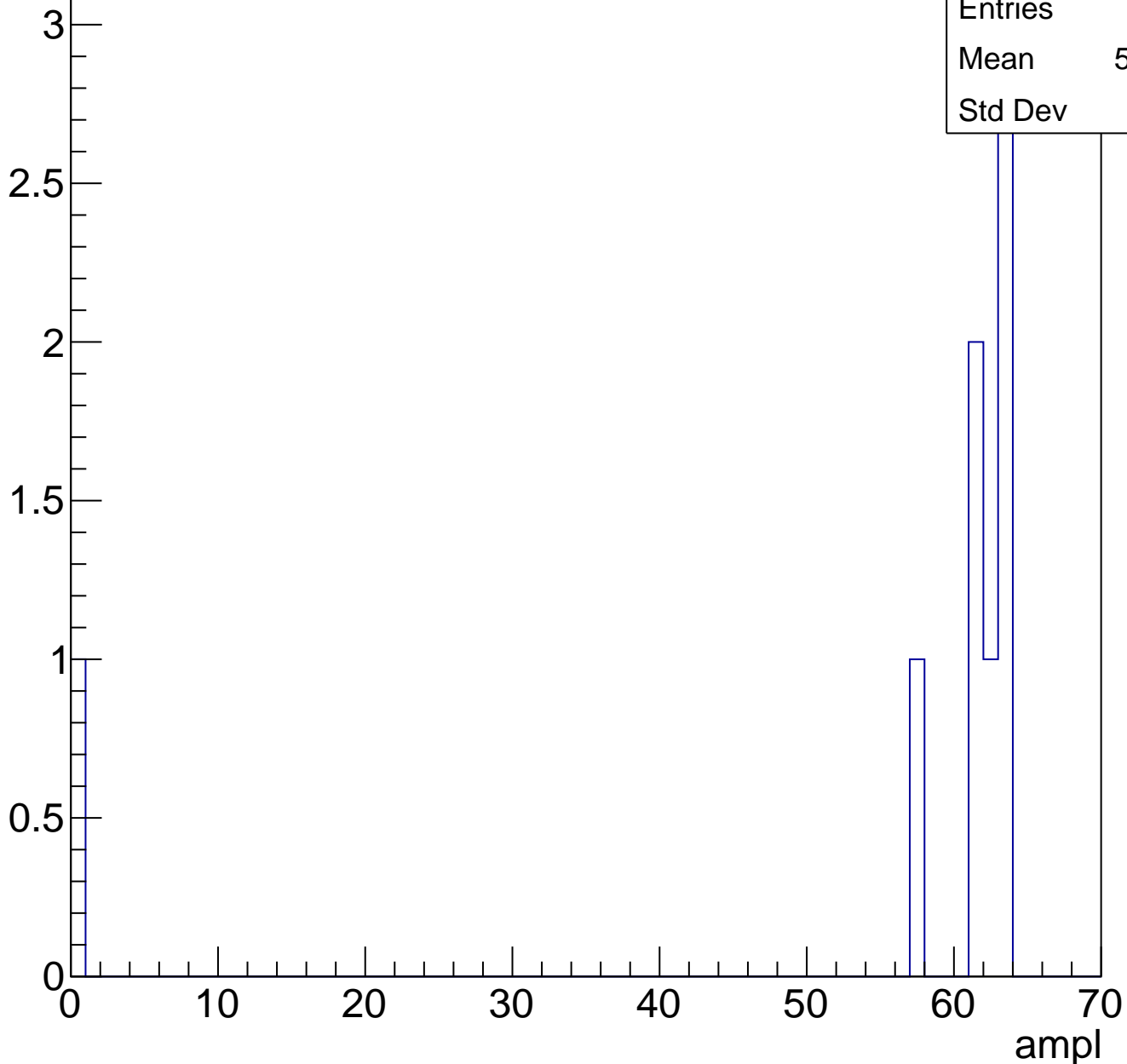
Entry



B1L103S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch69, adc0

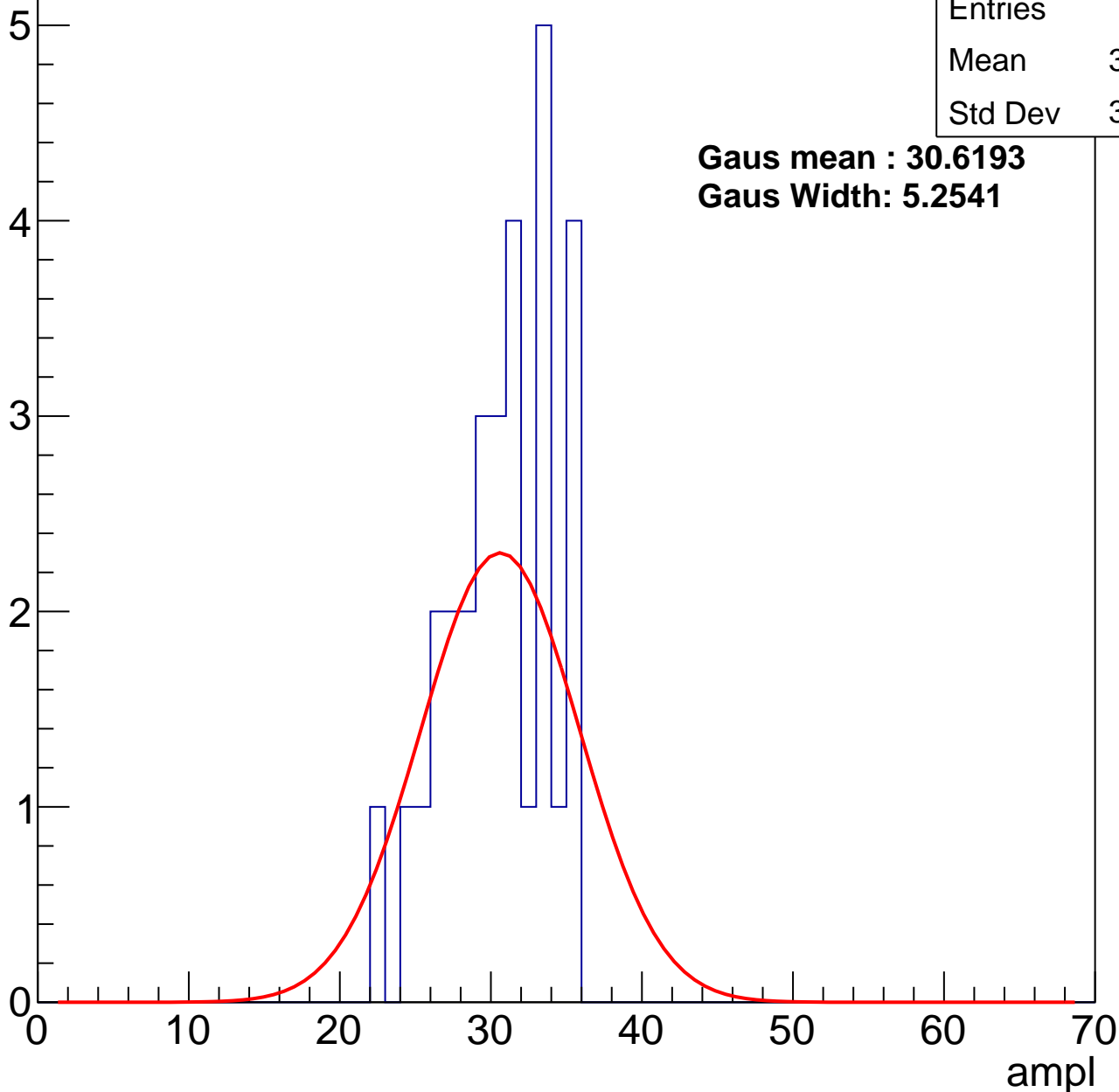
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	30.17
Std Dev	3.436

Gaus mean : 30.6193

Gaus Width: 5.2541



B1L103S, U15-ch69, adc1

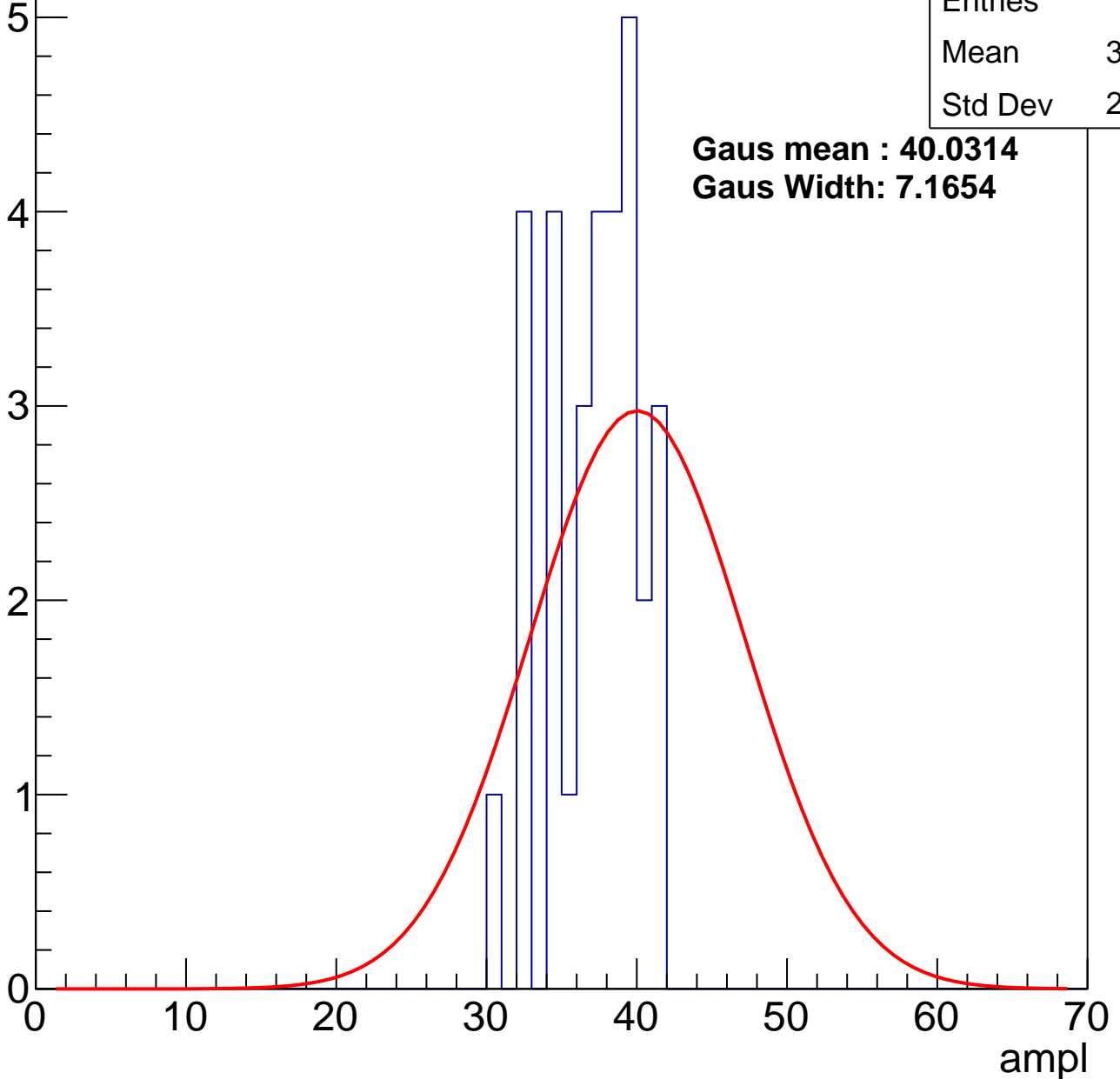
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	36.61
Std Dev	2.991

Gaus mean : 40.0314

Gaus Width: 7.1654



B1L103S, U15-ch69, adc2

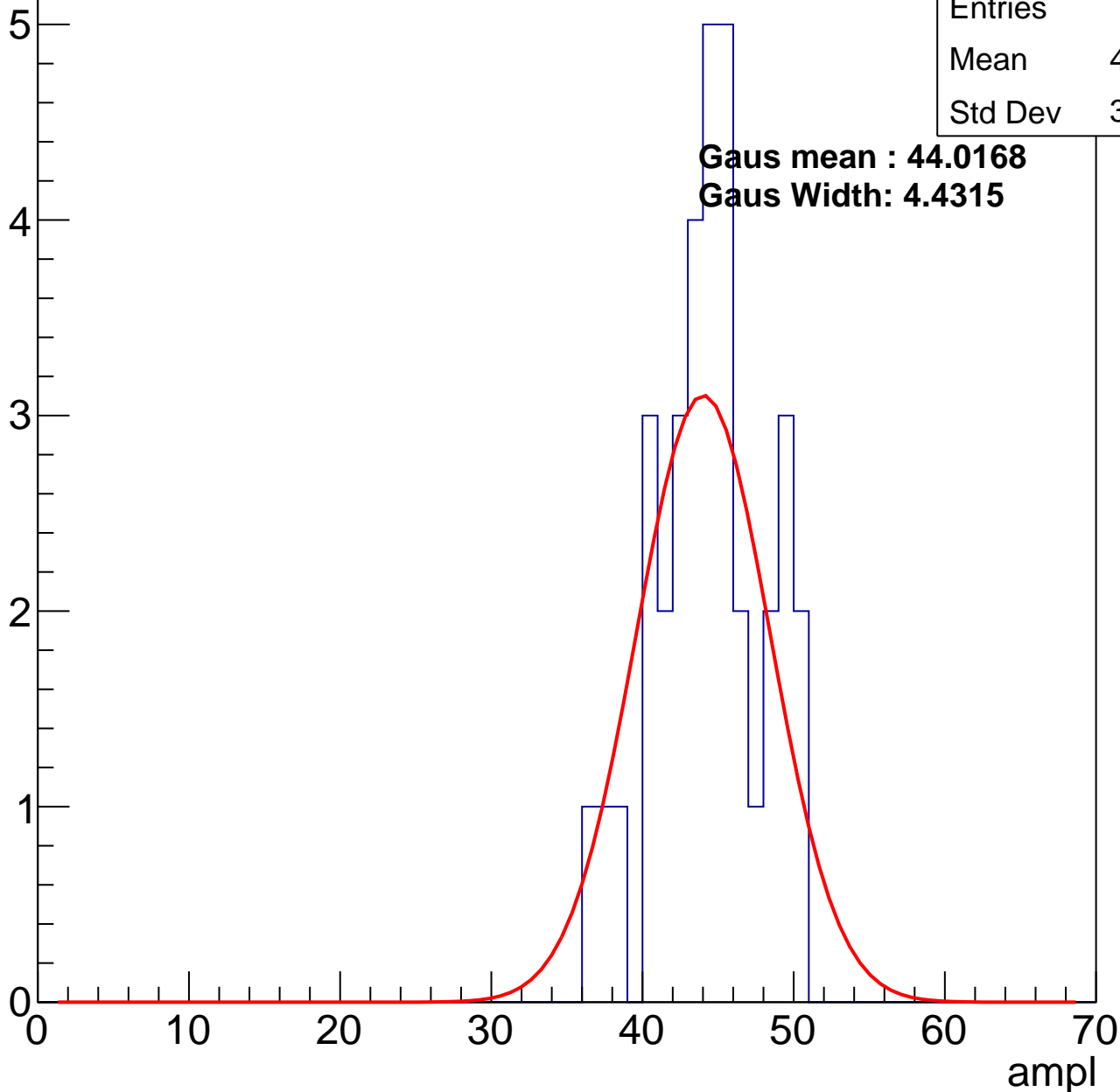
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	43.94
Std Dev	3.513

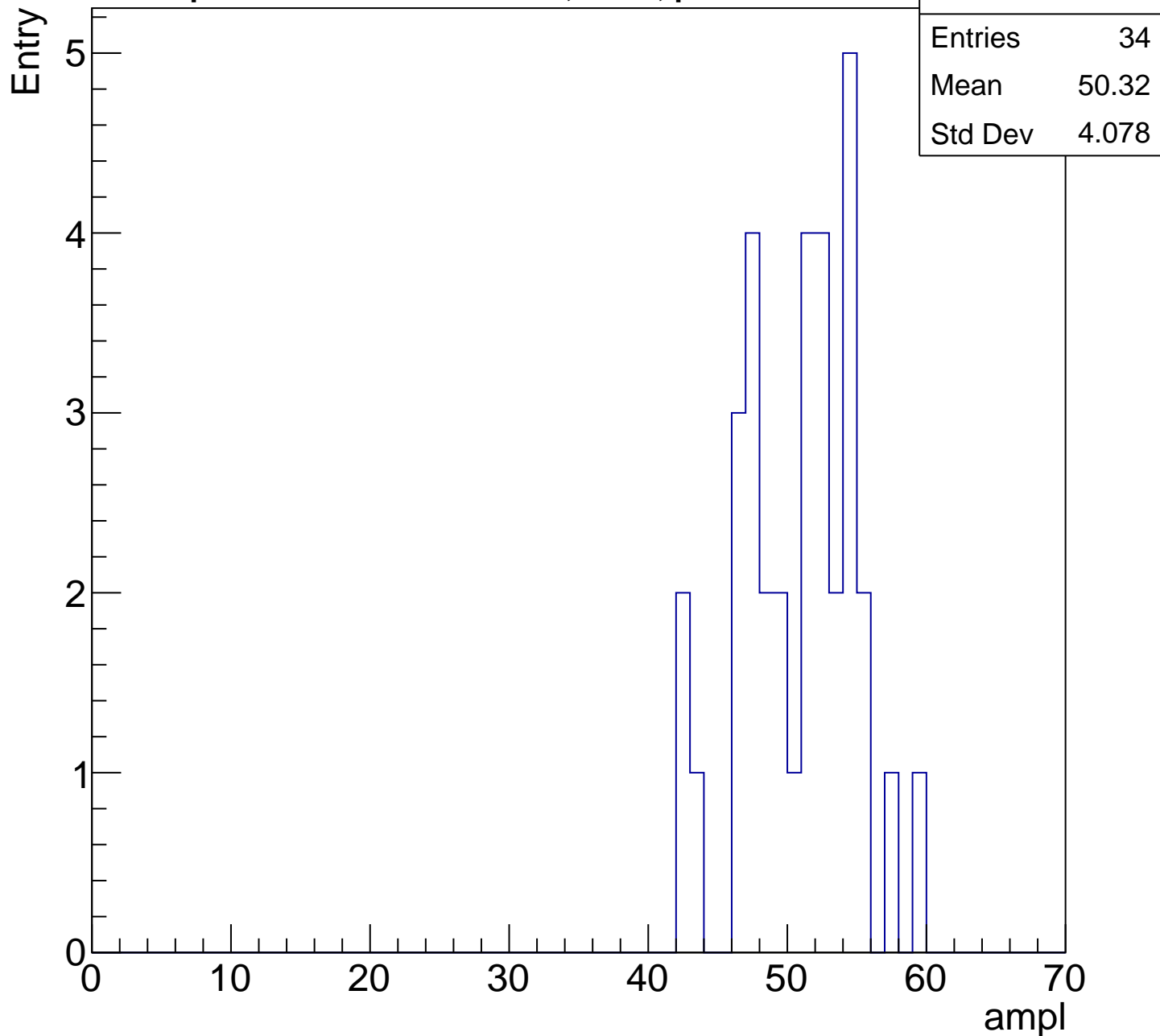
Gaus mean : 44.0168

Gaus Width: 4.4315



B1L103S, U15-ch69, adc3

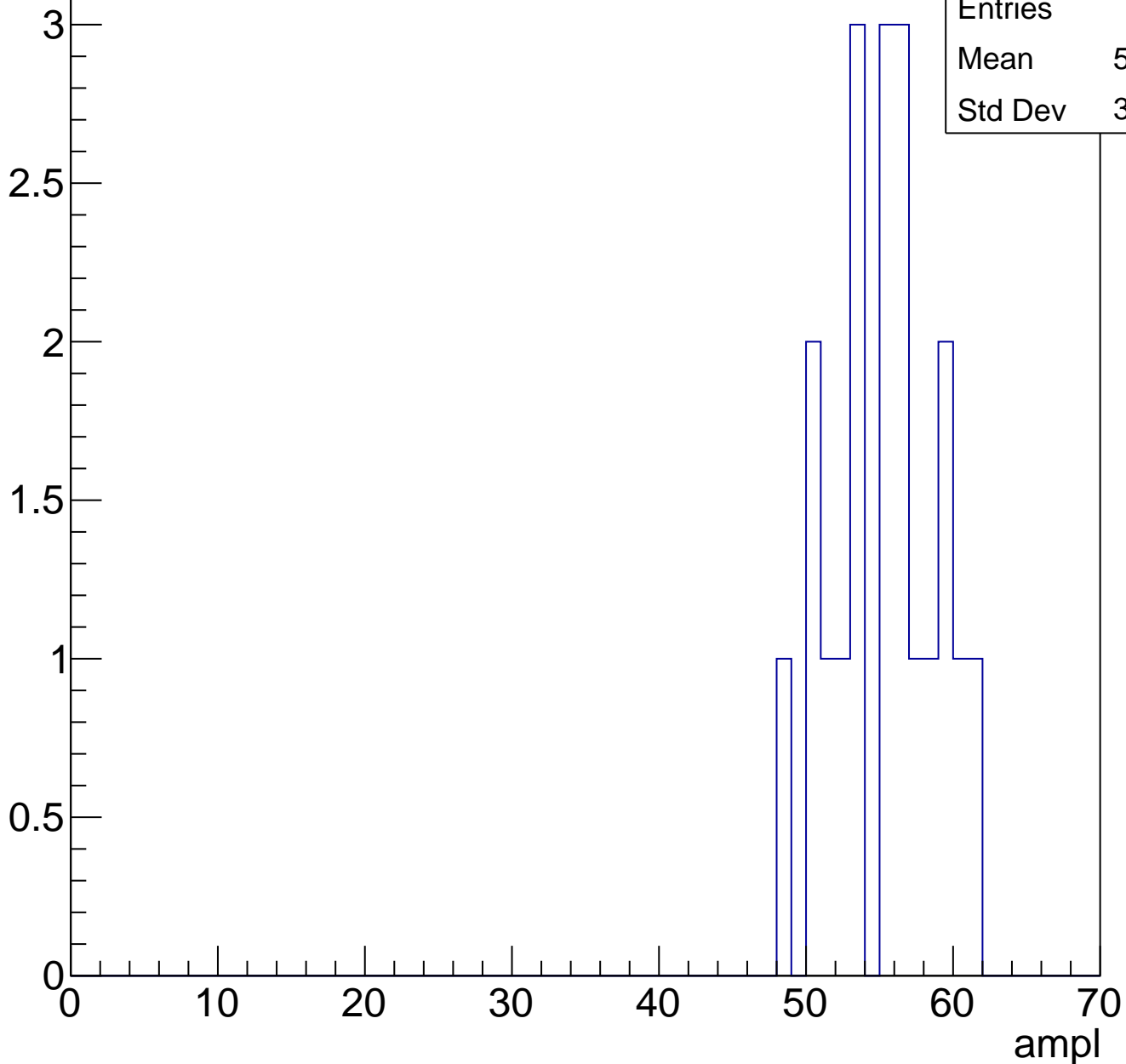
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U15-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

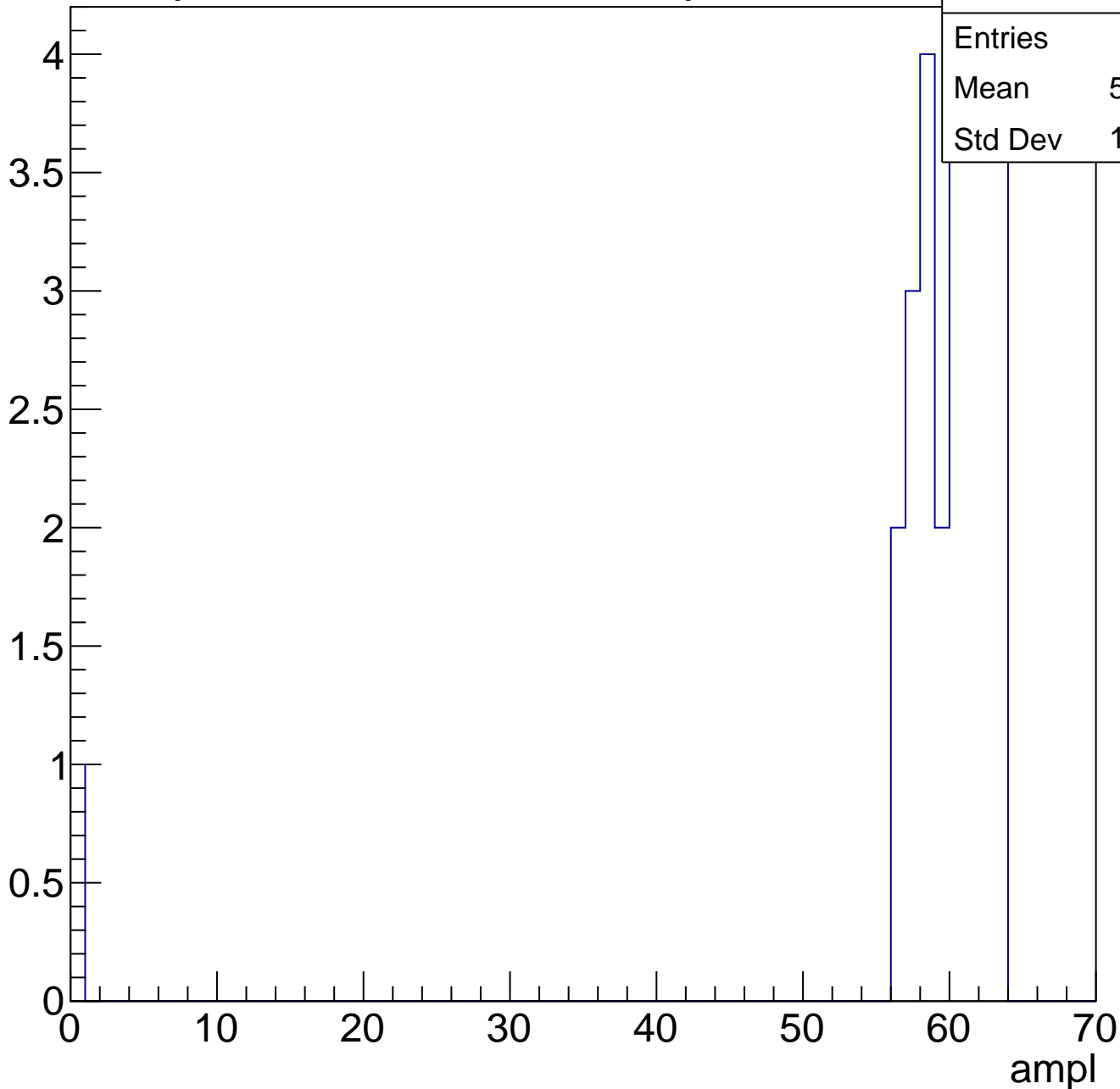
Entry



B1L103S, U15-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch70, adc0

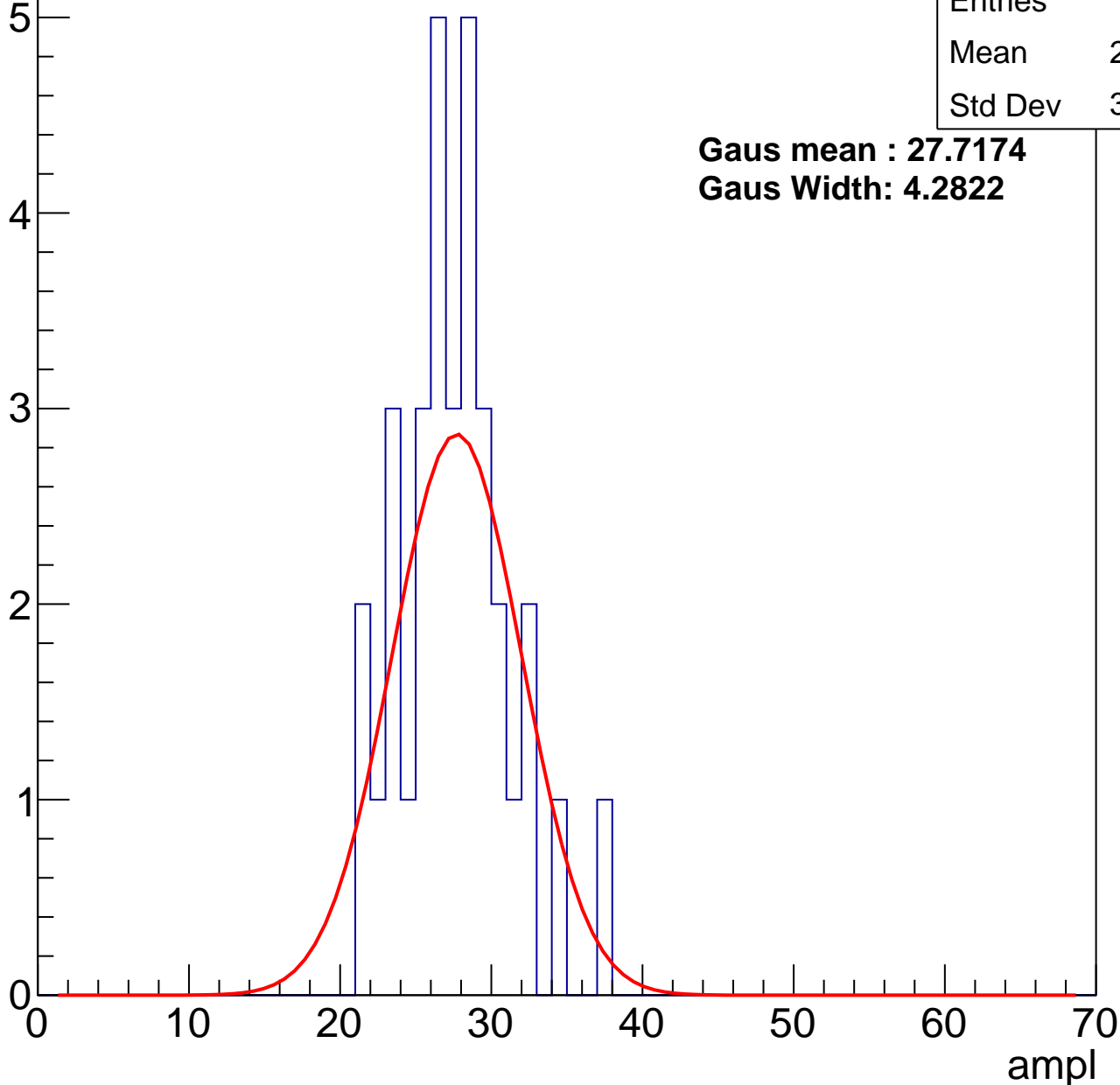
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	27.15
Std Dev	3.569

Gaus mean : 27.7174

Gaus Width: 4.2822



B1L103S, U15-ch70, adc1

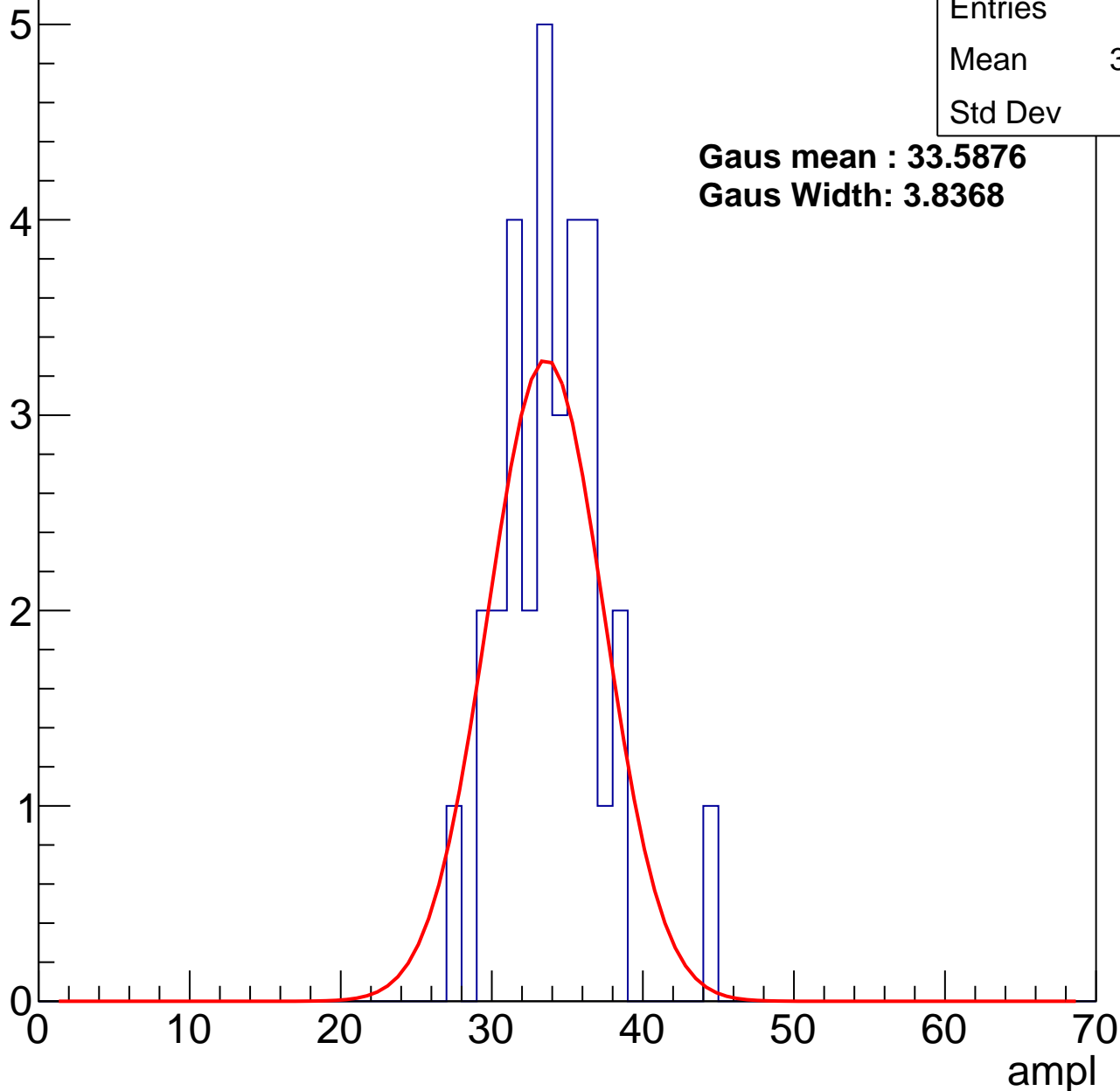
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	33.58
Std Dev	3.29

Gaus mean : 33.5876

Gaus Width: 3.8368



B1L103S, U15-ch70, adc2

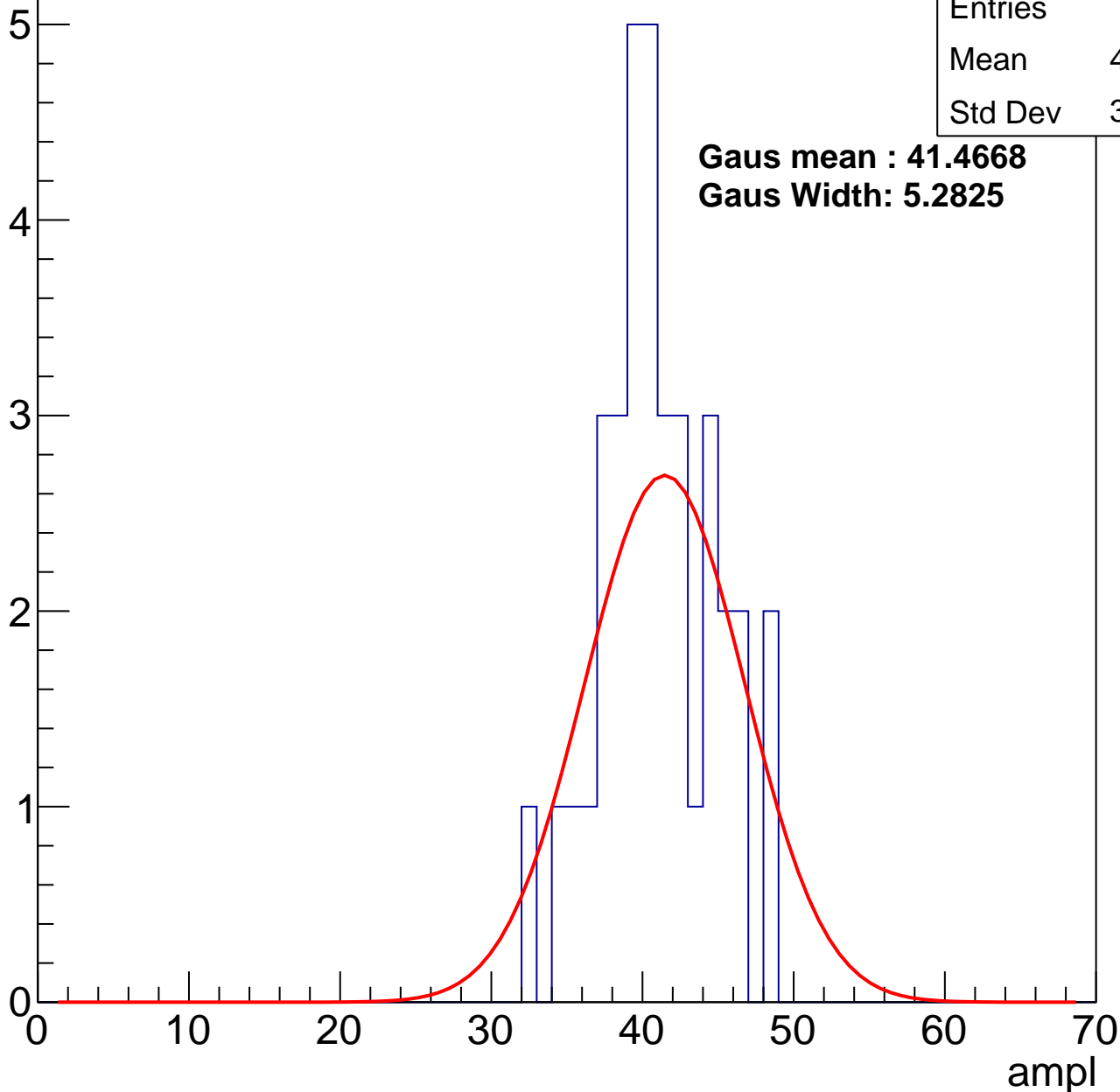
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	40.53
Std Dev	3.715

Gaus mean : 41.4668

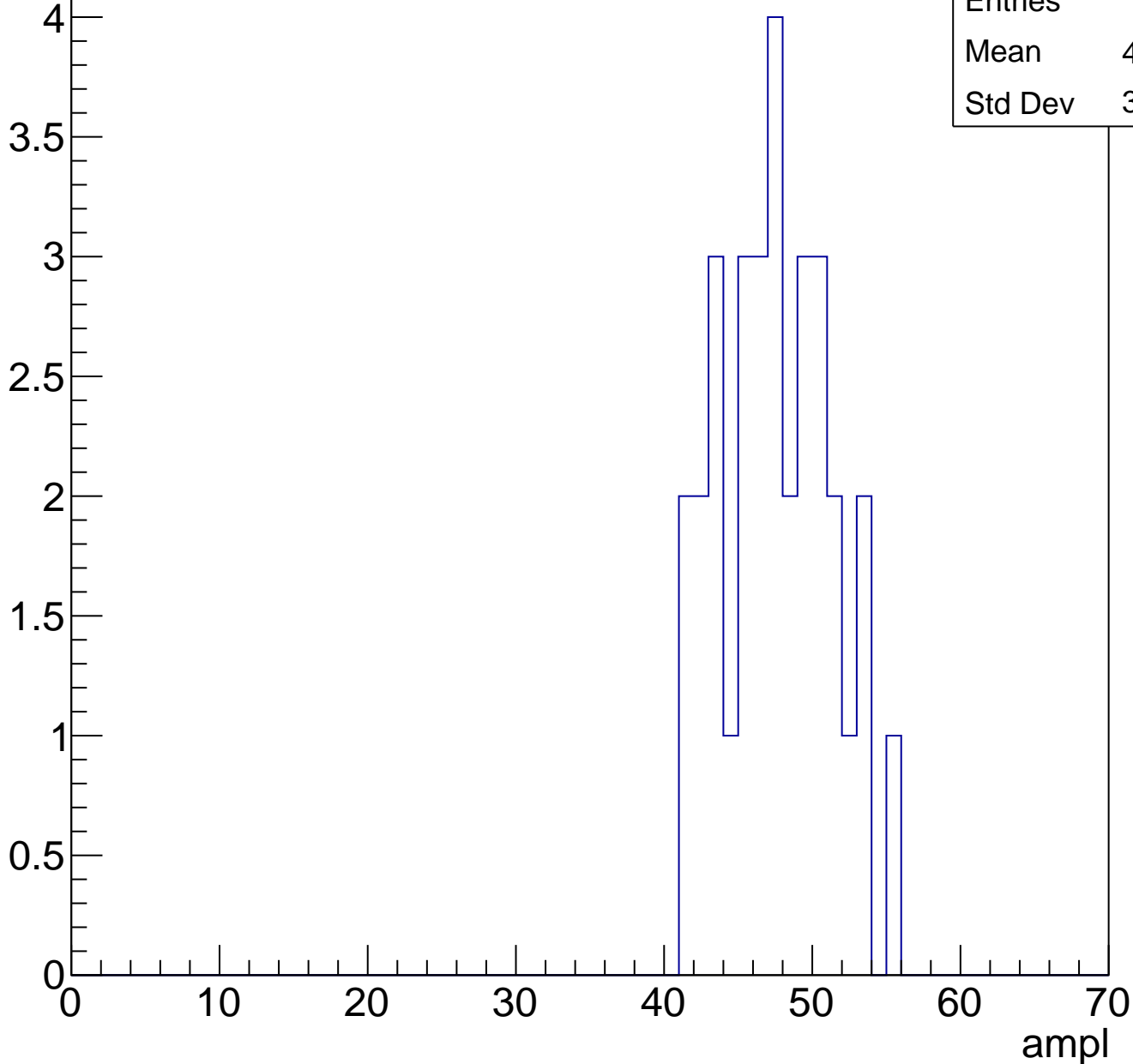
Gaus Width: 5.2825



B1L103S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

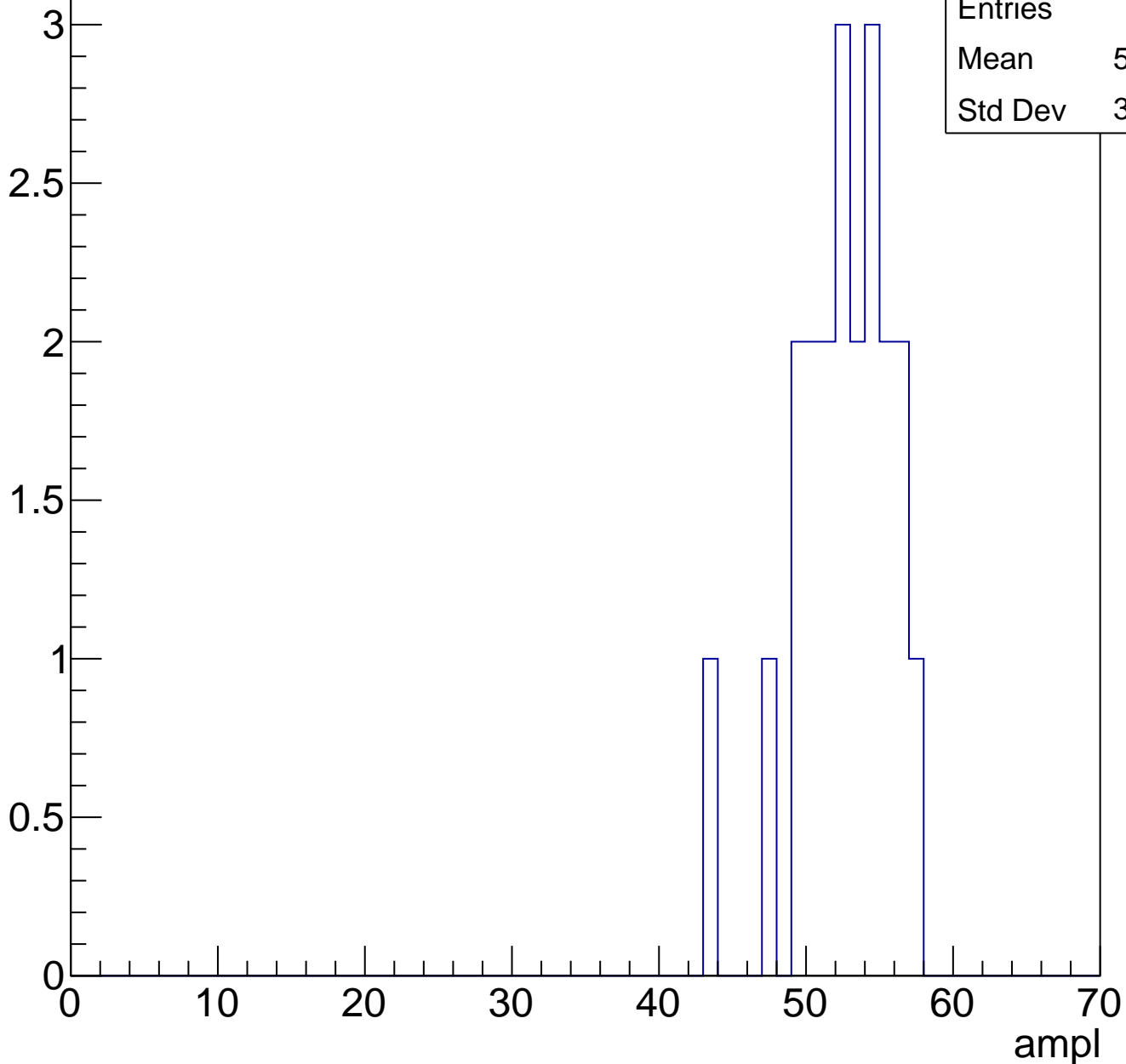
Entry



B1L103S, U15-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

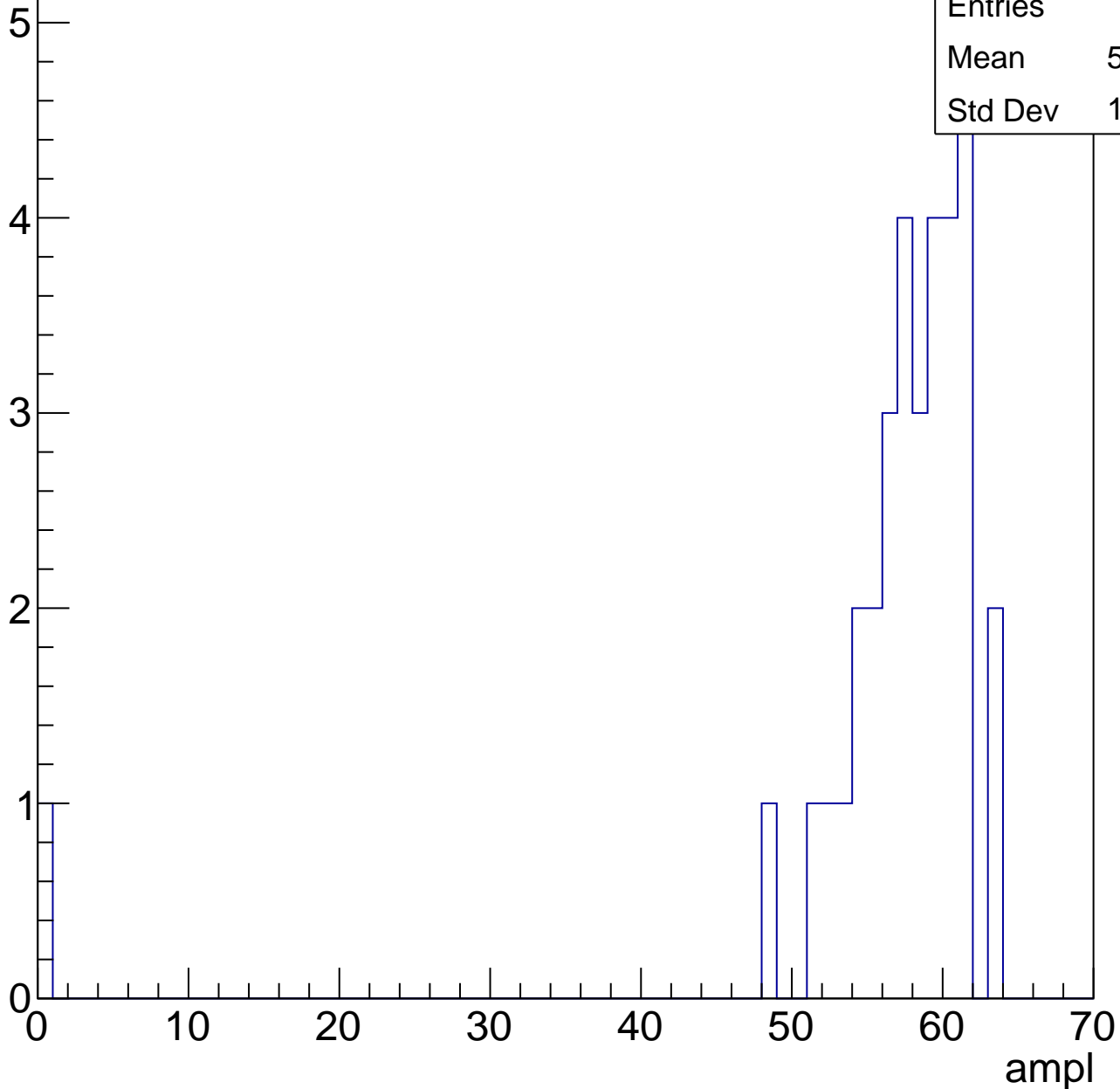


B1L103S, U15-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

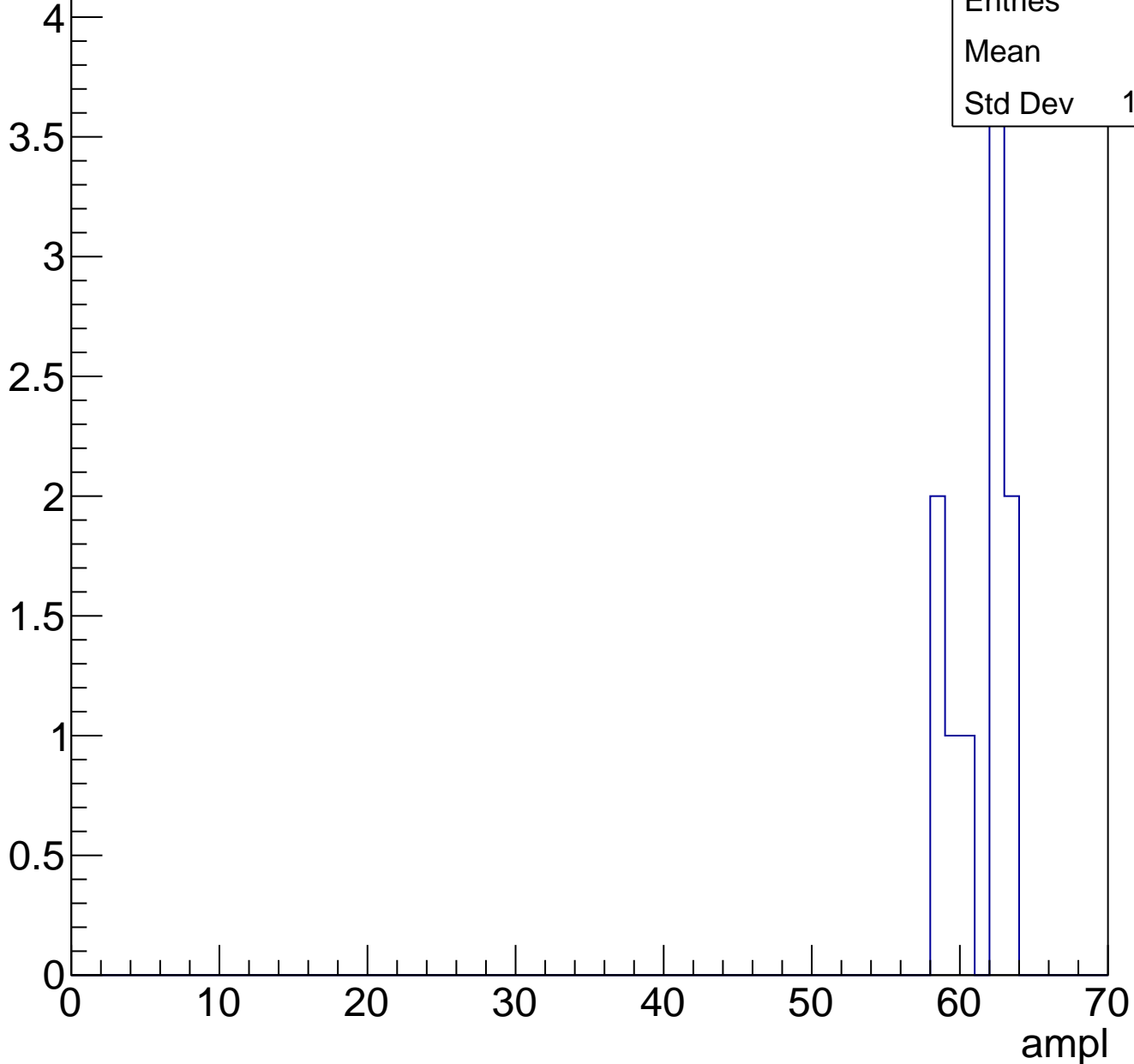
Entries	34
Mean	55.85
Std Dev	10.29



B1L103S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

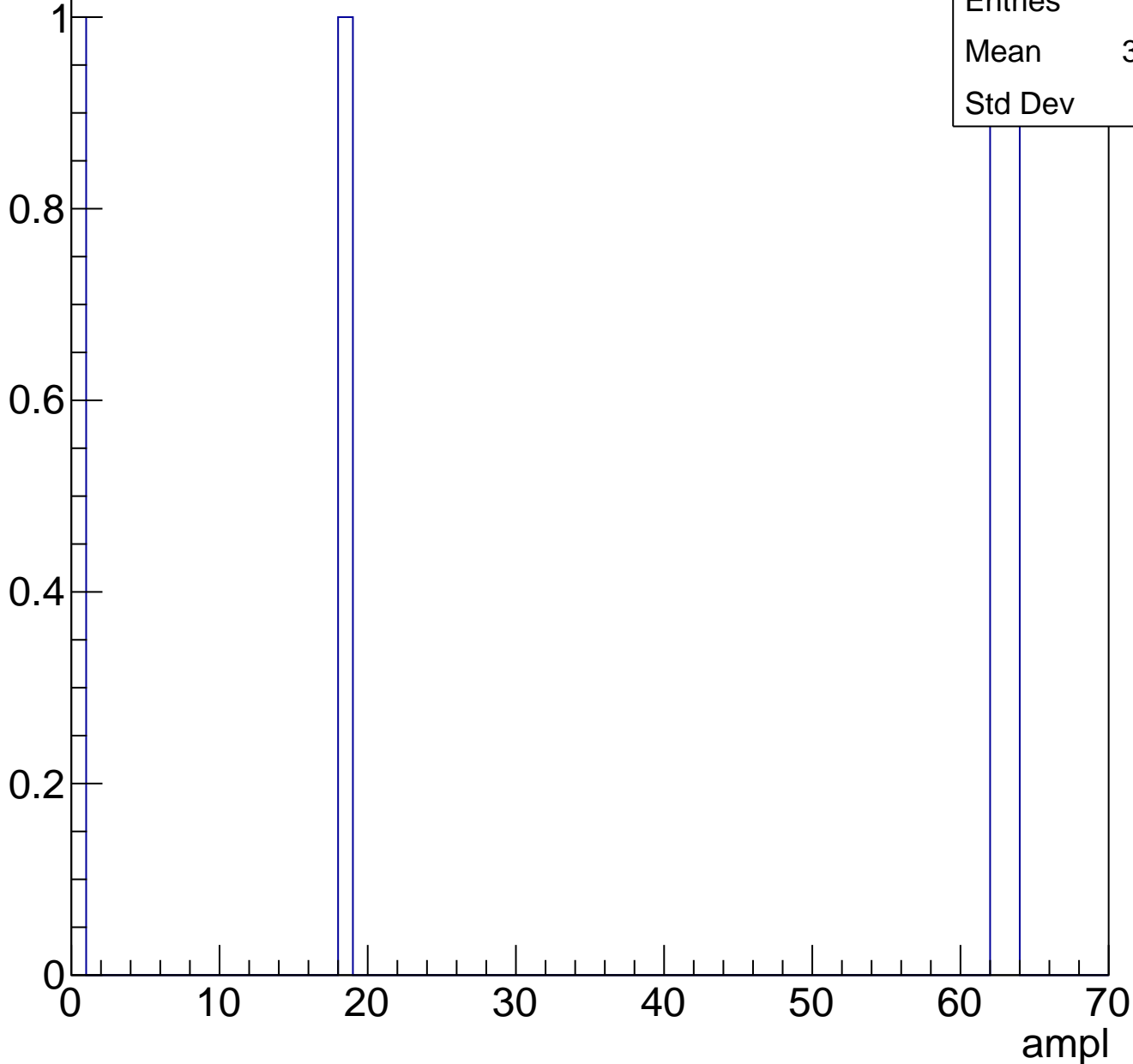
Entry



B1L103S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch71, adc0

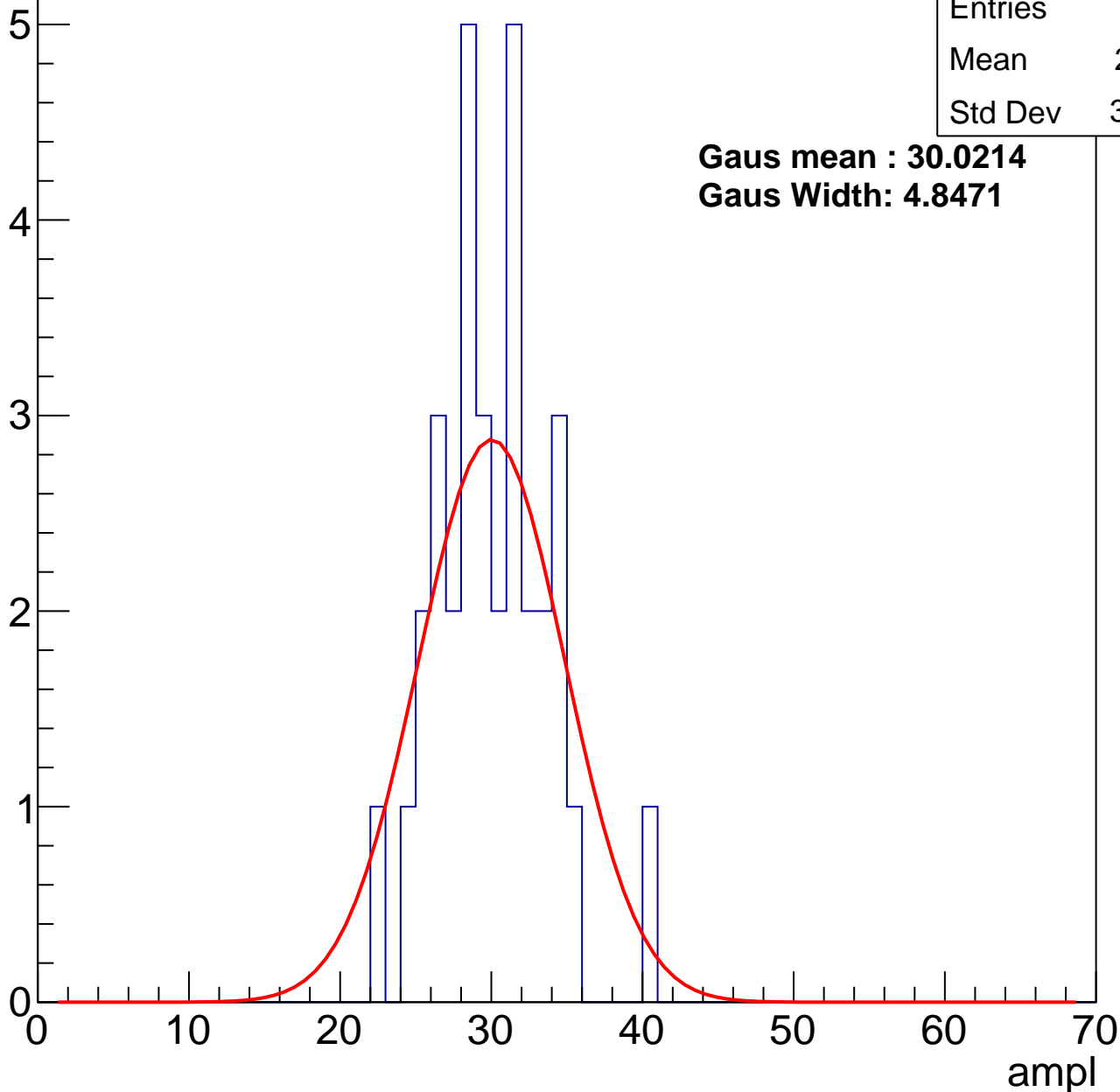
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	29.61
Std Dev	3.643

Gaus mean : 30.0214

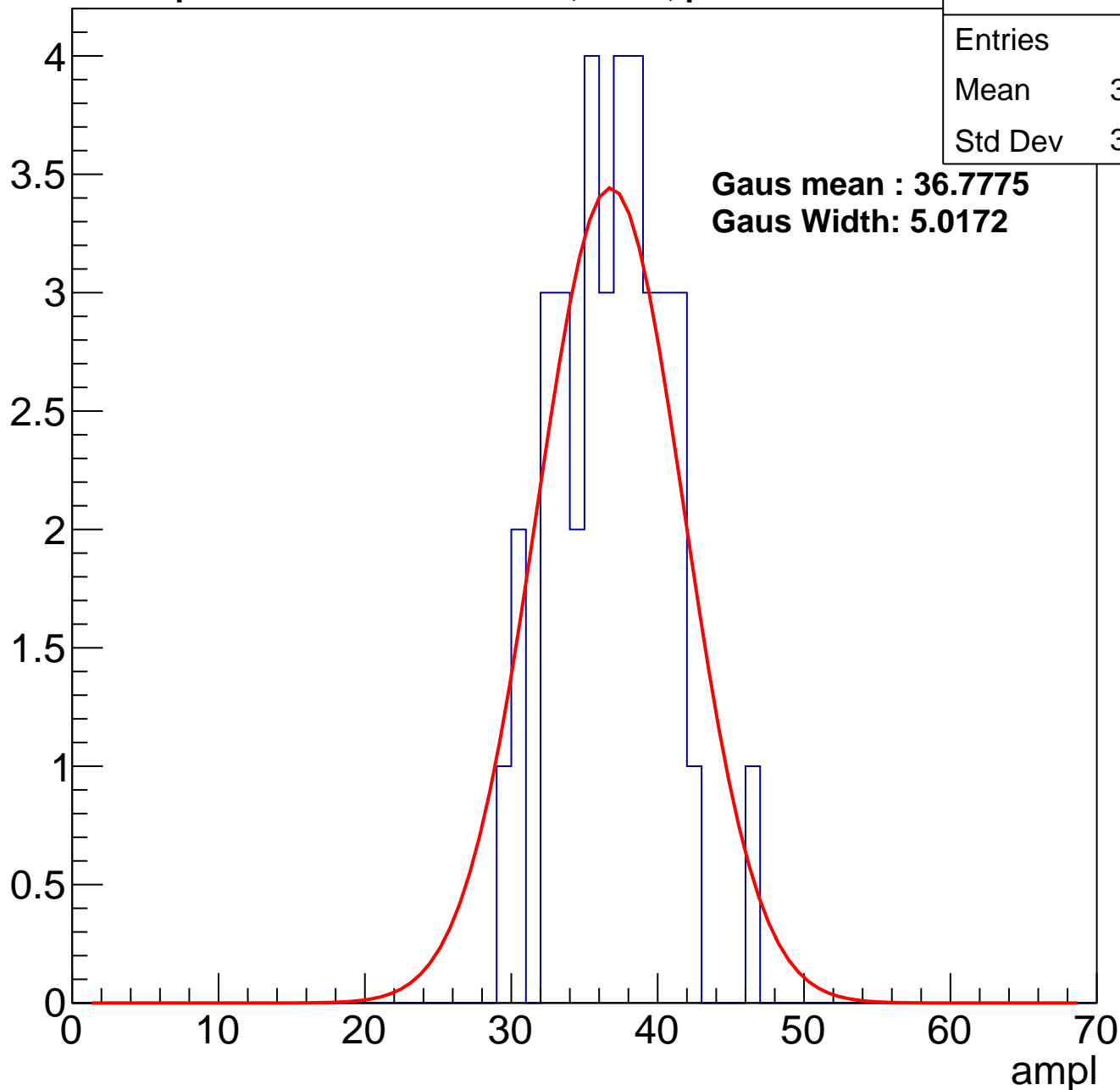
Gaus Width: 4.8471



B1L103S, U15-ch71, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch71, adc2

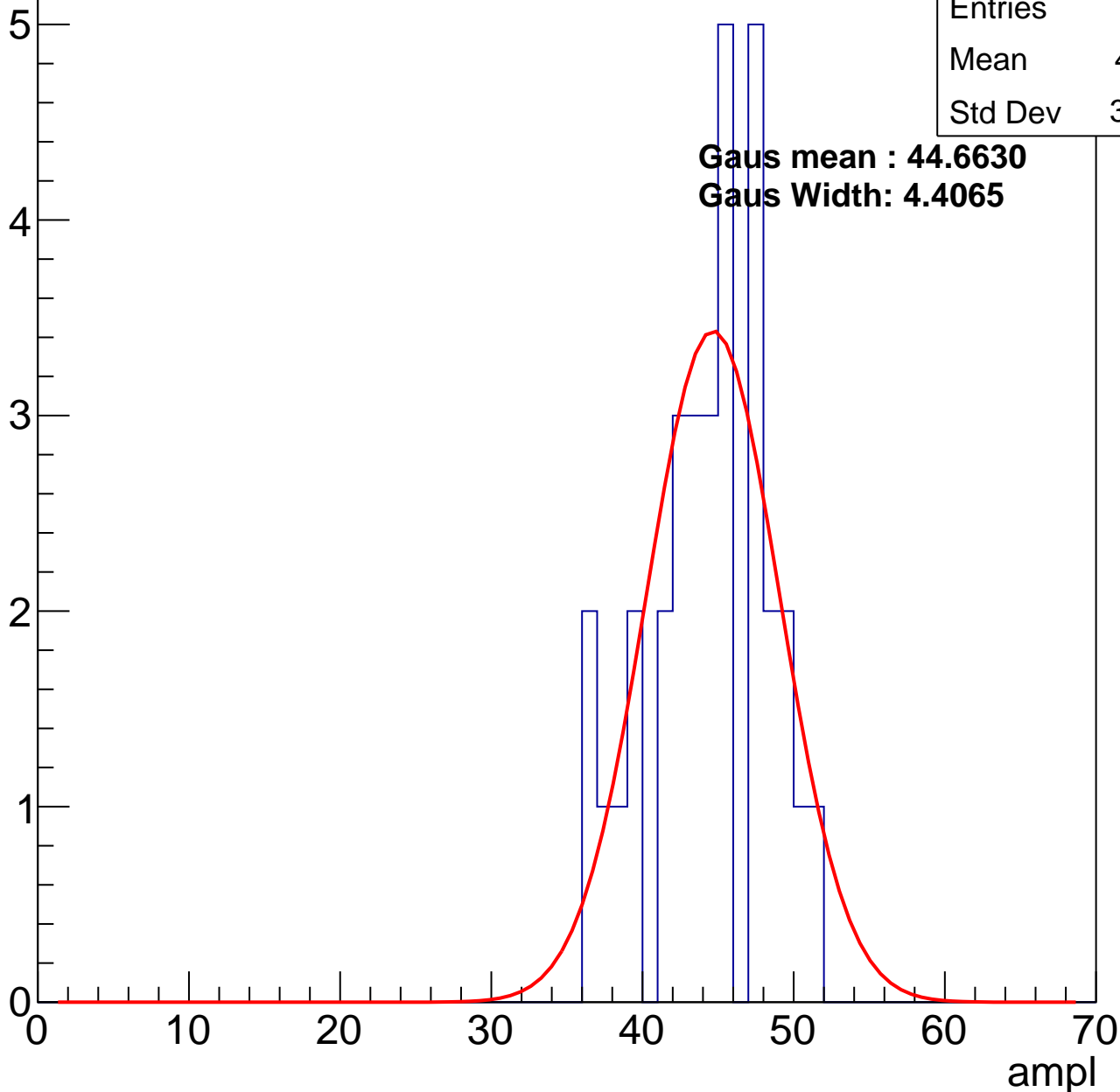
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	43.91
Std Dev	3.942

Gaus mean : 44.6630

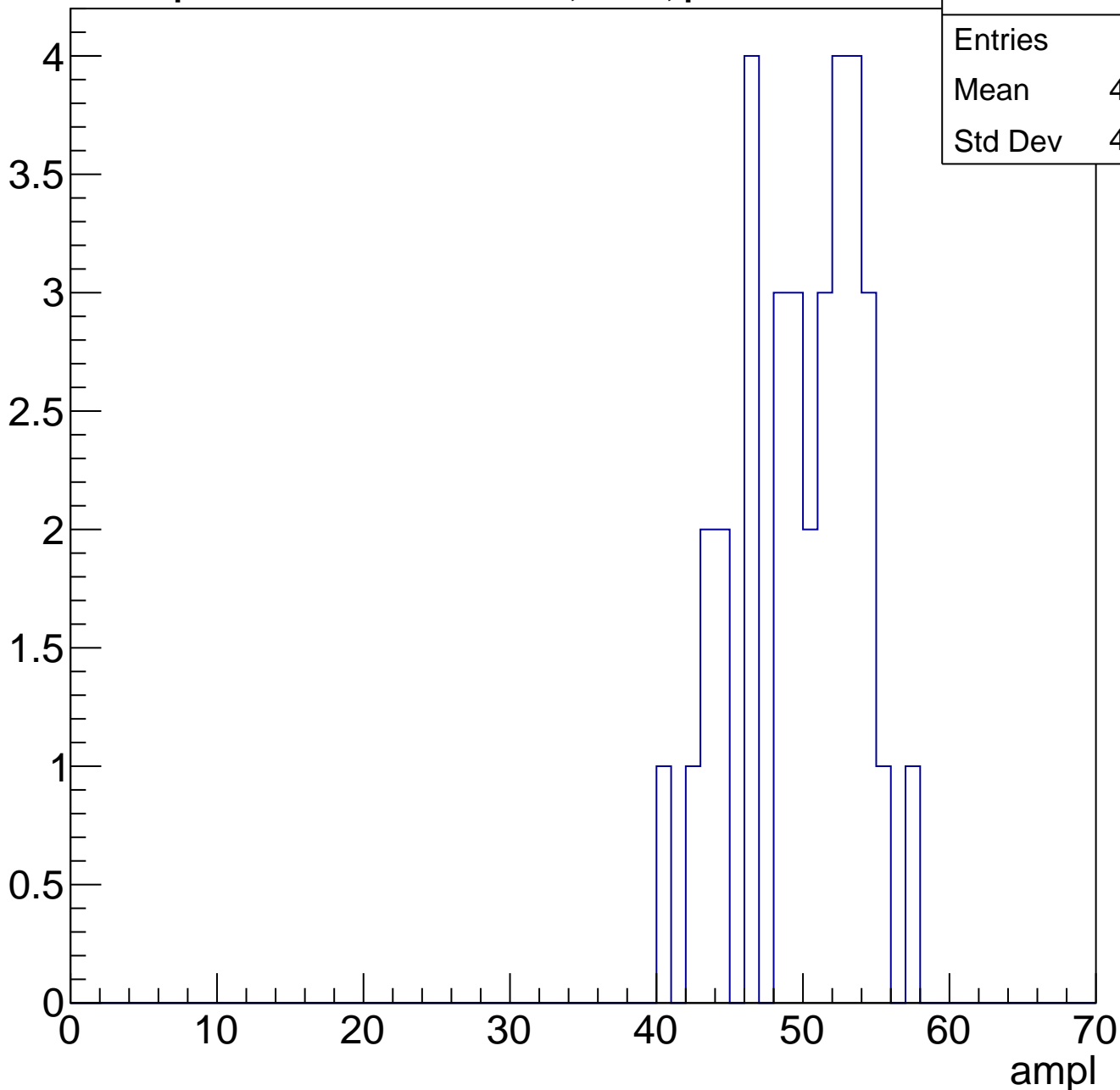
Gaus Width: 4.4065



B1L103S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

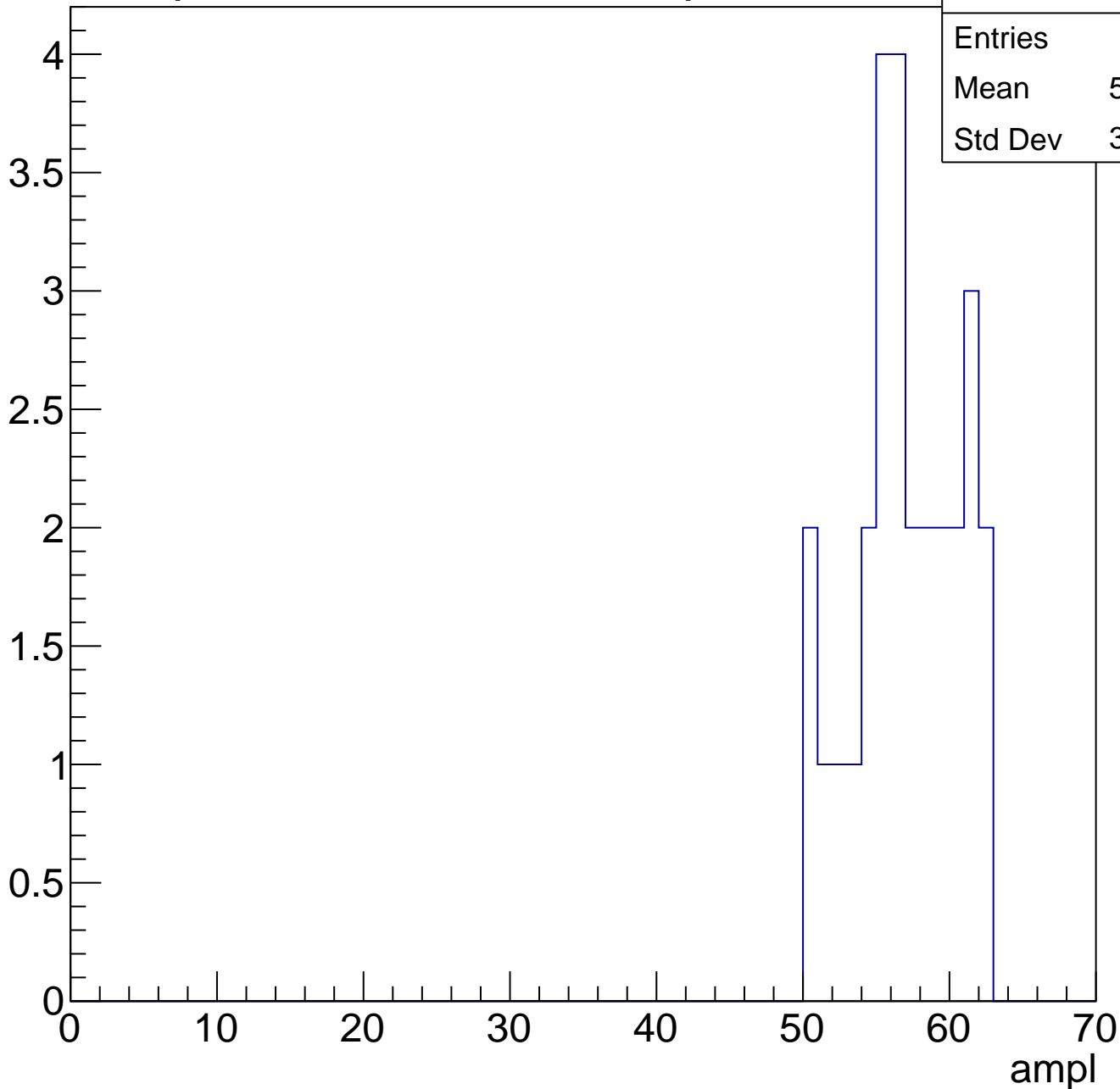


Entries	34
Mean	49.35
Std Dev	4.115

B1L103S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

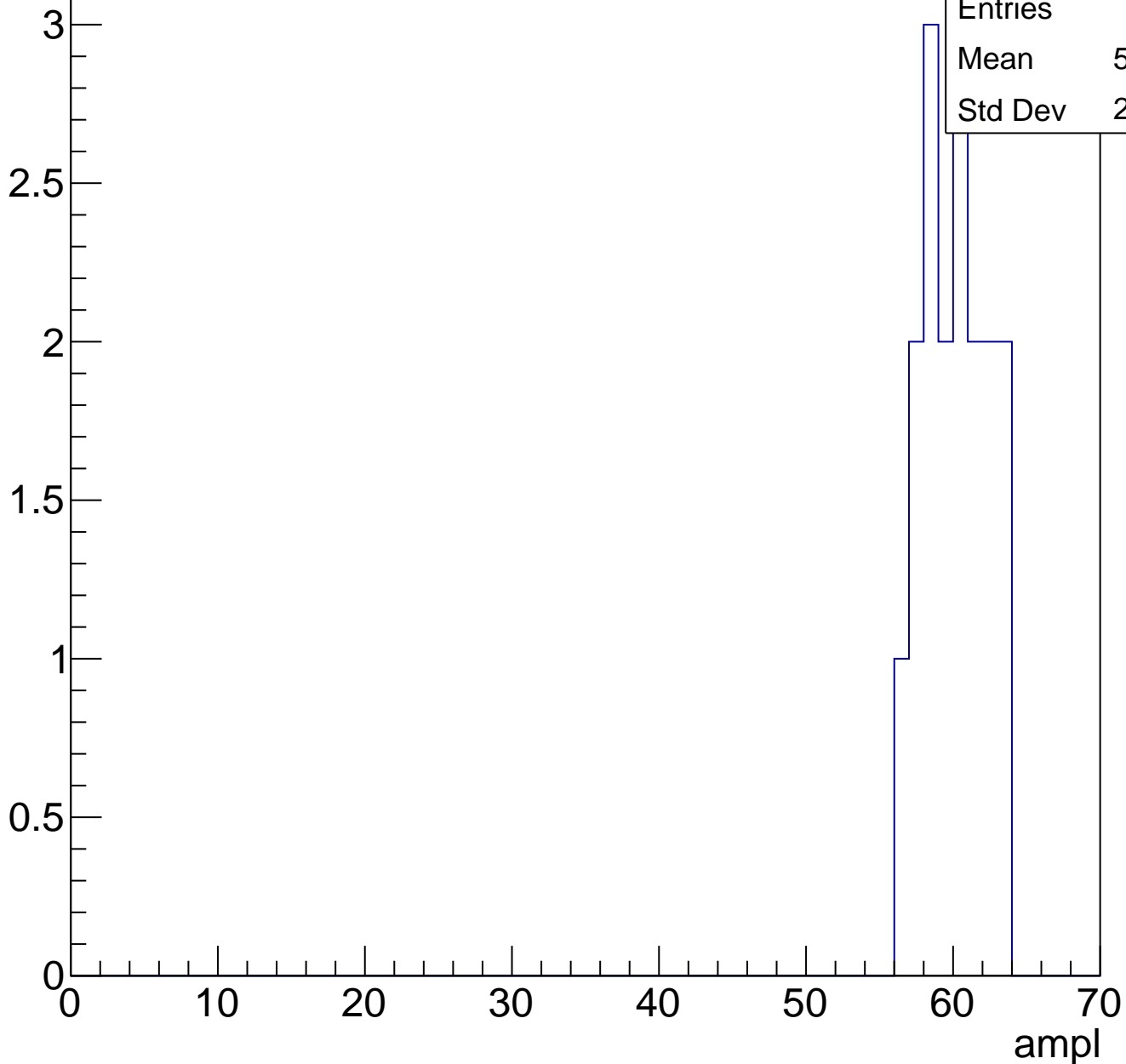
Entry



B1L103S, U15-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

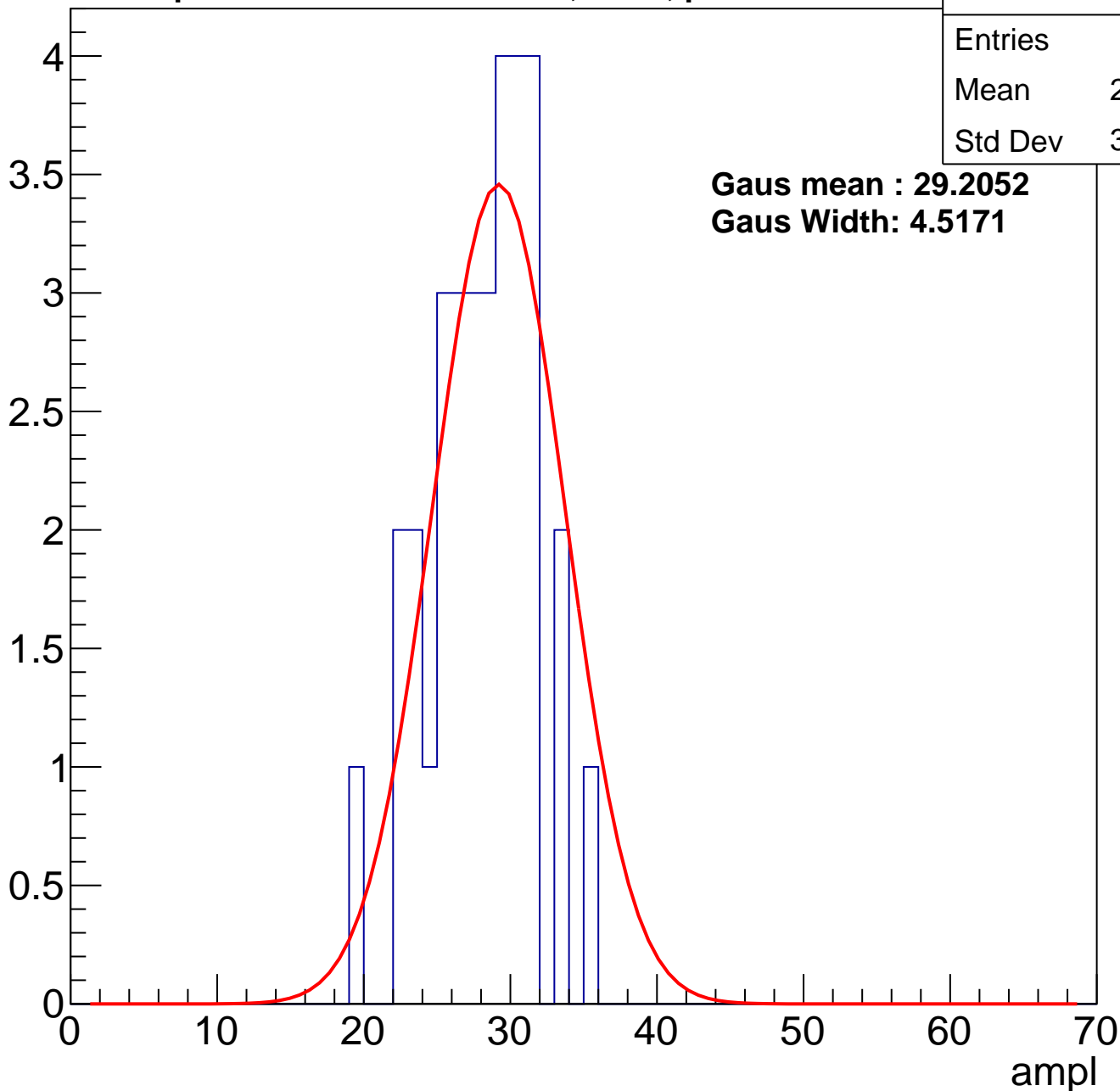


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch72, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	33
Mean	27.64
Std Dev	3.532

B1L103S, U15-ch72, adc1

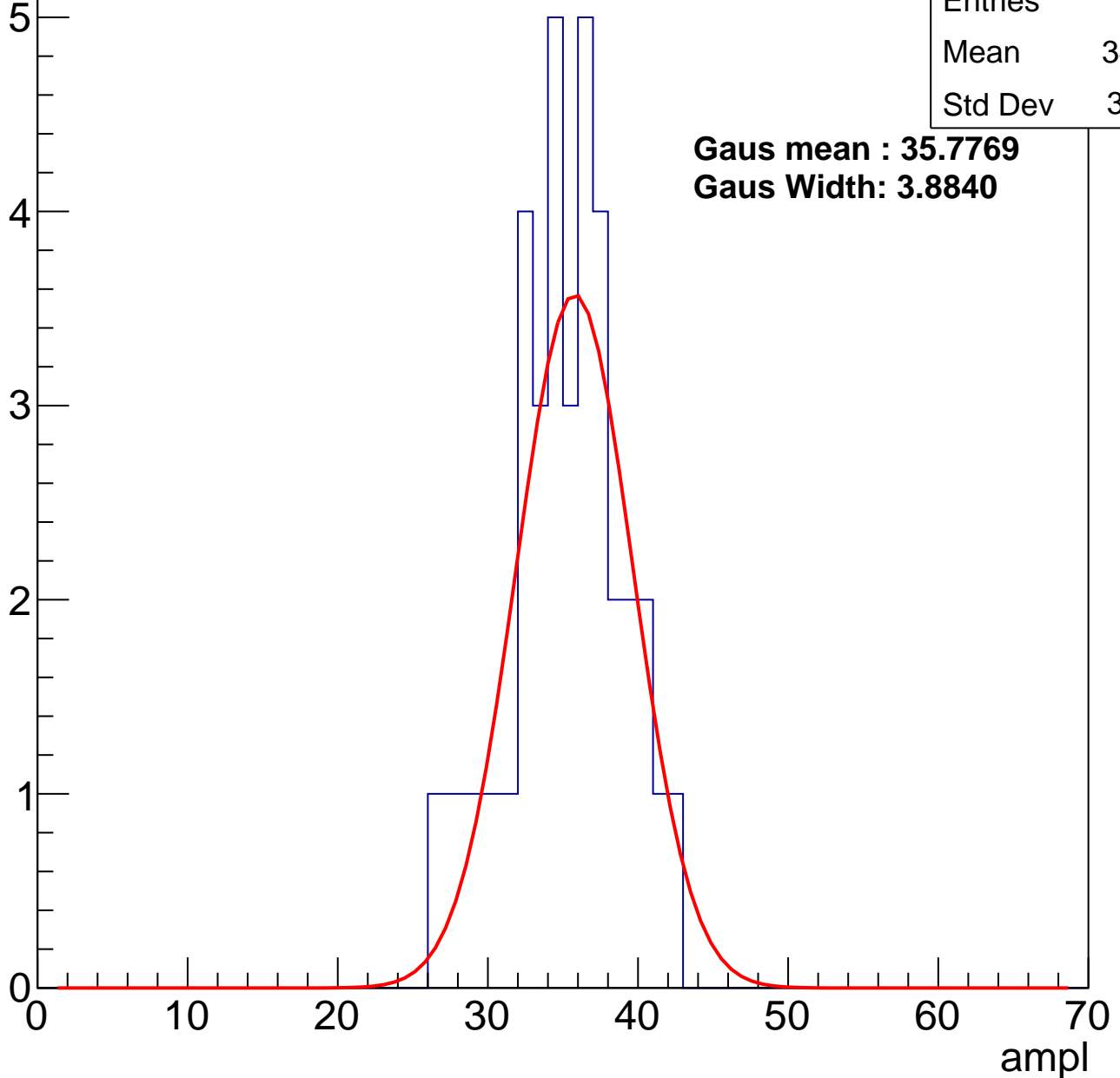
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	34.68
Std Dev	3.721

Gaus mean : 35.7769

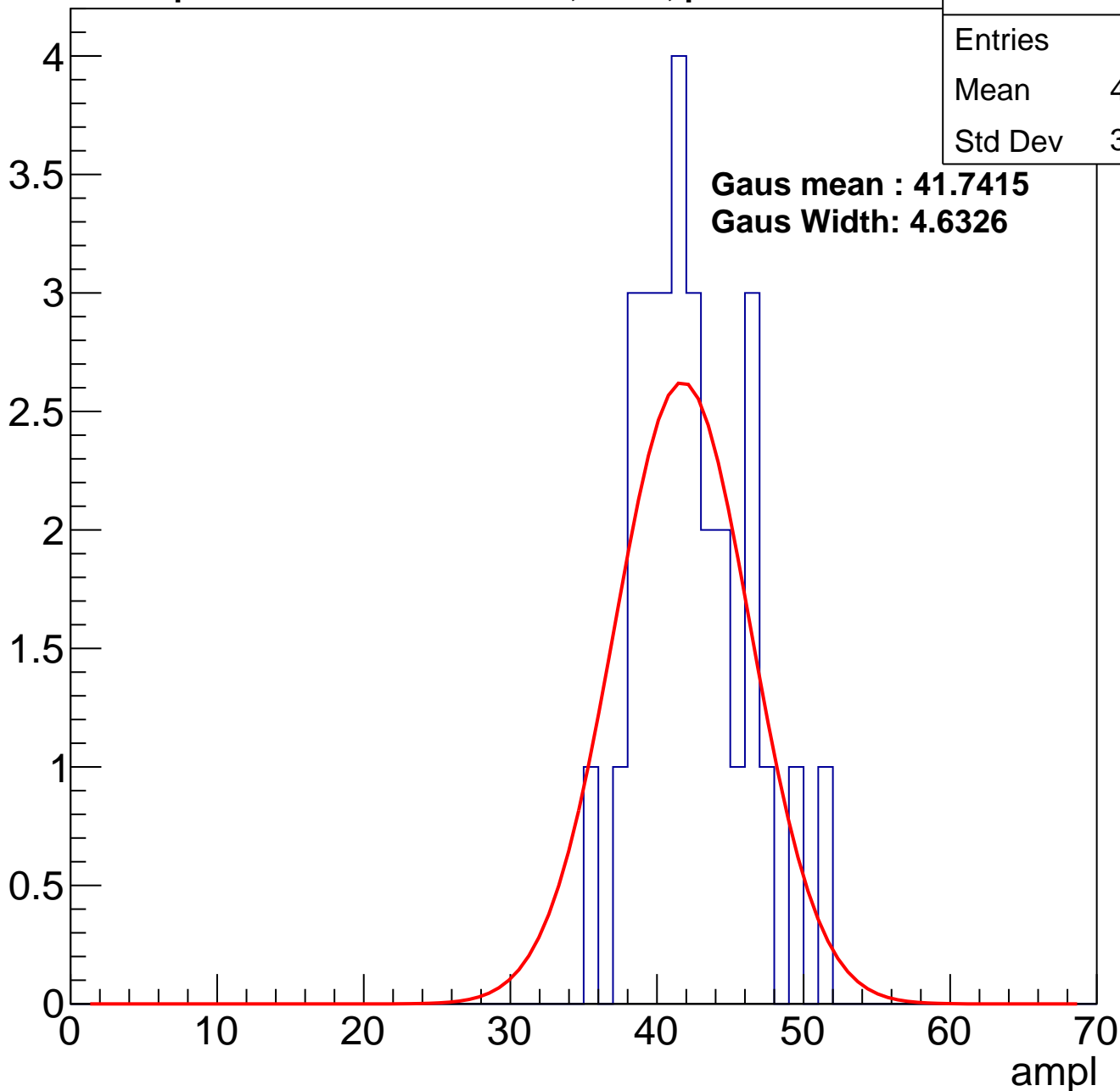
Gaus Width: 3.8840



B1L103S, U15-ch72, adc2

calib_packv5_042523_0143.root, FC#7, port C2

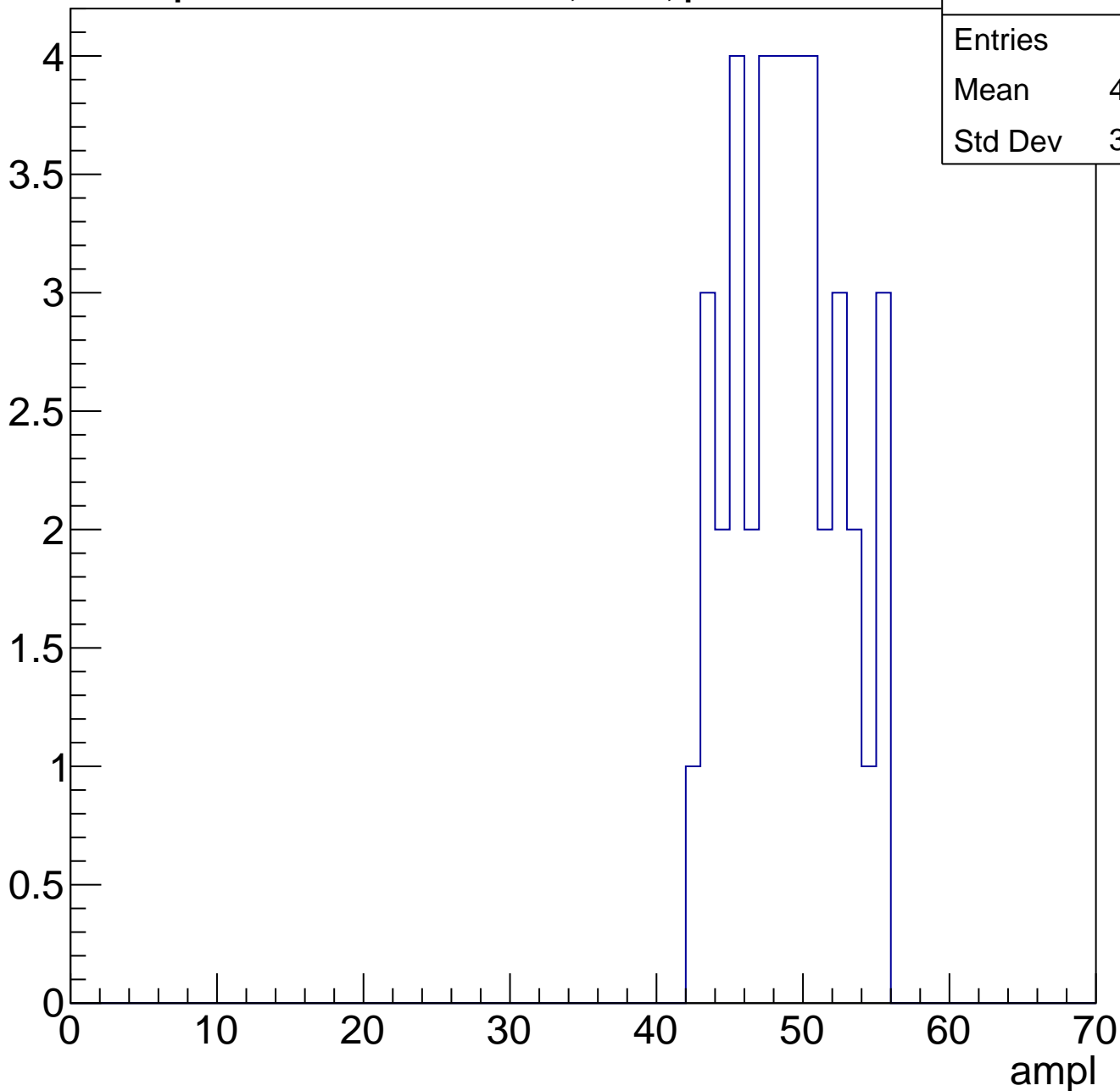
Entry



B1L103S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

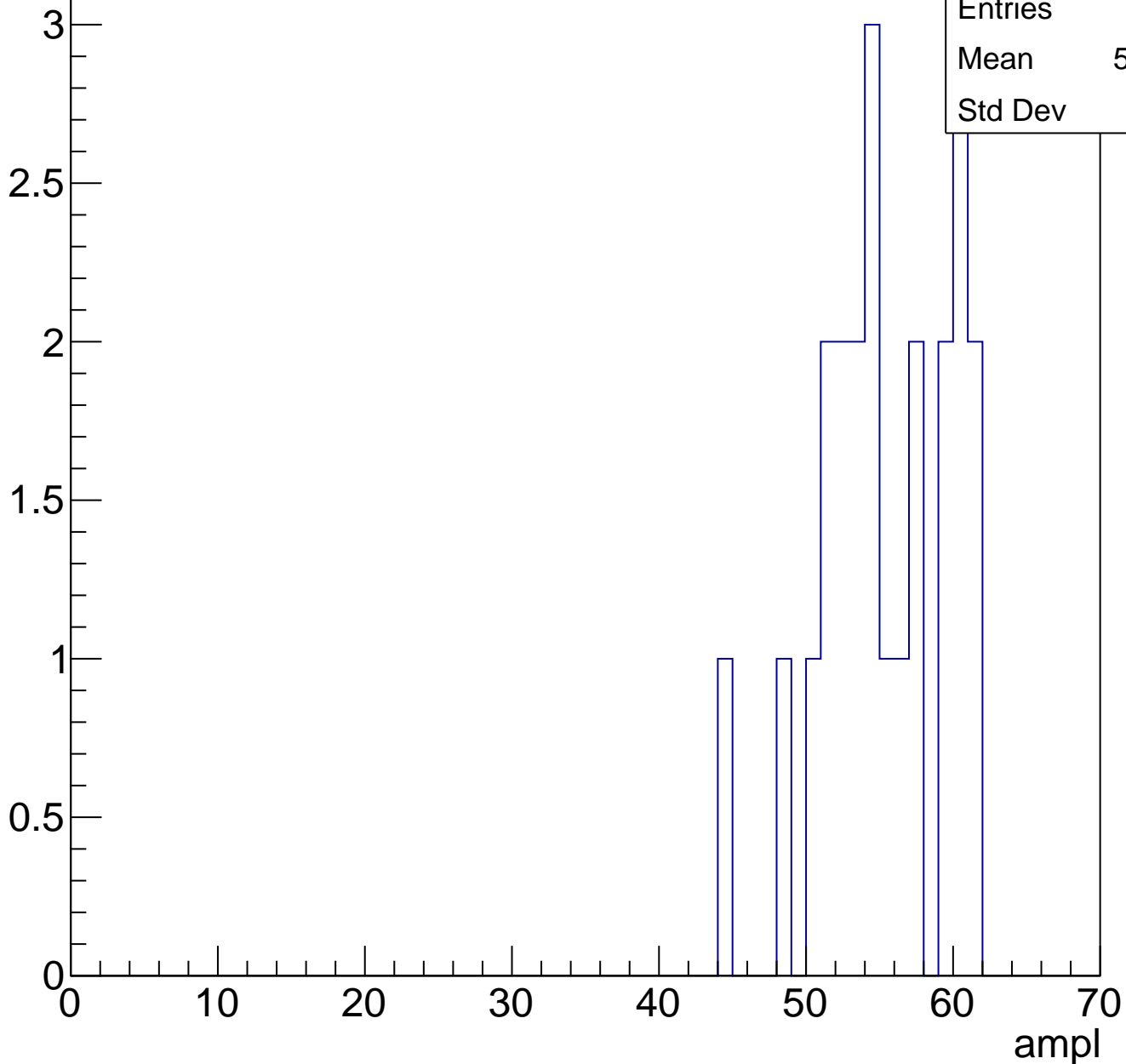


Entries	39
Mean	48.46
Std Dev	3.587

B1L103S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

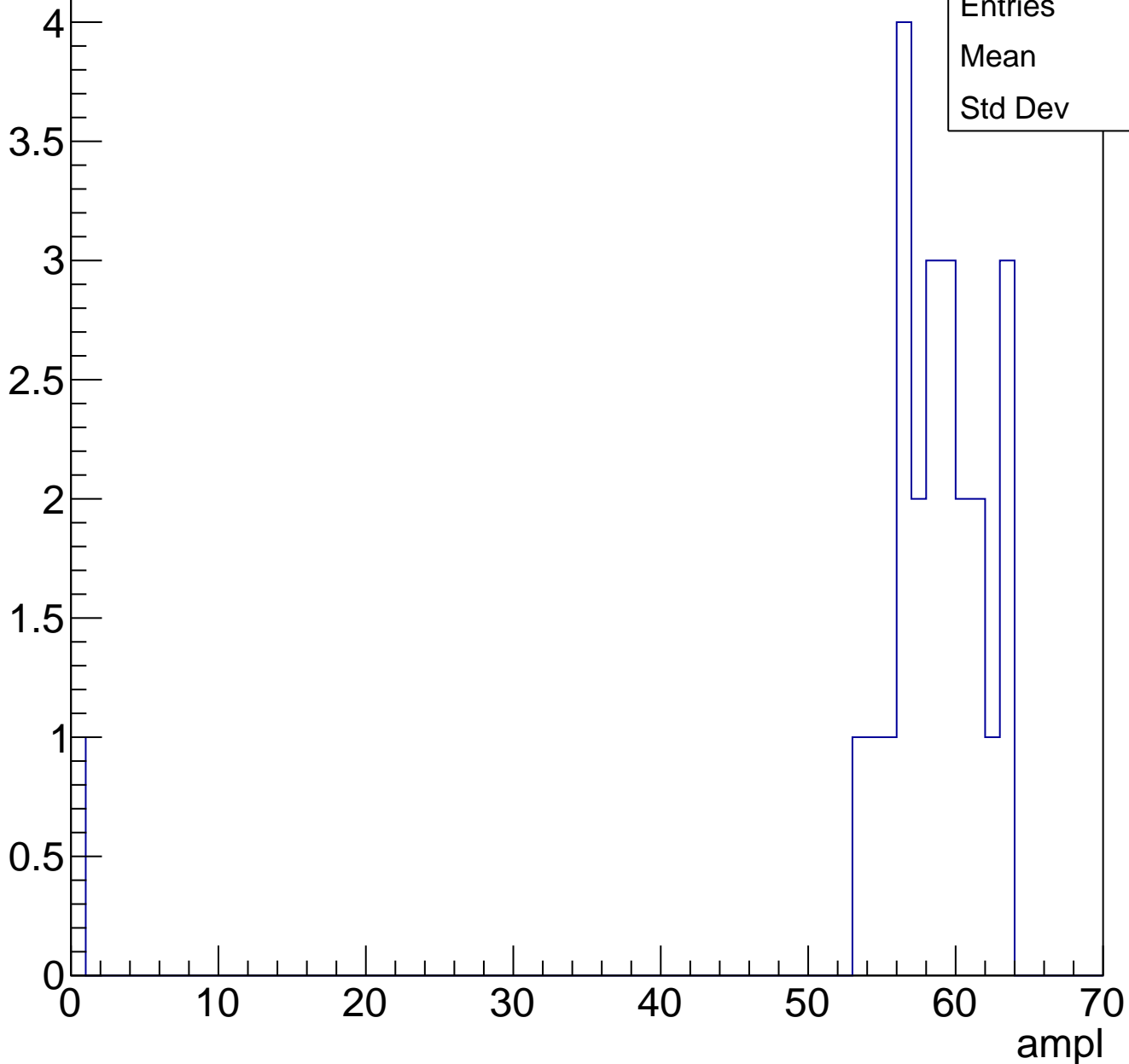
Entry



B1L103S, U15-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

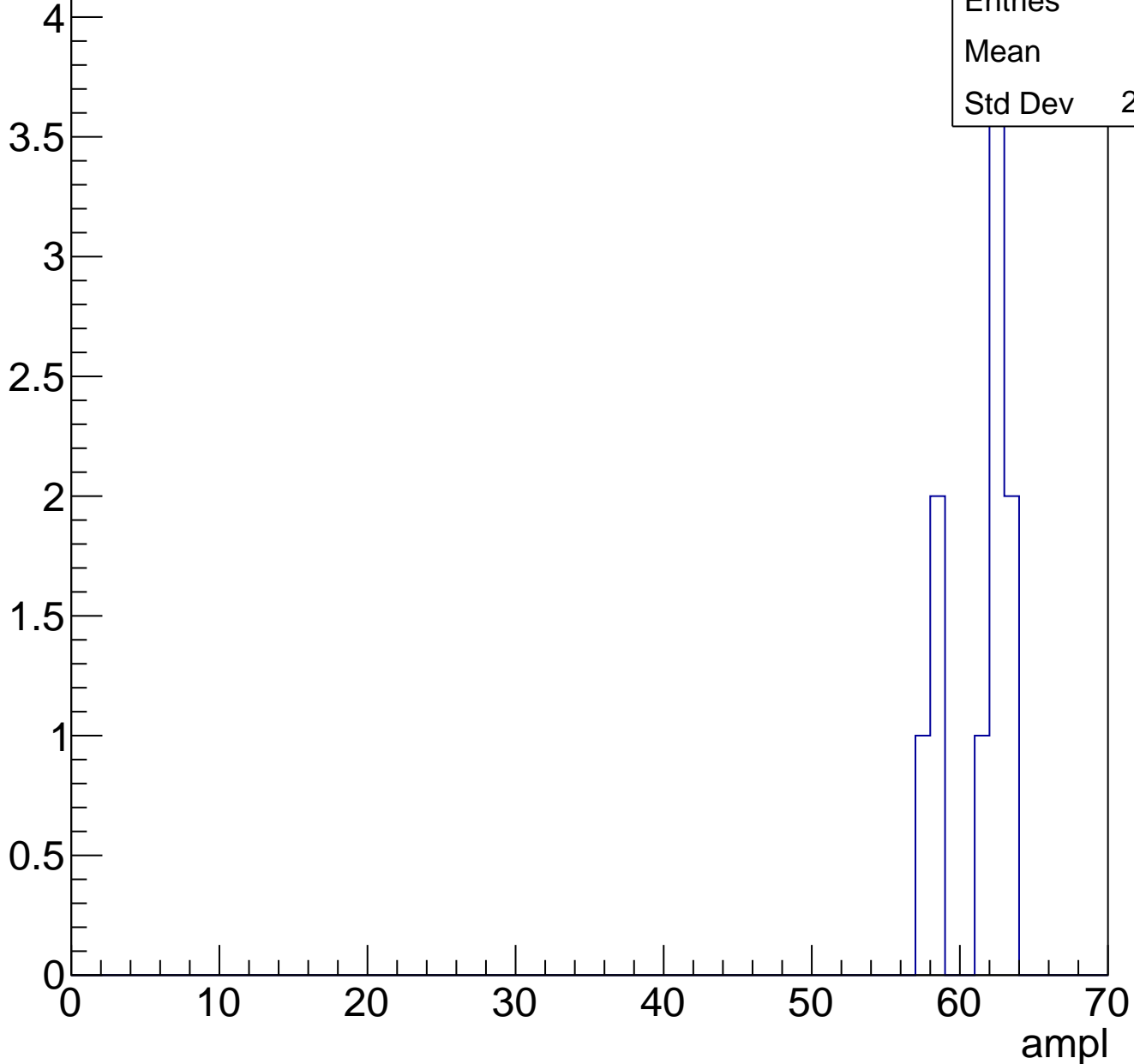


Entries	24
Mean	56
Std Dev	12

B1L103S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

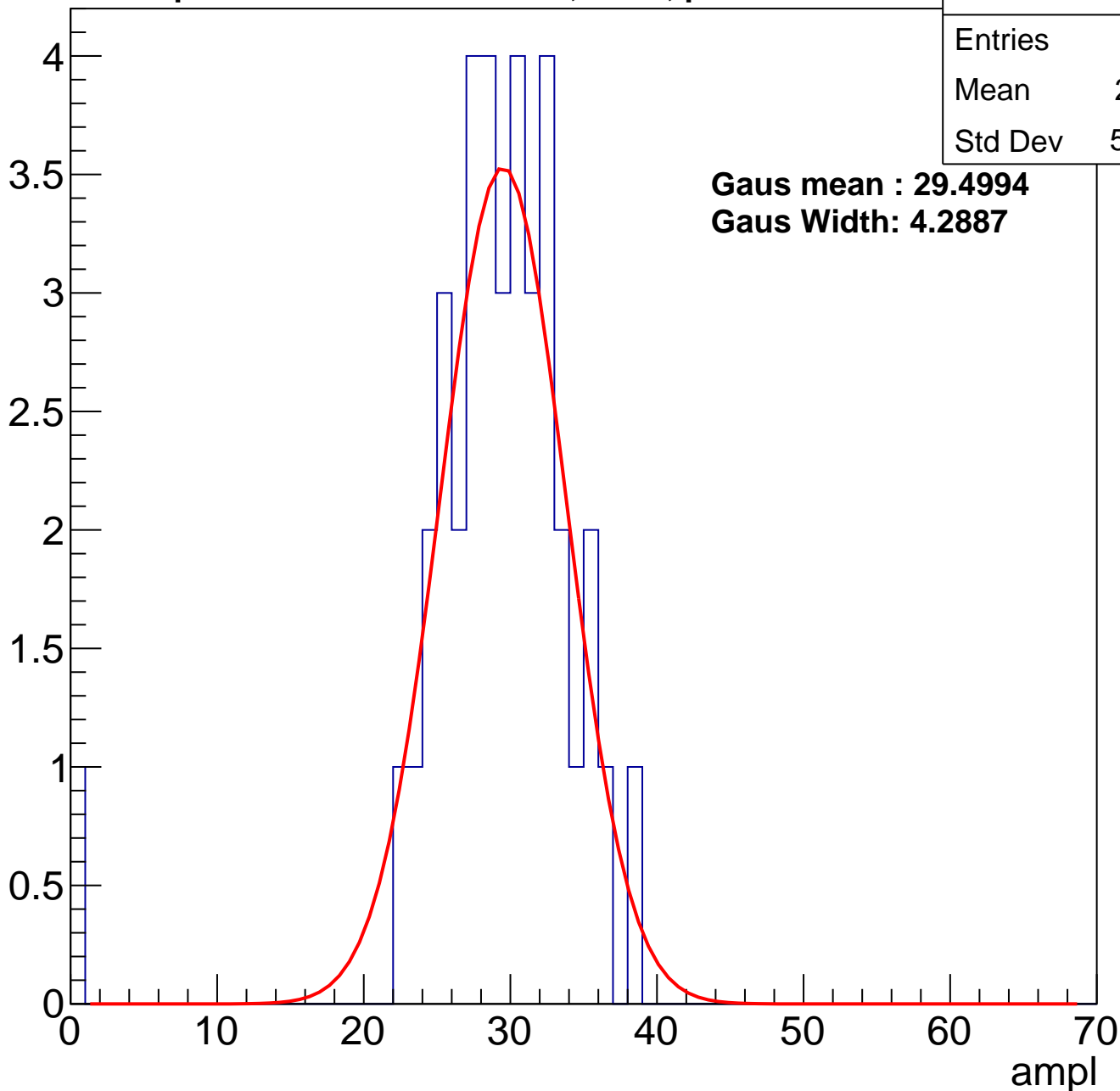
Entry



B1L103S, U15-ch73, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	39
Mean	28.51
Std Dev	5.913

Gaus mean : 29.4994
Gaus Width: 4.2887

B1L103S, U15-ch73, adc1

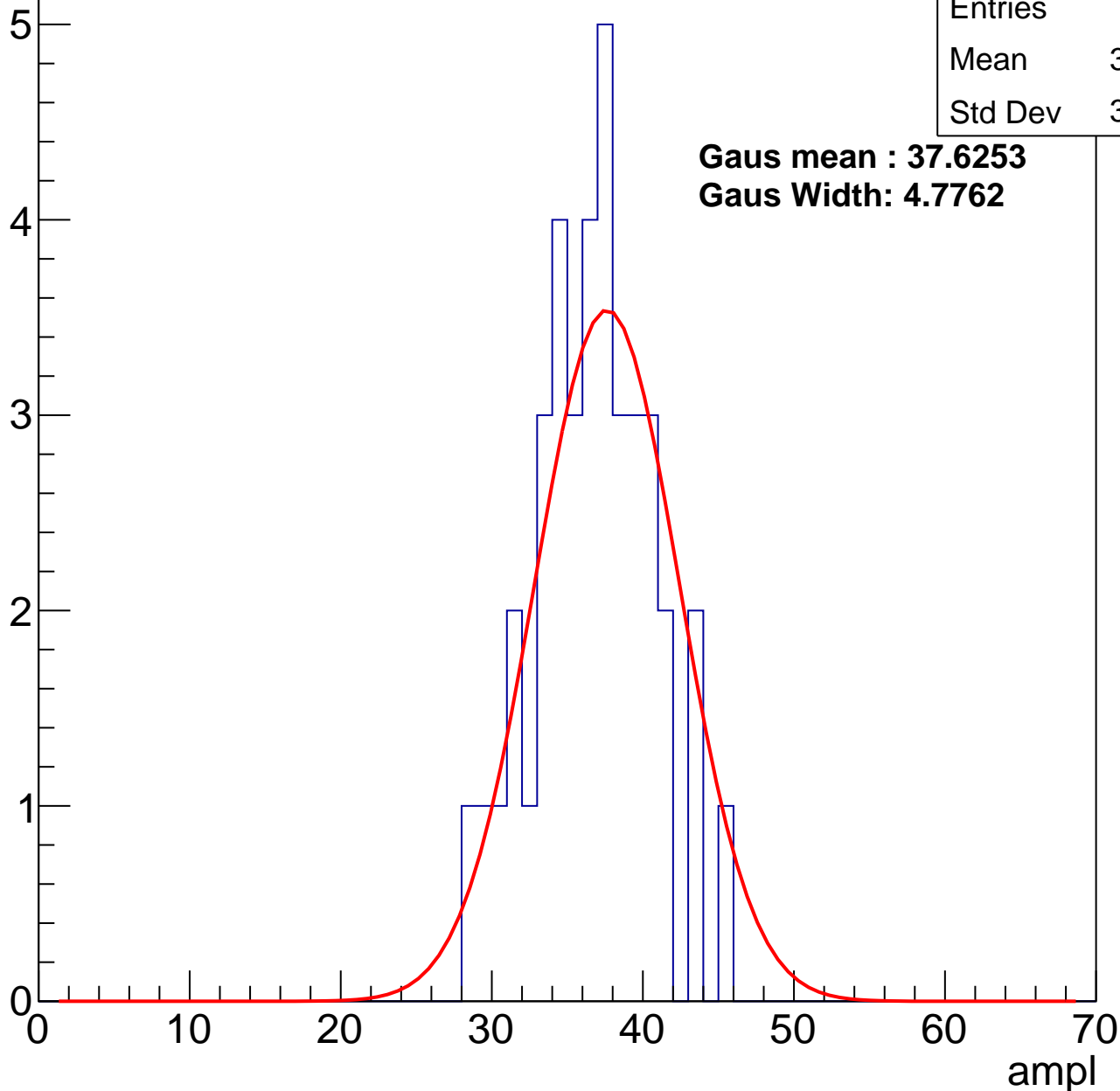
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	36.26
Std Dev	3.855

Gaus mean : 37.6253

Gaus Width: 4.7762



B1L103S, U15-ch73, adc2

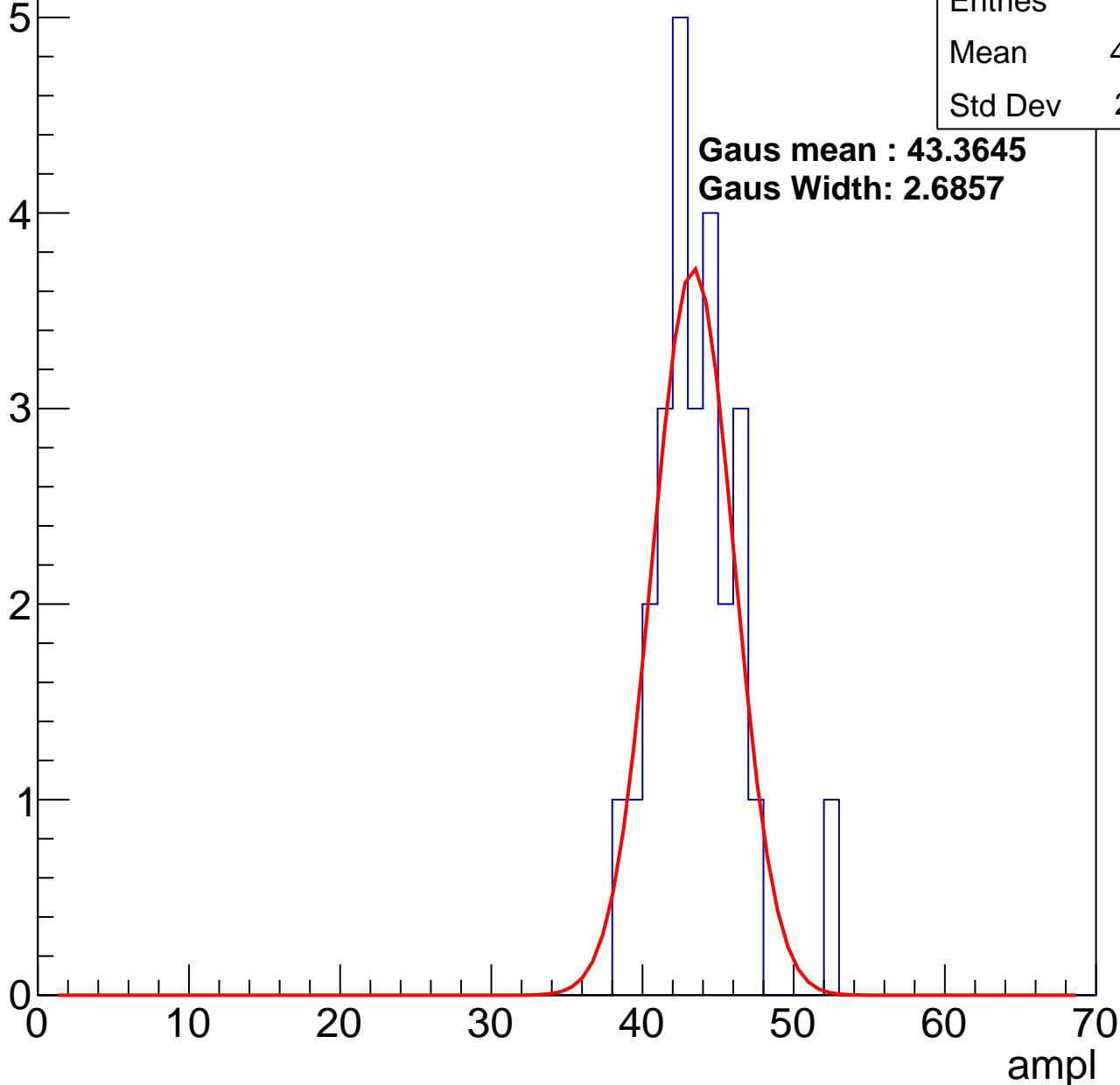
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	43.15
Std Dev	2.851

Gaus mean : 43.3645

Gaus Width: 2.6857

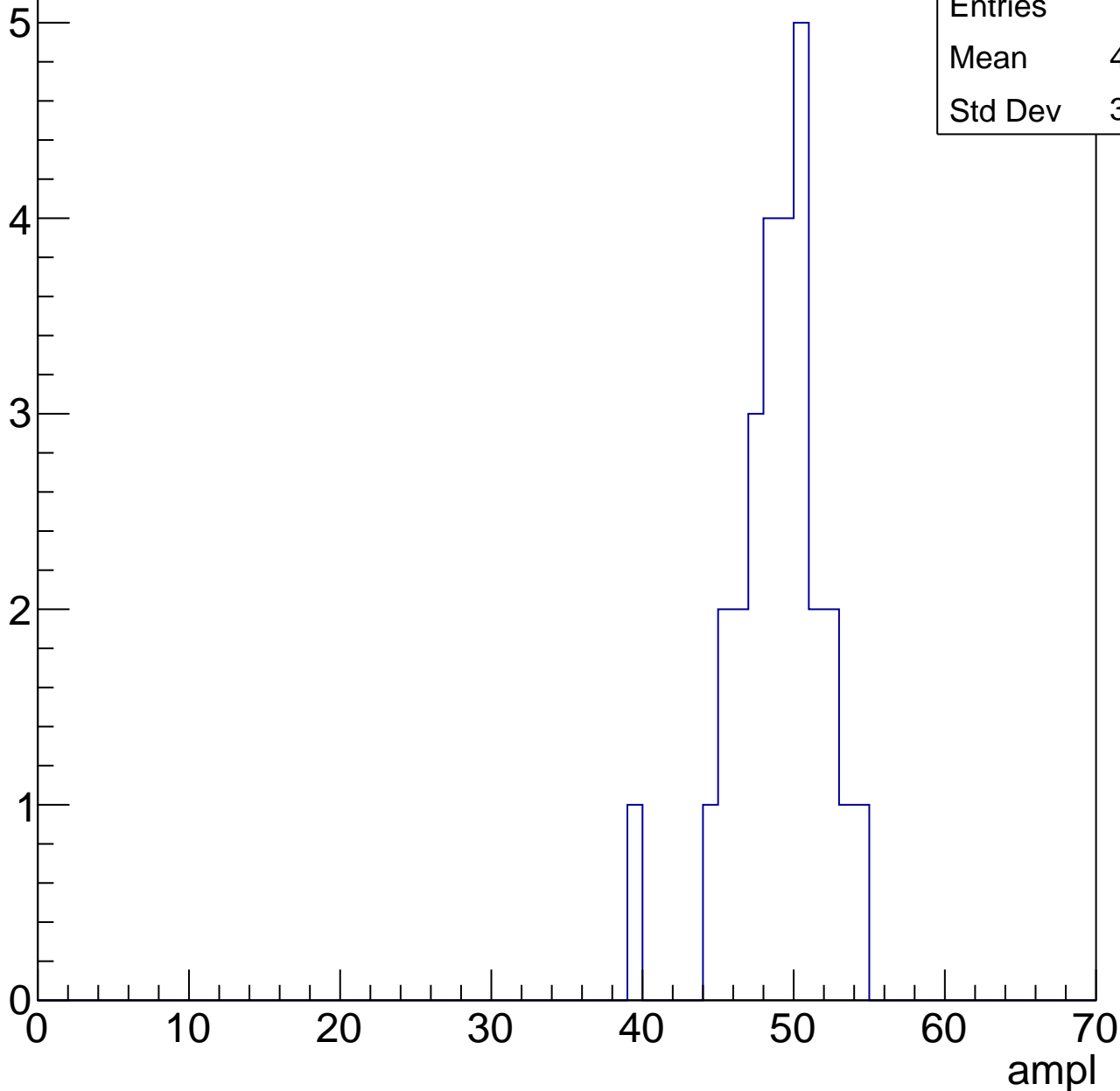


B1L103S, U15-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

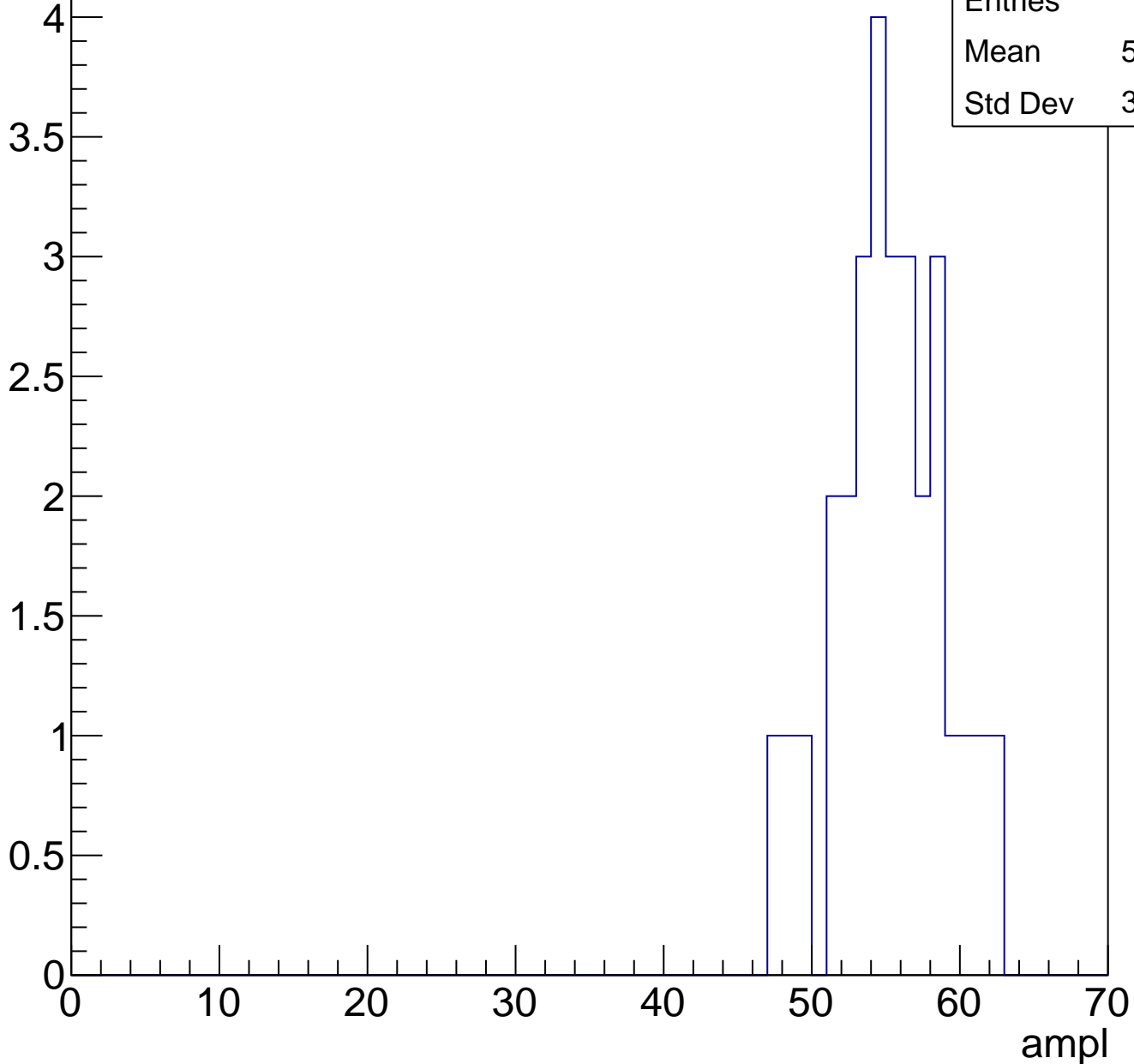
Entries	28
Mean	48.46
Std Dev	3.018



B1L103S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

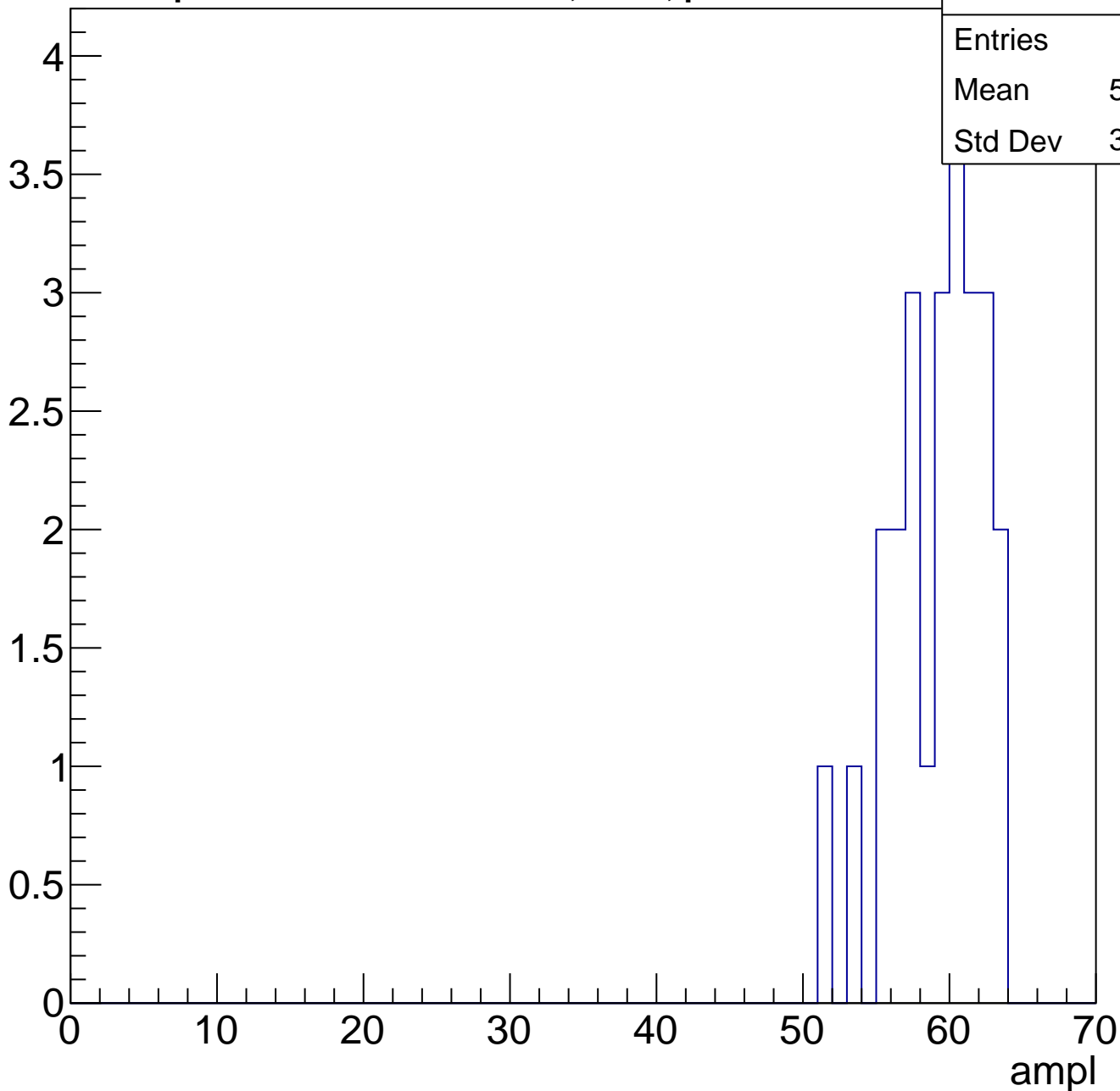


Entries	29
Mean	54.76
Std Dev	3.607

B1L103S, U15-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

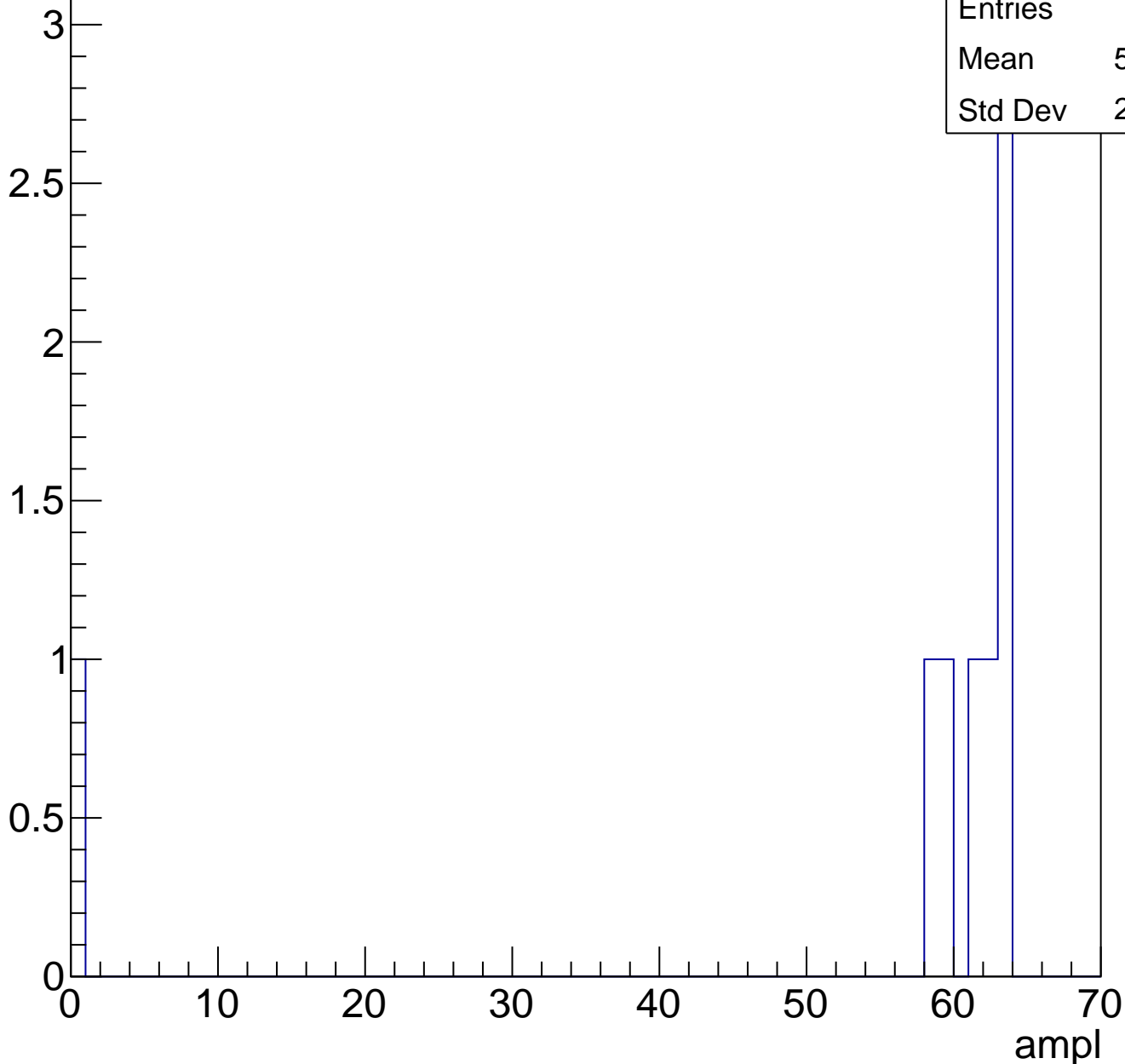
Entry



B1L103S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

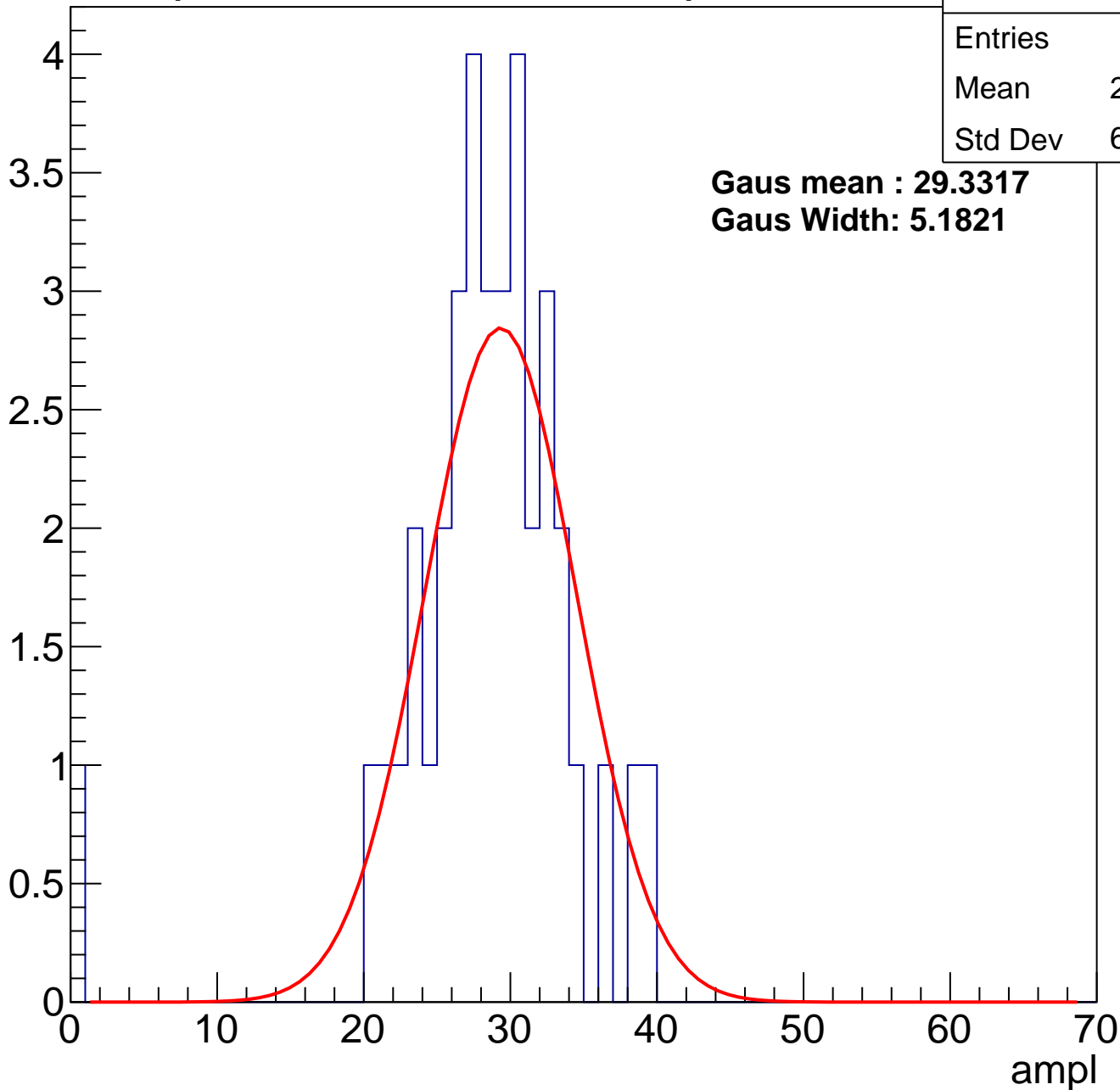


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch74, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	37
Mean	27.86
Std Dev	6.342

Gaus mean : 29.3317
Gaus Width: 5.1821

B1L103S, U15-ch74, adc1

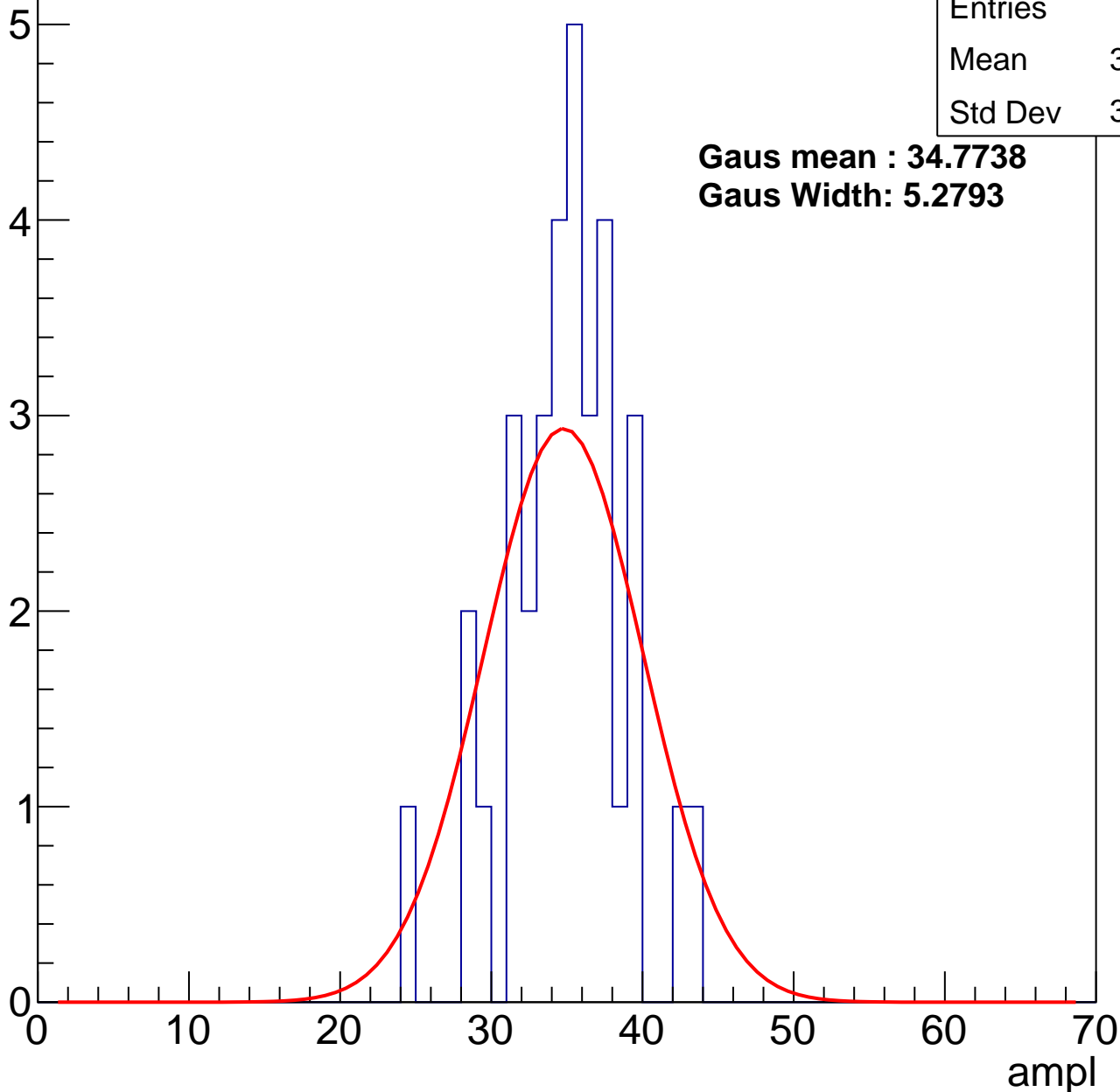
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	34.47
Std Dev	3.897

Gaus mean : 34.7738

Gaus Width: 5.2793



B1L103S, U15-ch74, adc2

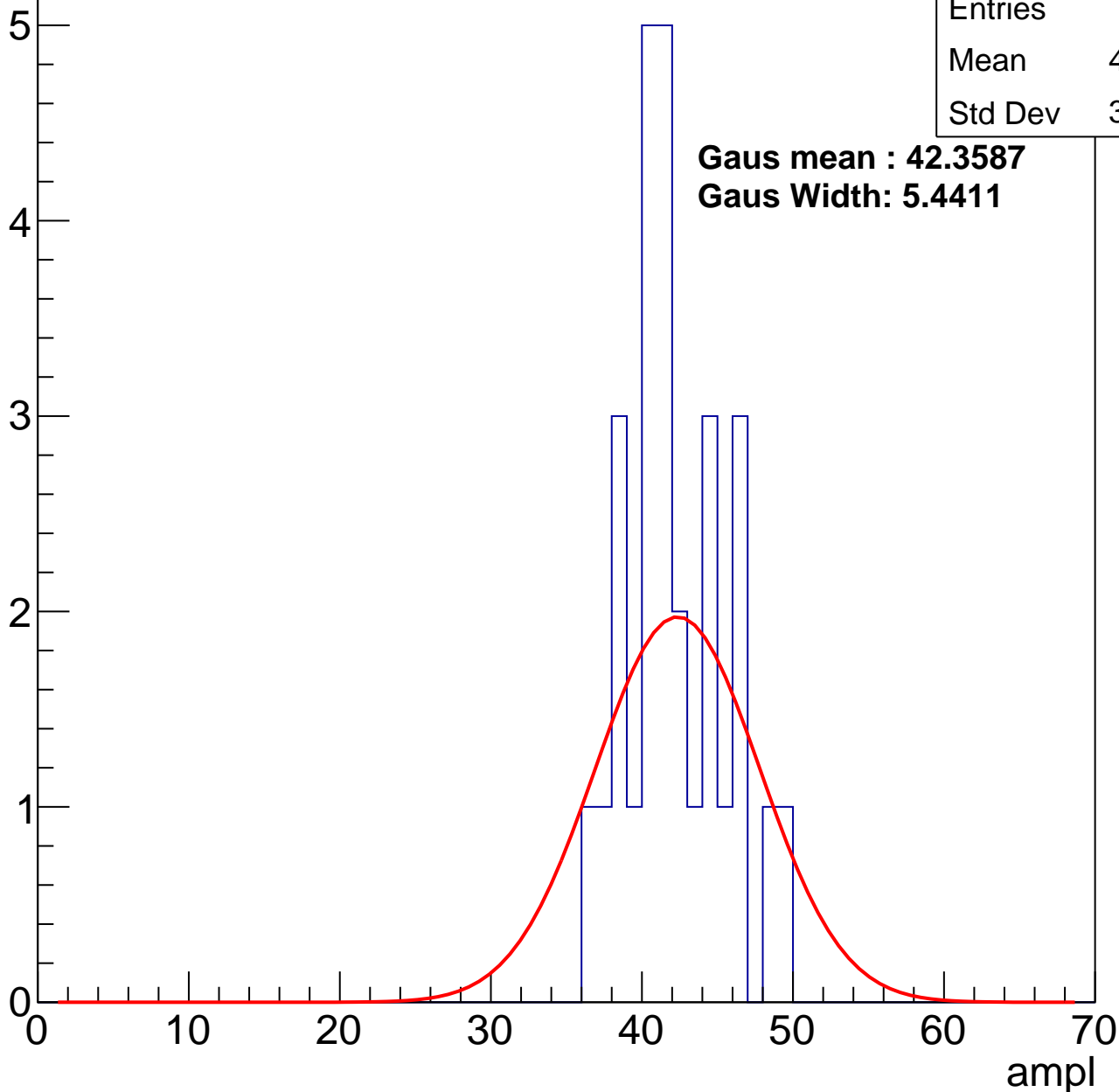
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	41.79
Std Dev	3.255

Gaus mean : 42.3587

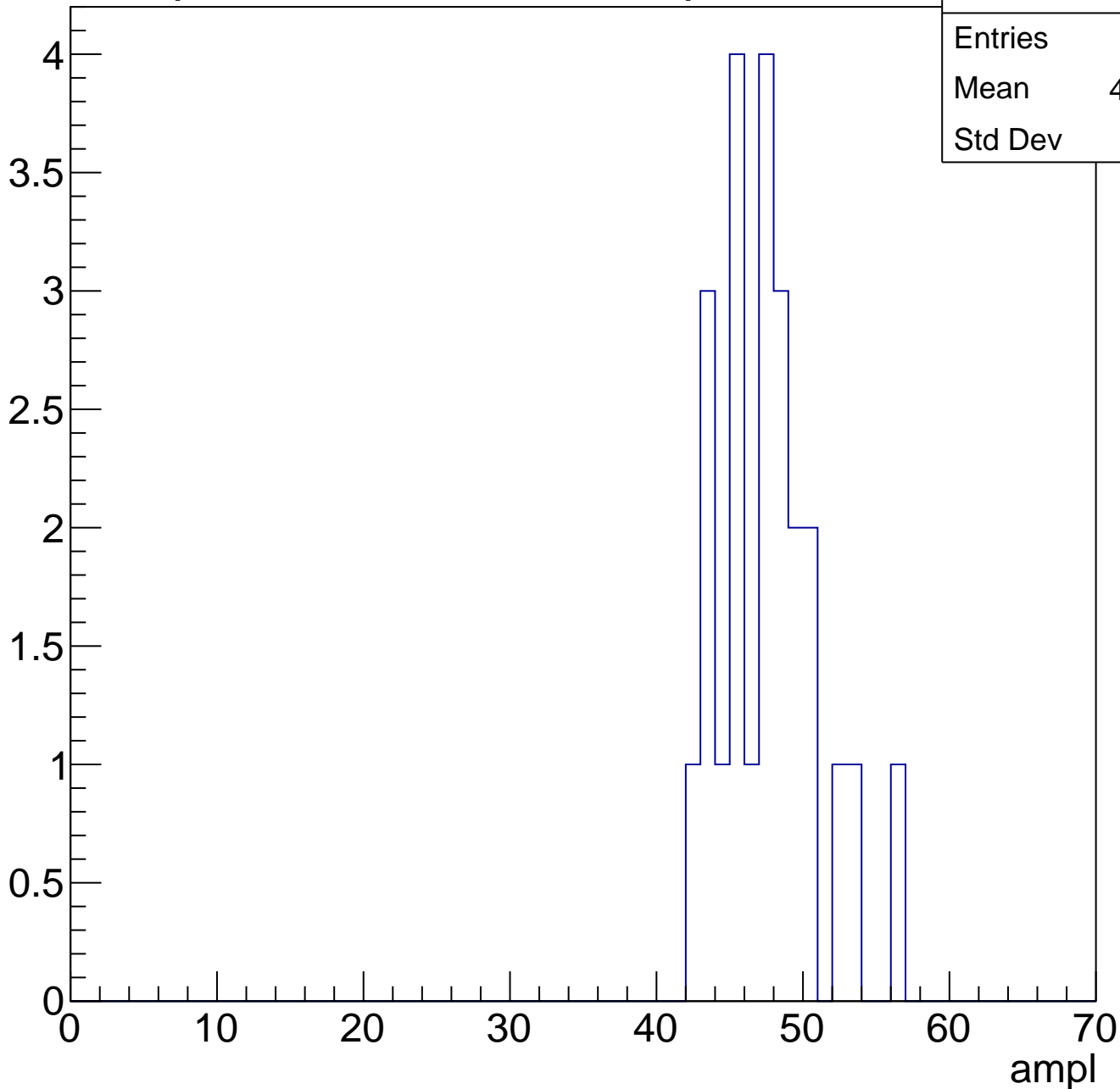
Gaus Width: 5.4411



B1L103S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

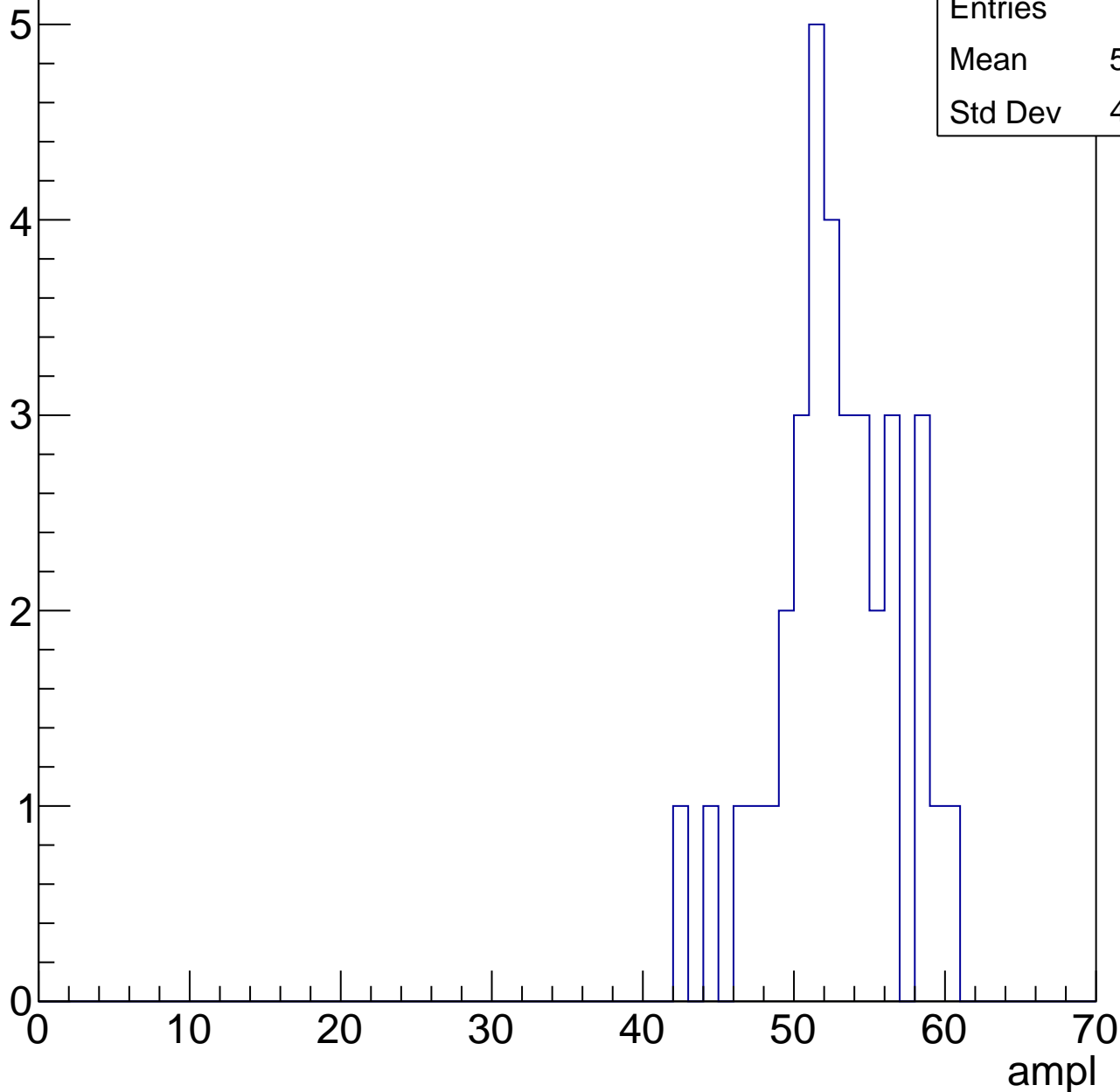


B1L103S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

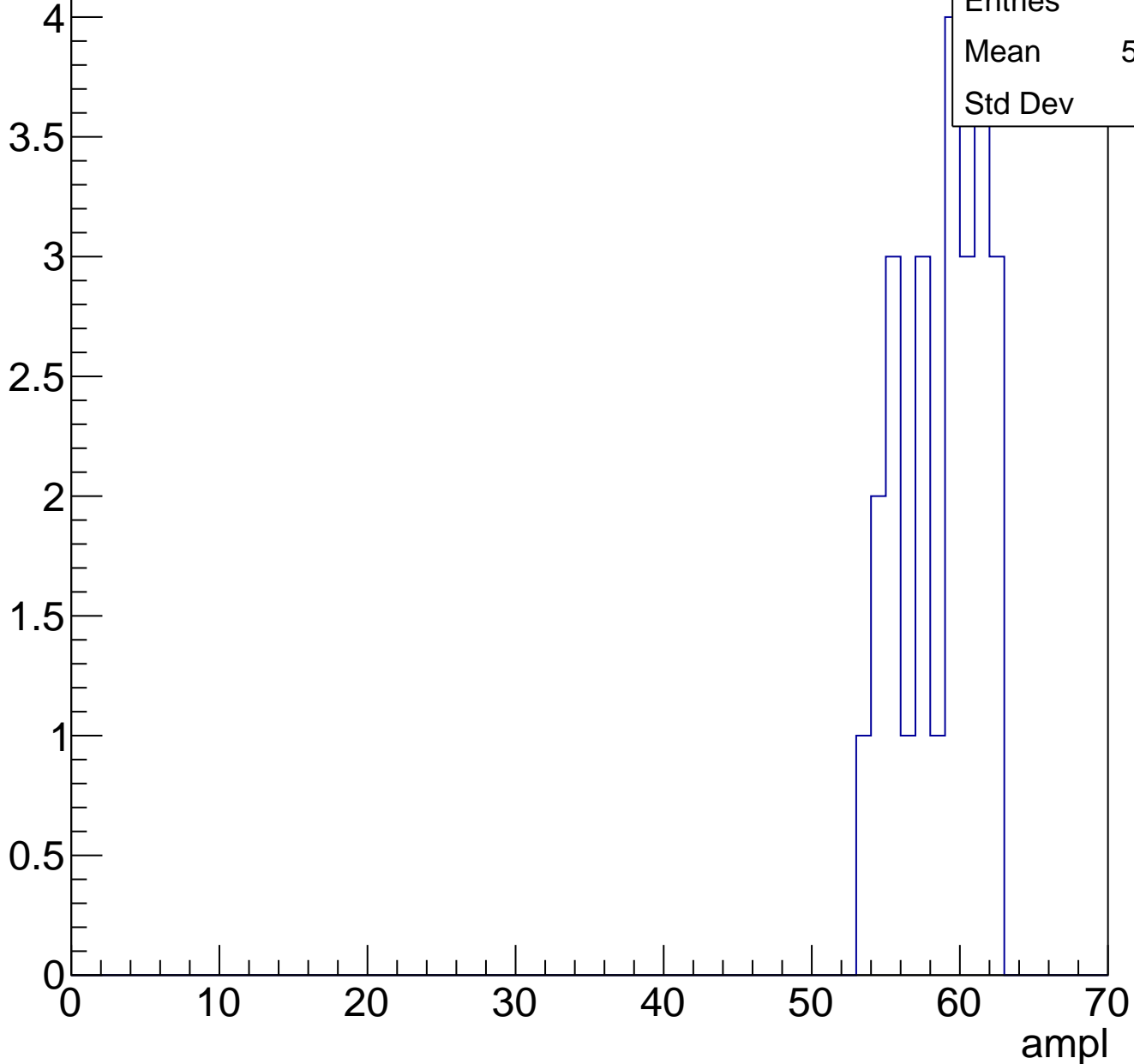
Entries	35
Mean	52.29
Std Dev	4.054



B1L103S, U15-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

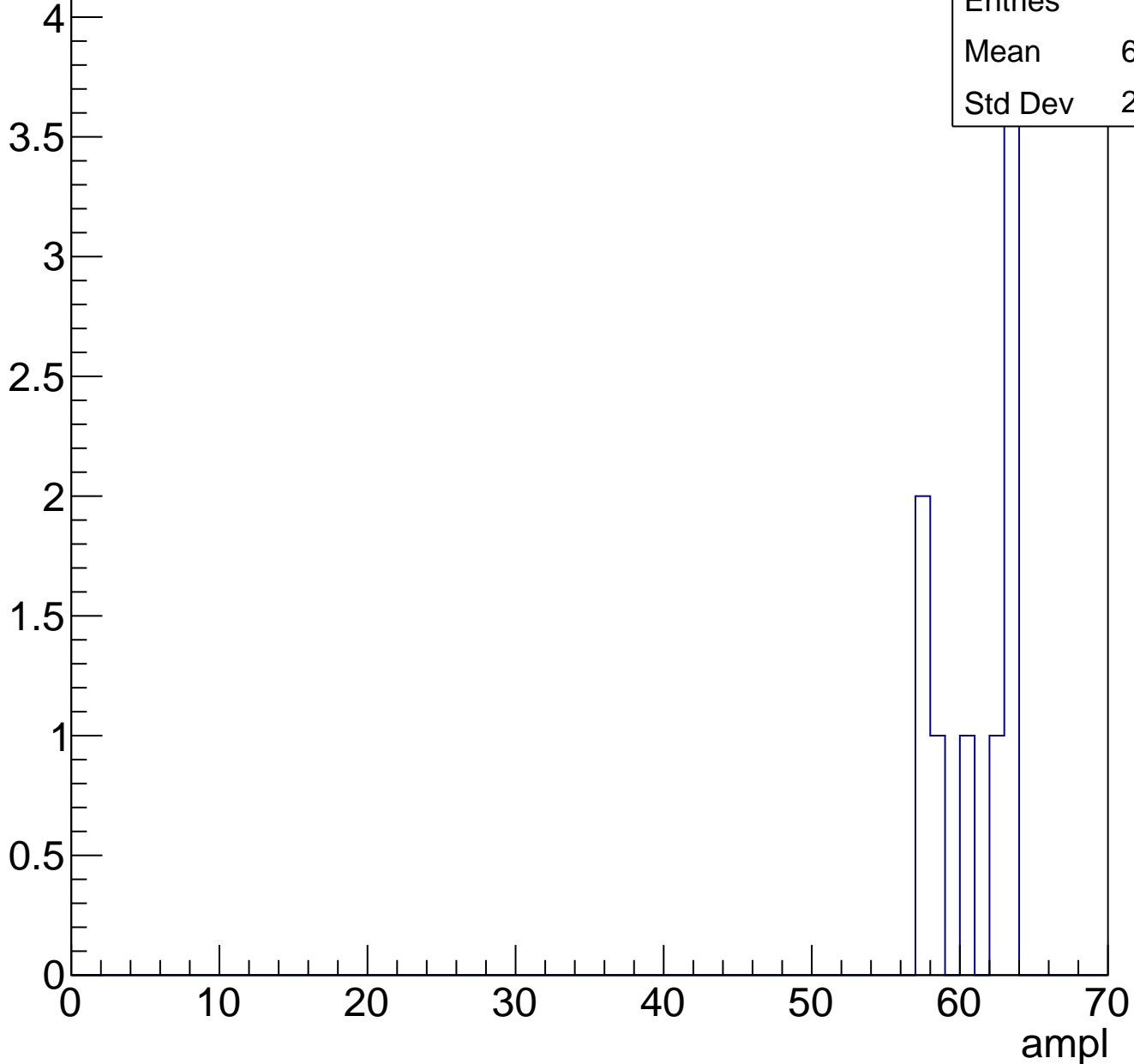


Entries	25
Mean	58.28
Std Dev	2.75

B1L103S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

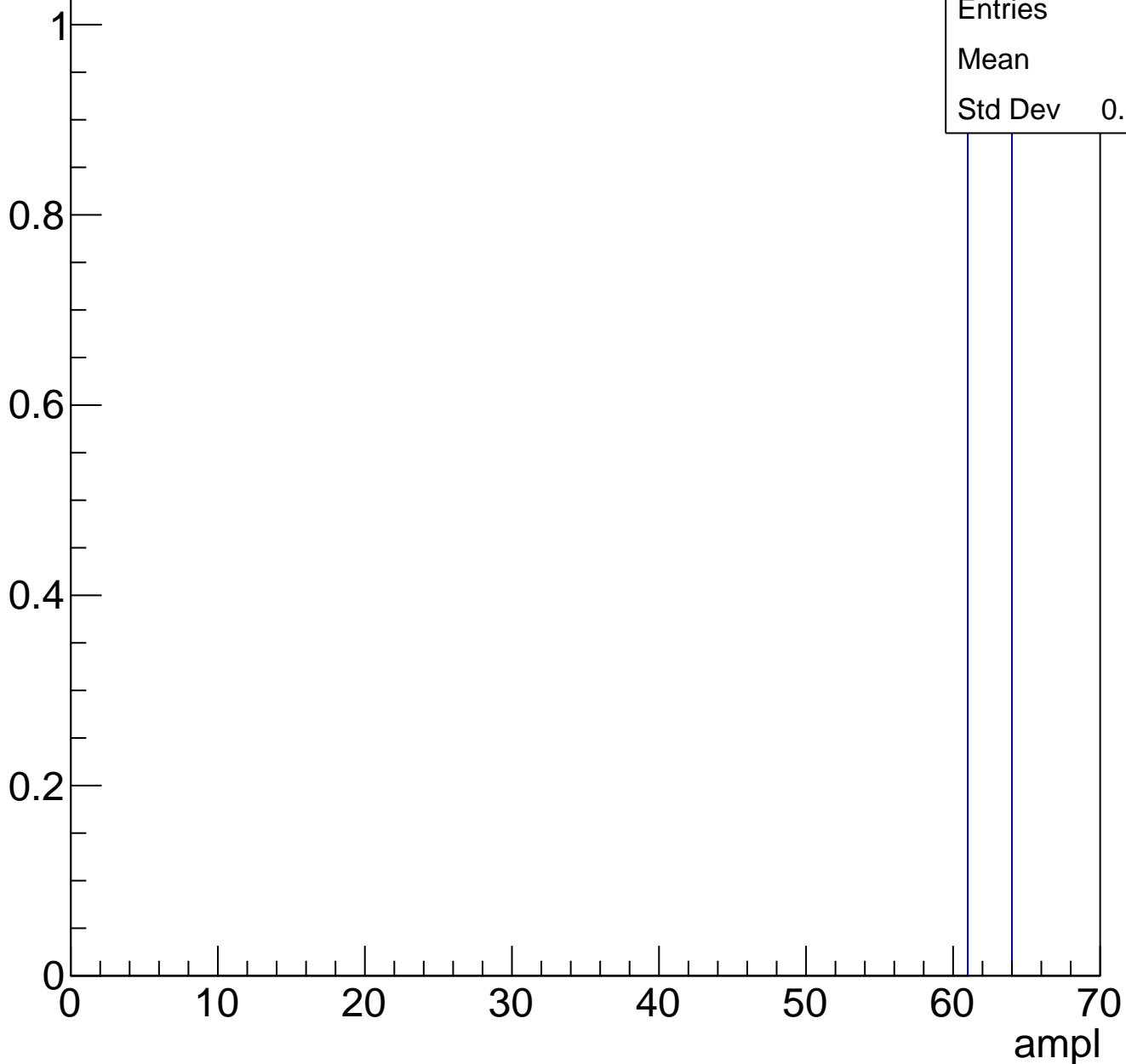


Entries	9
Mean	60.67
Std Dev	2.539

B1L103S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

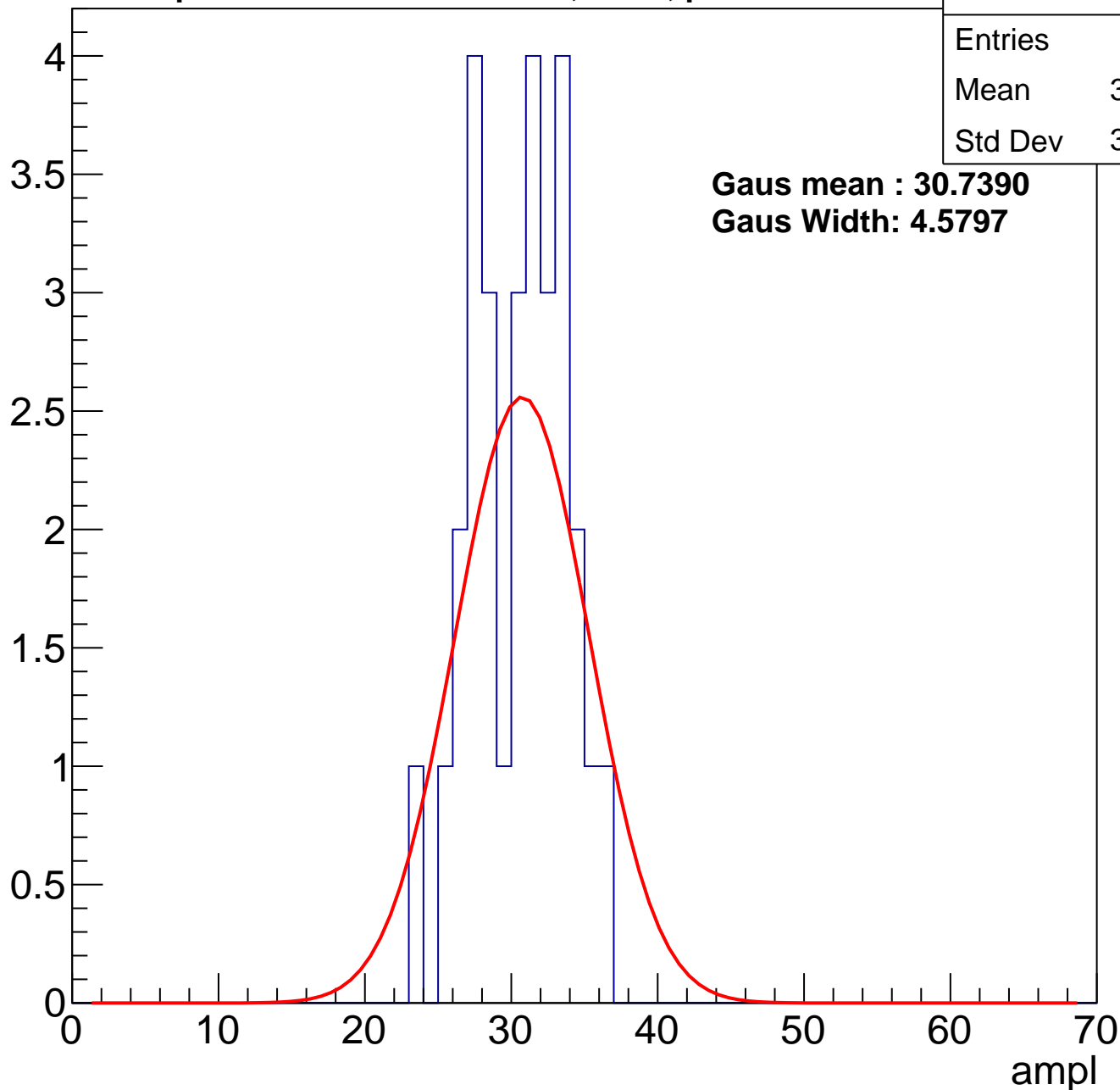
Entry



B1L103S, U15-ch75, adc0

calib_packv5_042523_0143.root, FC#7, port C2

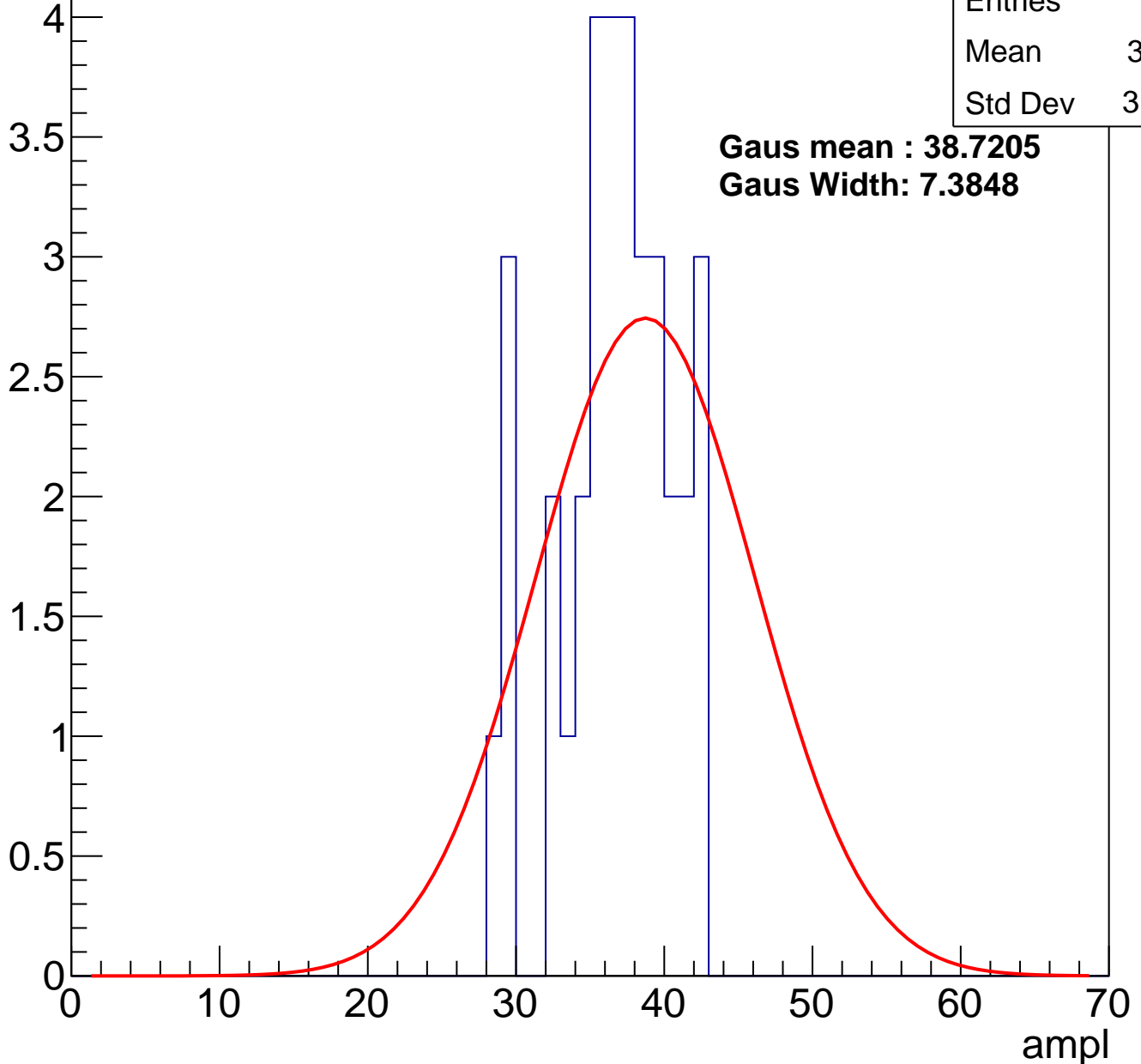
Entry



B1L103S, U15-ch75, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch75, adc2

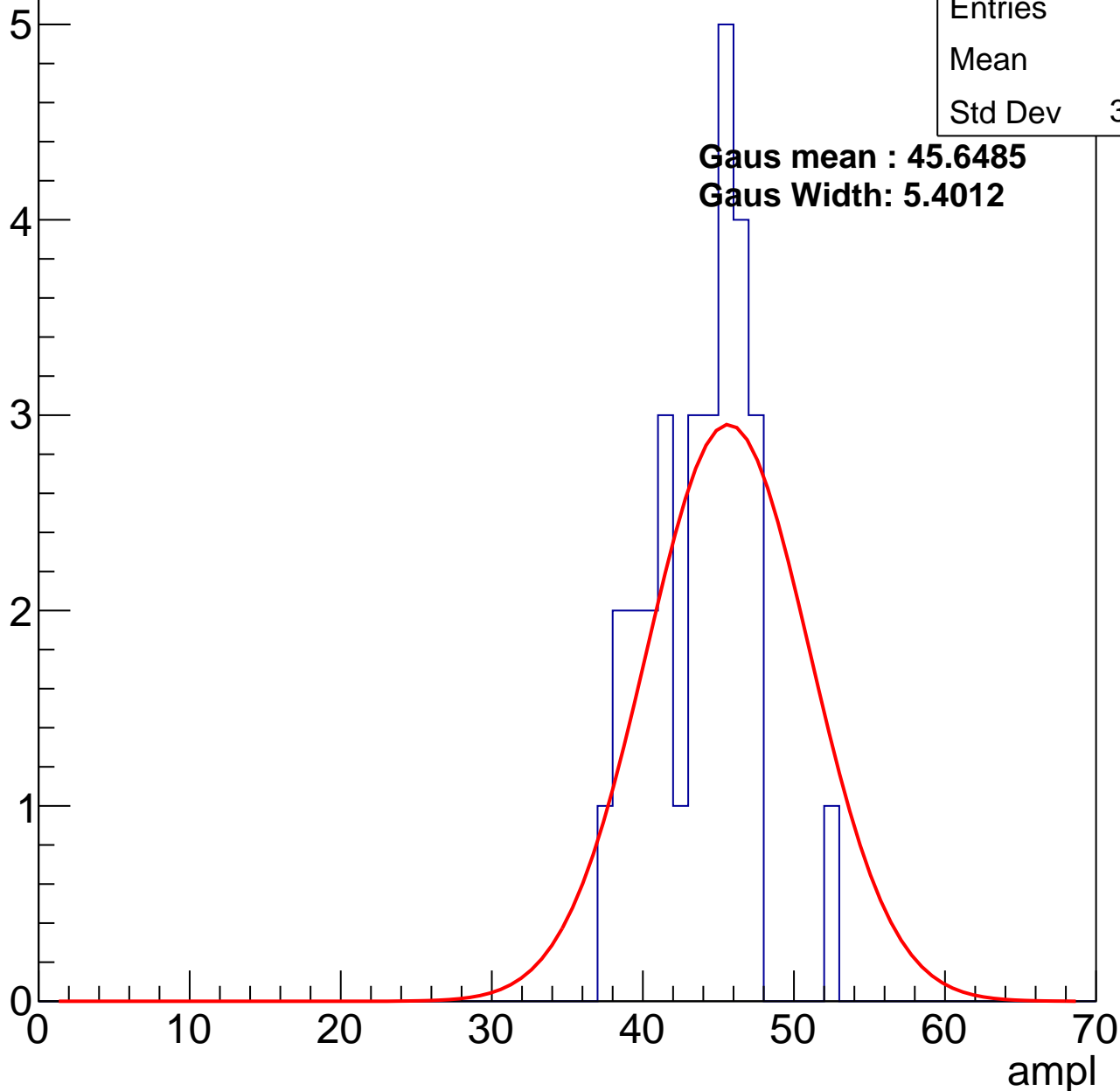
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	43.3
Std Dev	3.338

Gaus mean : 45.6485

Gaus Width: 5.4012

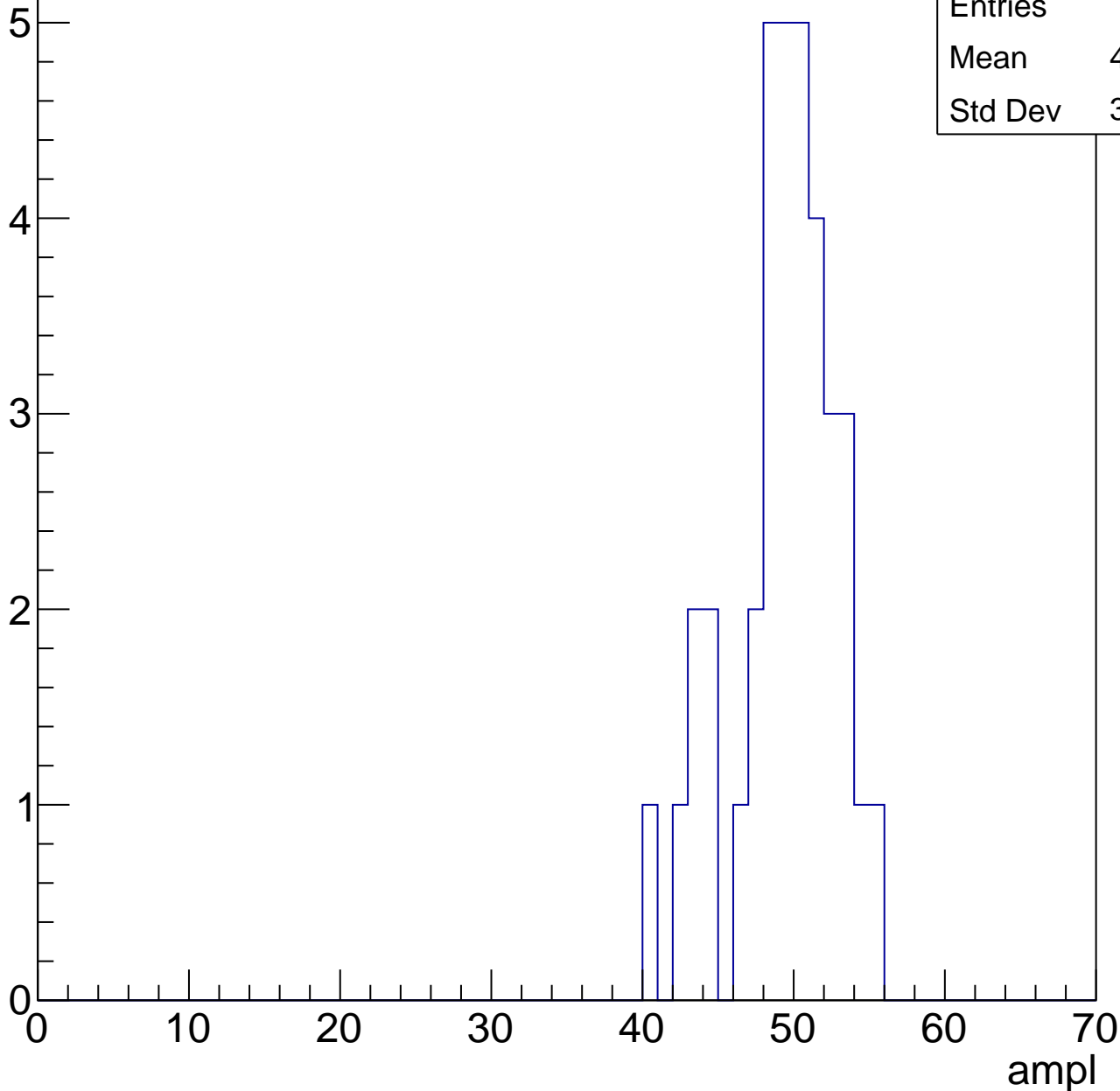


B1L103S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

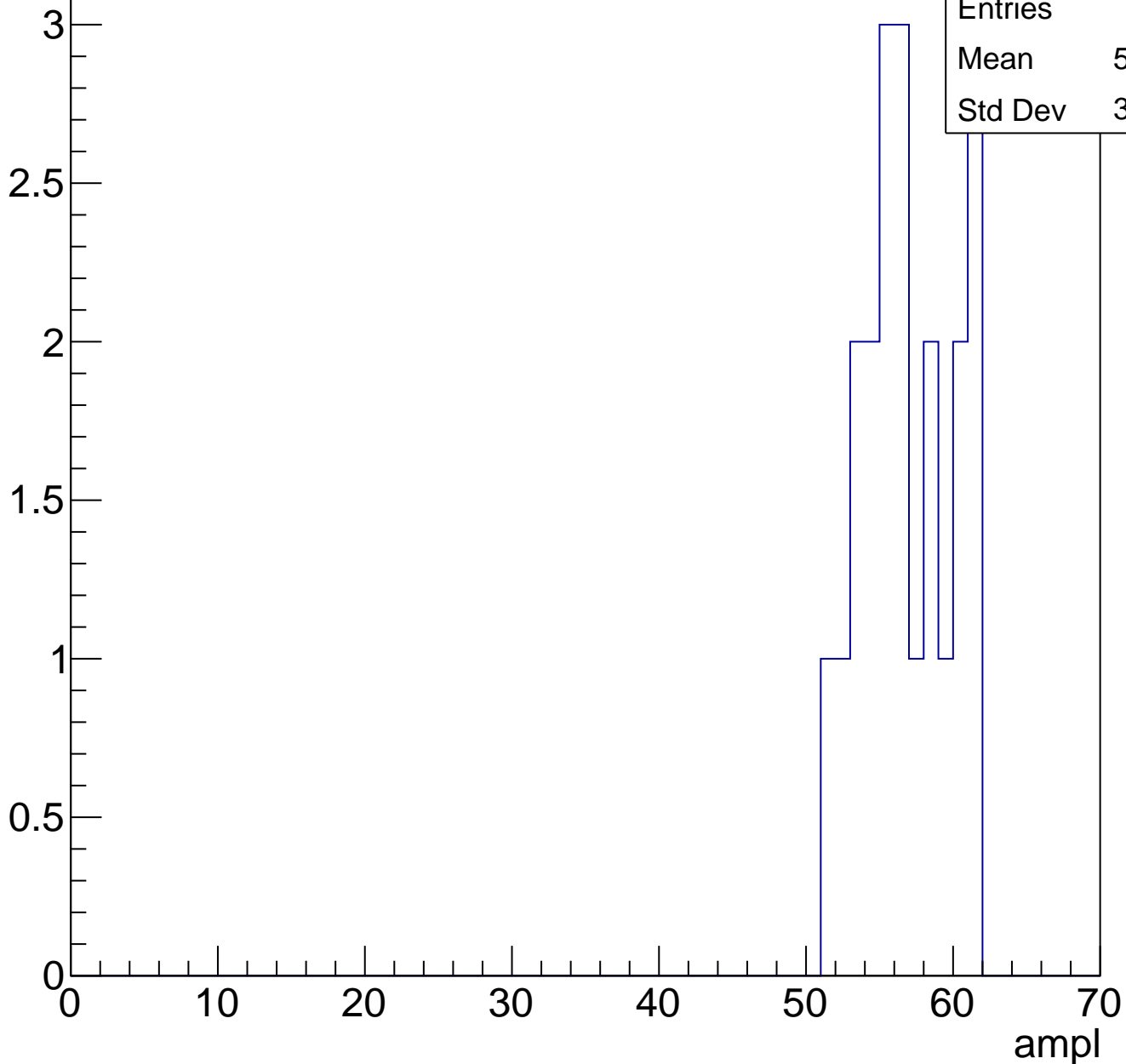
Entries	36
Mean	48.86
Std Dev	3.457



B1L103S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch75, adc5

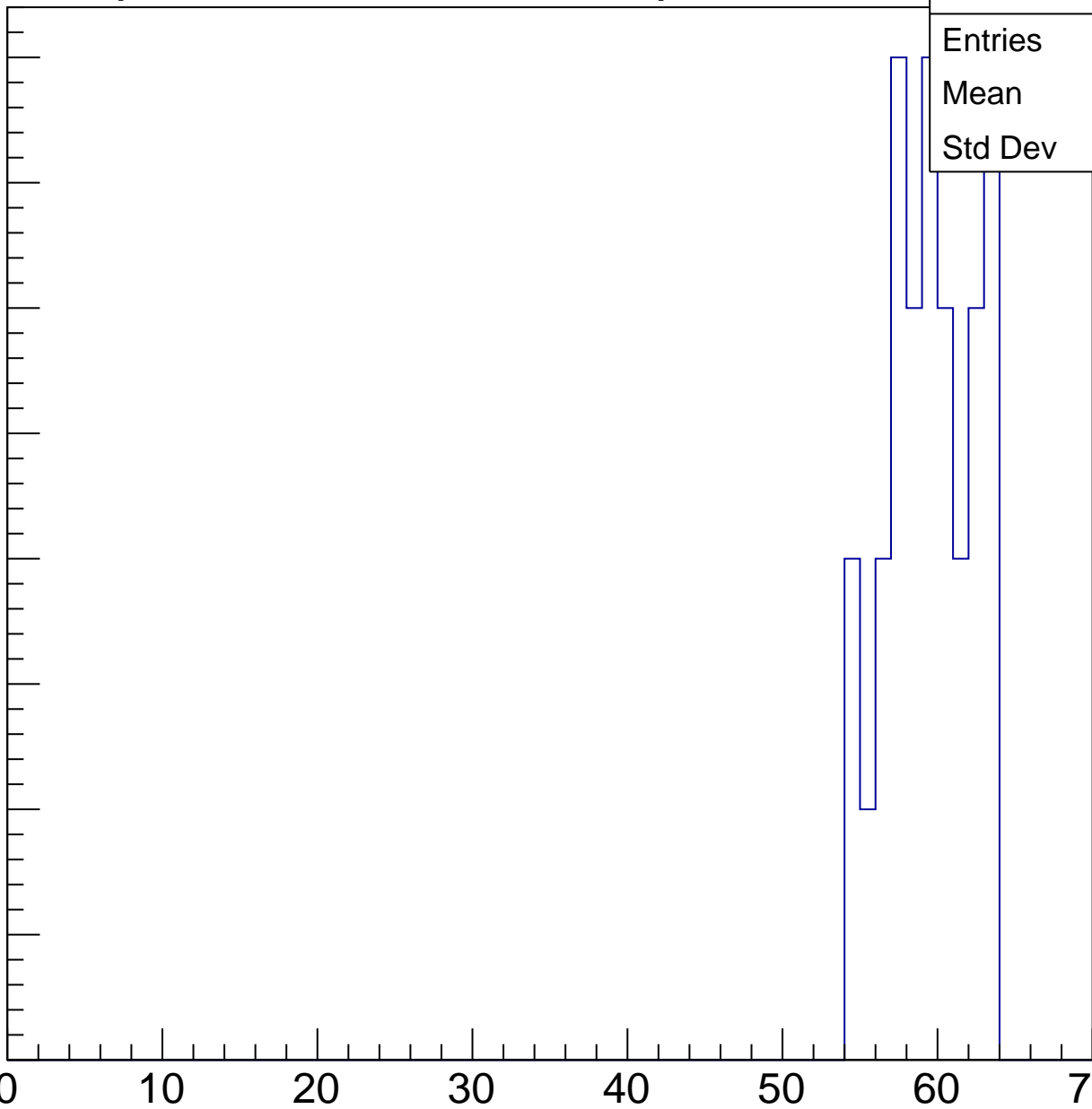
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	28
Mean	59.04
Std Dev	2.706

ampl



B1L103S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch76, adc0

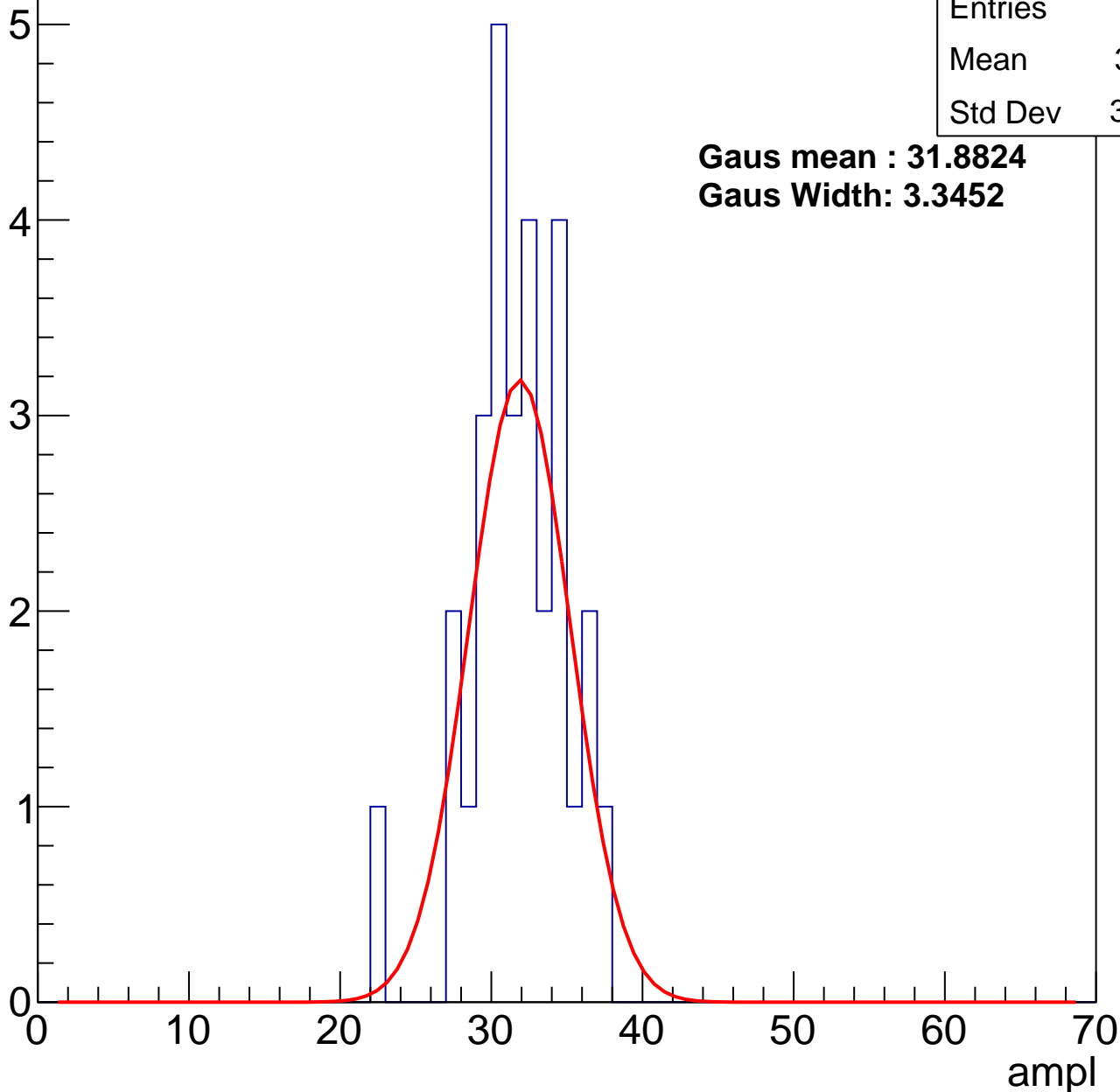
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	31.31
Std Dev	3.142

Gaus mean : 31.8824

Gaus Width: 3.3452



B1L103S, U15-ch76, adc1

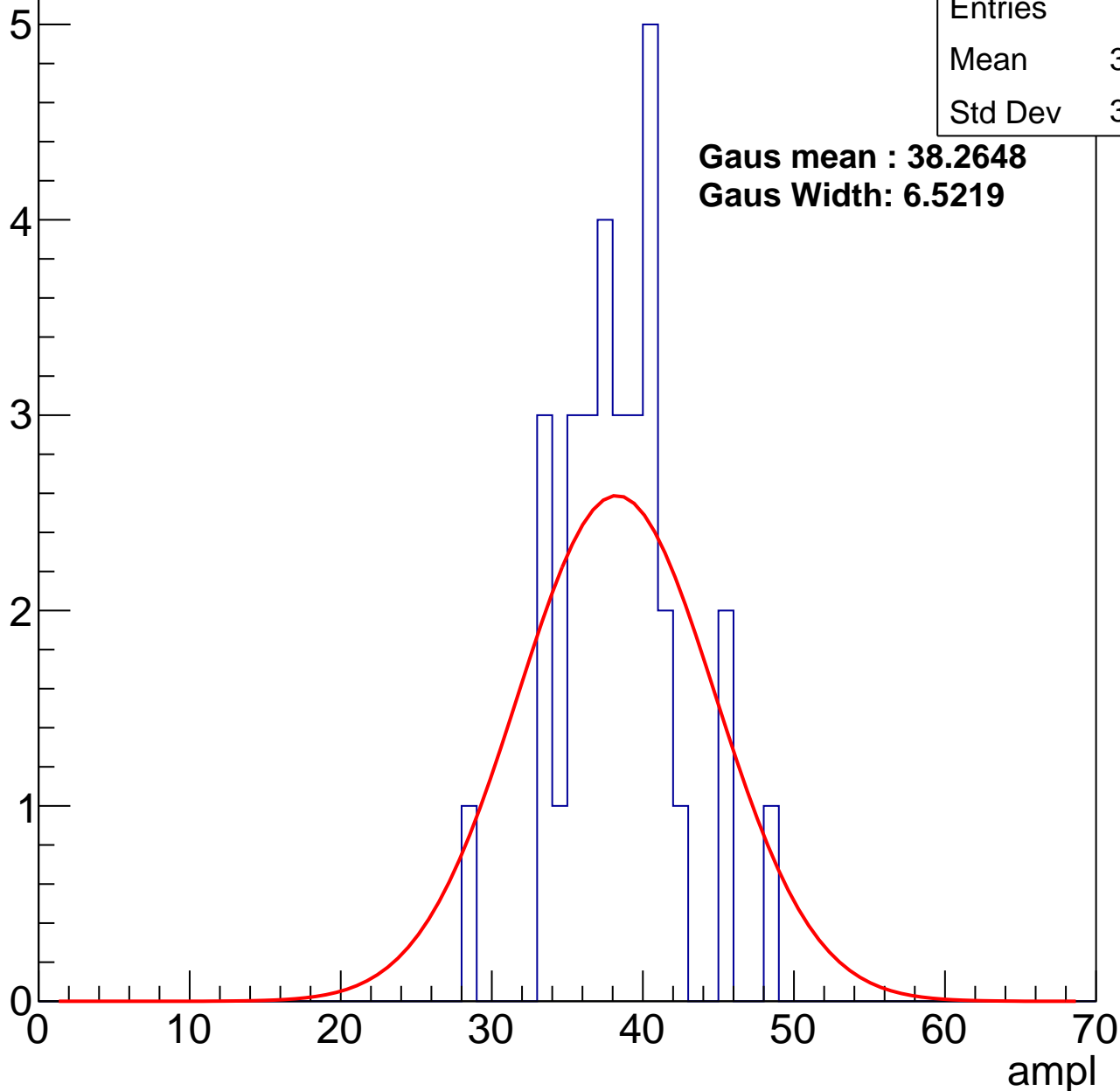
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	37.97
Std Dev	3.909

Gaus mean : 38.2648

Gaus Width: 6.5219



B1L103S, U15-ch76, adc2

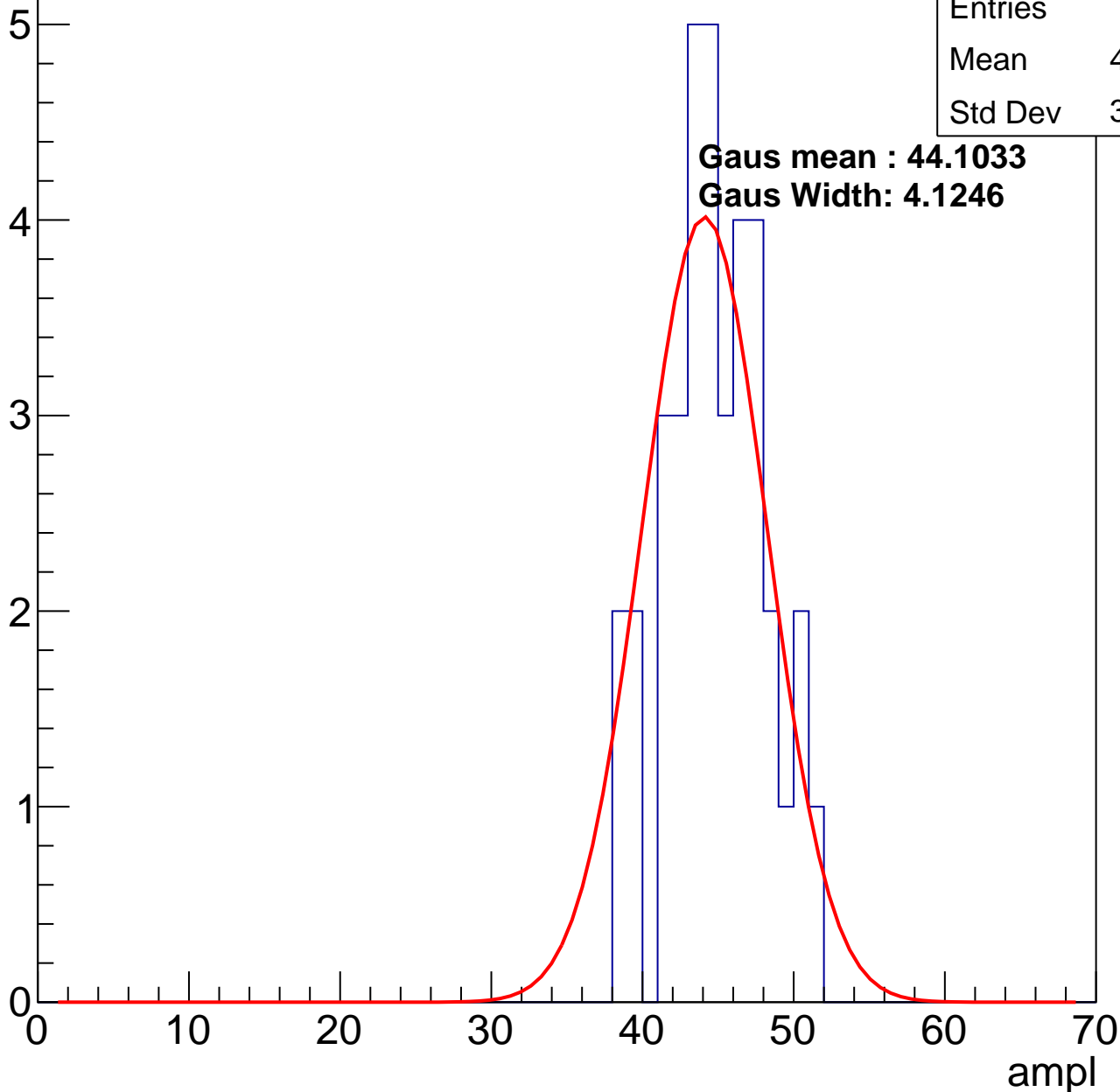
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	44.35
Std Dev	3.265

Gaus mean : 44.1033

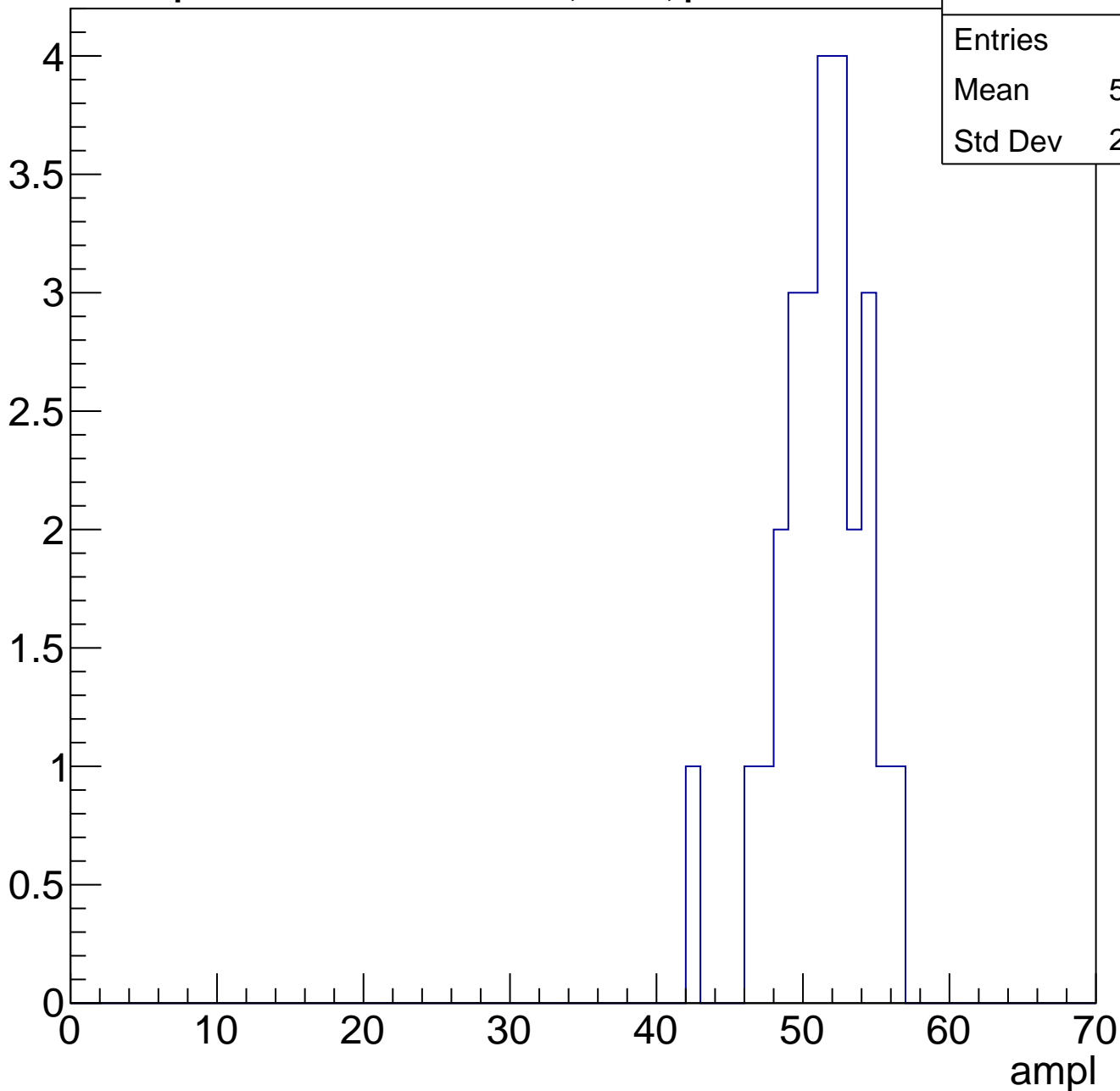
Gaus Width: 4.1246



B1L103S, U15-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

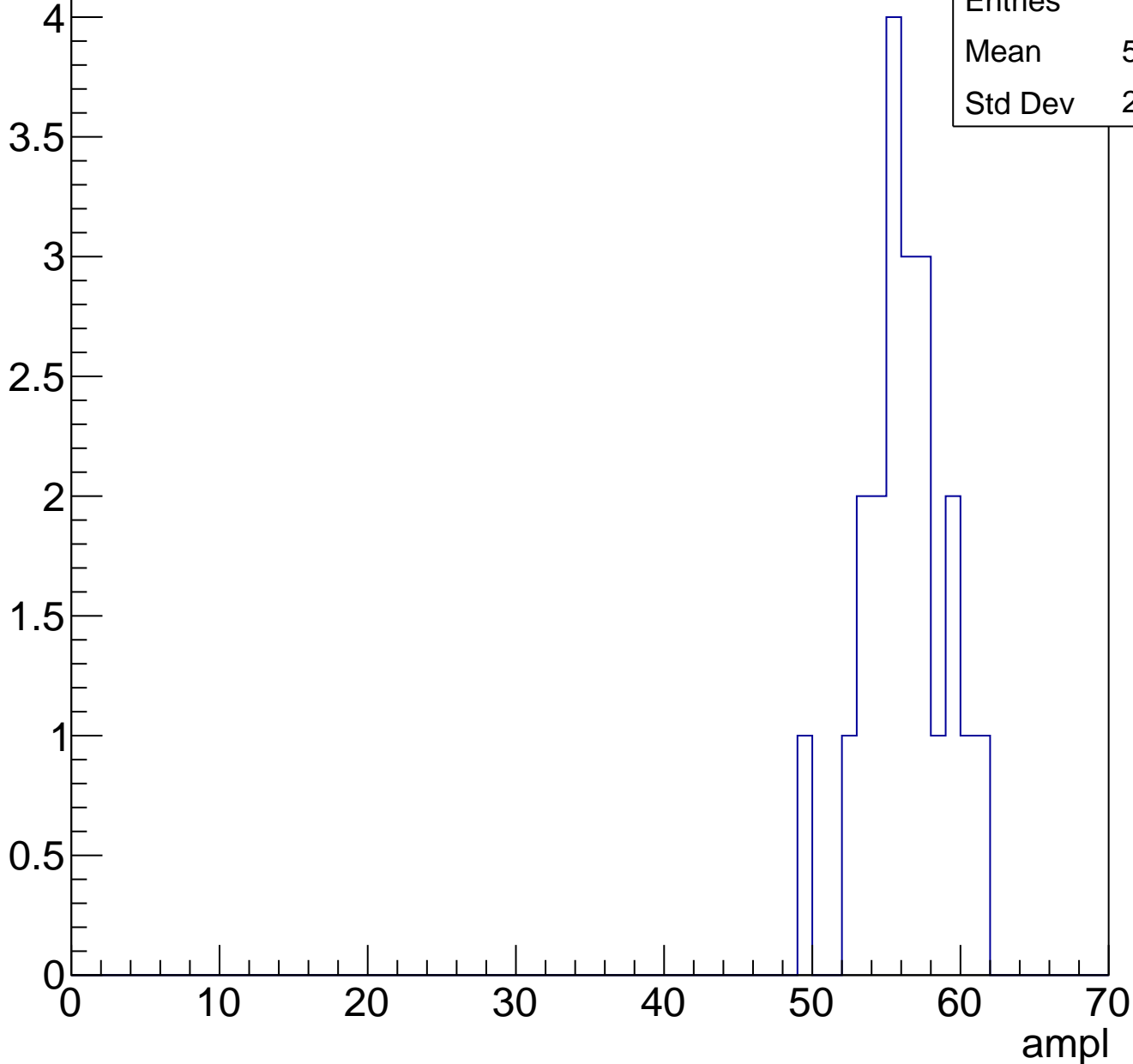


Entries	26
Mean	50.73
Std Dev	2.994

B1L103S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

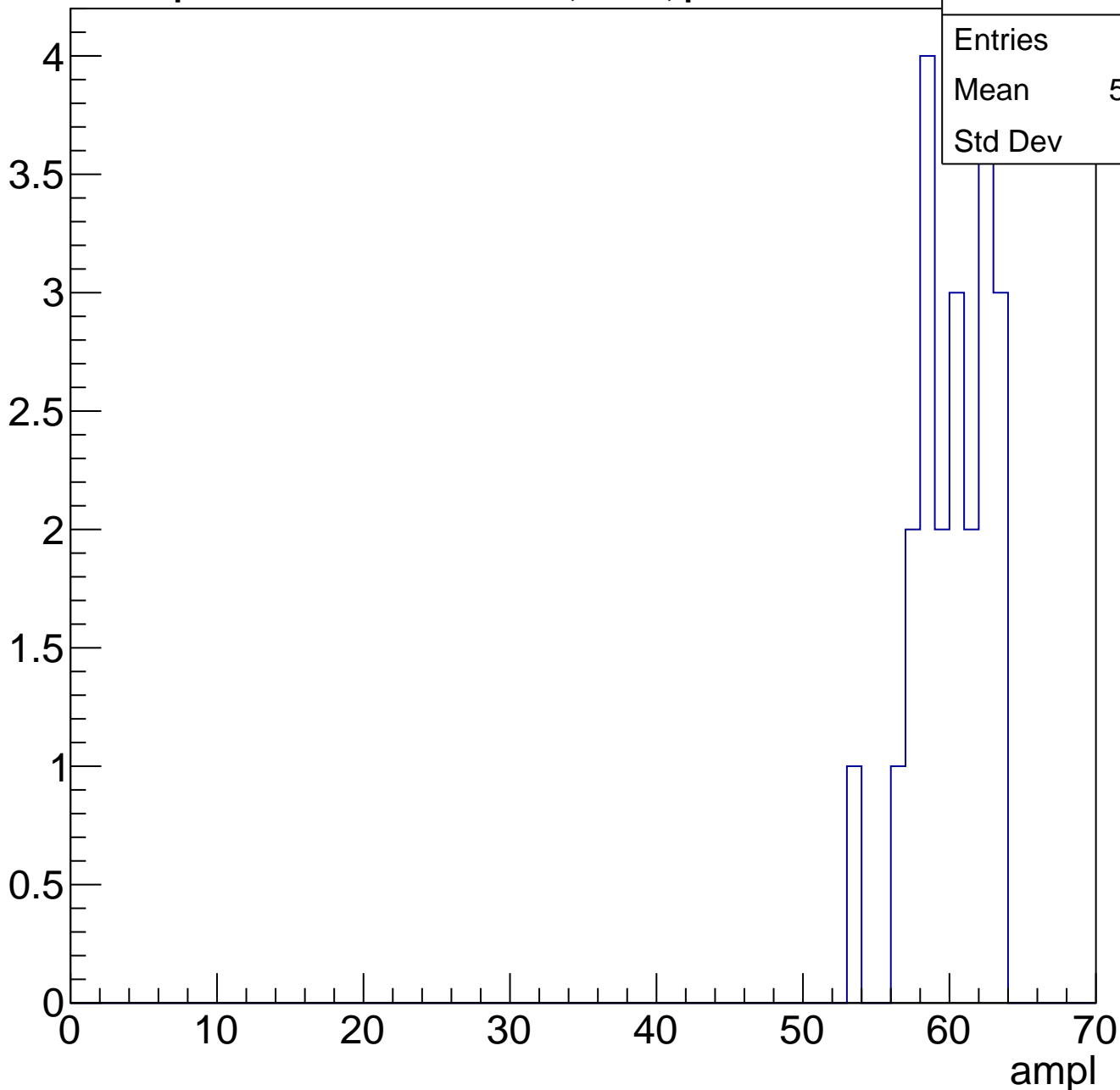
Entry



B1L103S, U15-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

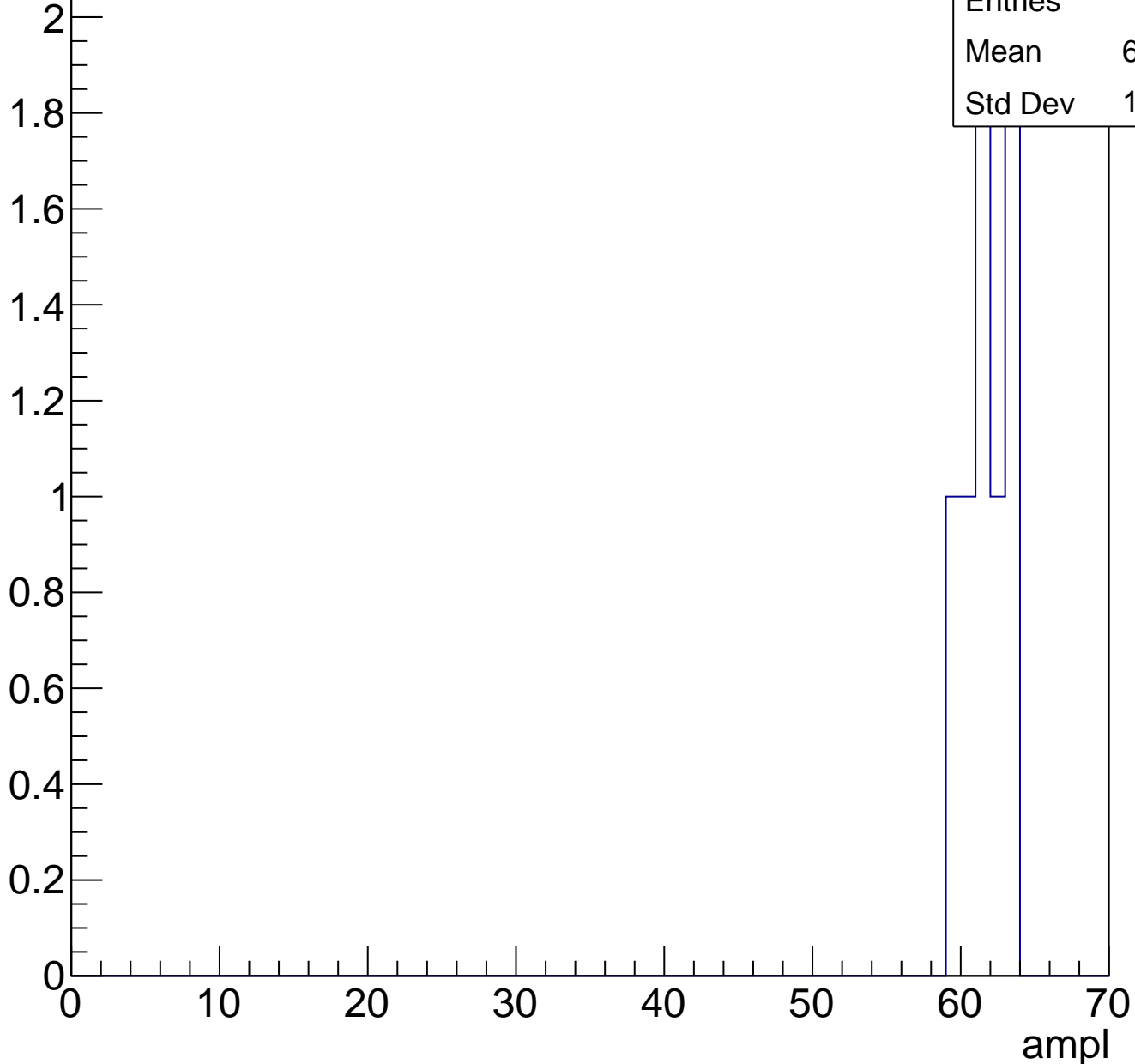
Entry



B1L103S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

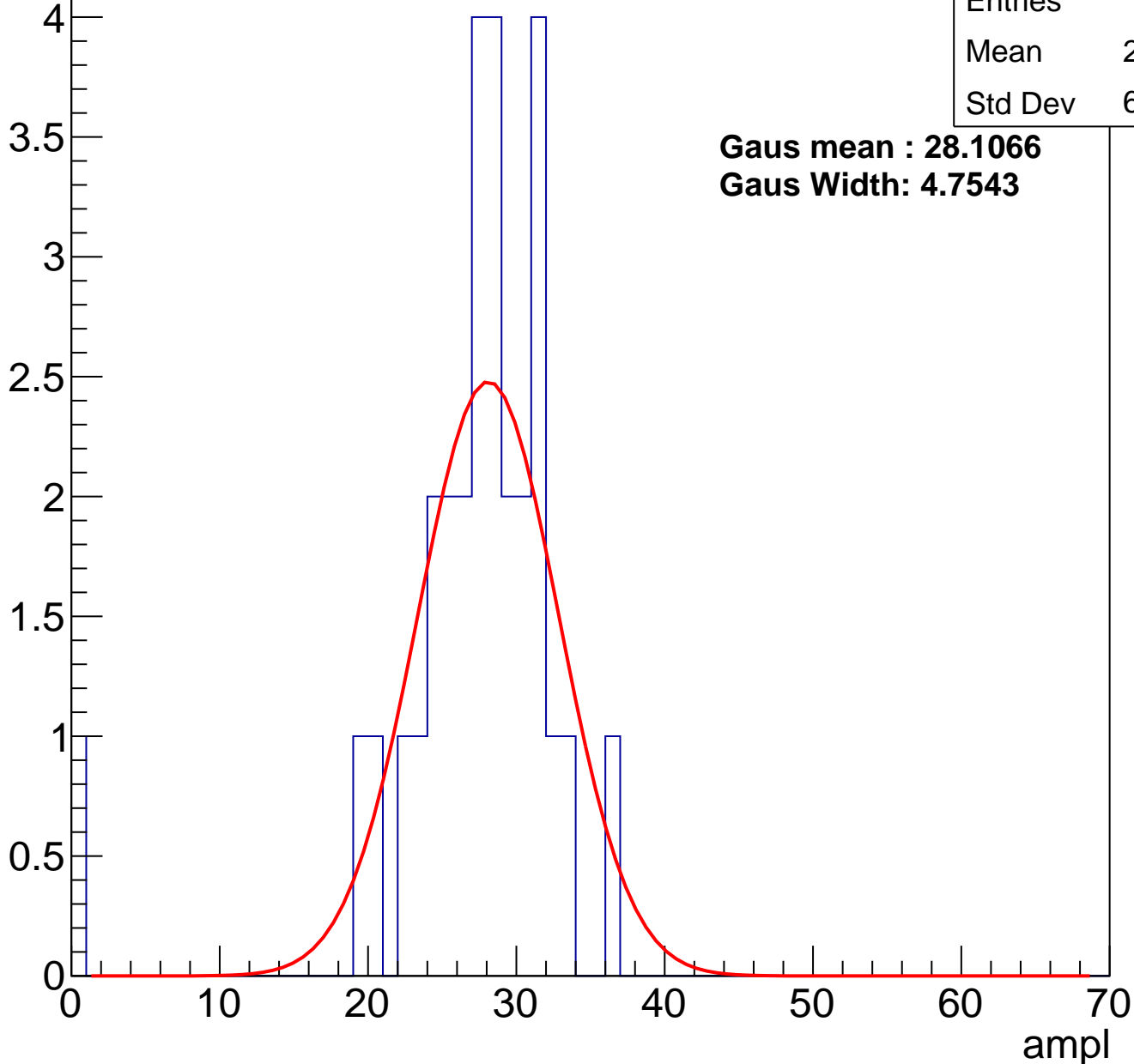


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch77, adc0

calib_packv5_042523_0143.root, FC#7, port C2

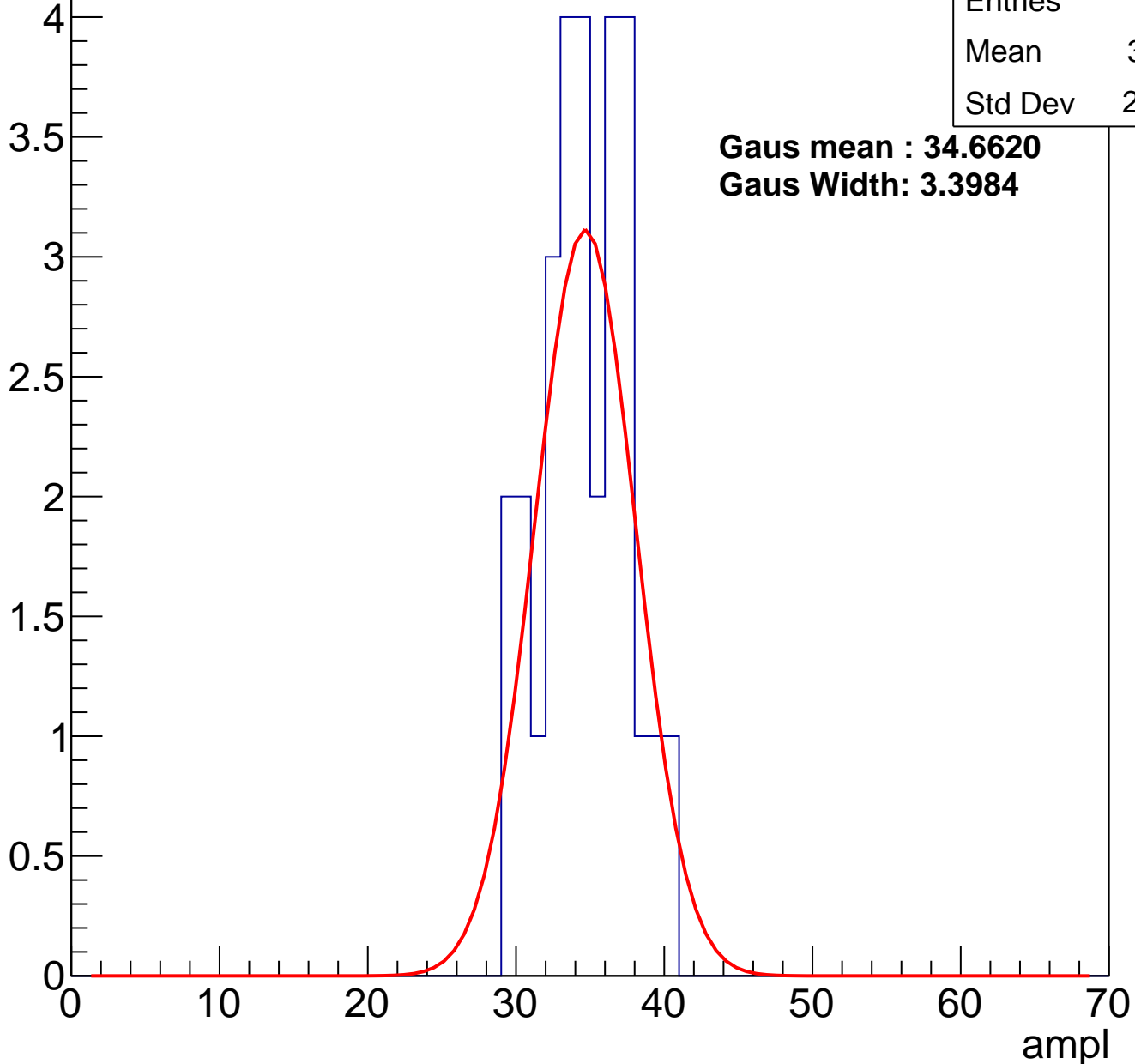
Entry



B1L103S, U15-ch77, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	29
Mean	34.21
Std Dev	2.857

B1L103S, U15-ch77, adc2

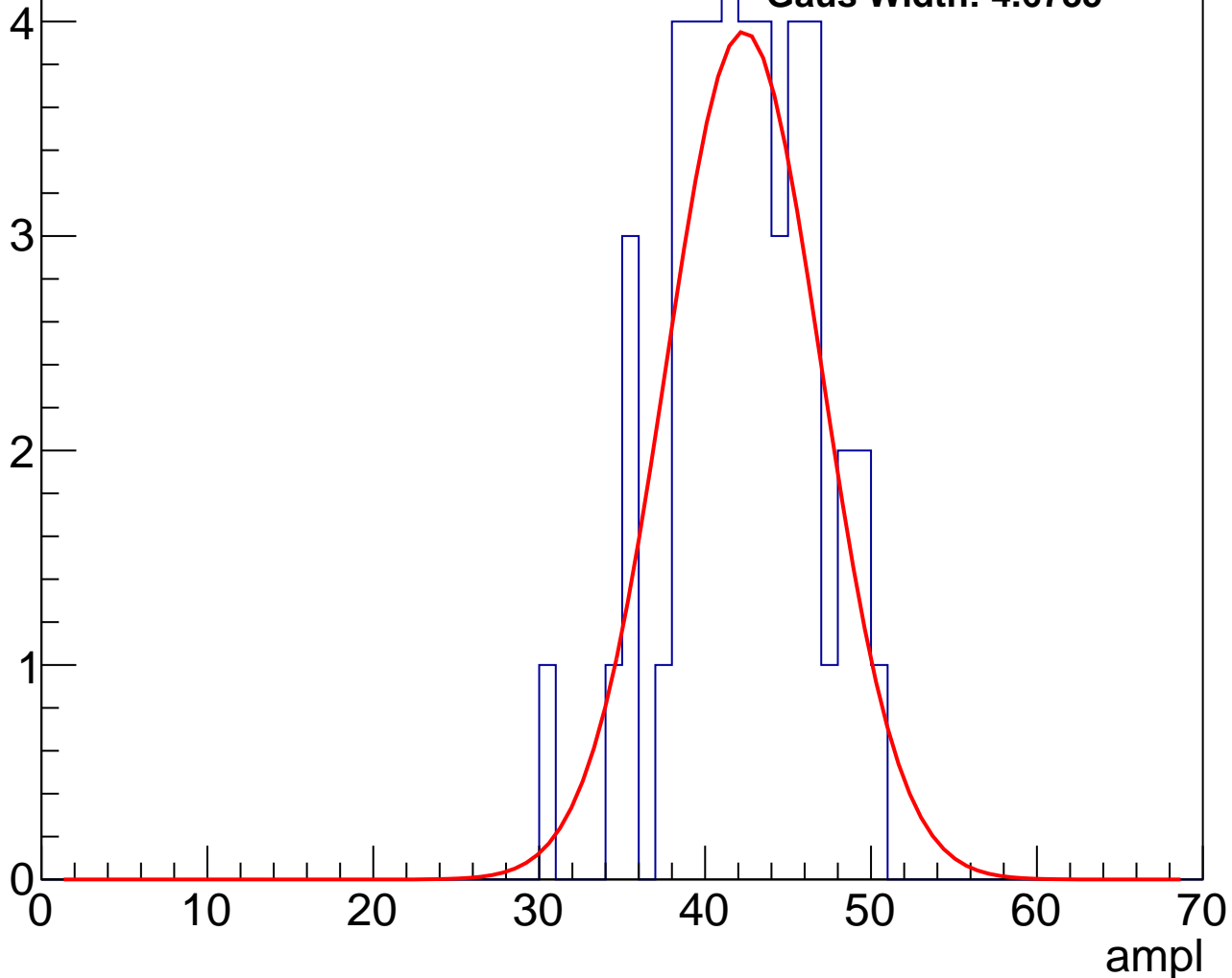
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	41.79
Std Dev	4.267

Gaus mean : 42.3262

Gaus Width: 4.6735



B1L103S, U15-ch77, adc3

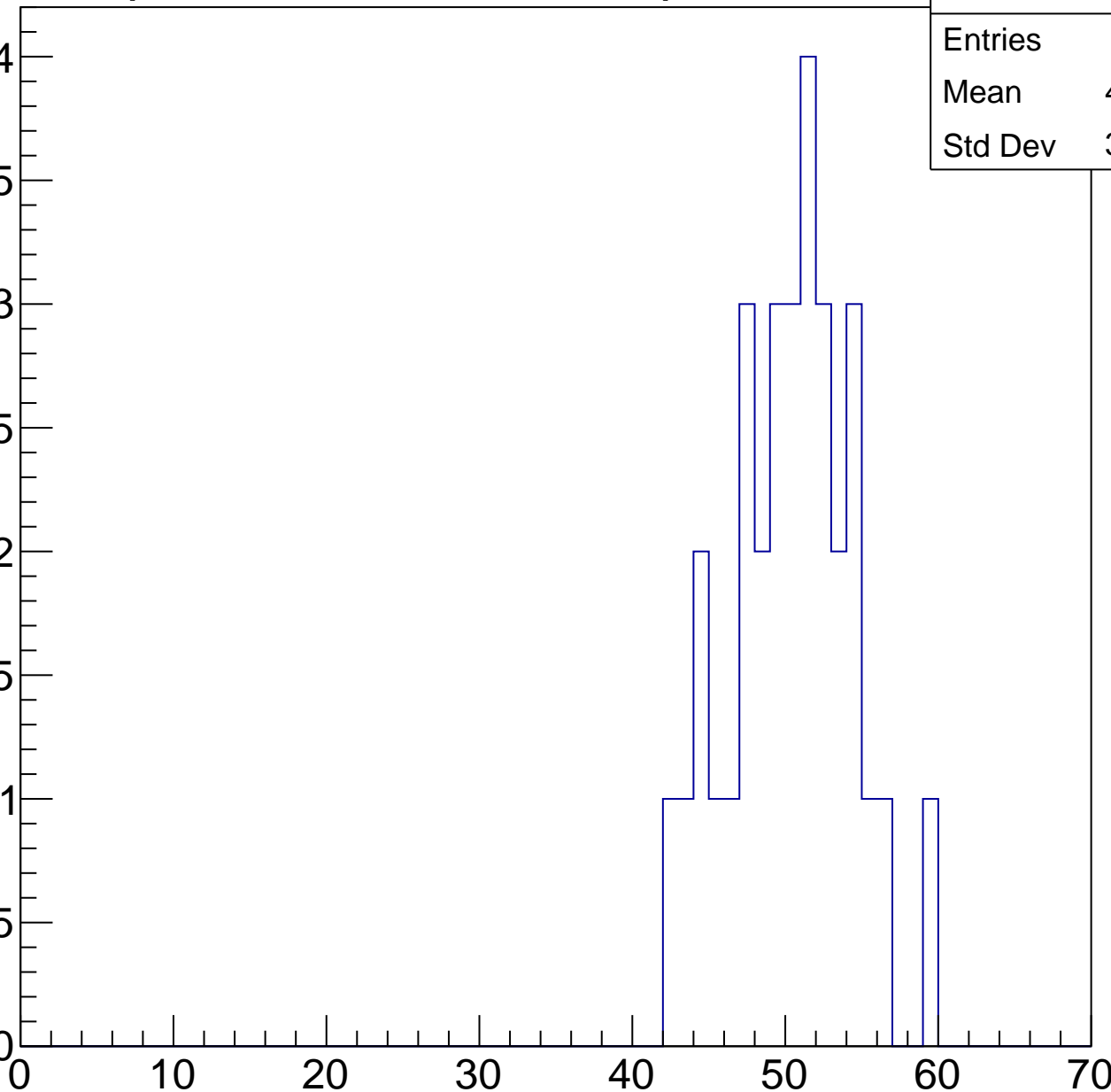
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	32
Mean	49.88
Std Dev	3.903

ampl



B1L103S, U15-ch77, adc4

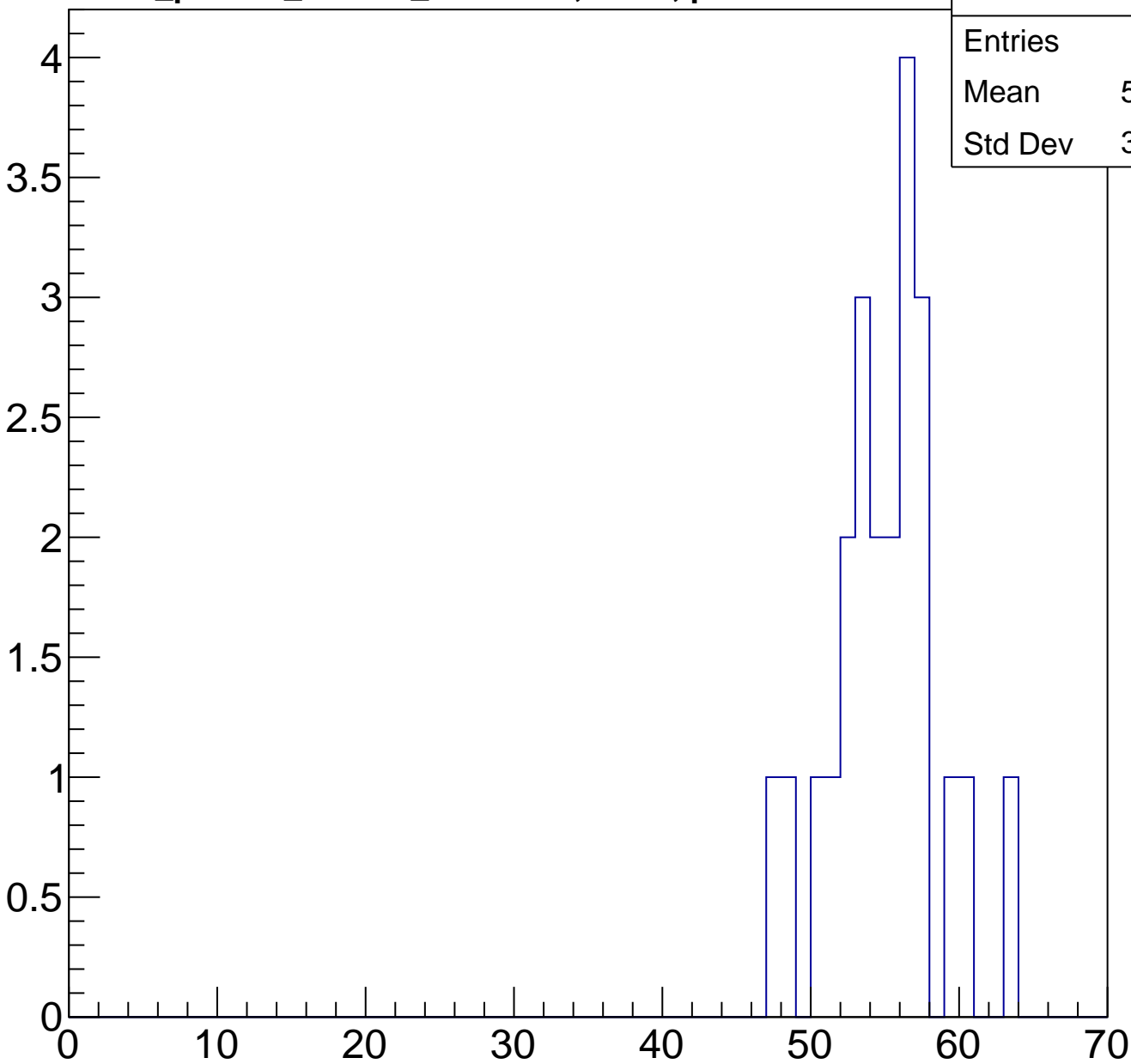
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	23
Mean	54.52
Std Dev	3.622

ampl

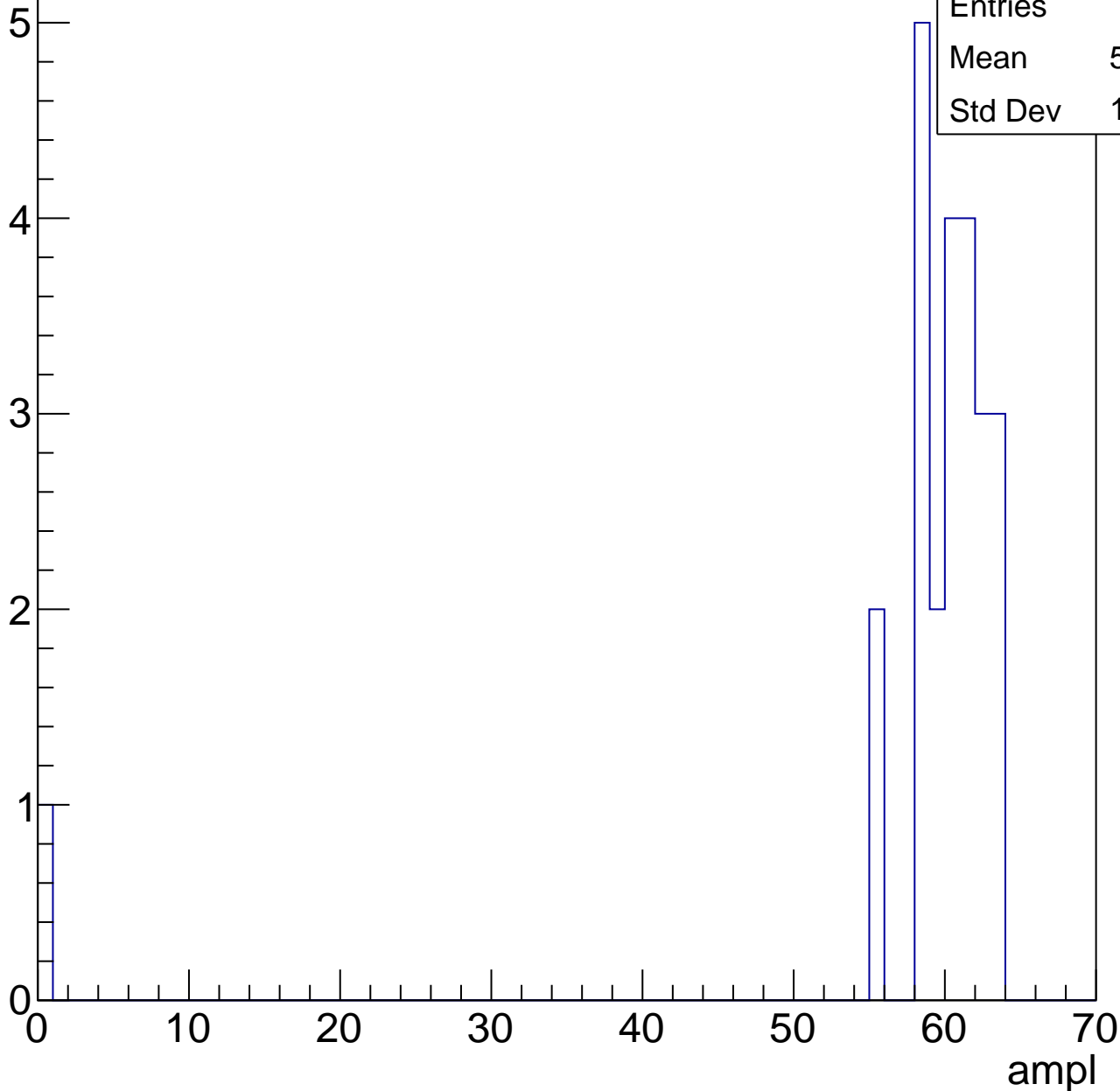


B1L103S, U15-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	57.38
Std Dev	12.16



B1L103S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

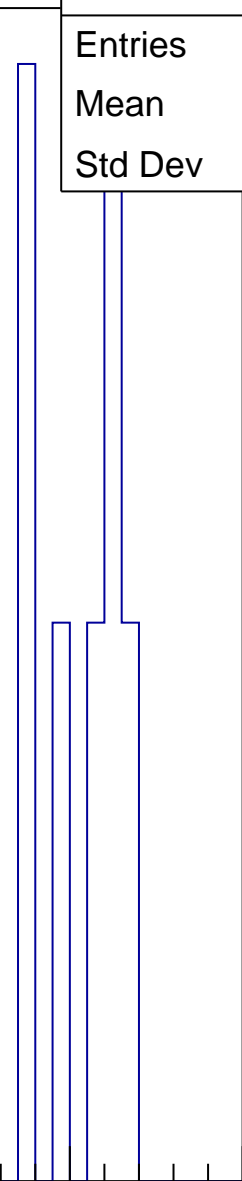
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	60.14
Std Dev	2.295

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch78, adc0

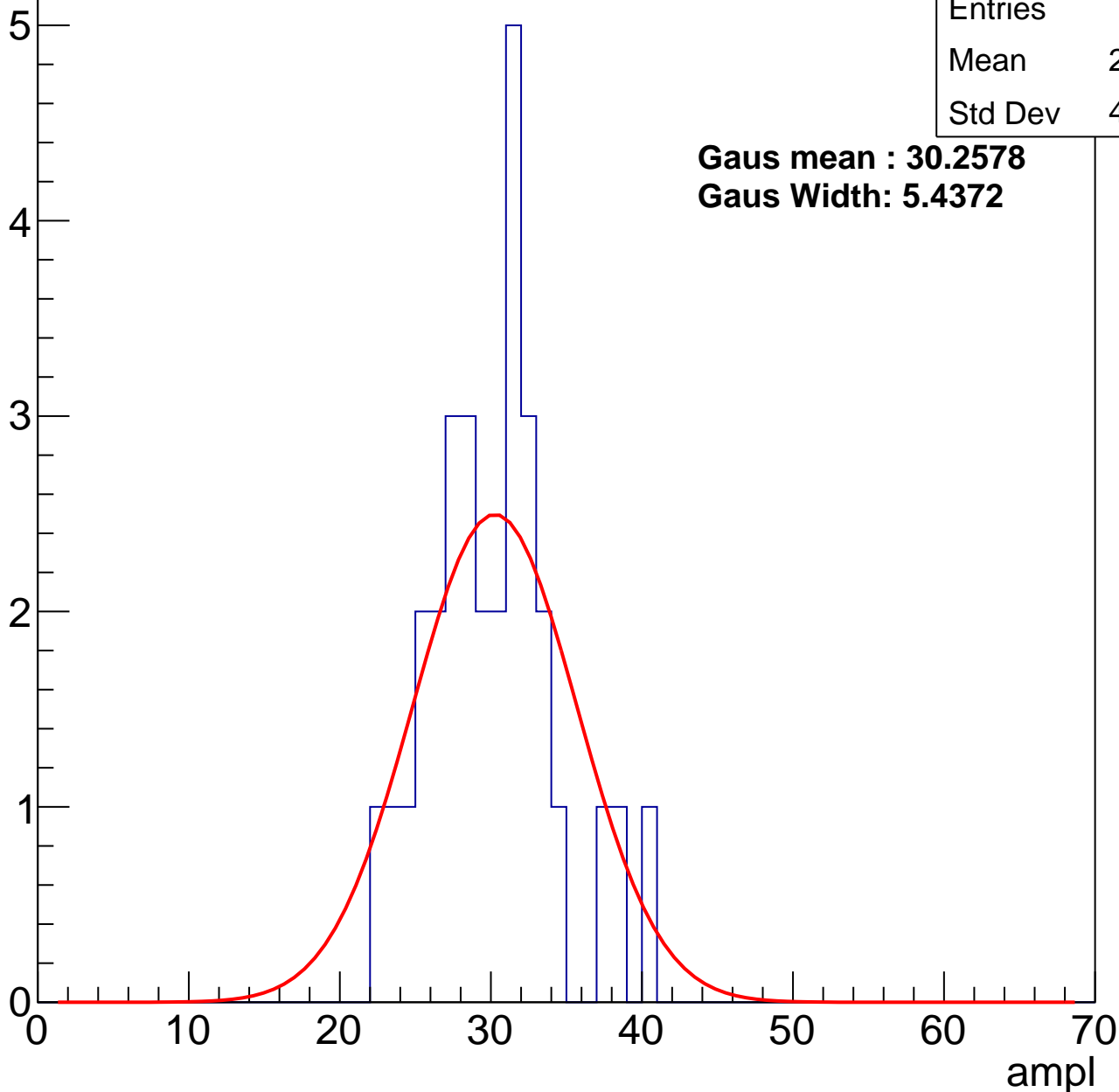
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	29.68
Std Dev	4.146

Gaus mean : 30.2578

Gaus Width: 5.4372



B1L103S, U15-ch78, adc1

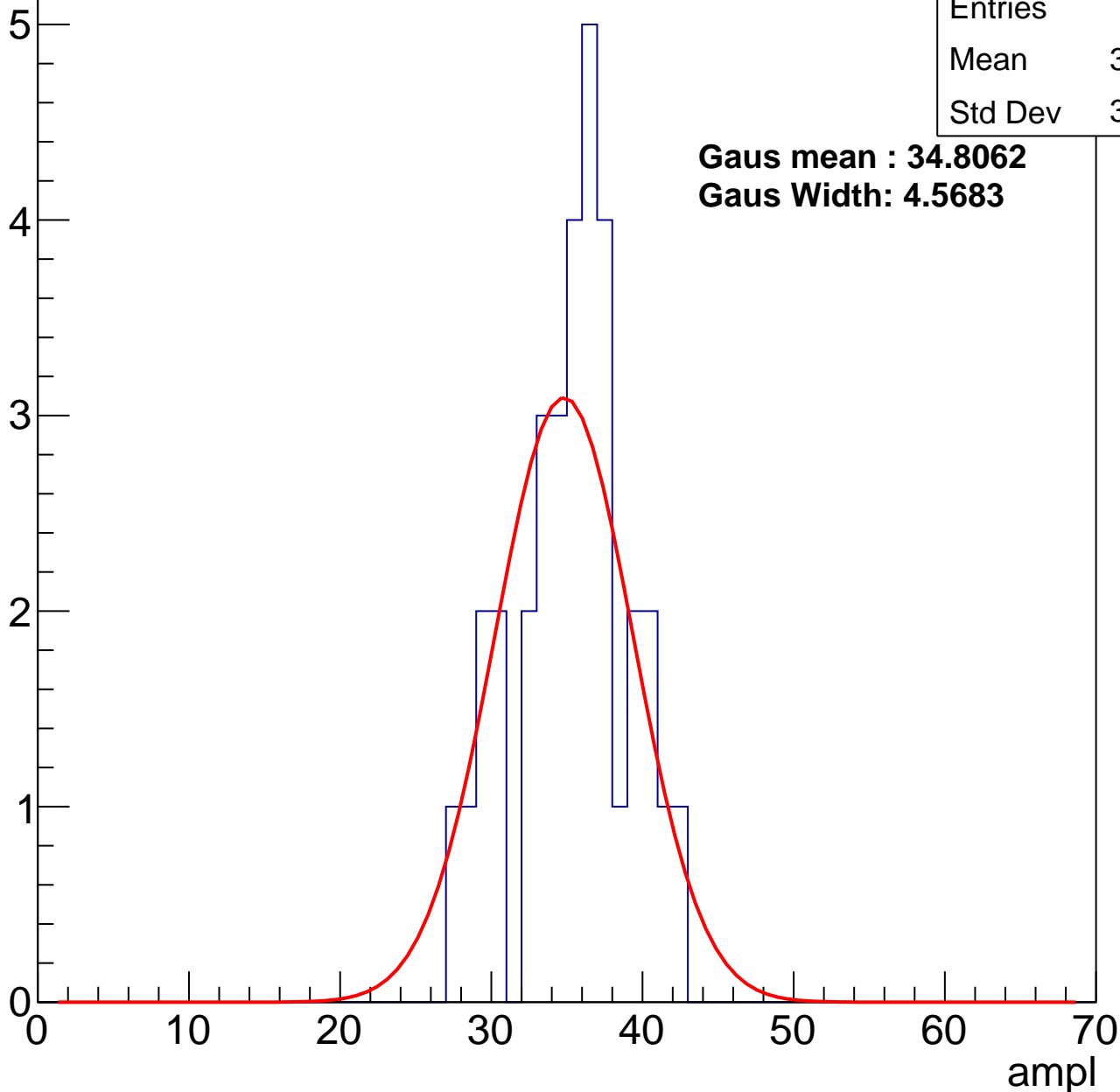
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	34.85
Std Dev	3.695

Gaus mean : 34.8062

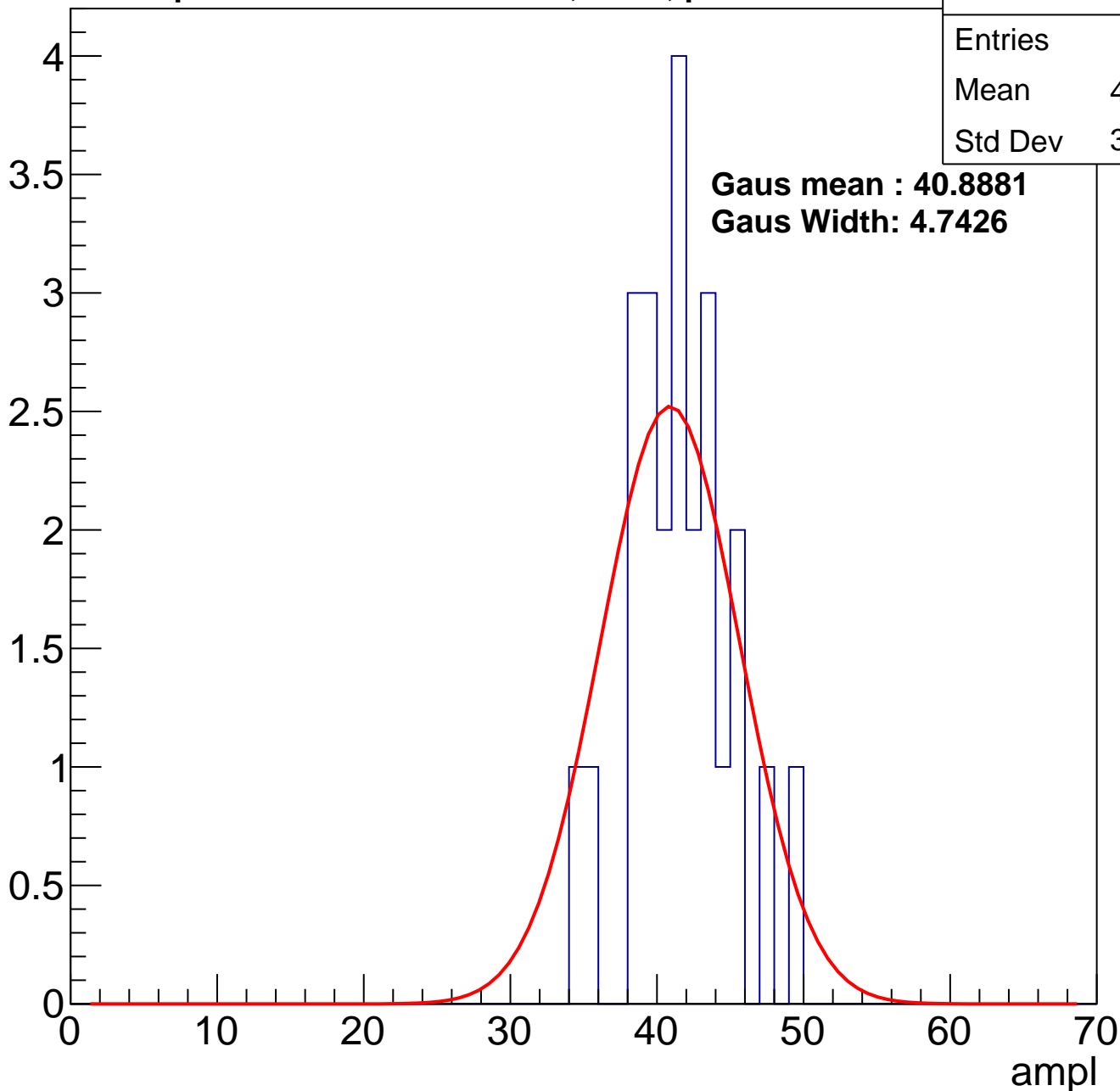
Gaus Width: 4.5683



B1L103S, U15-ch78, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

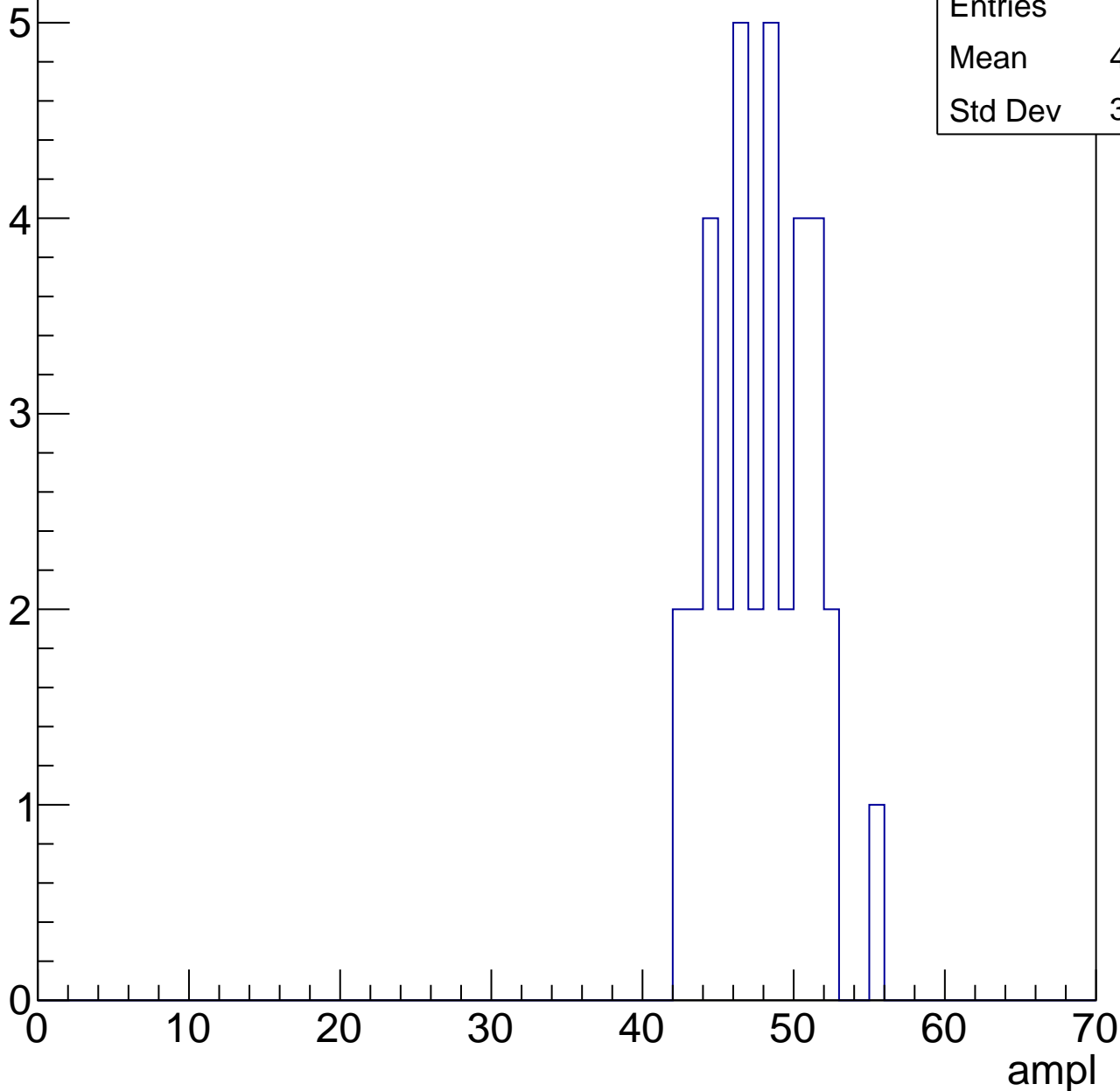


B1L103S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

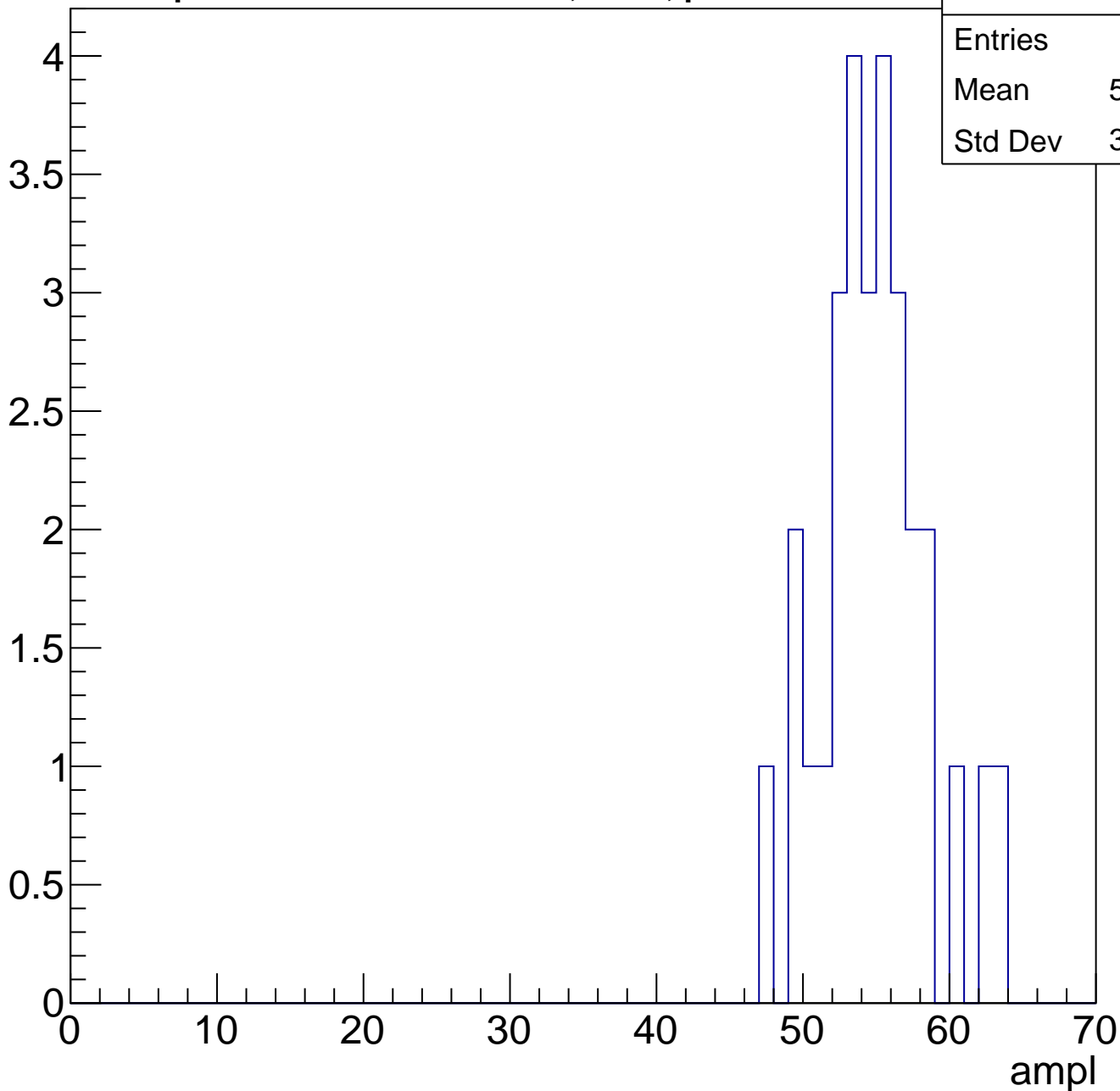
Entries	35
Mean	47.46
Std Dev	3.165



B1L103S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

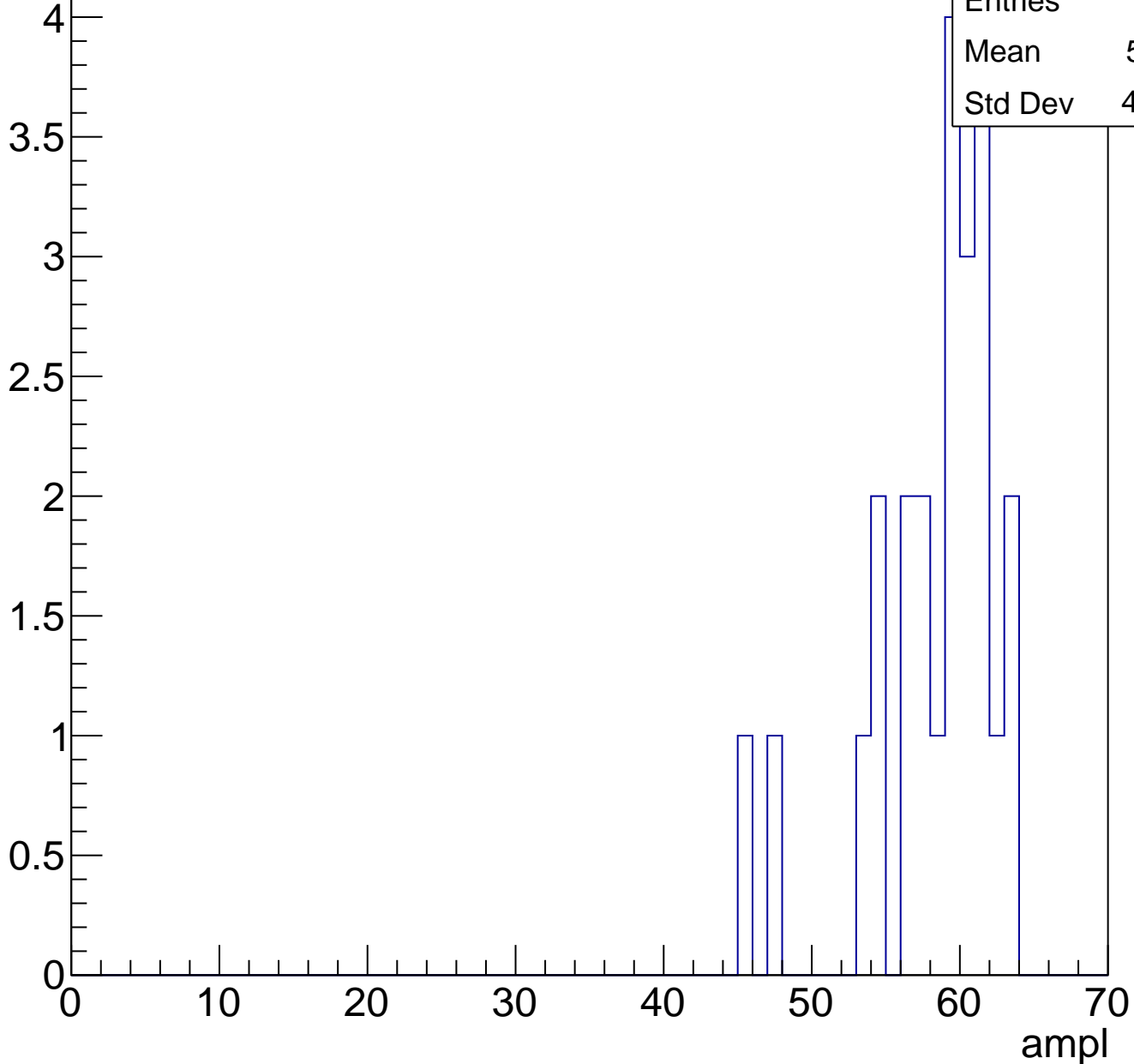
Entry



B1L103S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

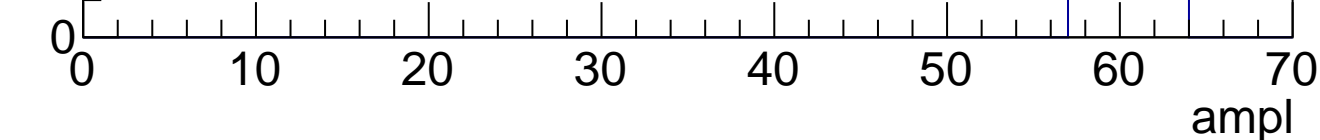
50

60

70

ampl

Entries	11
Mean	60.45
Std Dev	2.061



B1L103S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch79, adc0

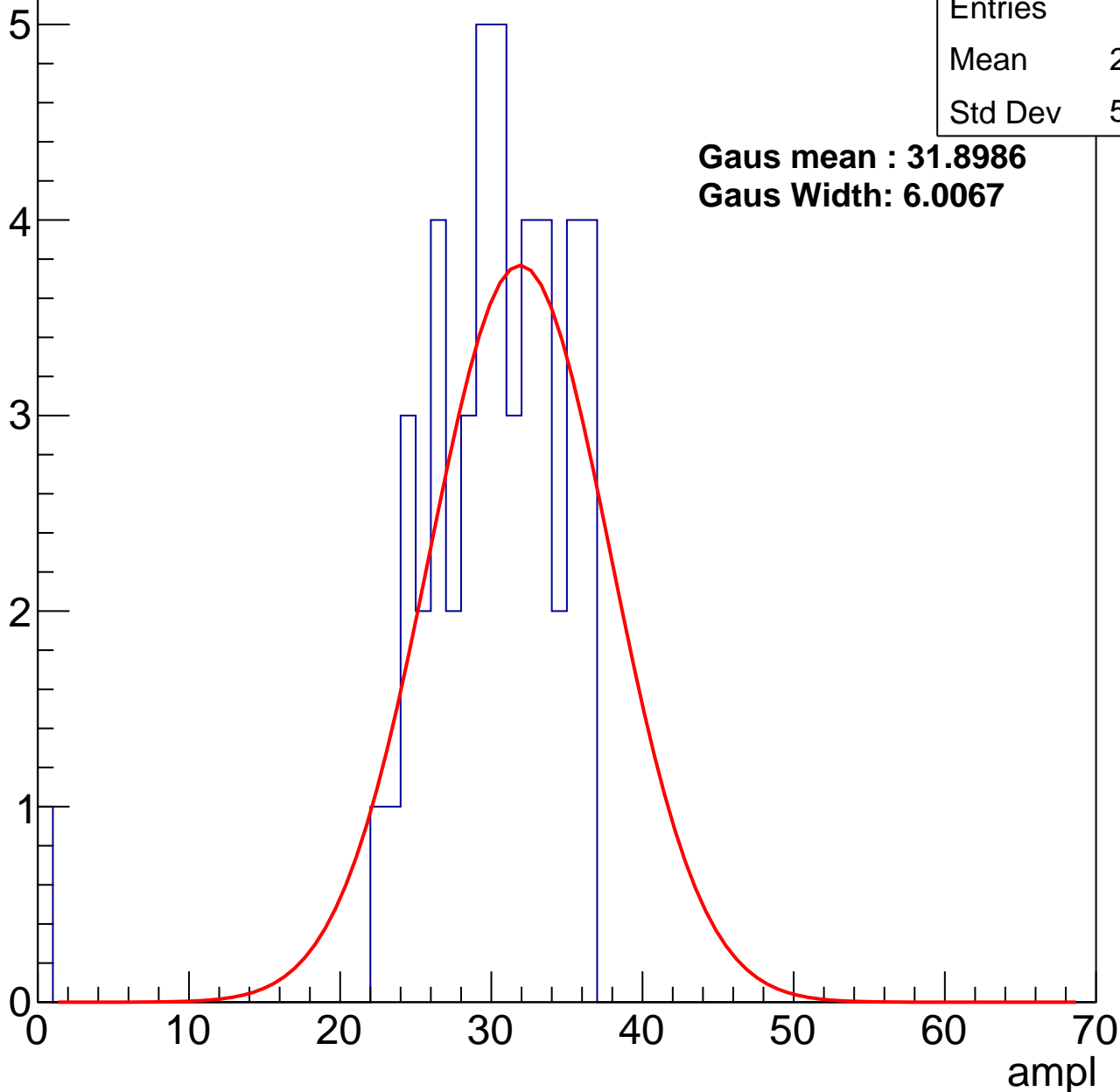
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	29.35
Std Dev	5.739

Gaus mean : 31.8986

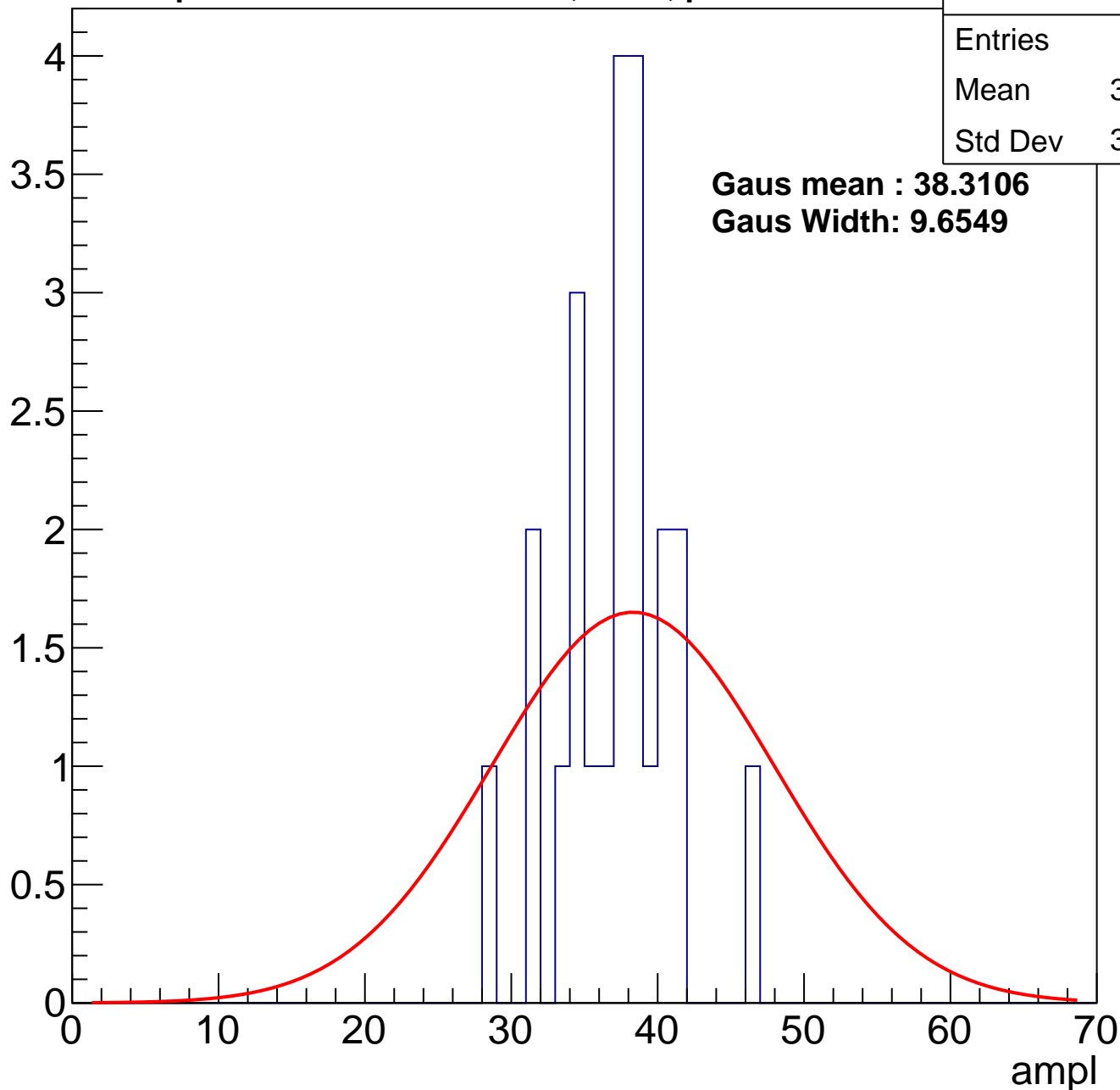
Gaus Width: 6.0067



B1L103S, U15-ch79, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch79, adc2

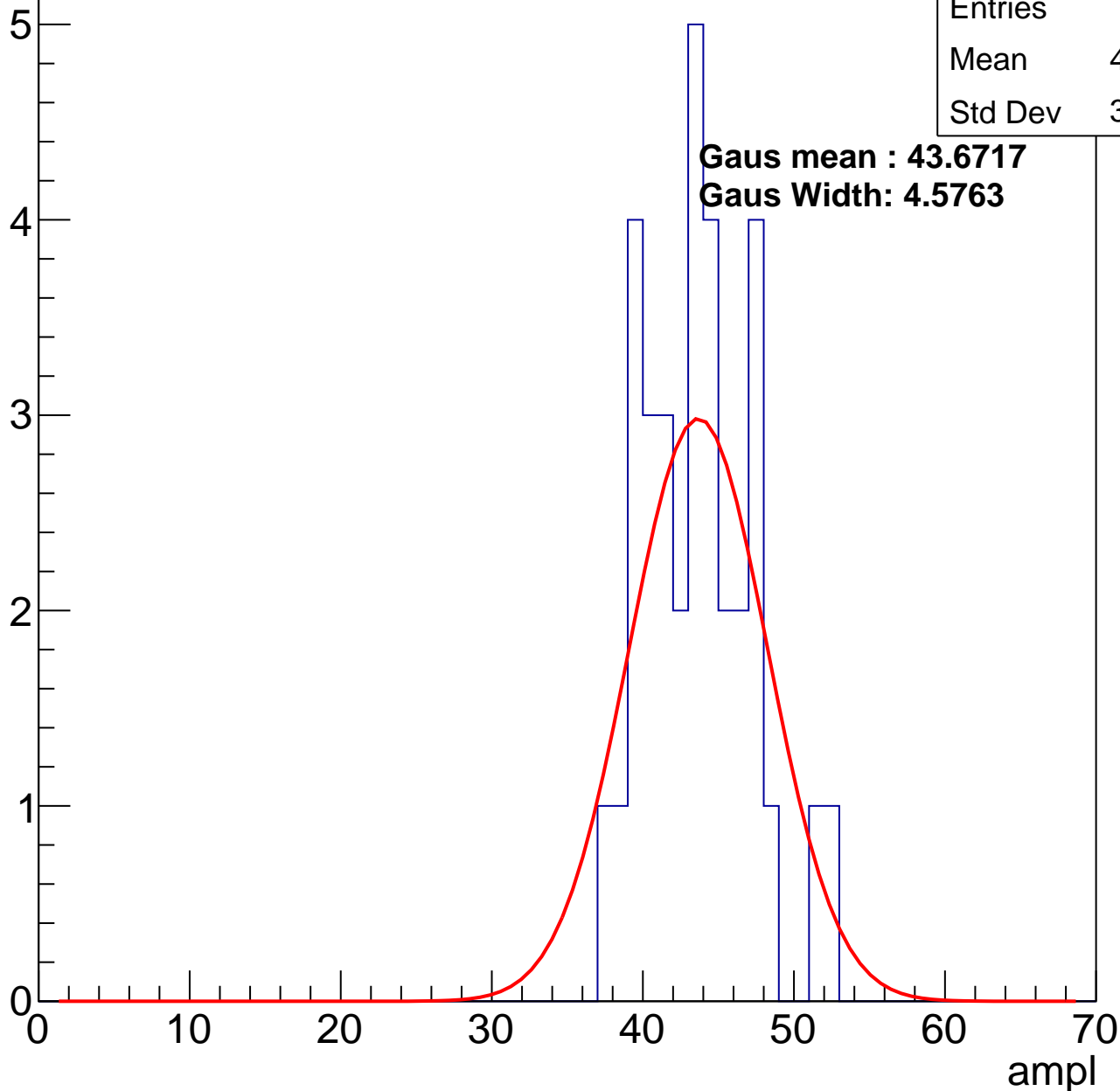
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	43.24
Std Dev	3.548

Gaus mean : 43.6717

Gaus Width: 4.5763

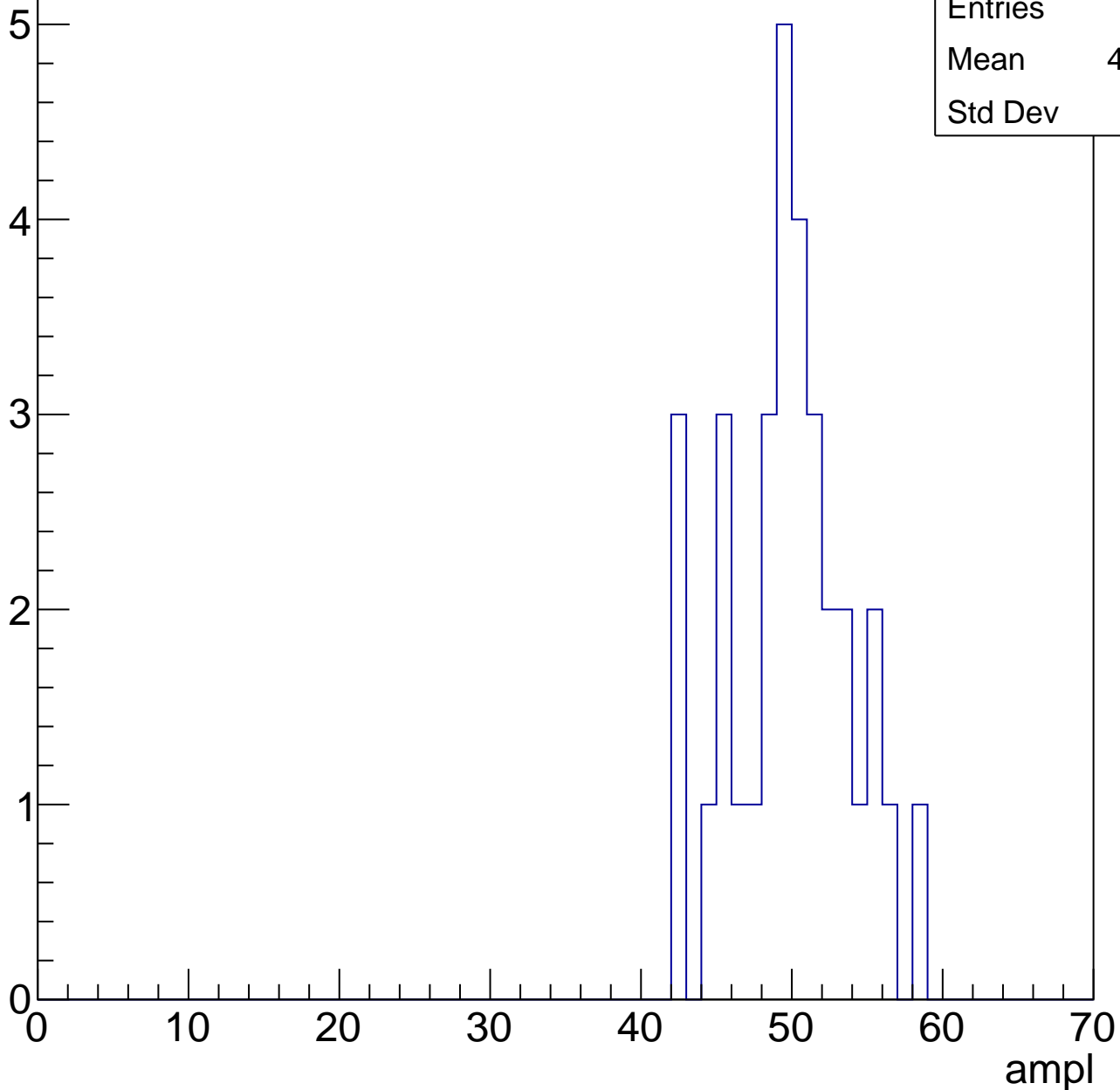


B1L103S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

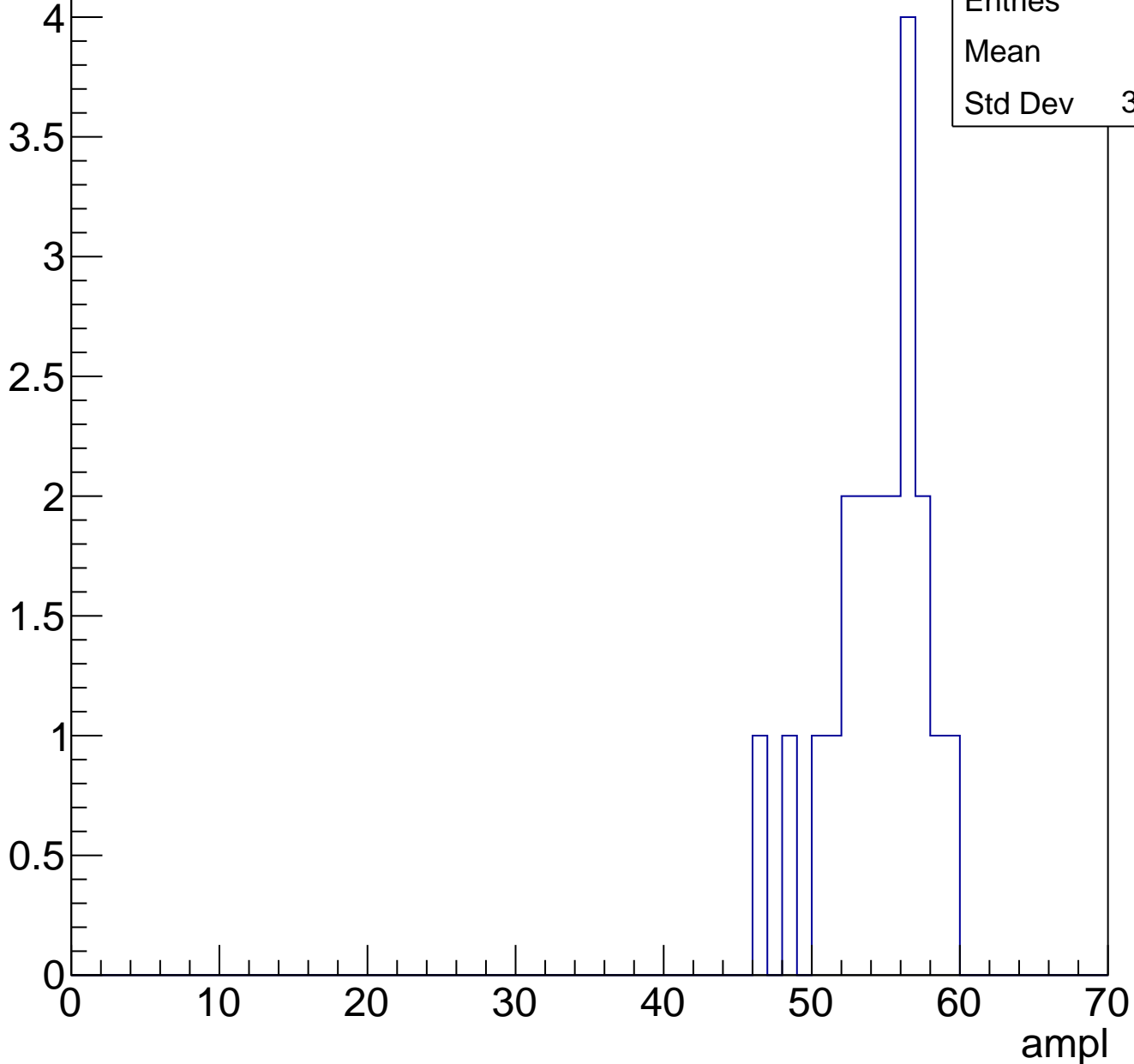
Entries	33
Mean	49.33
Std Dev	3.99



B1L103S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

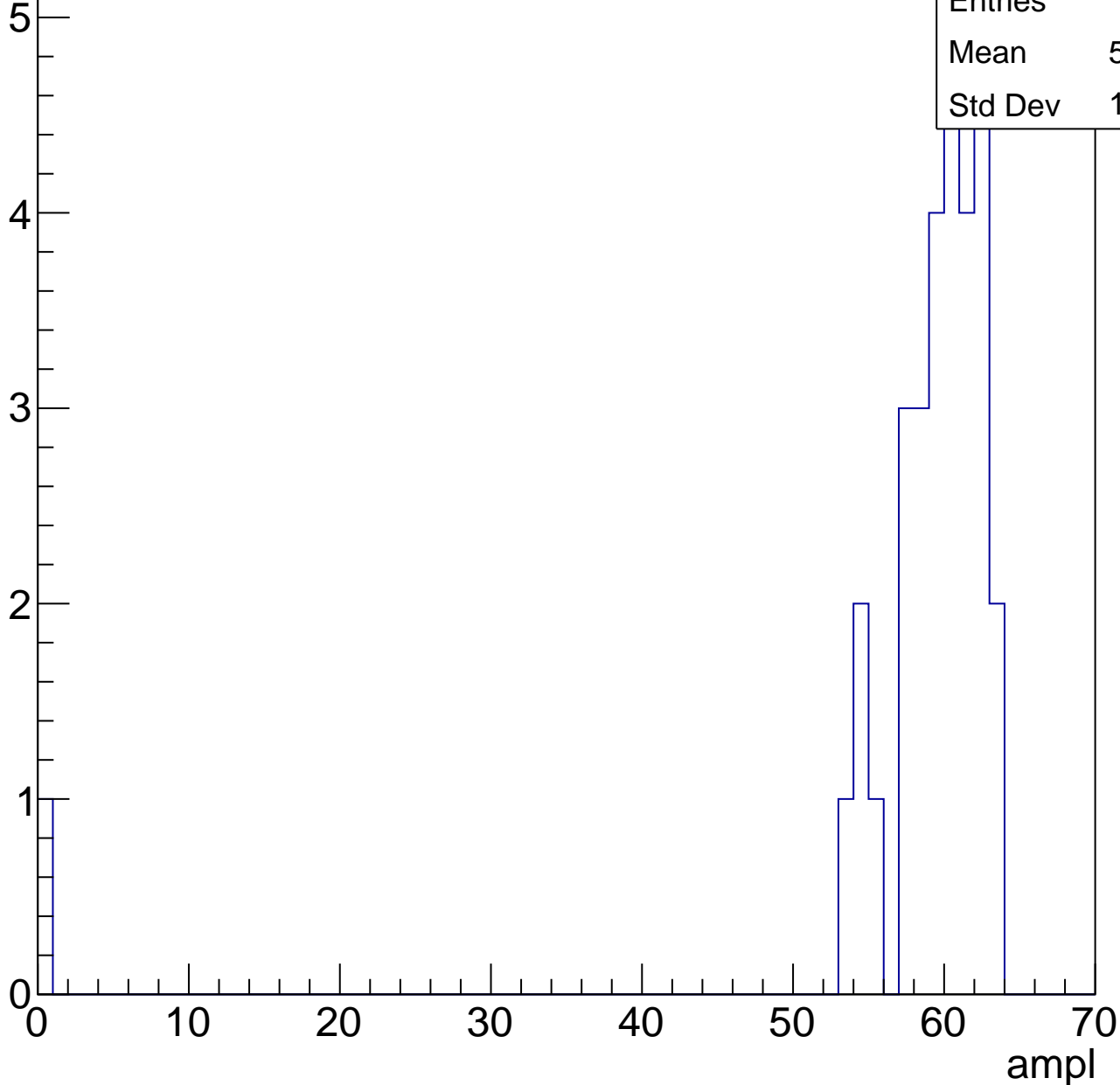


B1L103S, U15-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	57.32
Std Dev	10.79



B1L103S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	62.33
Std Dev	0.9428

B1L103S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch80, adc0

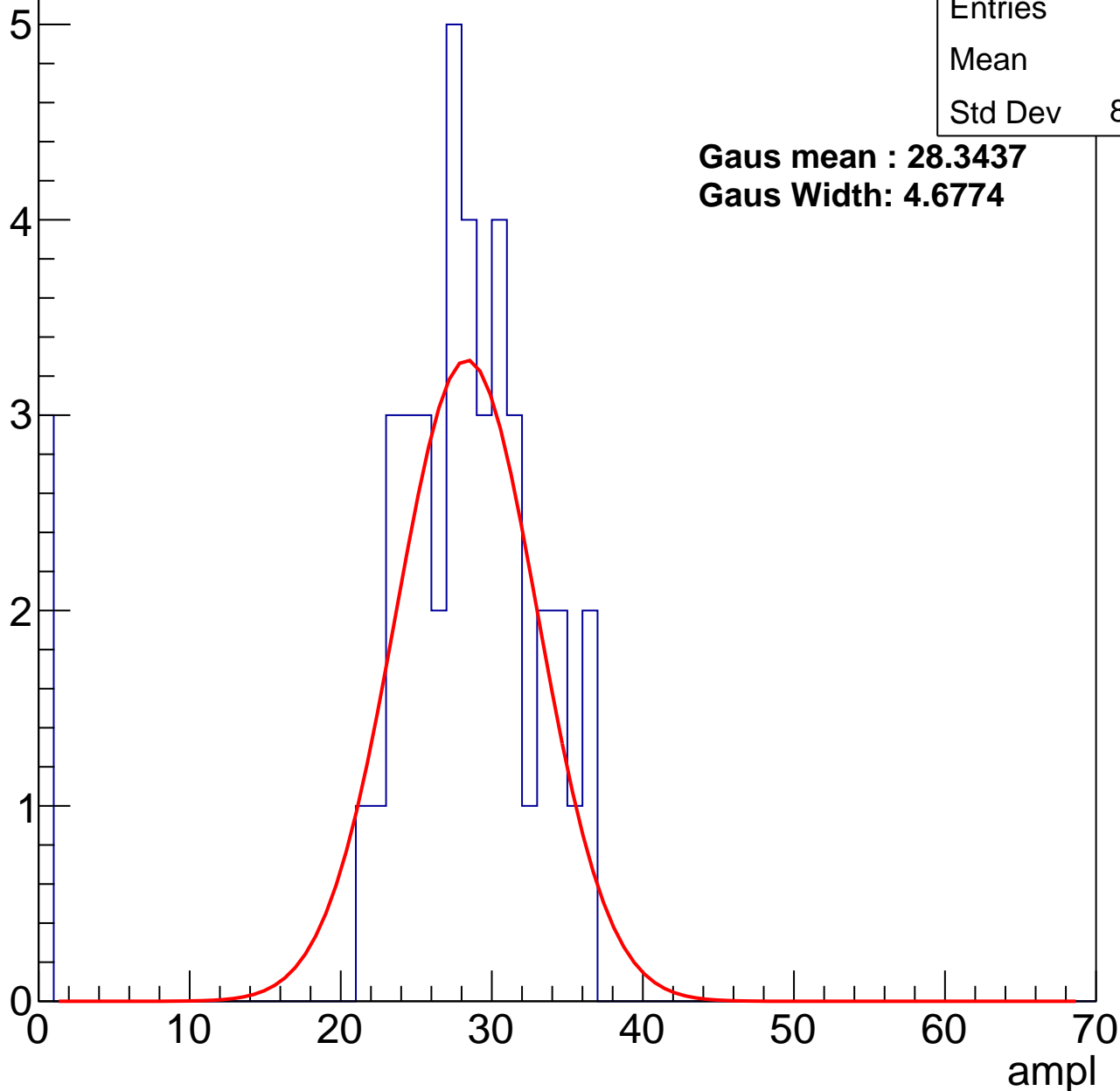
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	26.3
Std Dev	8.123

Gaus mean : 28.3437

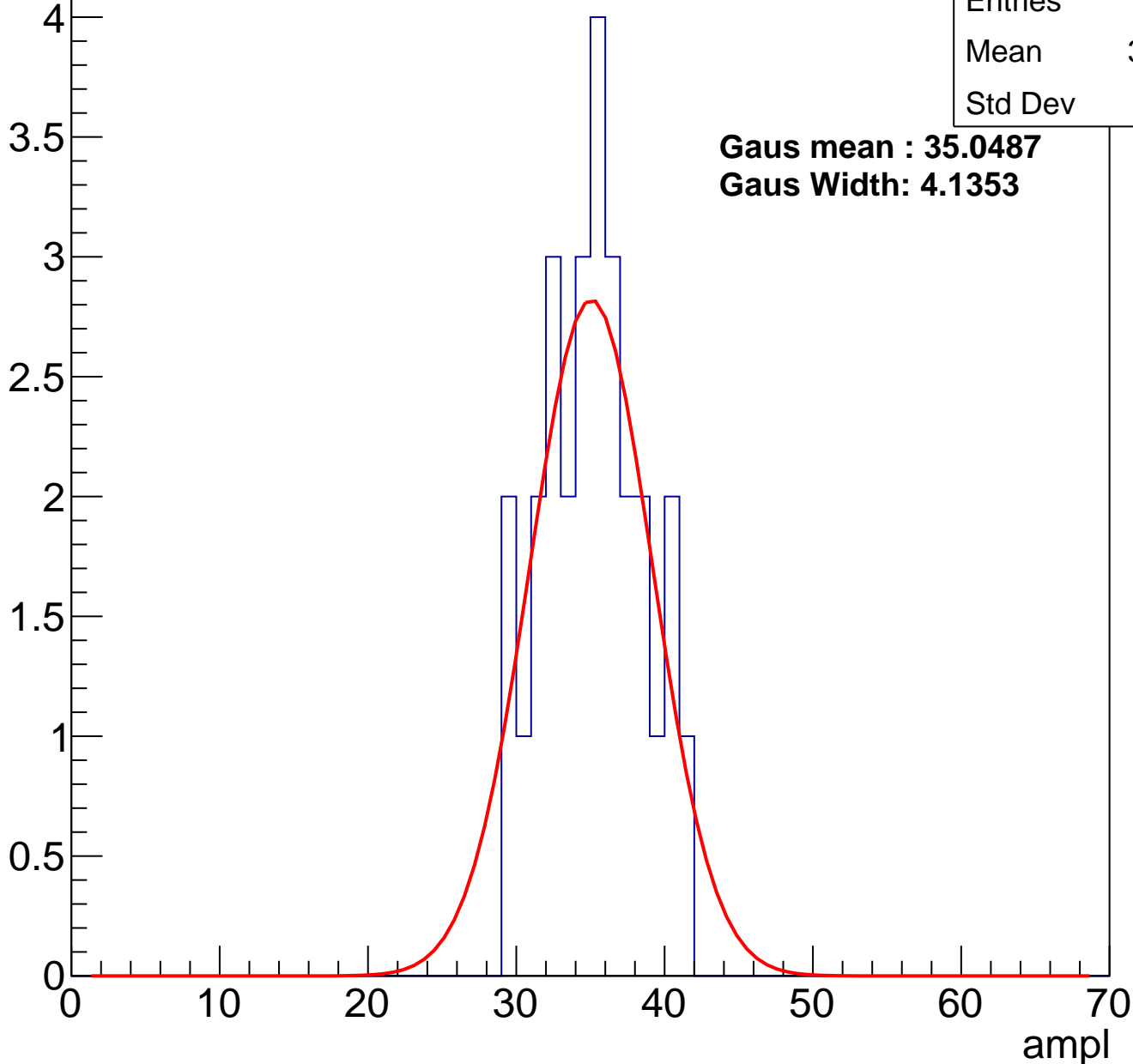
Gaus Width: 4.6774



B1L103S, U15-ch80, adc1

calib_packv5_042523_0143.root, FC#7, port C2

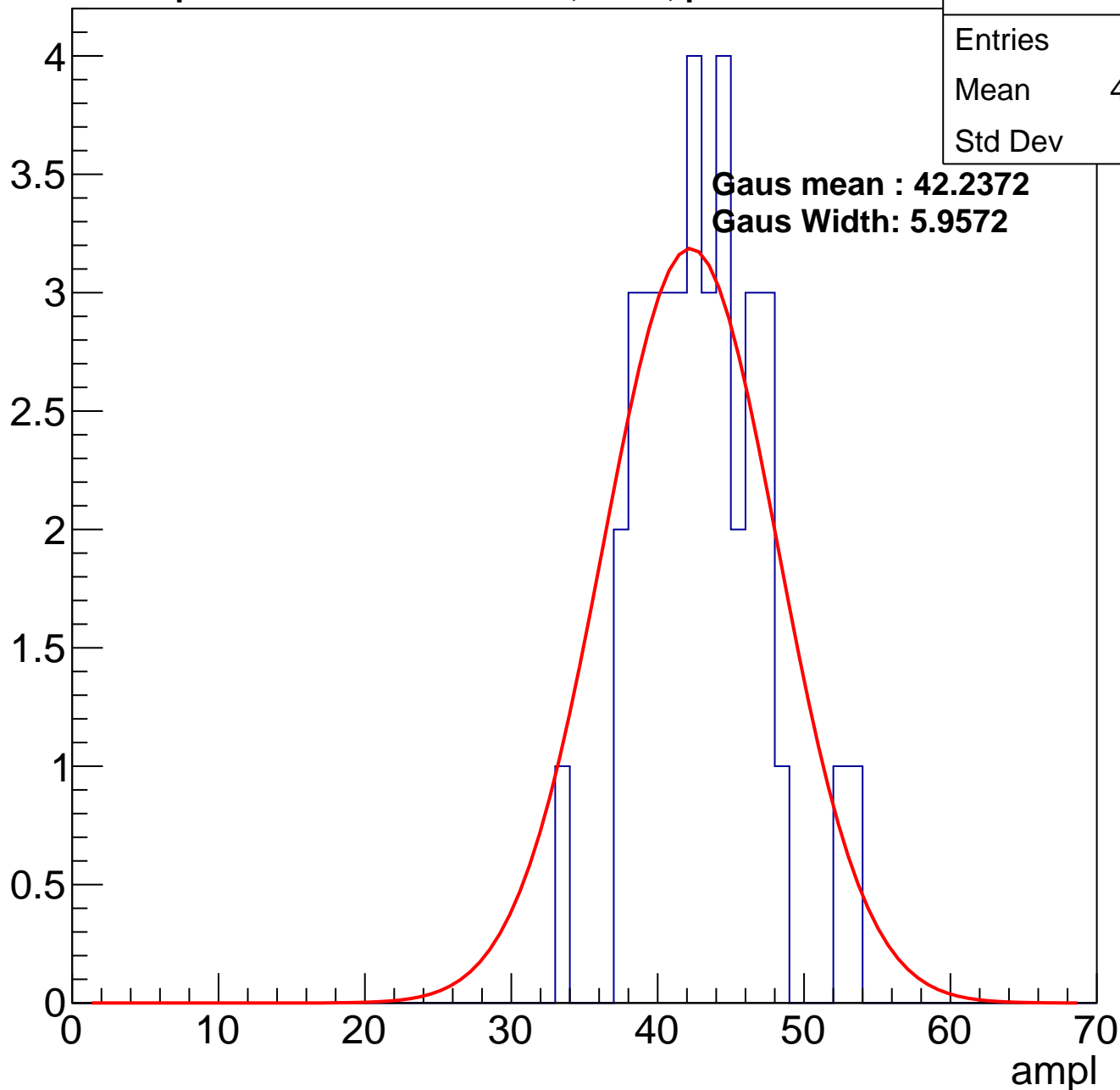
Entry



B1L103S, U15-ch80, adc2

calib_packv5_042523_0143.root, FC#7, port C2

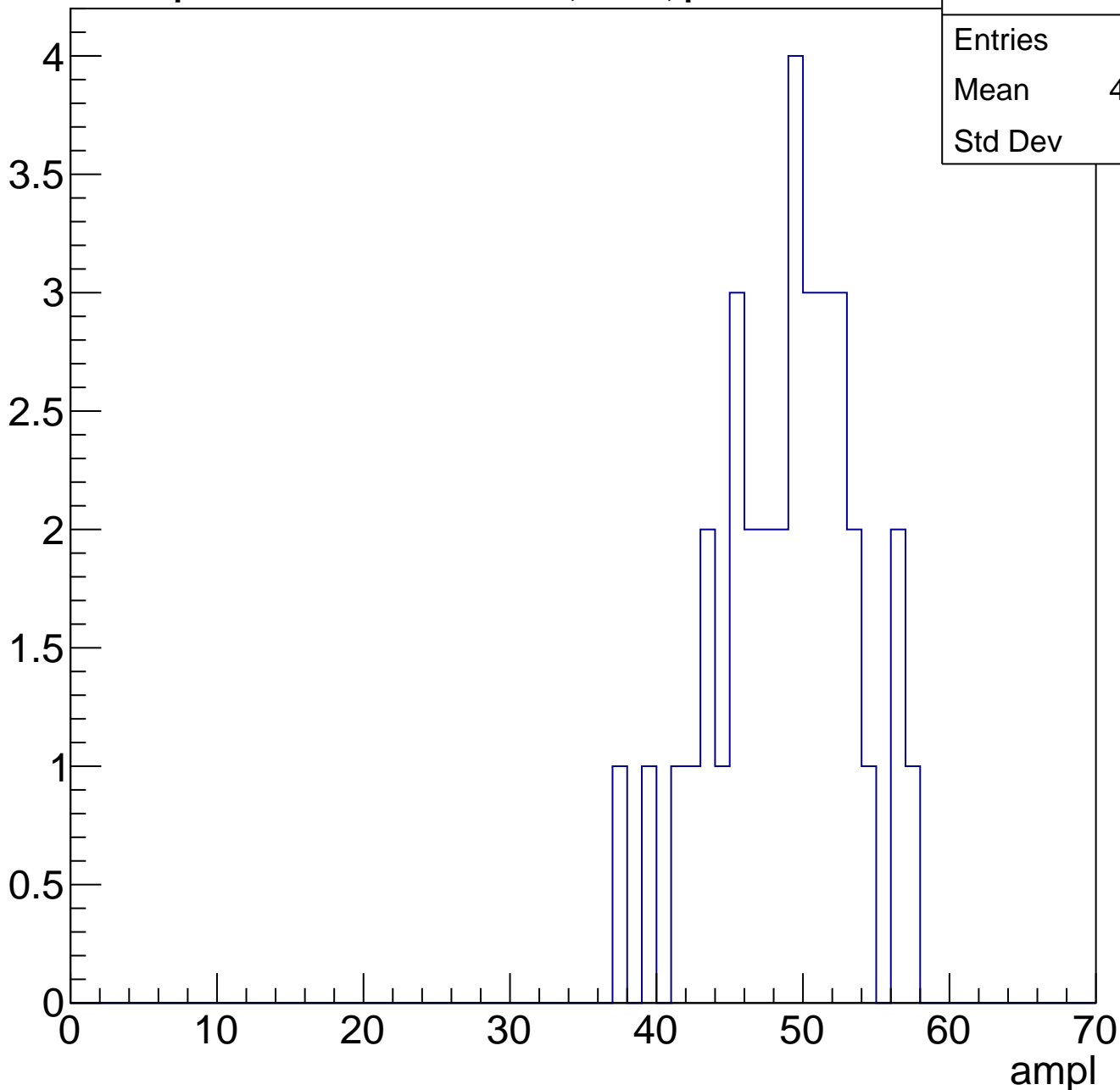
Entry



B1L103S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

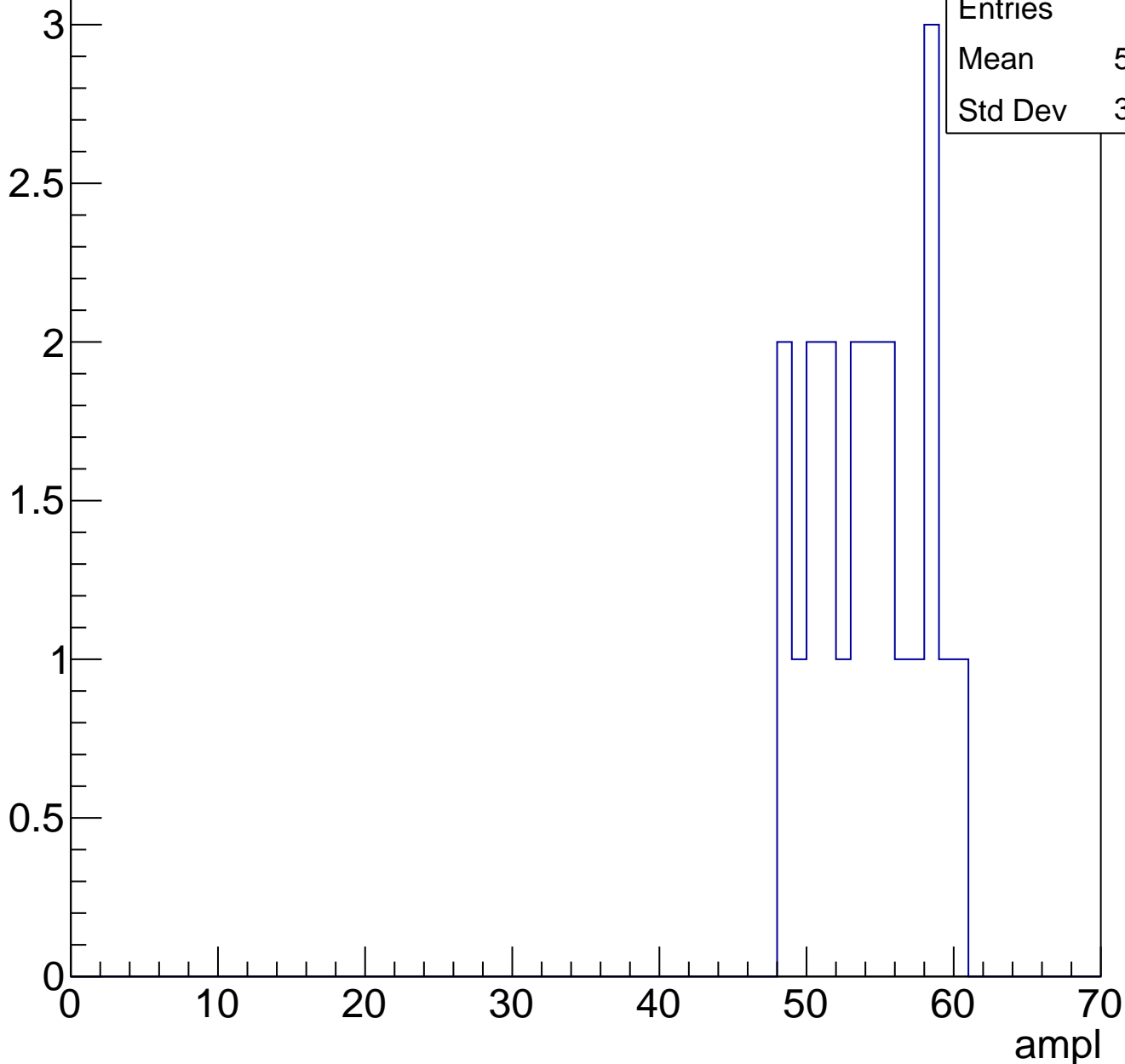
Entry



B1L103S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

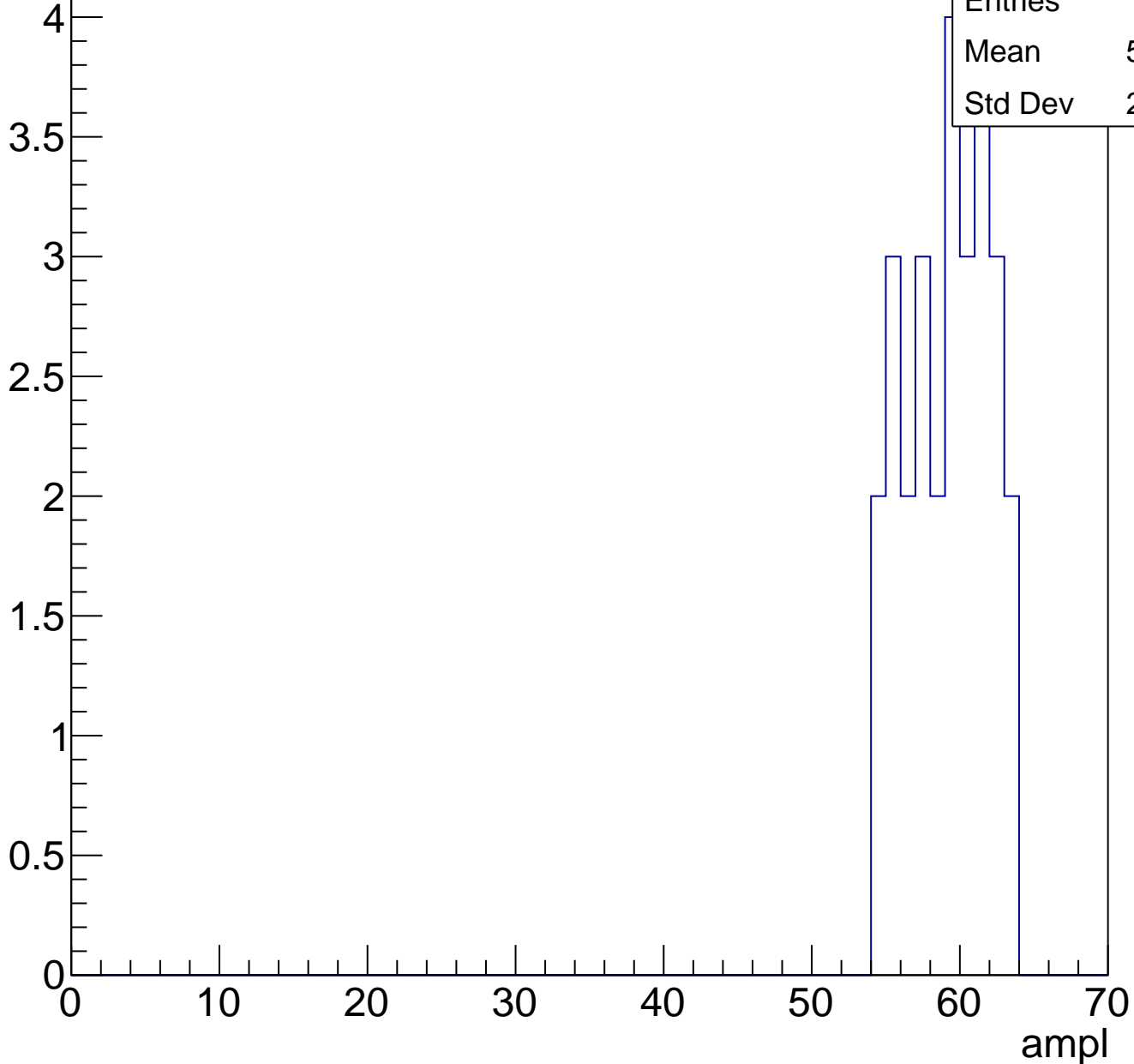
Entry



B1L103S, U15-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	28
Mean	58.71
Std Dev	2.711

B1L103S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch81, adc0

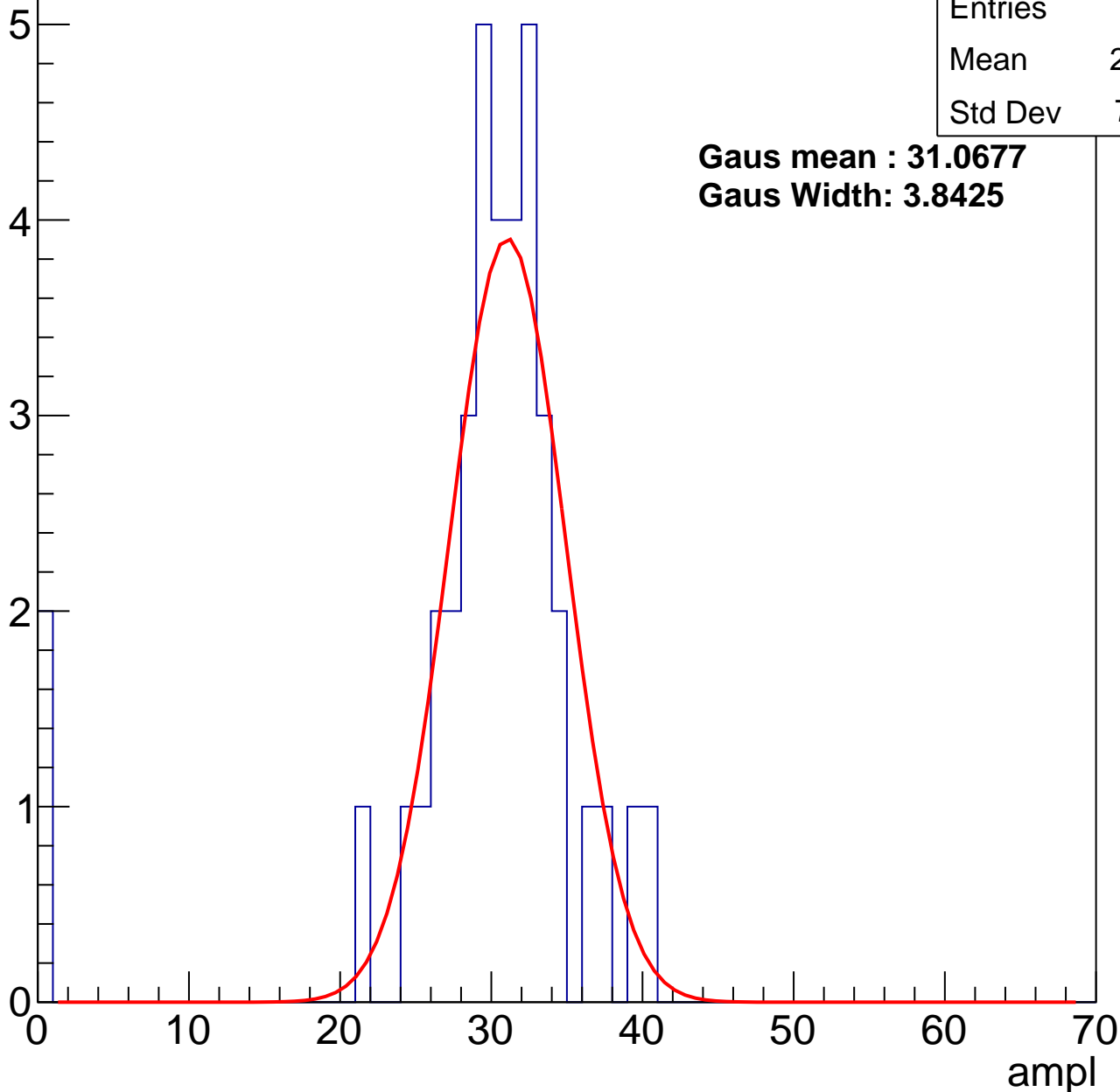
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	28.92
Std Dev	7.701

Gaus mean : 31.0677

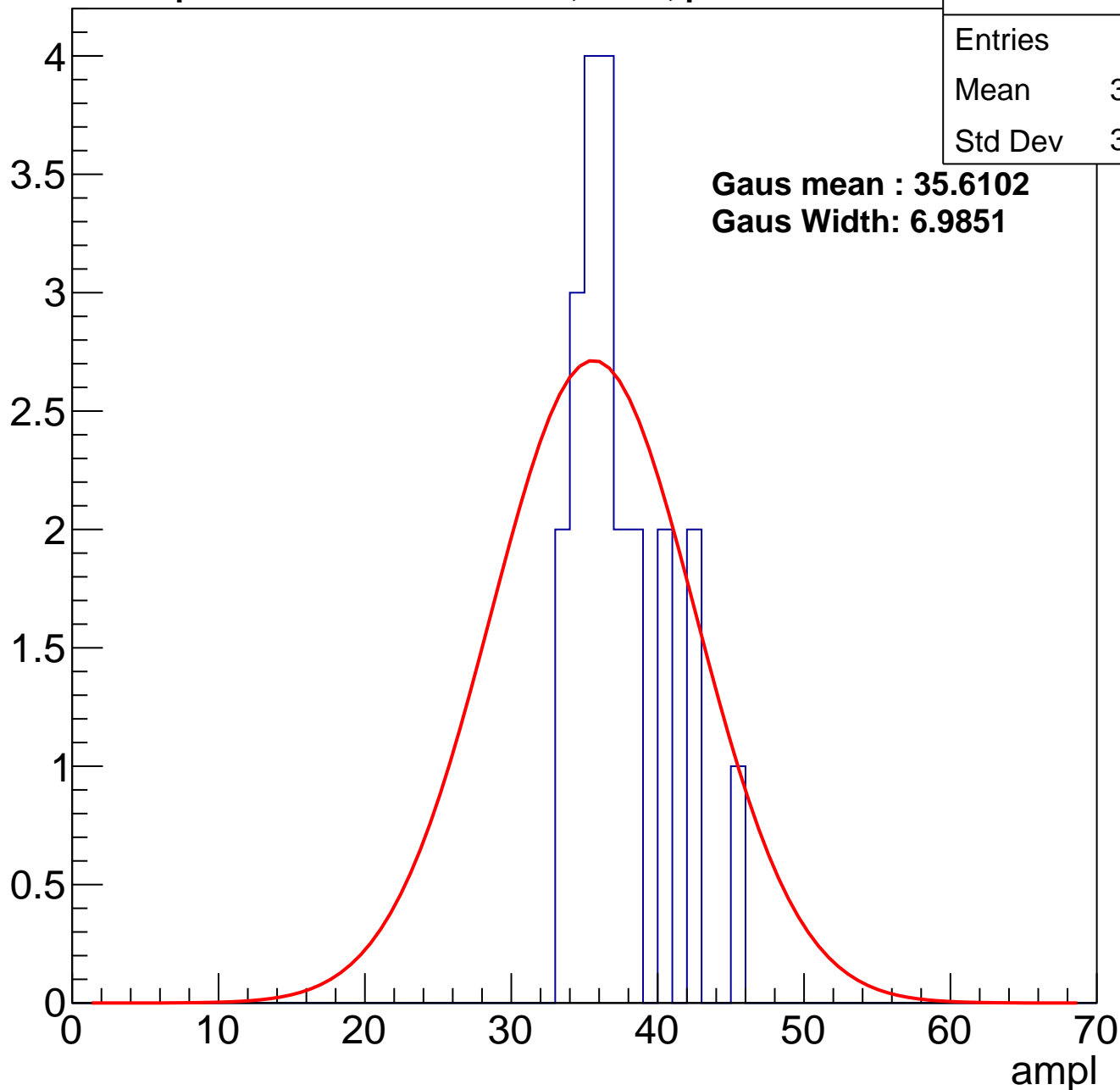
Gaus Width: 3.8425



B1L103S, U15-ch81, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch81, adc2

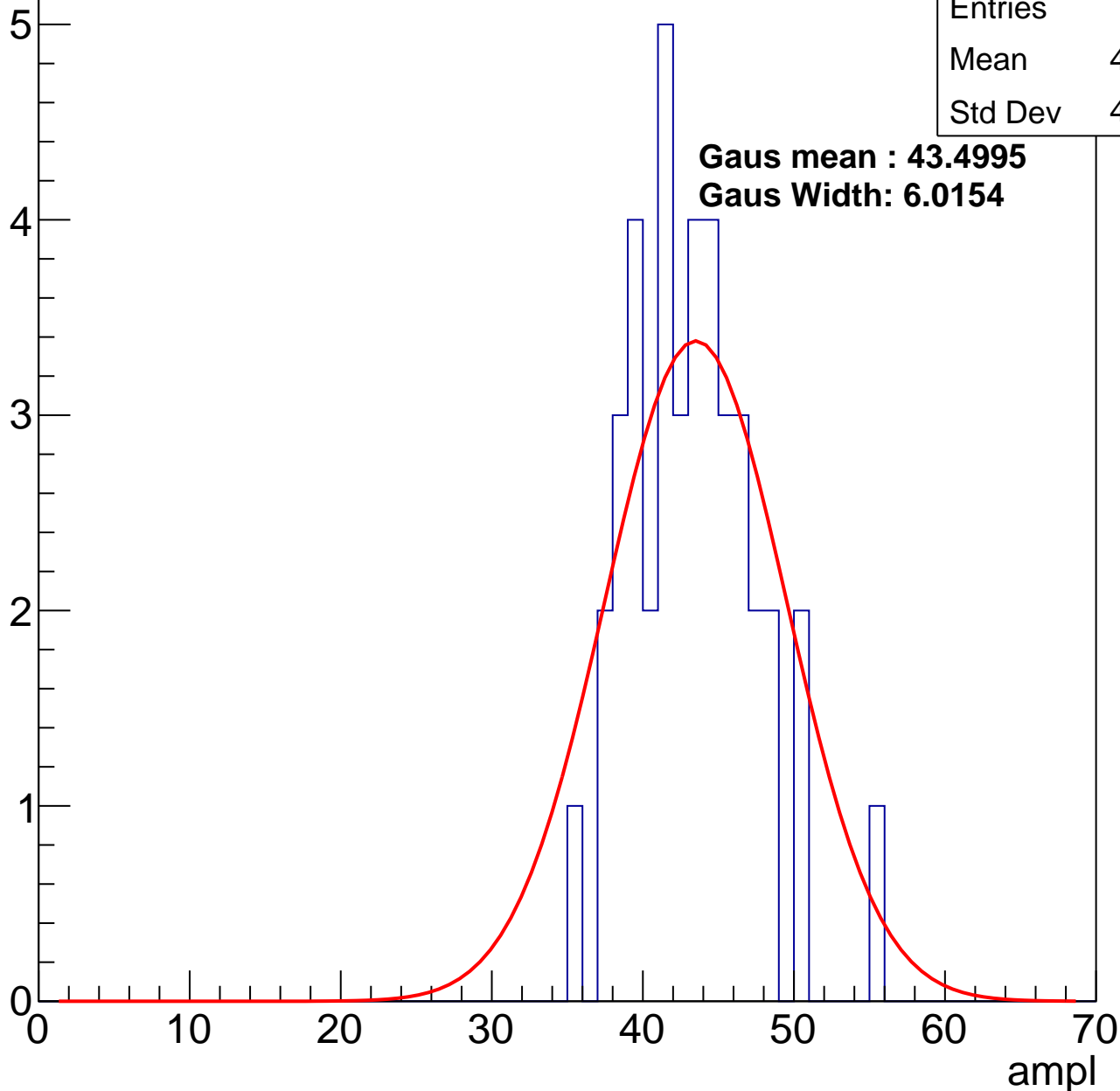
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	42.83
Std Dev	4.078

Gaus mean : 43.4995

Gaus Width: 6.0154

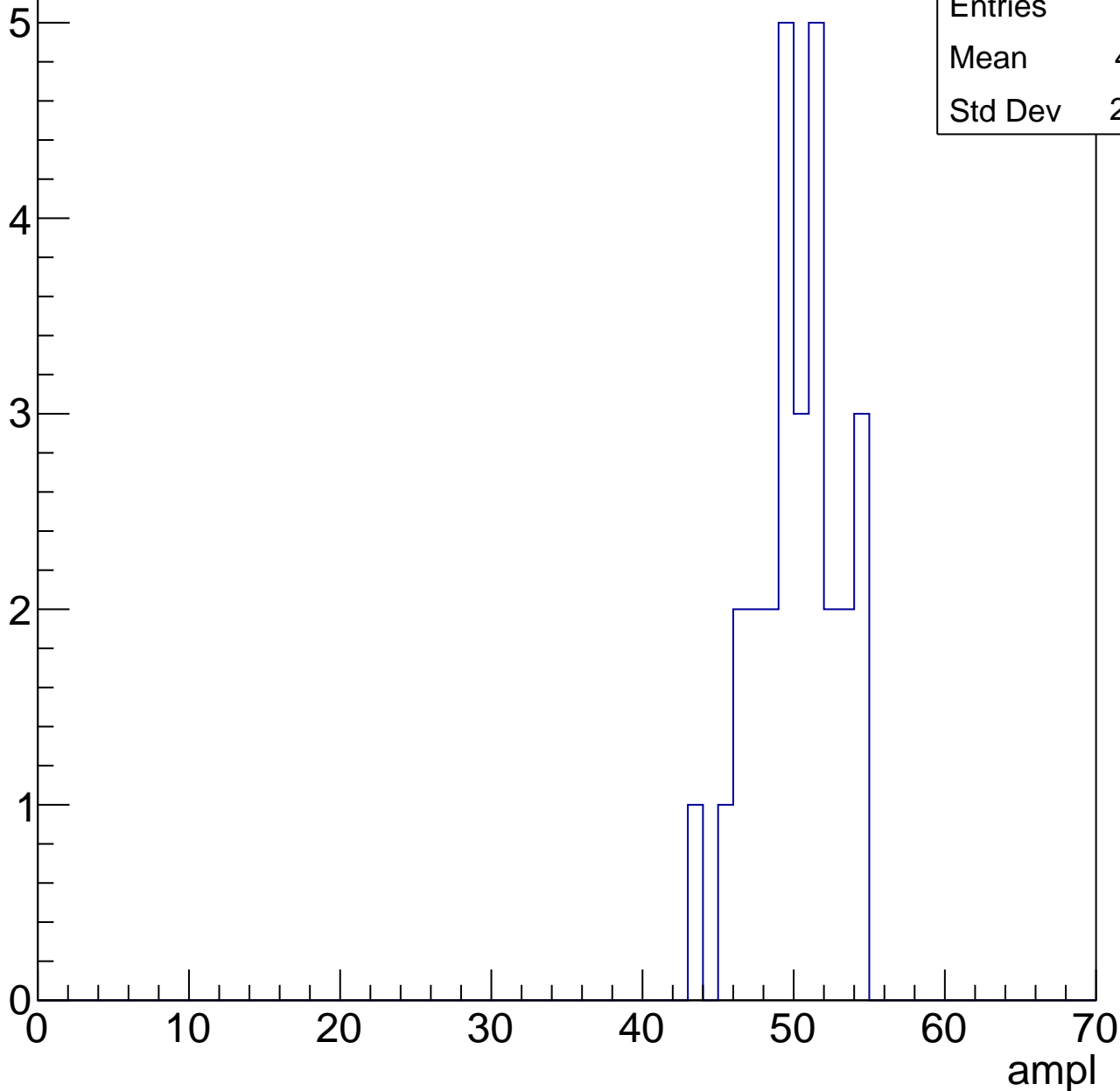


B1L103S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	49.71
Std Dev	2.763

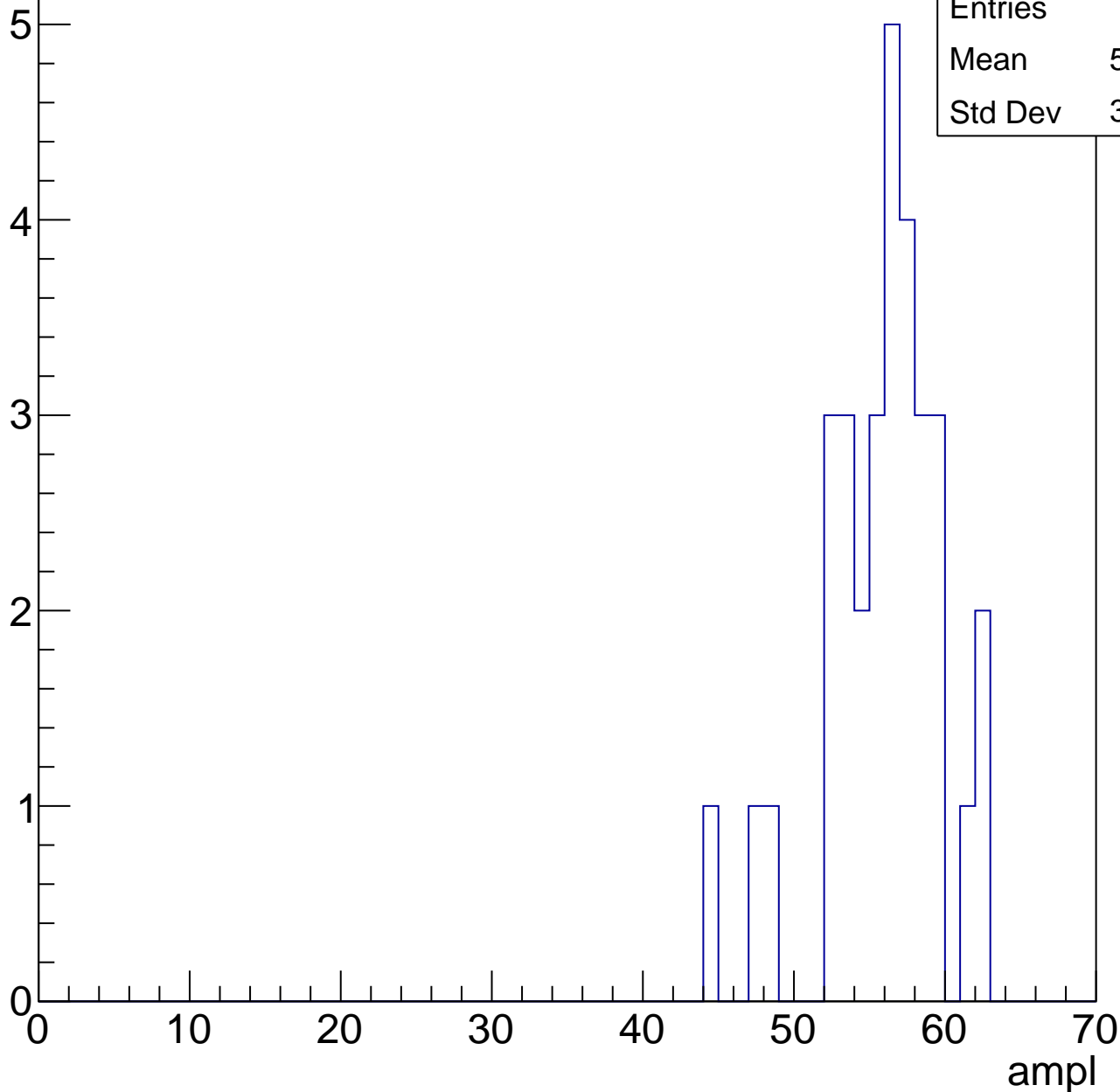


B1L103S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	55.34
Std Dev	3.958

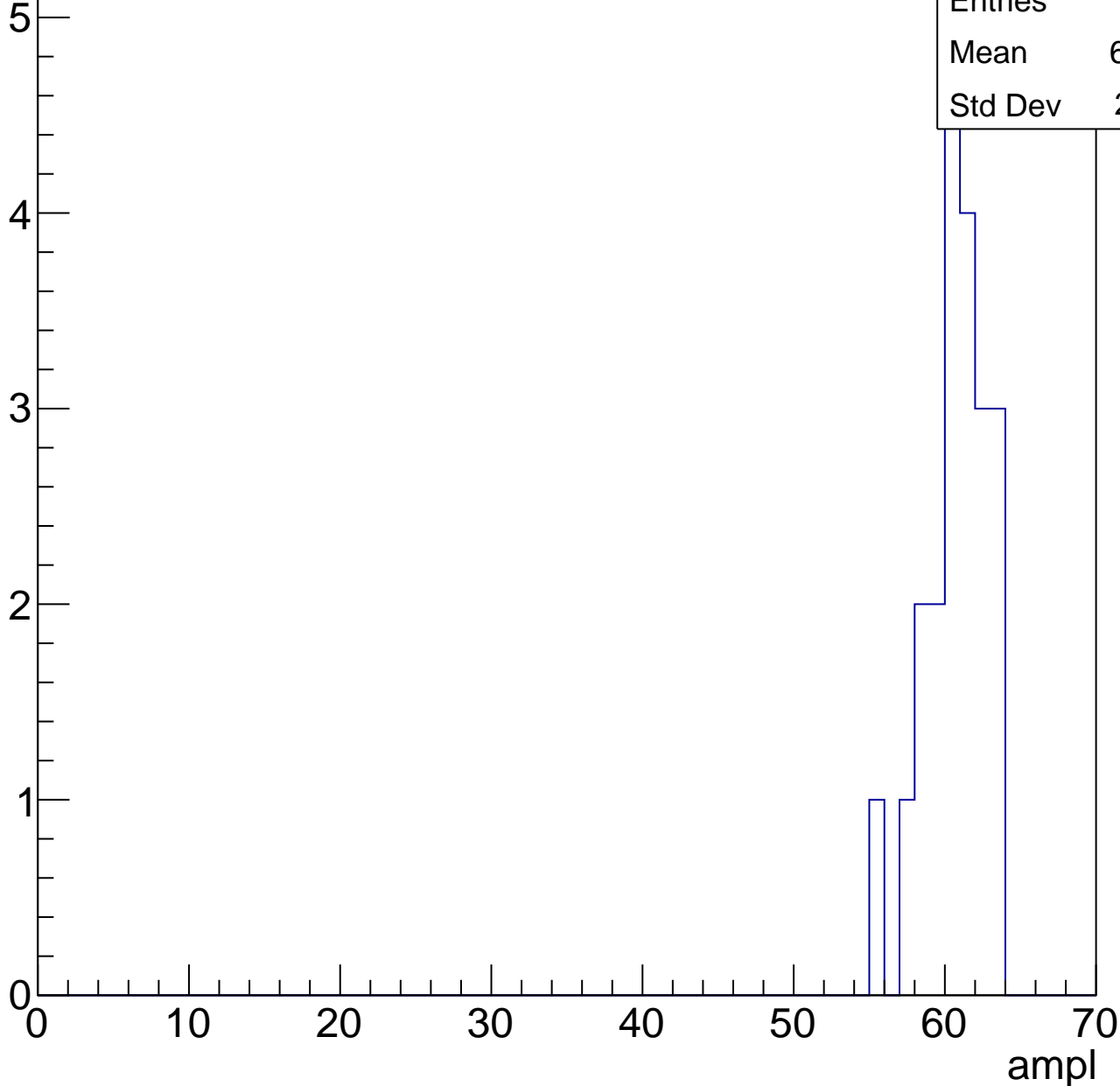


B1L103S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	60.24
Std Dev	2.021



B1L103S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

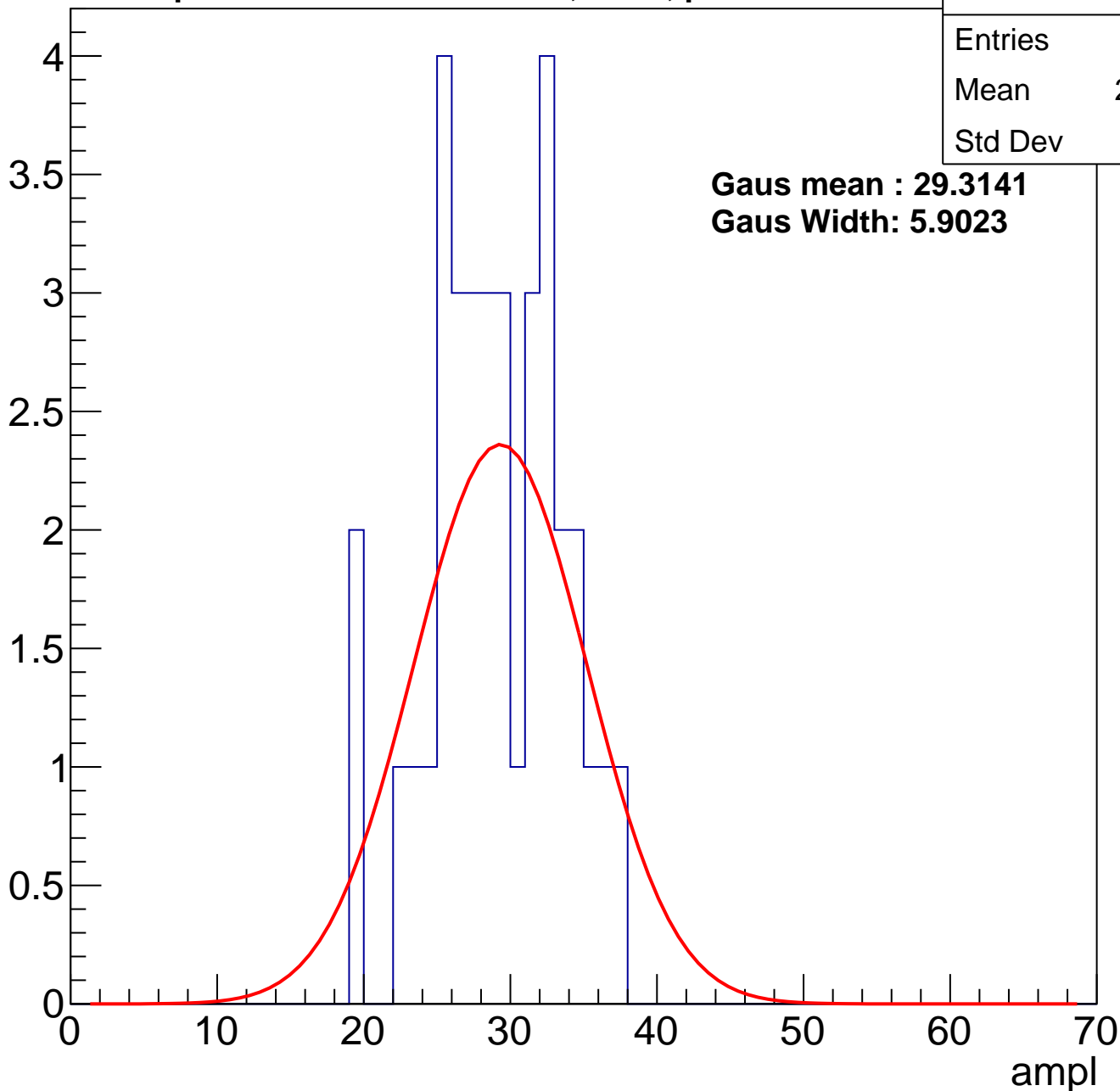


Entries	1
Mean	63
Std Dev	0

B1L103S, U15-ch82, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	36
Mean	28.61
Std Dev	4.38

B1L103S, U15-ch82, adc1

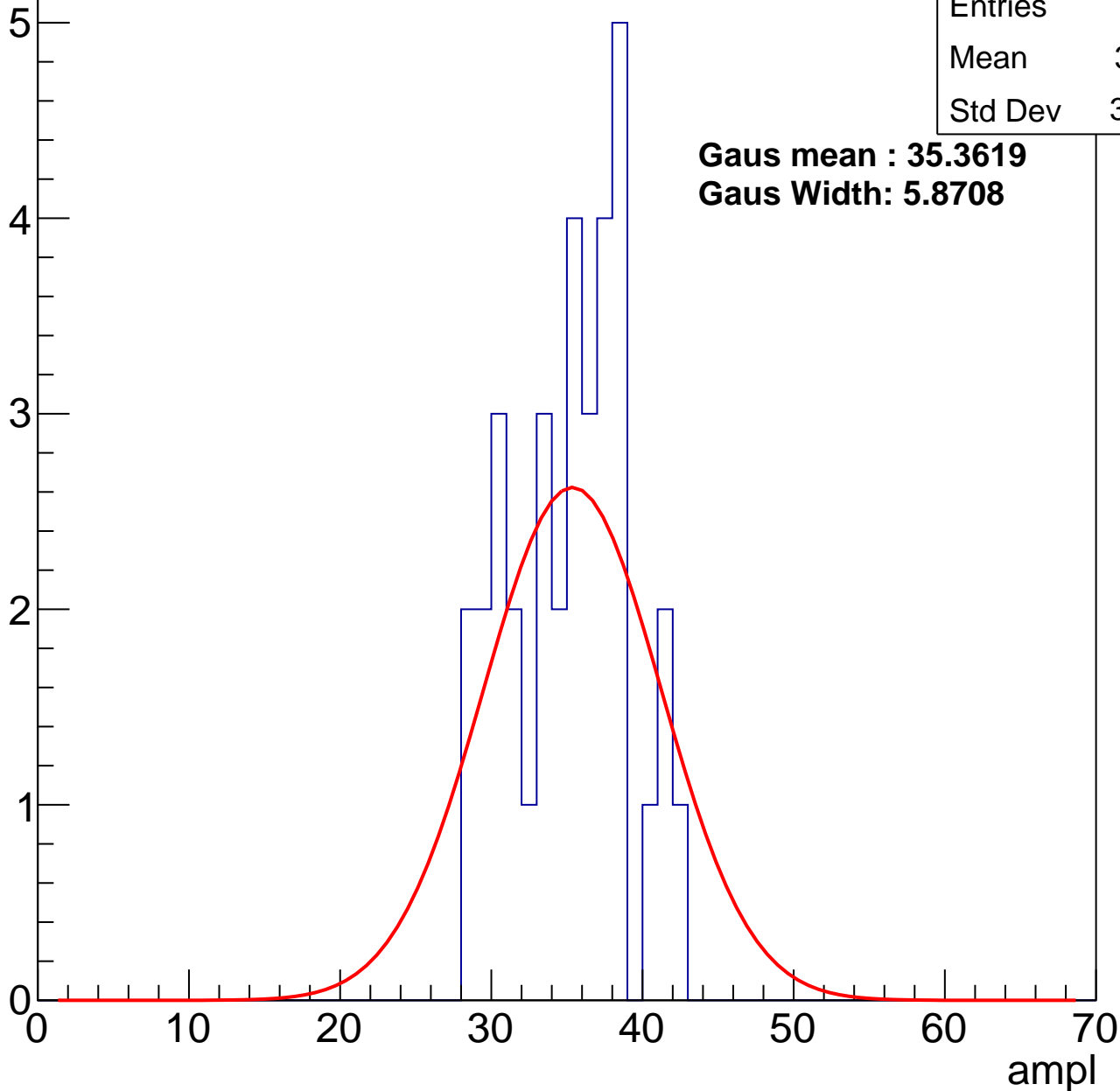
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	34.71
Std Dev	3.807

Gaus mean : 35.3619

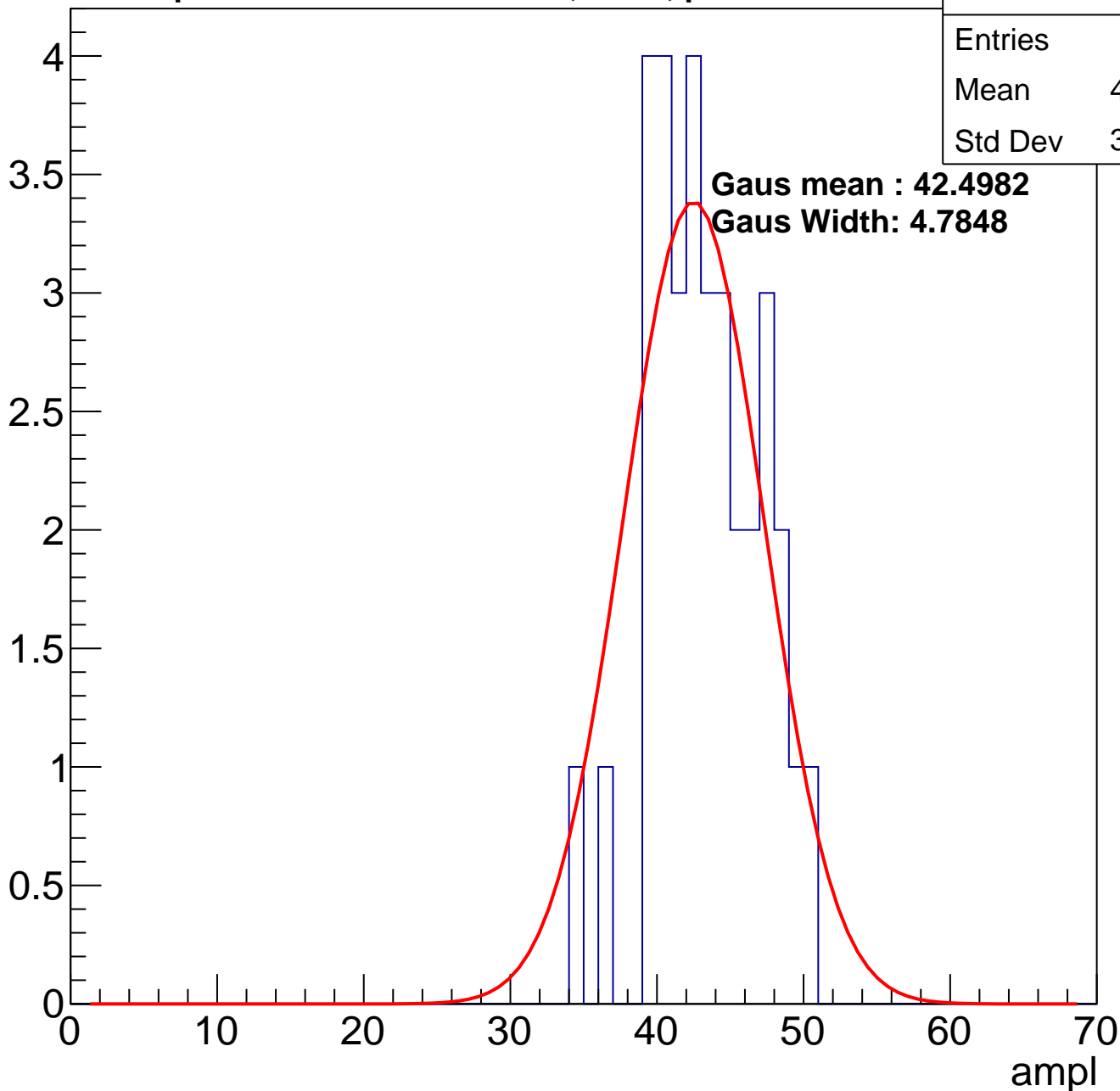
Gaus Width: 5.8708



B1L103S, U15-ch82, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

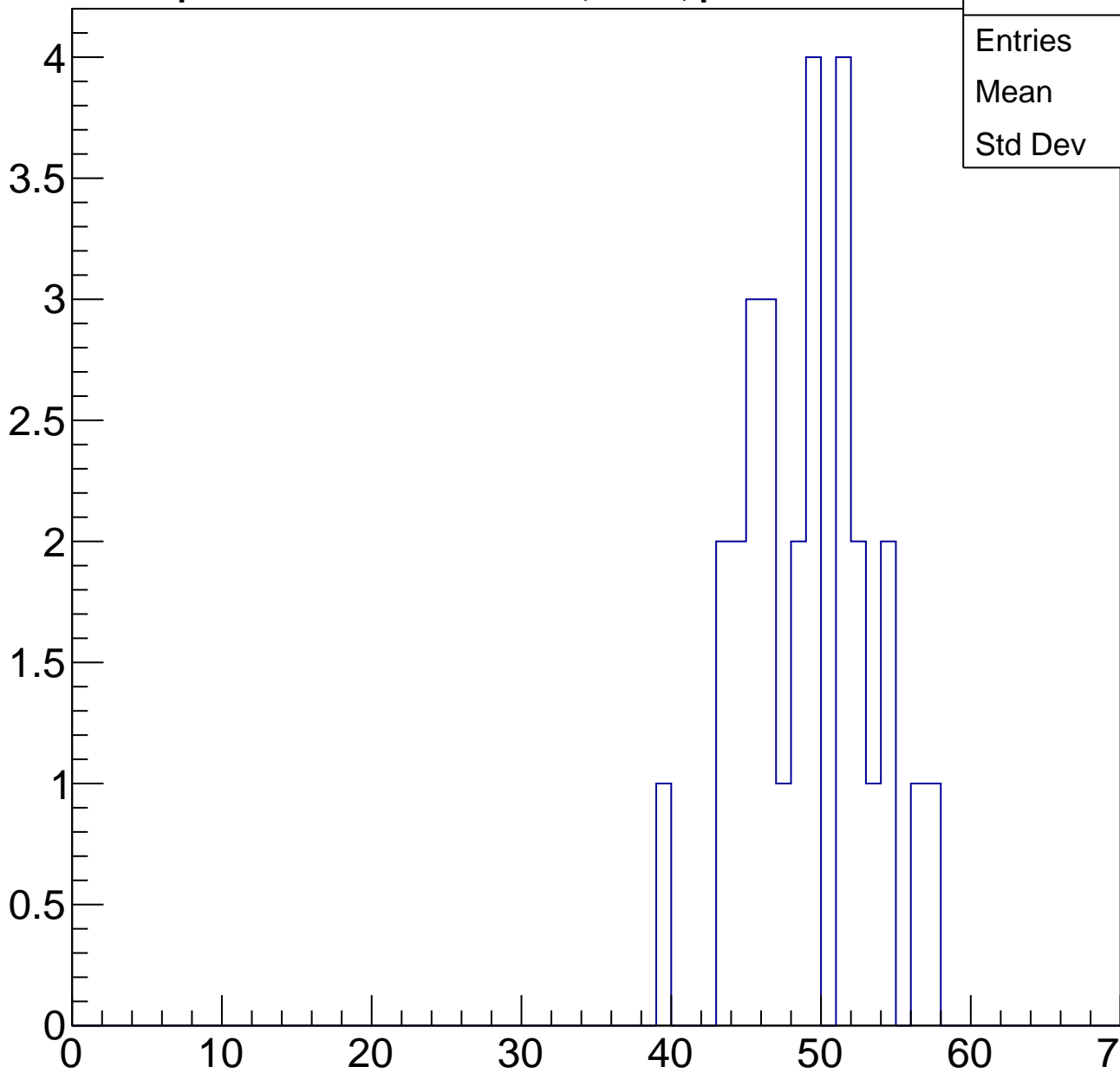
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	29
Mean	48.52
Std Dev	4.19

0 10 20 30 40 50 60 70

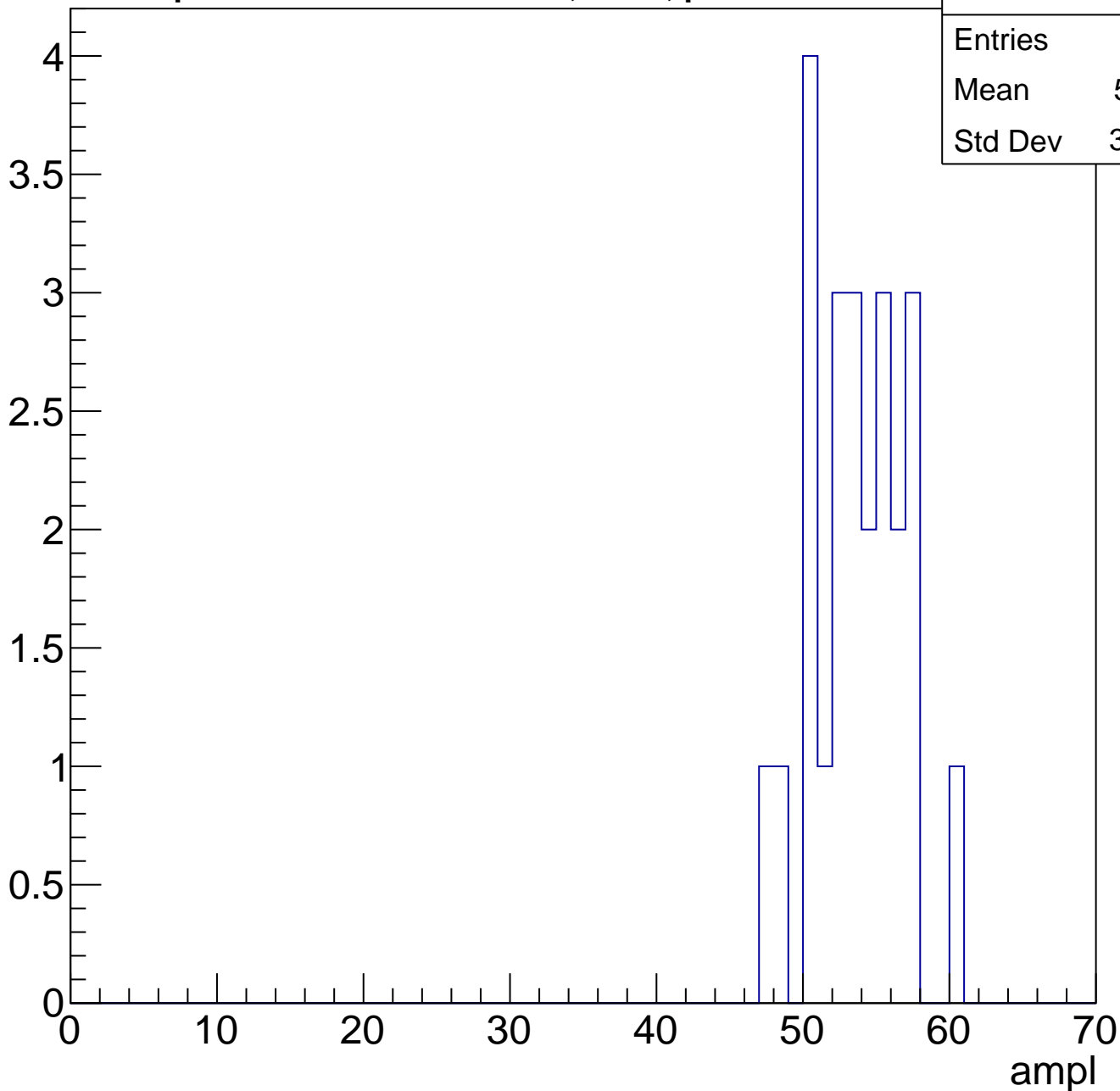
ampl



B1L103S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

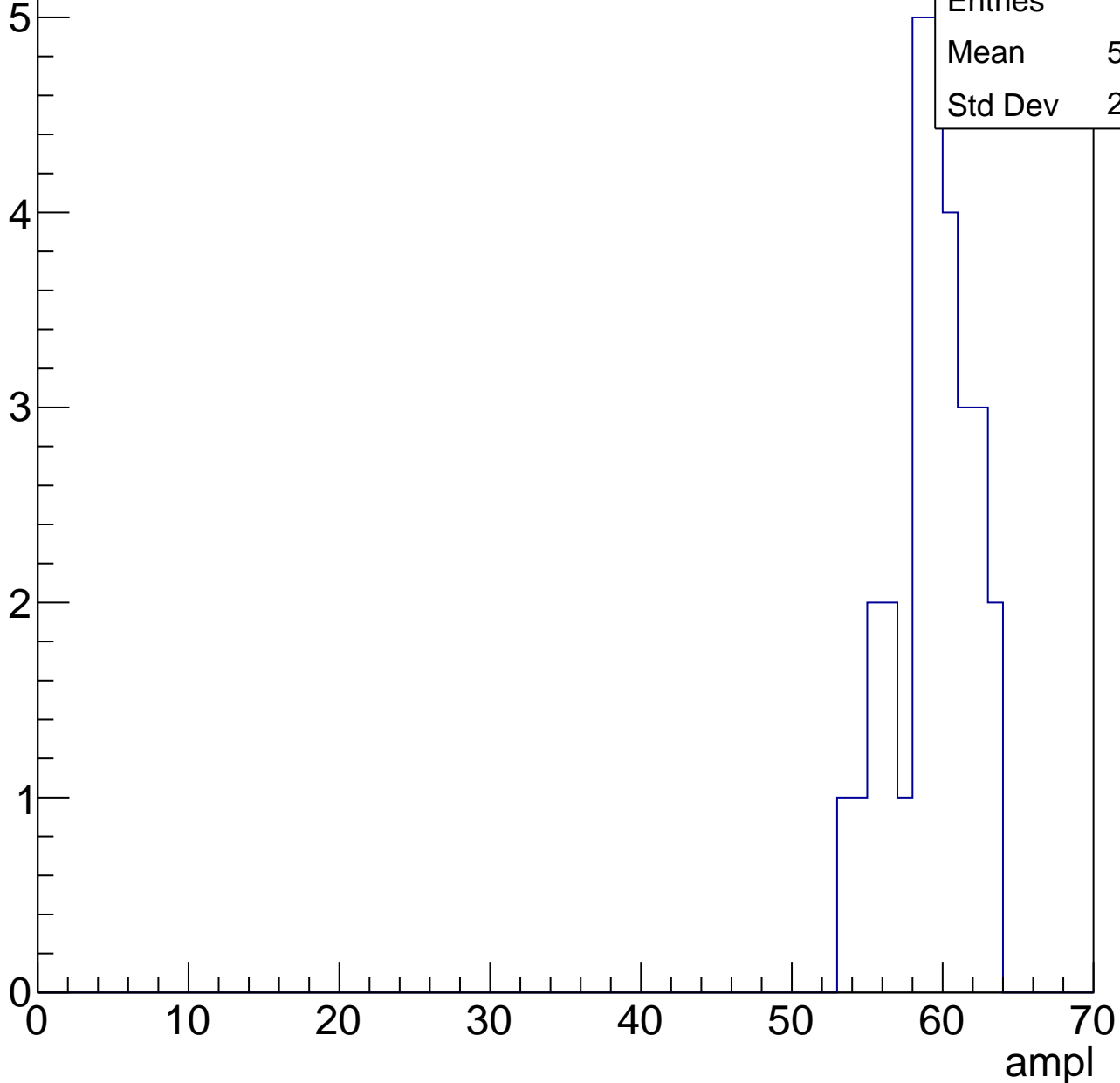
Entry



B1L103S, U15-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

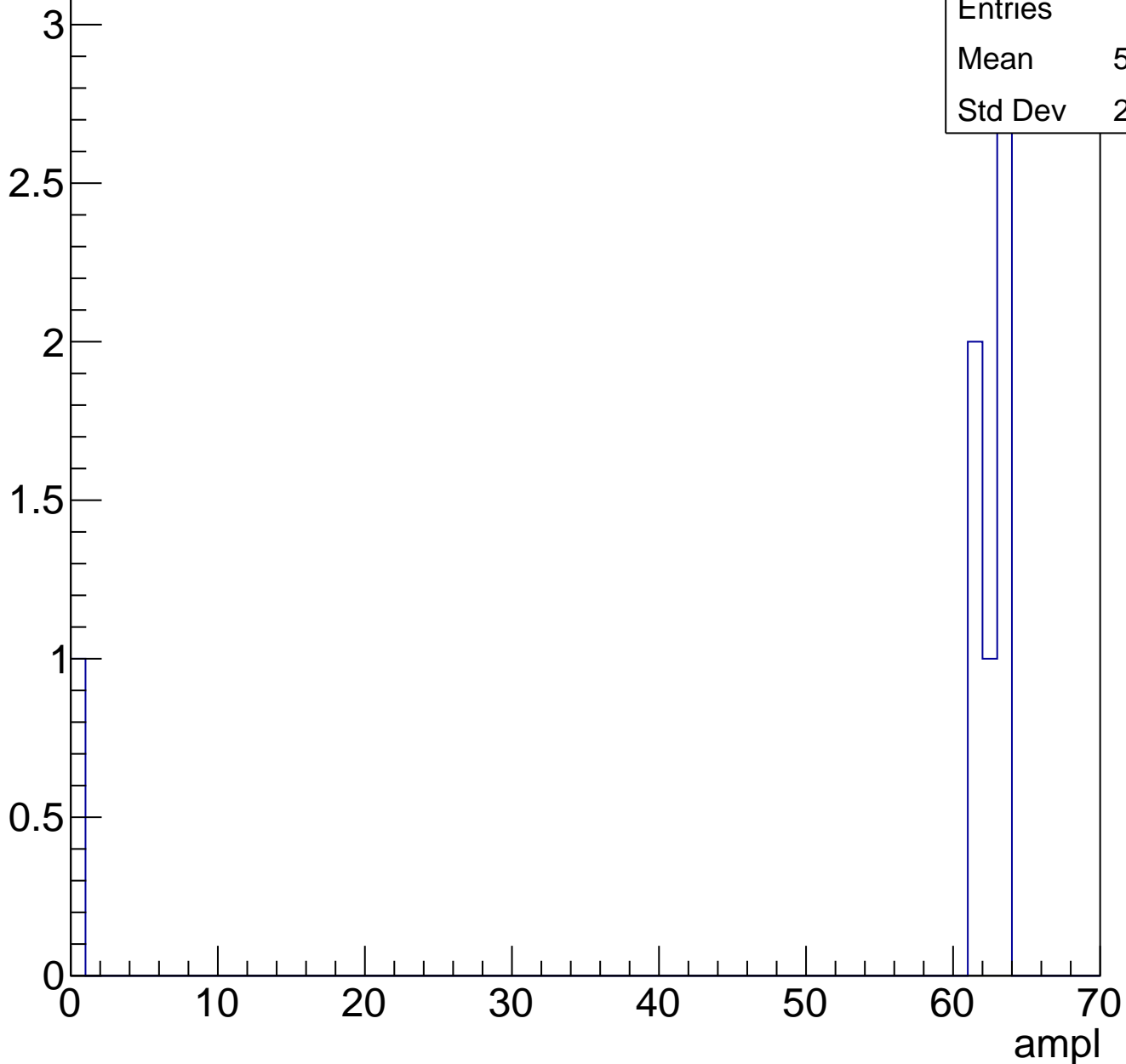
Entry



B1L103S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	7
Mean	53.29
Std Dev	21.77

B1L103S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	62
Std Dev	0

B1L103S, U15-ch83, adc0

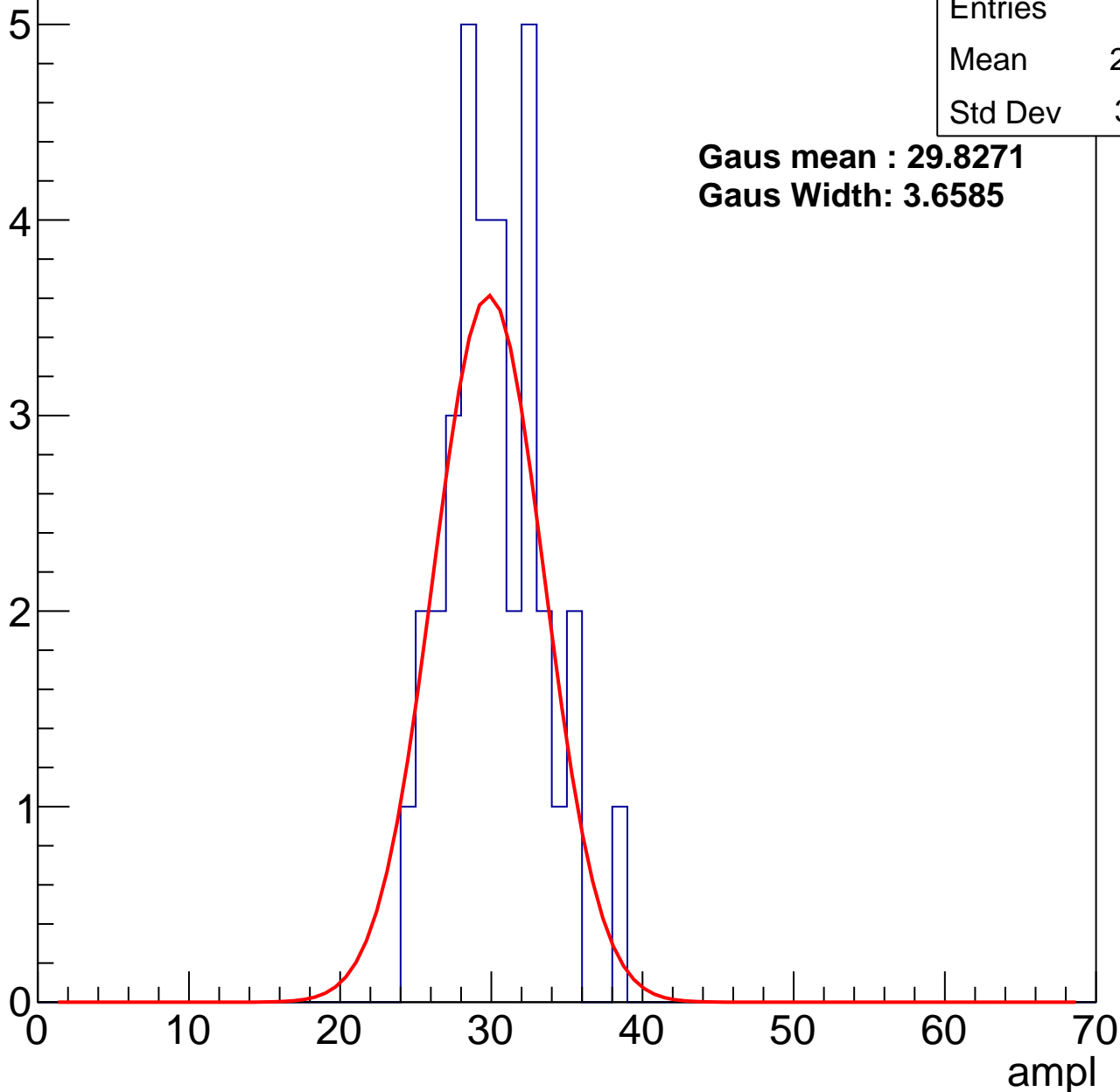
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	29.79
Std Dev	3.151

Gaus mean : 29.8271

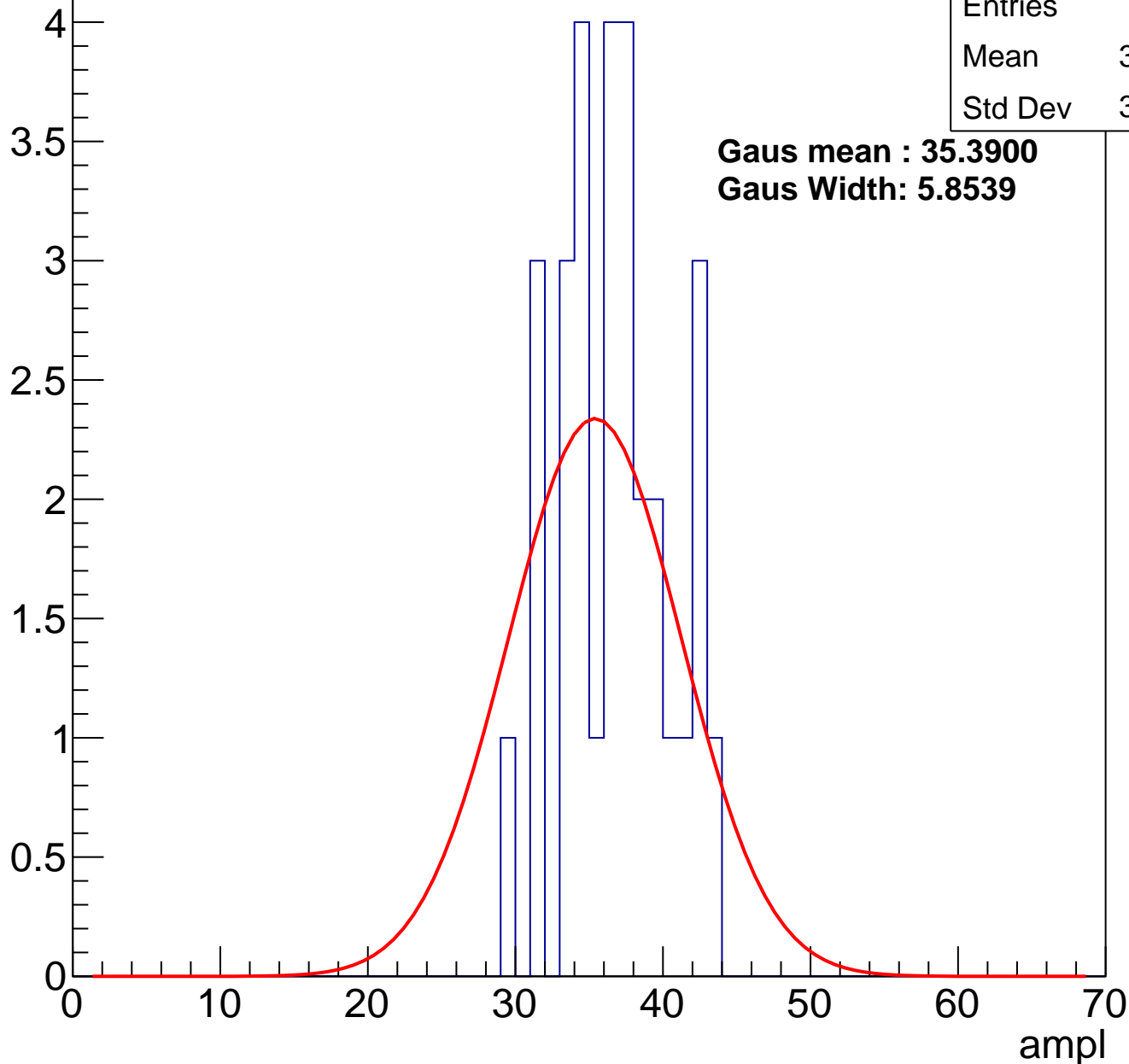
Gaus Width: 3.6585



B1L103S, U15-ch83, adc1

calib_packv5_042523_0143.root, FC#7, port C2

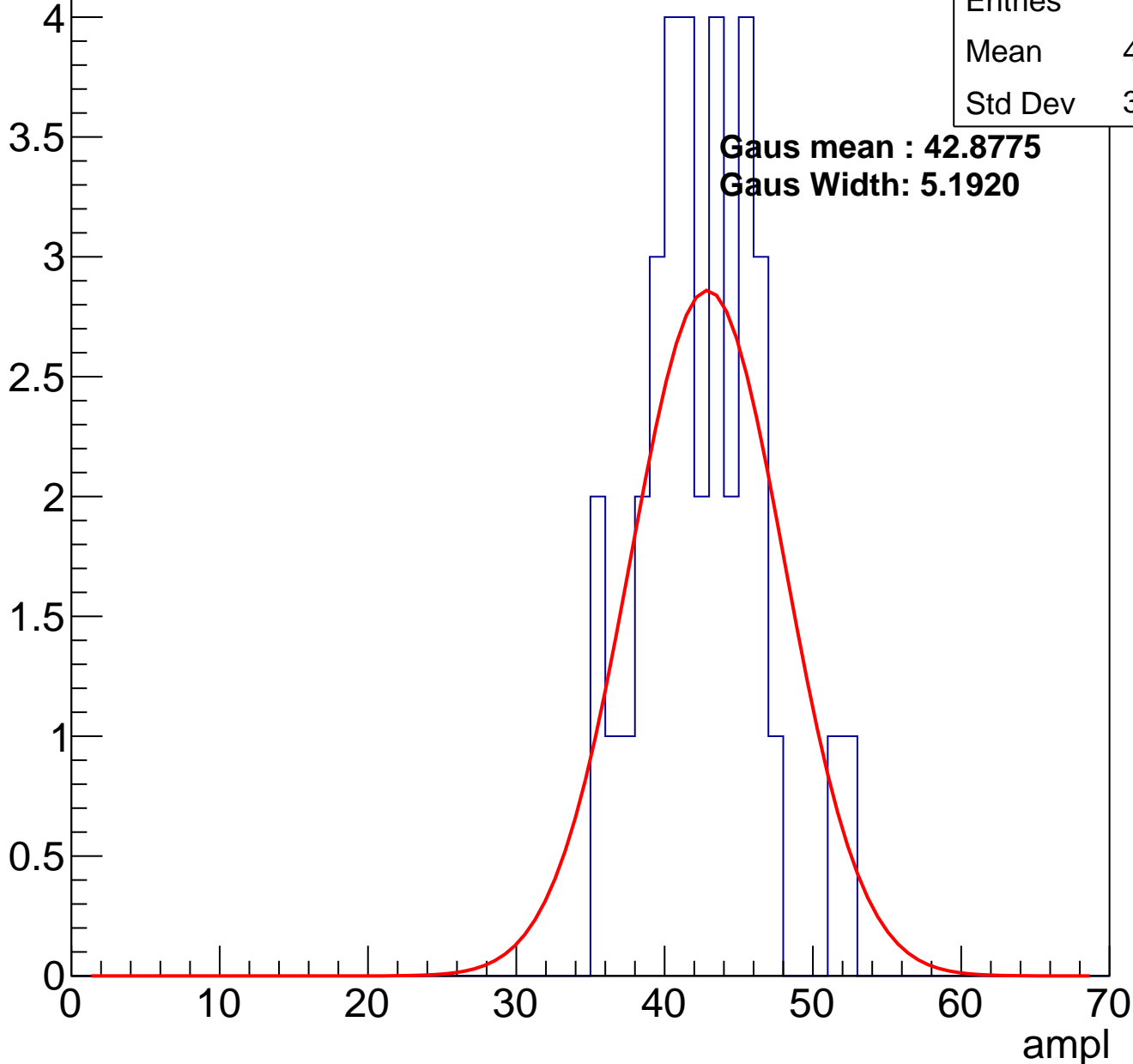
Entry



B1L103S, U15-ch83, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

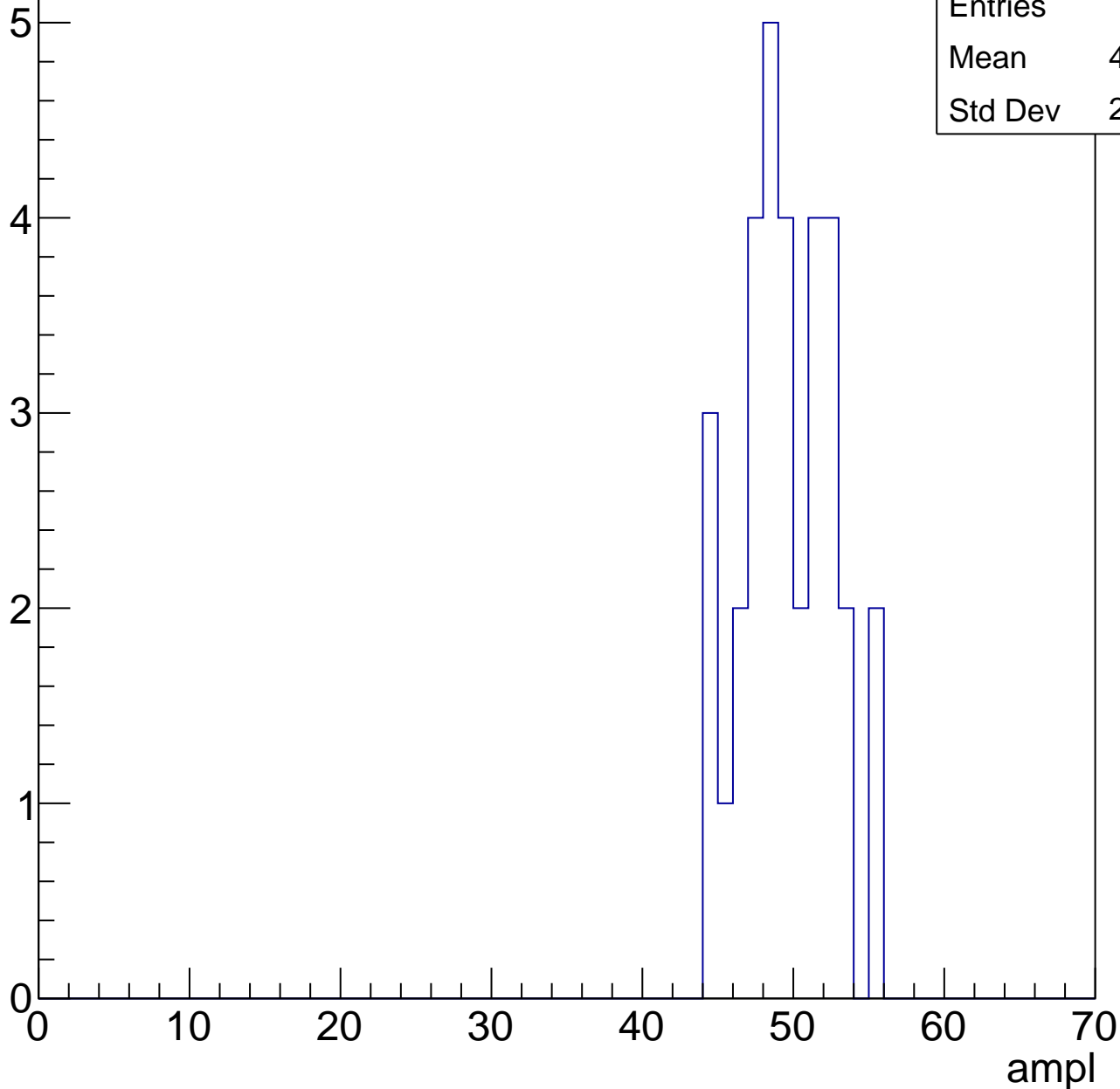


B1L103S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	49.12
Std Dev	2.952

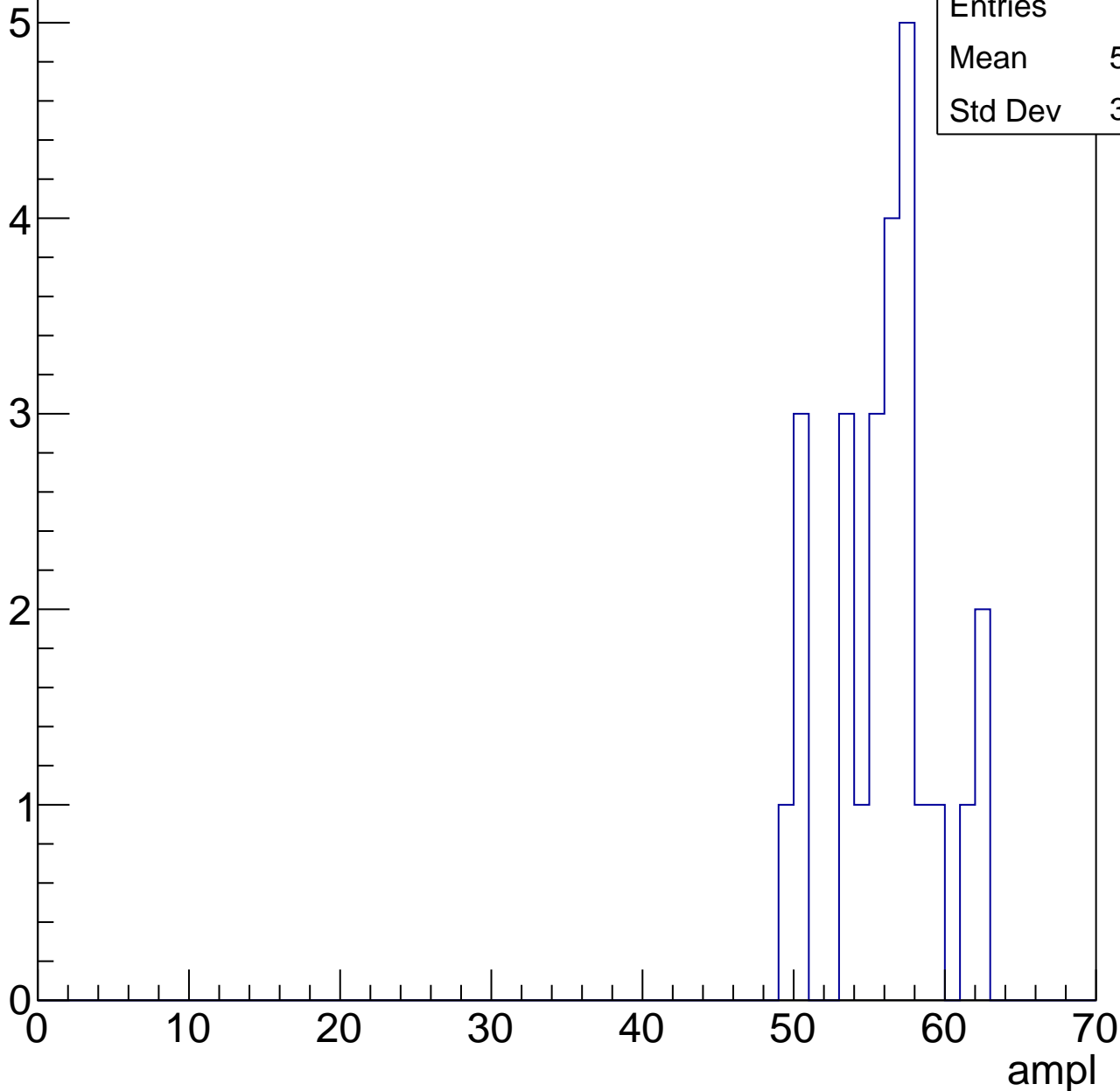


B1L103S, U15-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

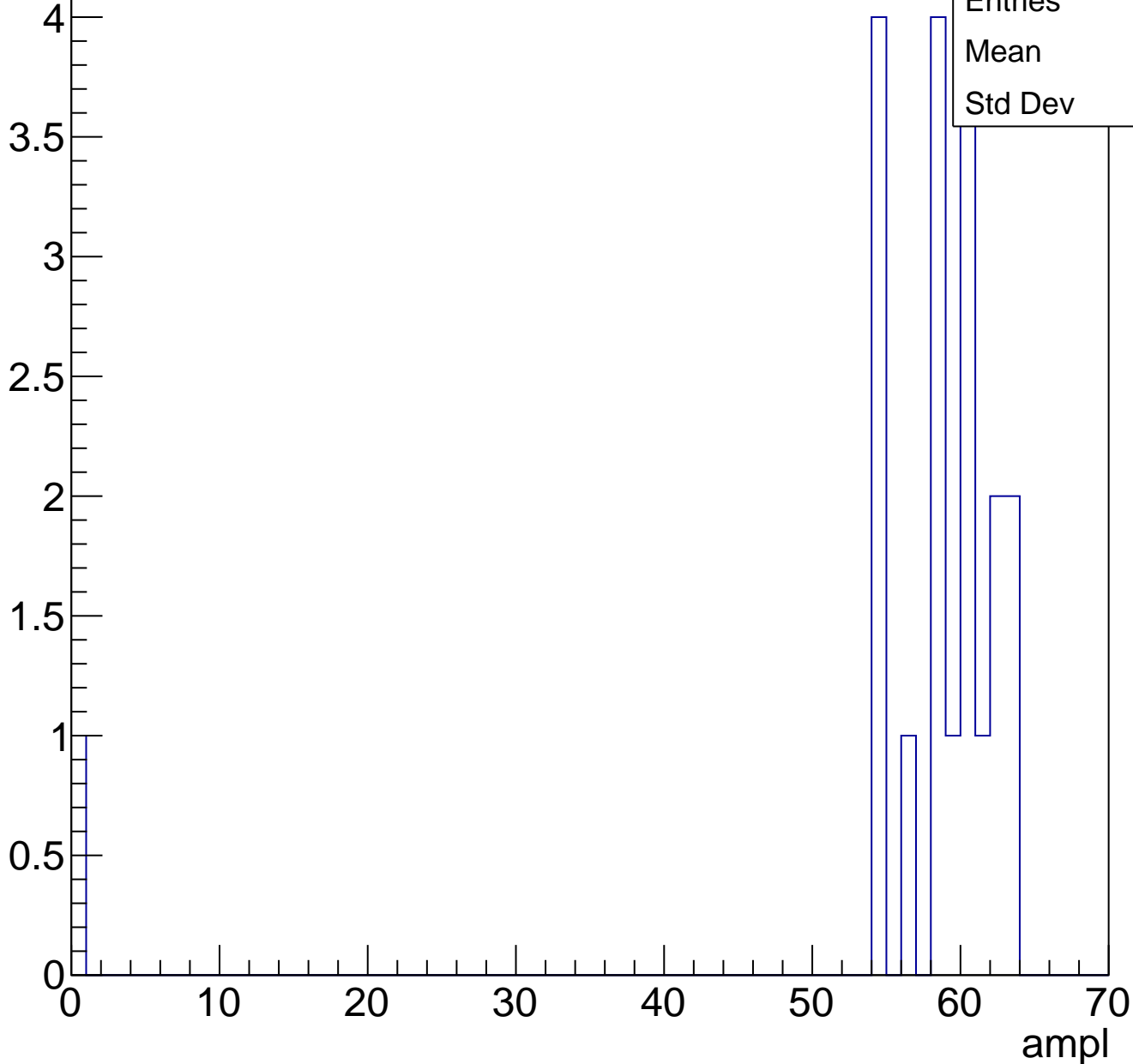
Entries	25
Mean	55.52
Std Dev	3.465



B1L103S, U15-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61
Std Dev	1.537

B1L103S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

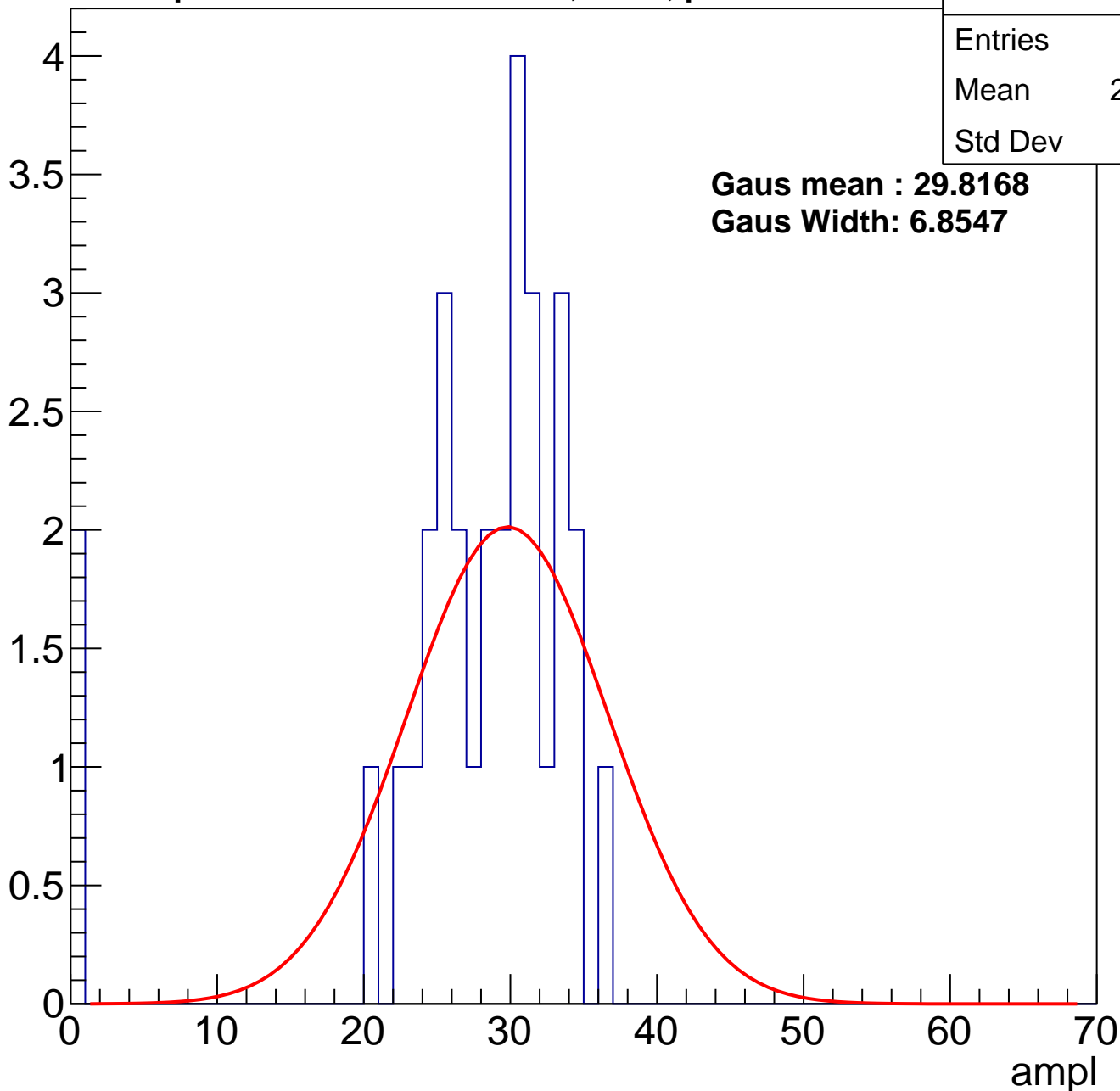


Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch84, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch84, adc1

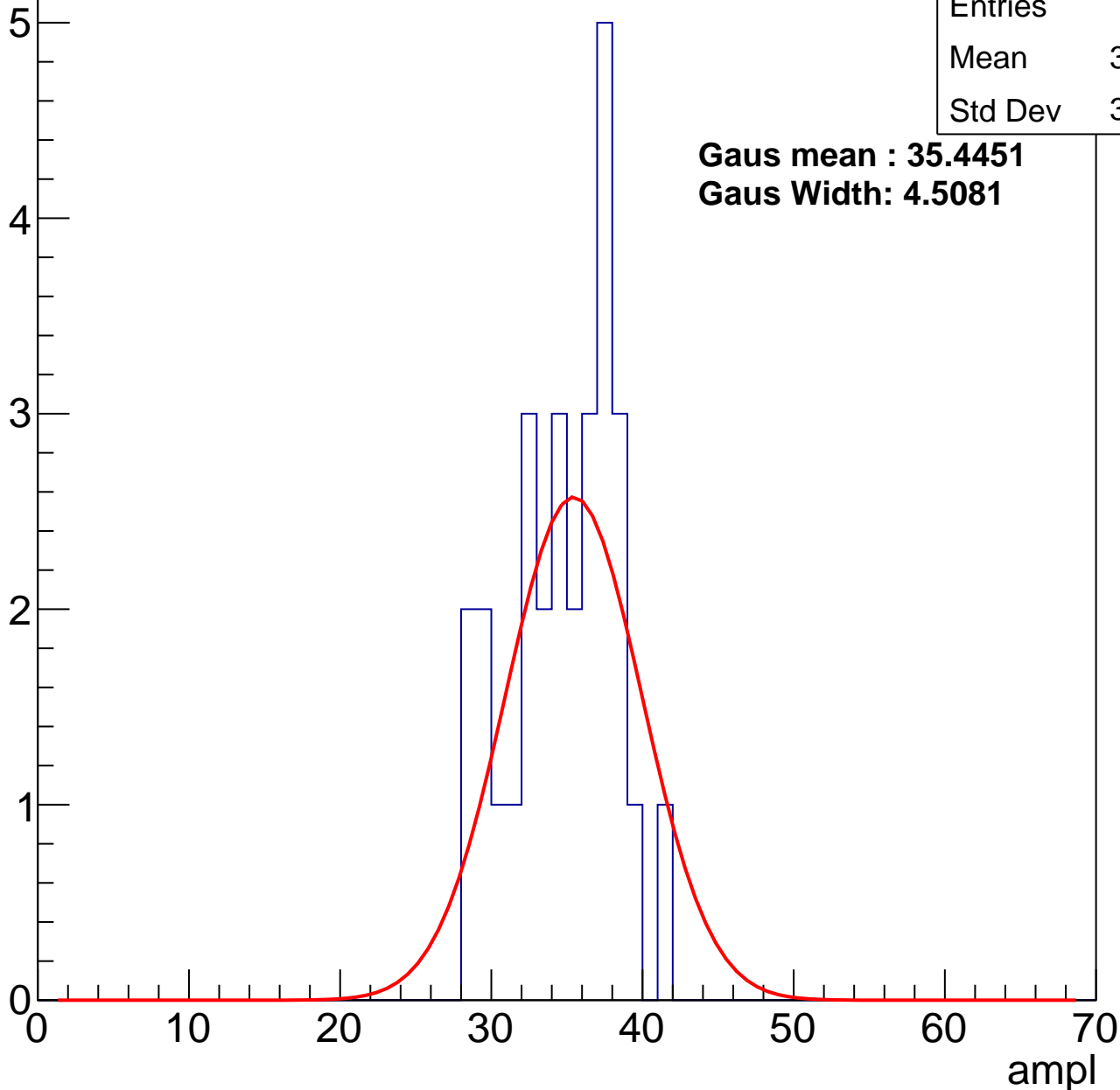
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	34.34
Std Dev	3.417

Gaus mean : 35.4451

Gaus Width: 4.5081



B1L103S, U15-ch84, adc2

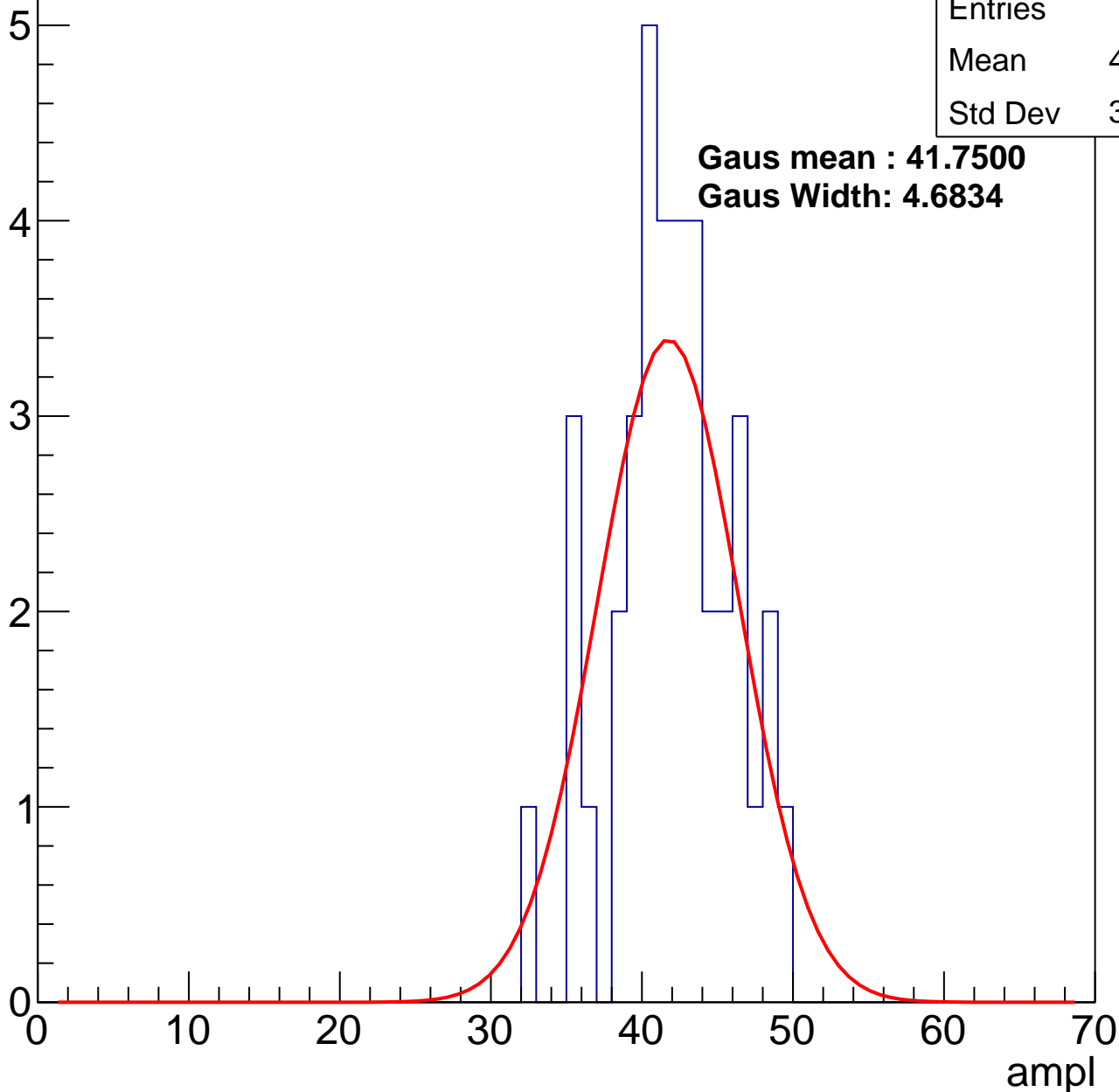
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	41.53
Std Dev	3.912

Gaus mean : 41.7500

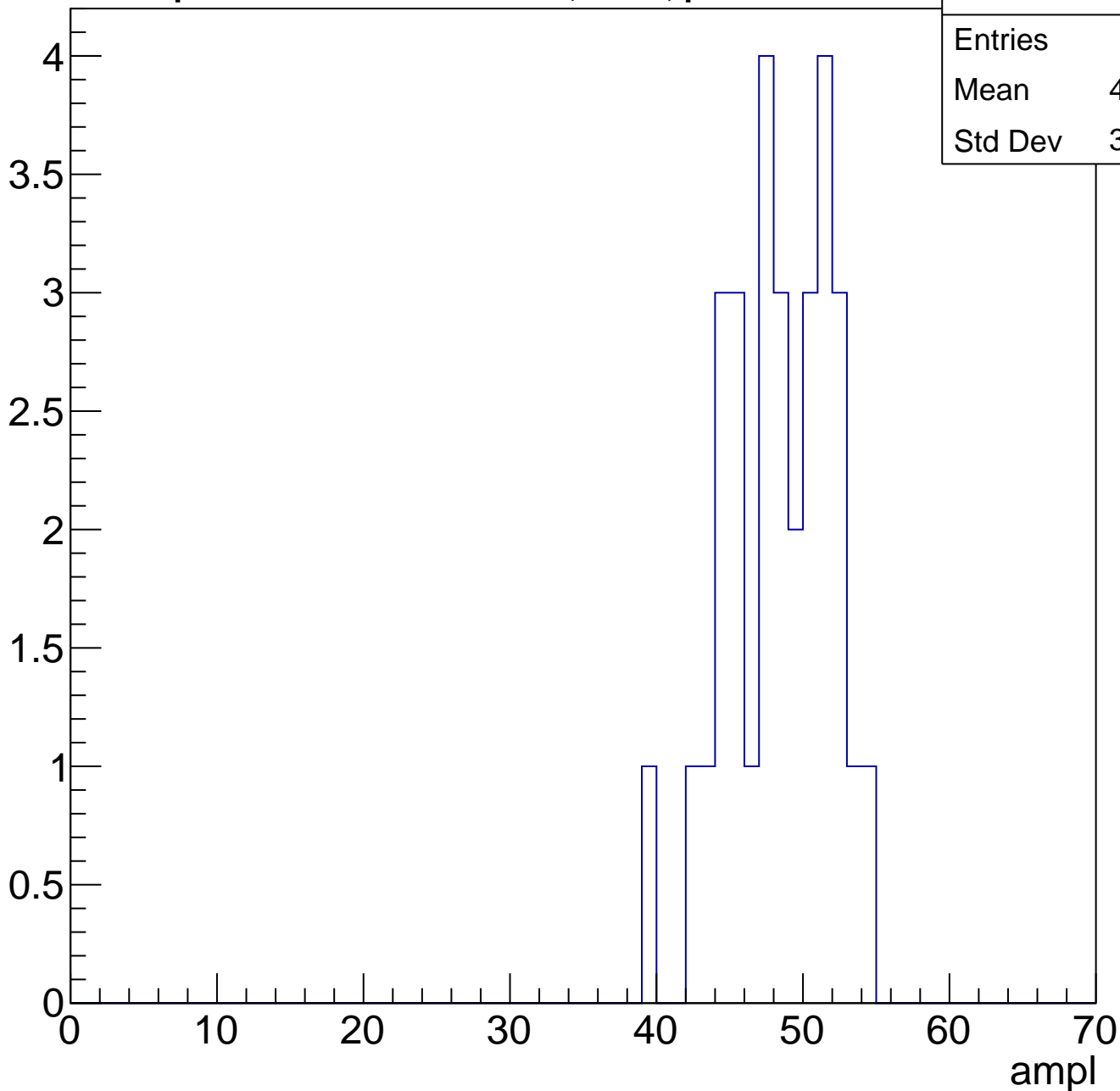
Gaus Width: 4.6834



B1L103S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

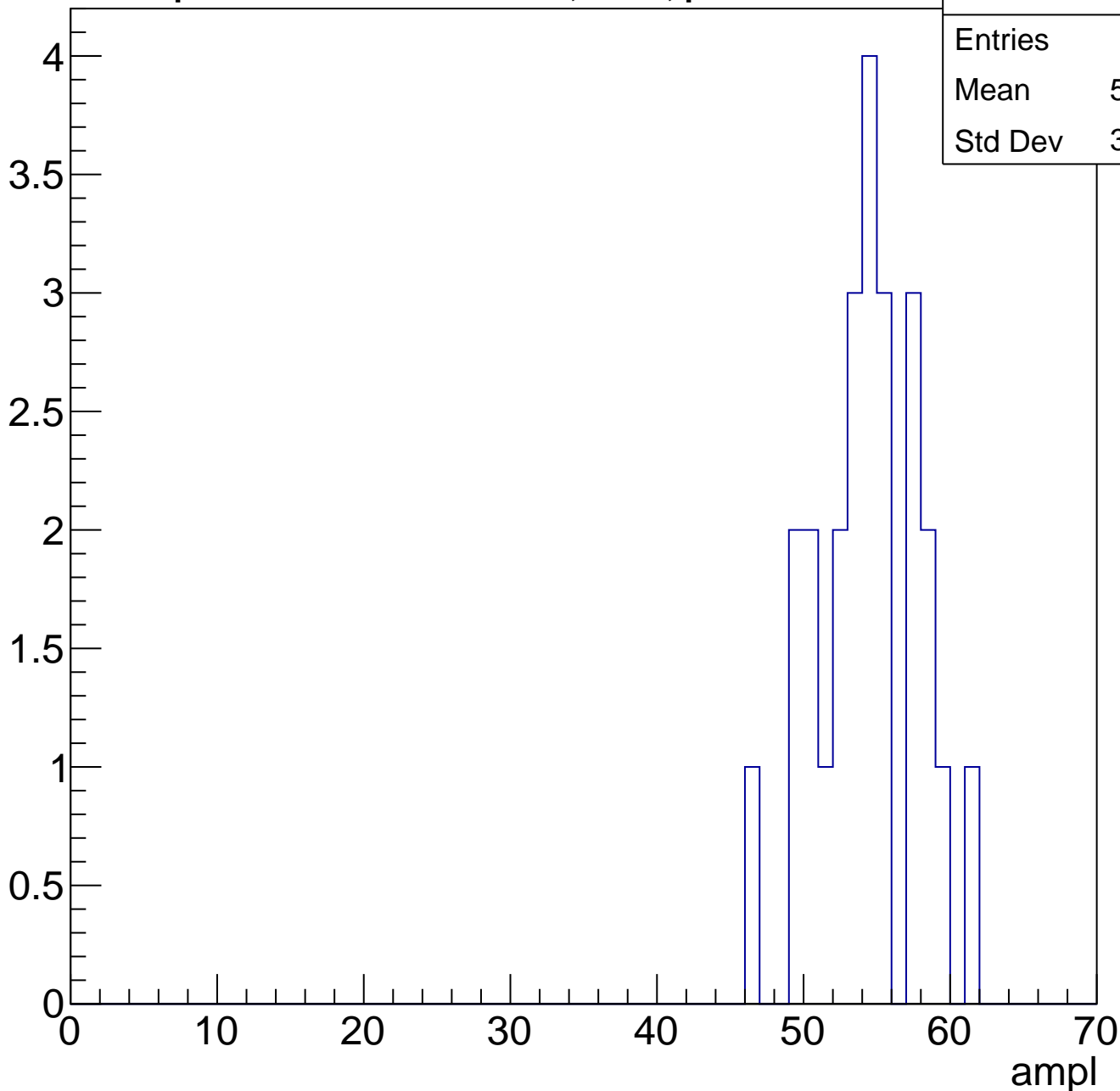
Entry



B1L103S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



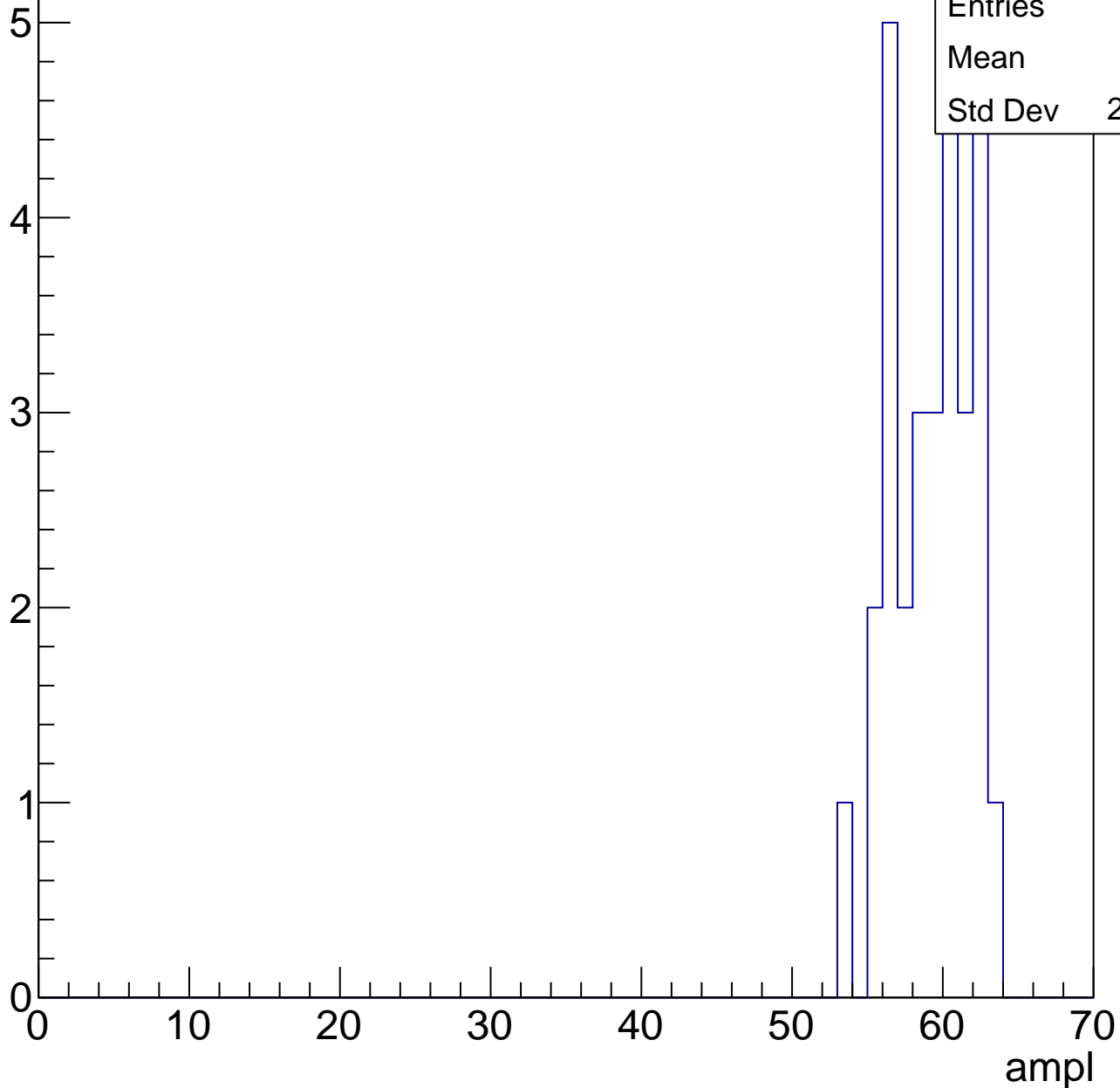
Entries	25
Mean	53.84
Std Dev	3.472

B1L103S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

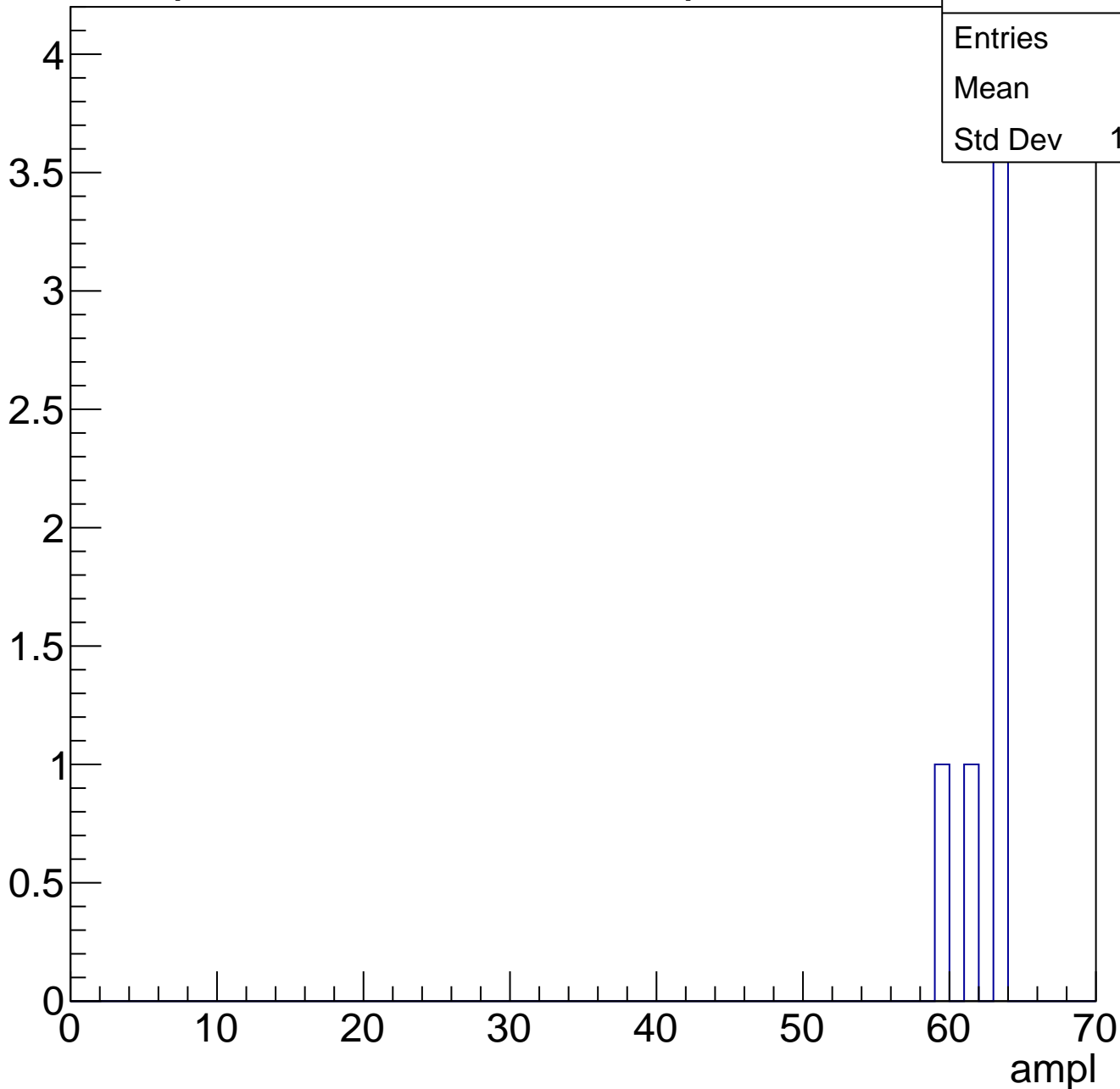
Entries	30
Mean	58.8
Std Dev	2.587



B1L103S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

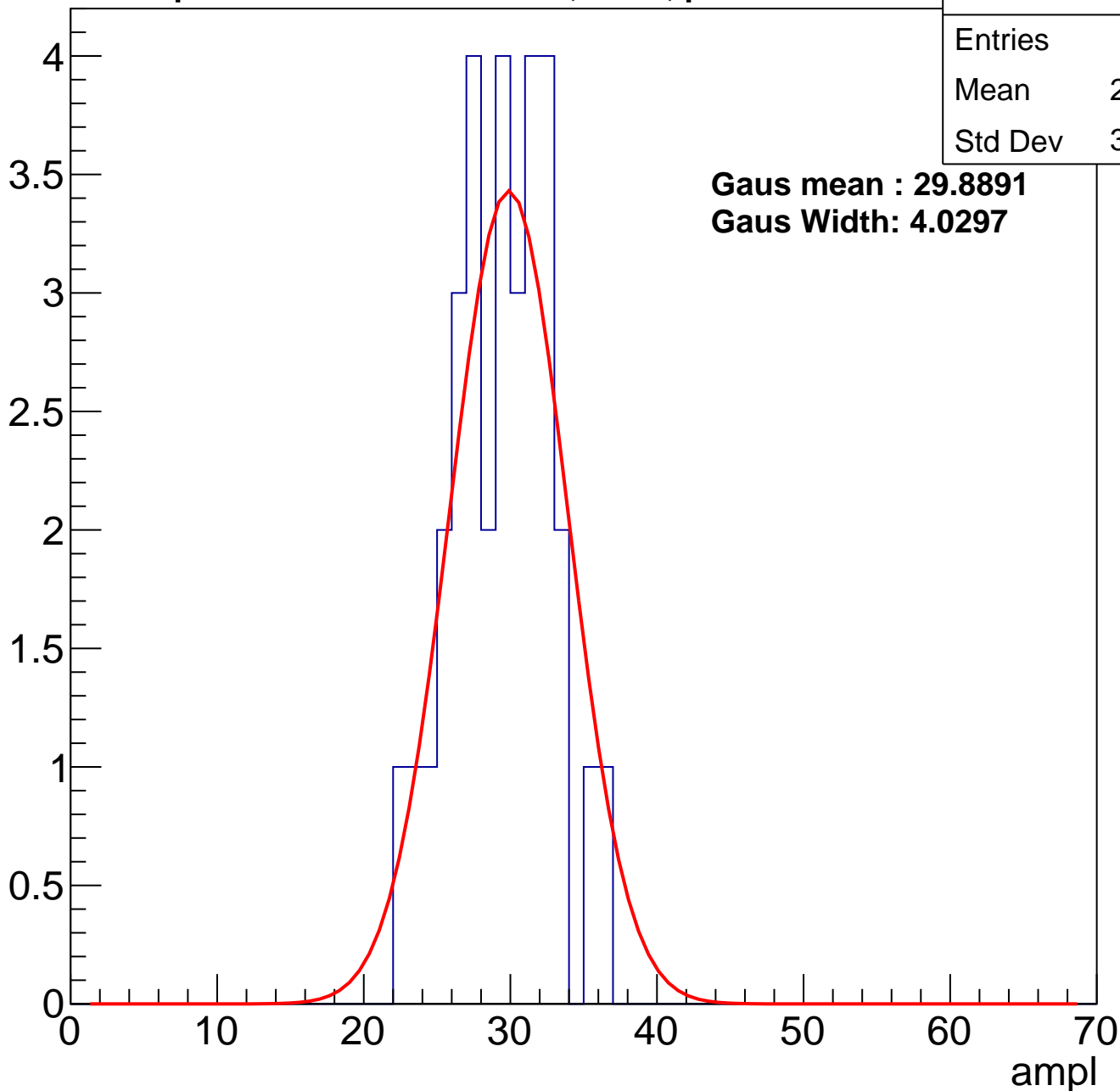


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch85, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

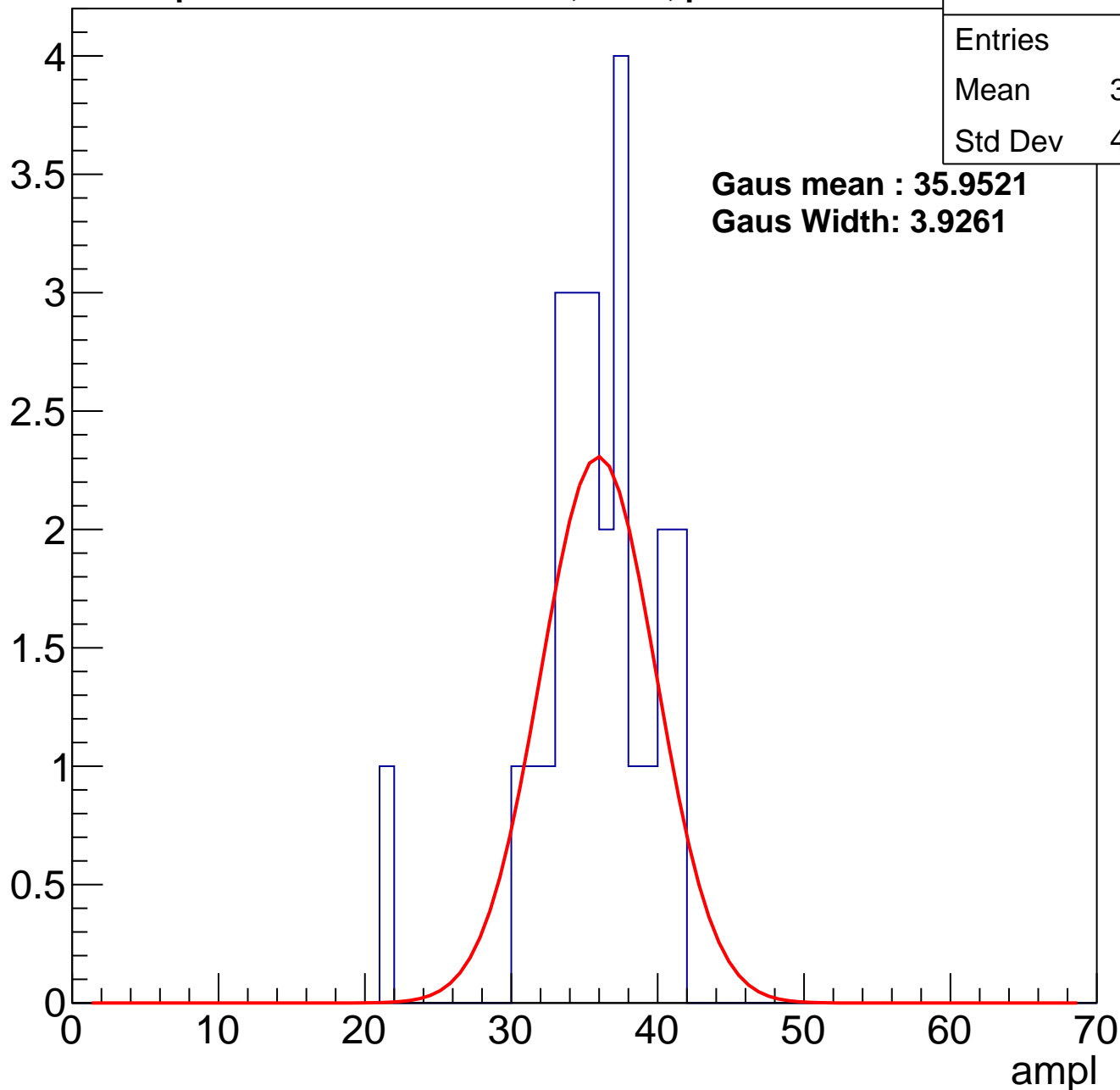


Entries	33
Mean	28.97
Std Dev	3.298

B1L103S, U15-ch85, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch85, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	40
Std Dev	3.016

Gaus mean : 40.9313

Gaus Width: 3.5770

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

5

4

3

2

1

0

0

10

20

30

40

50

60

70

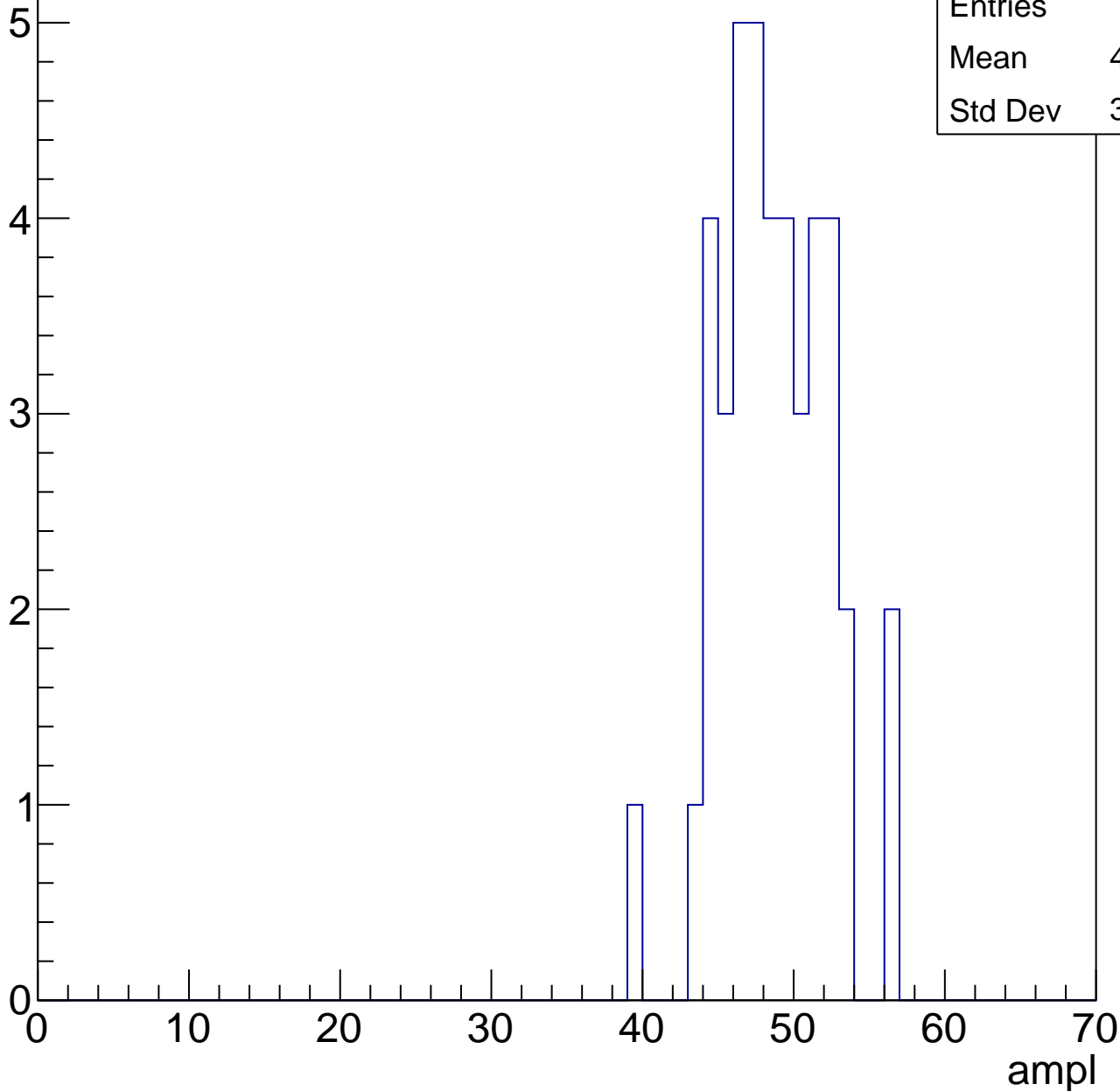
ampl

B1L103S, U15-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

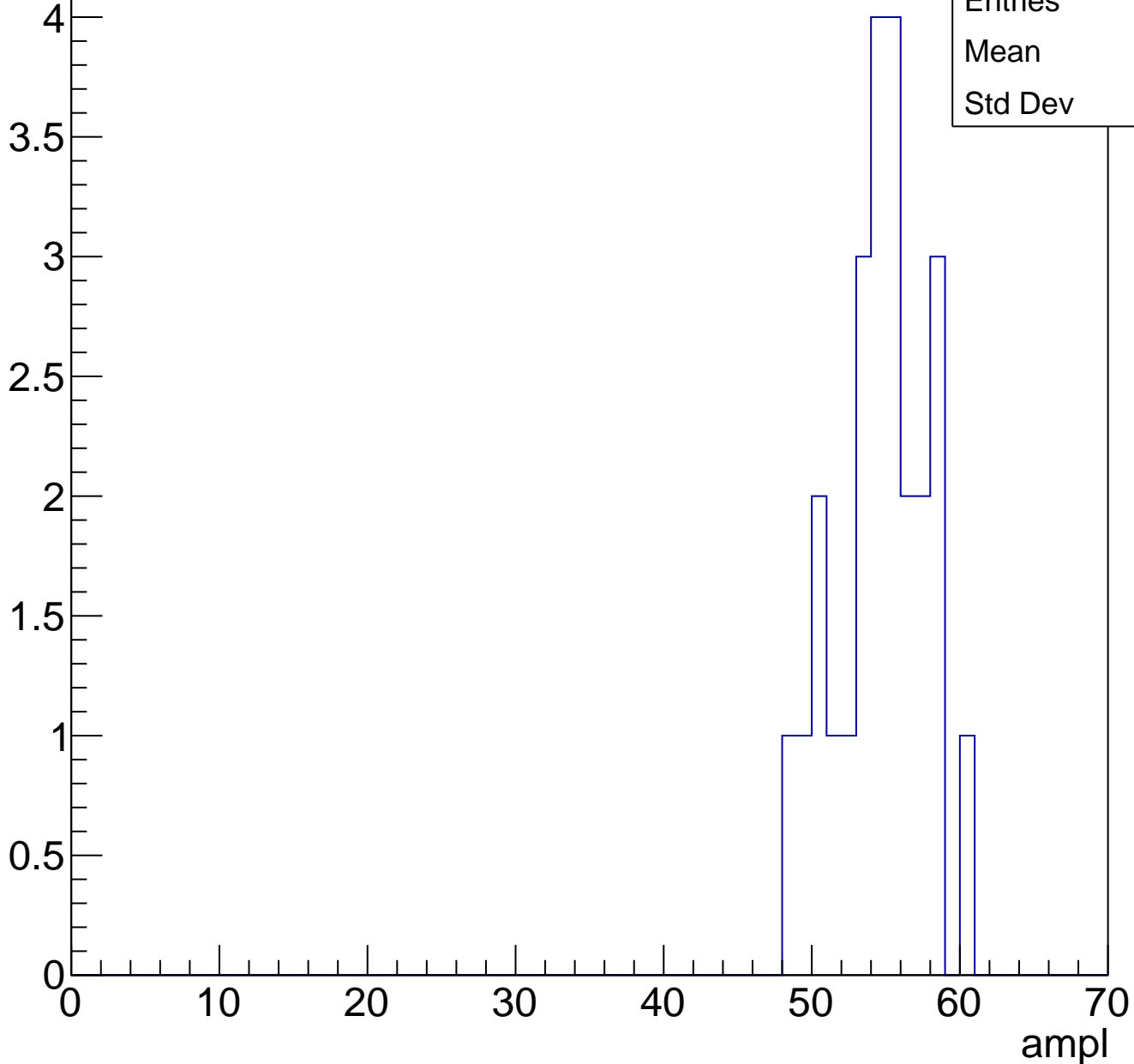
Entries	42
Mean	48.24
Std Dev	3.497



B1L103S, U15-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	25
Mean	54.2
Std Dev	2.98

B1L103S, U15-ch85, adc5

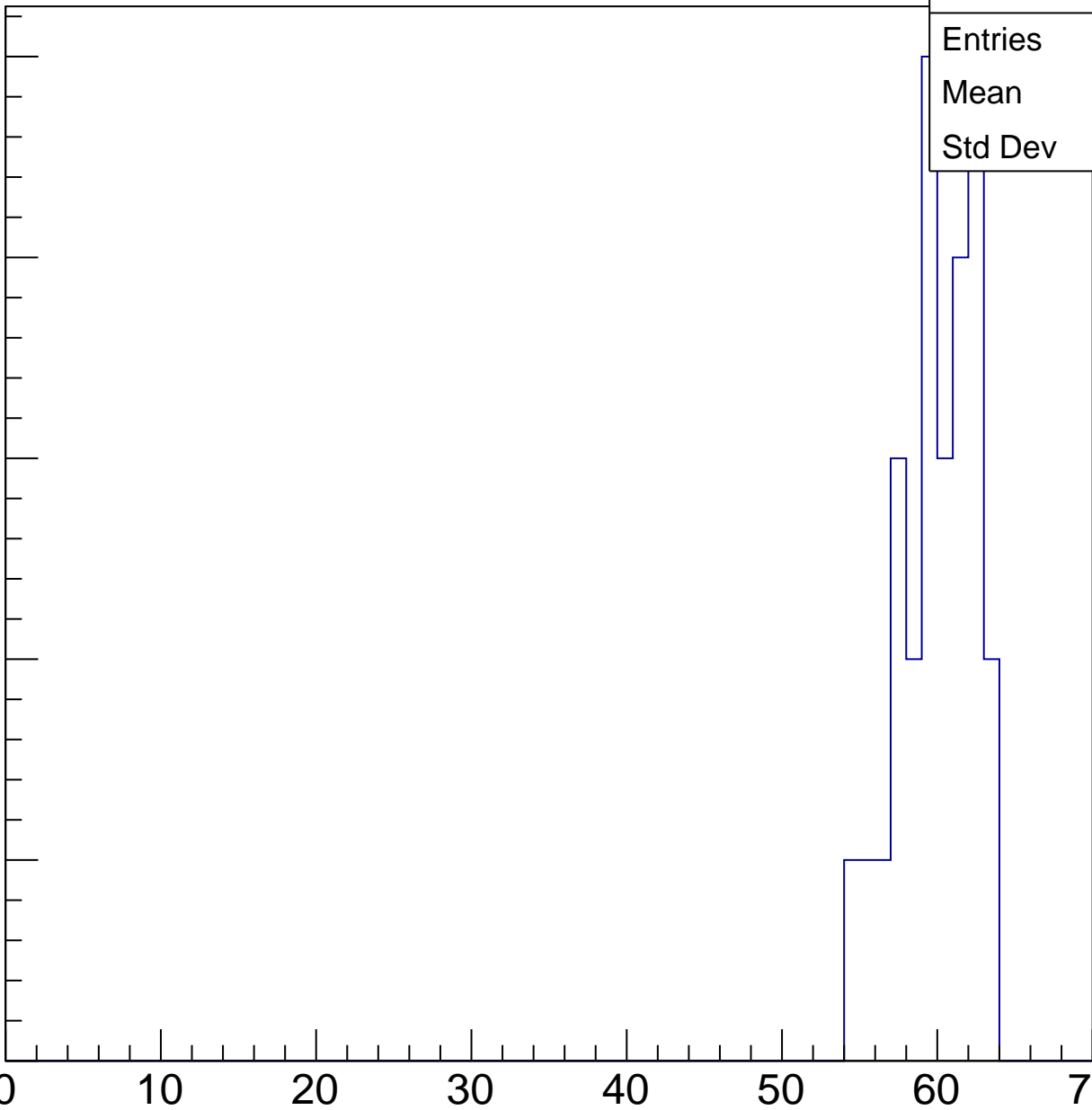
calib_packv5_042523_0143.root, FC#7, port C2

Entry

5
4
3
2
1
0

Entries	27
Mean	59.52
Std Dev	2.378

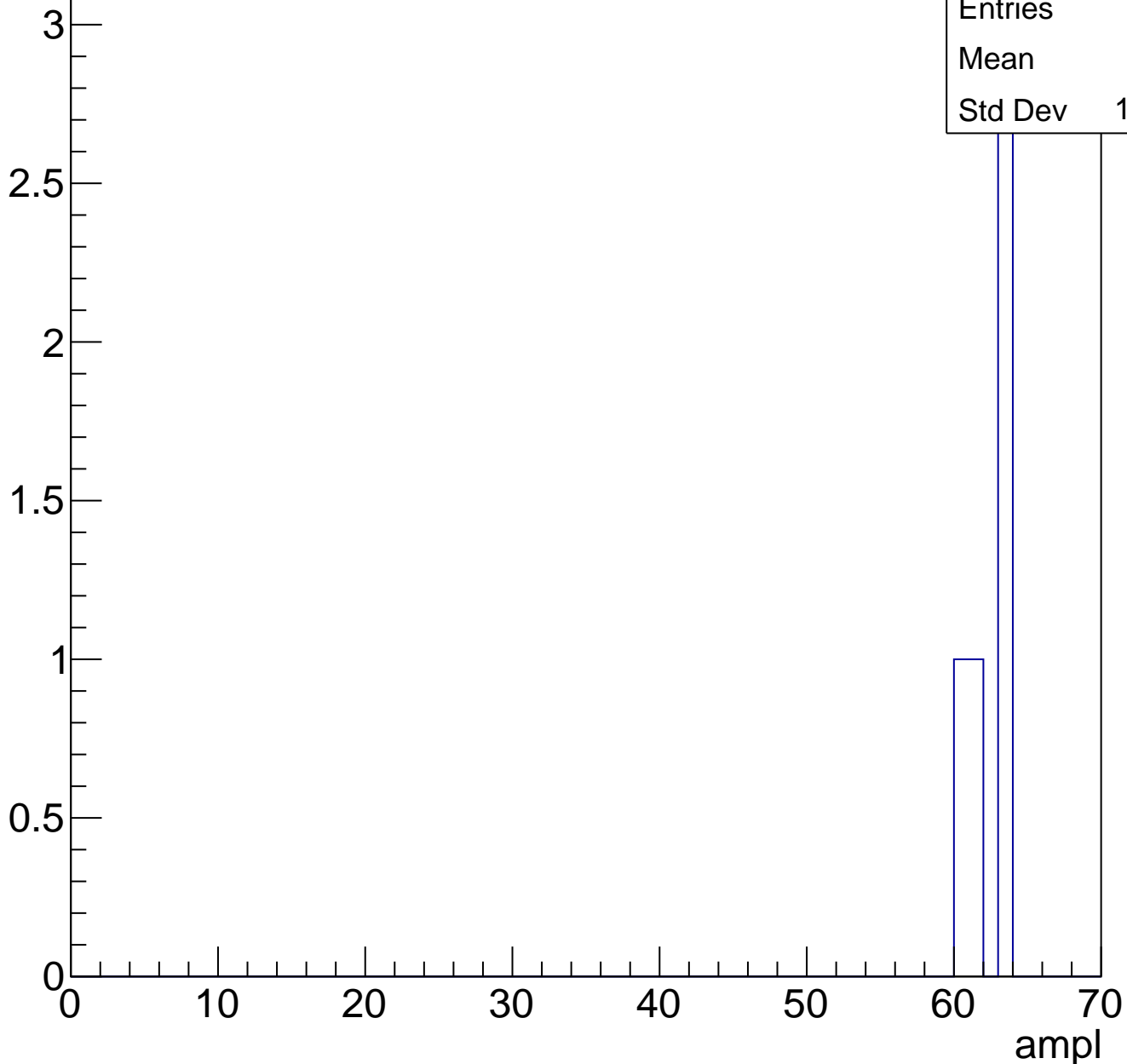
ampl



B1L103S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch86, adc0

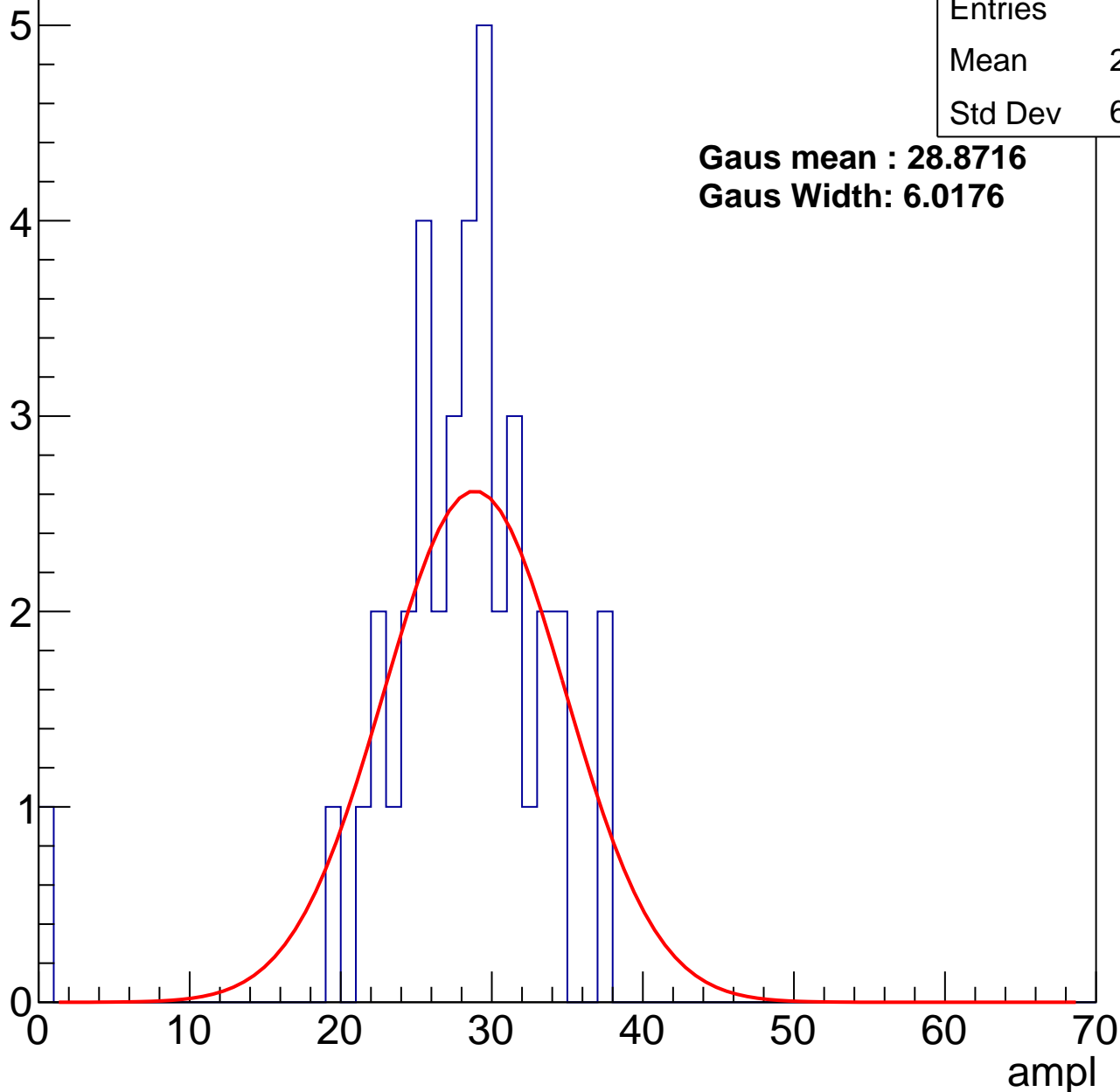
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	27.32
Std Dev	6.096

Gaus mean : 28.8716

Gaus Width: 6.0176



B1L103S, U15-ch86, adc1

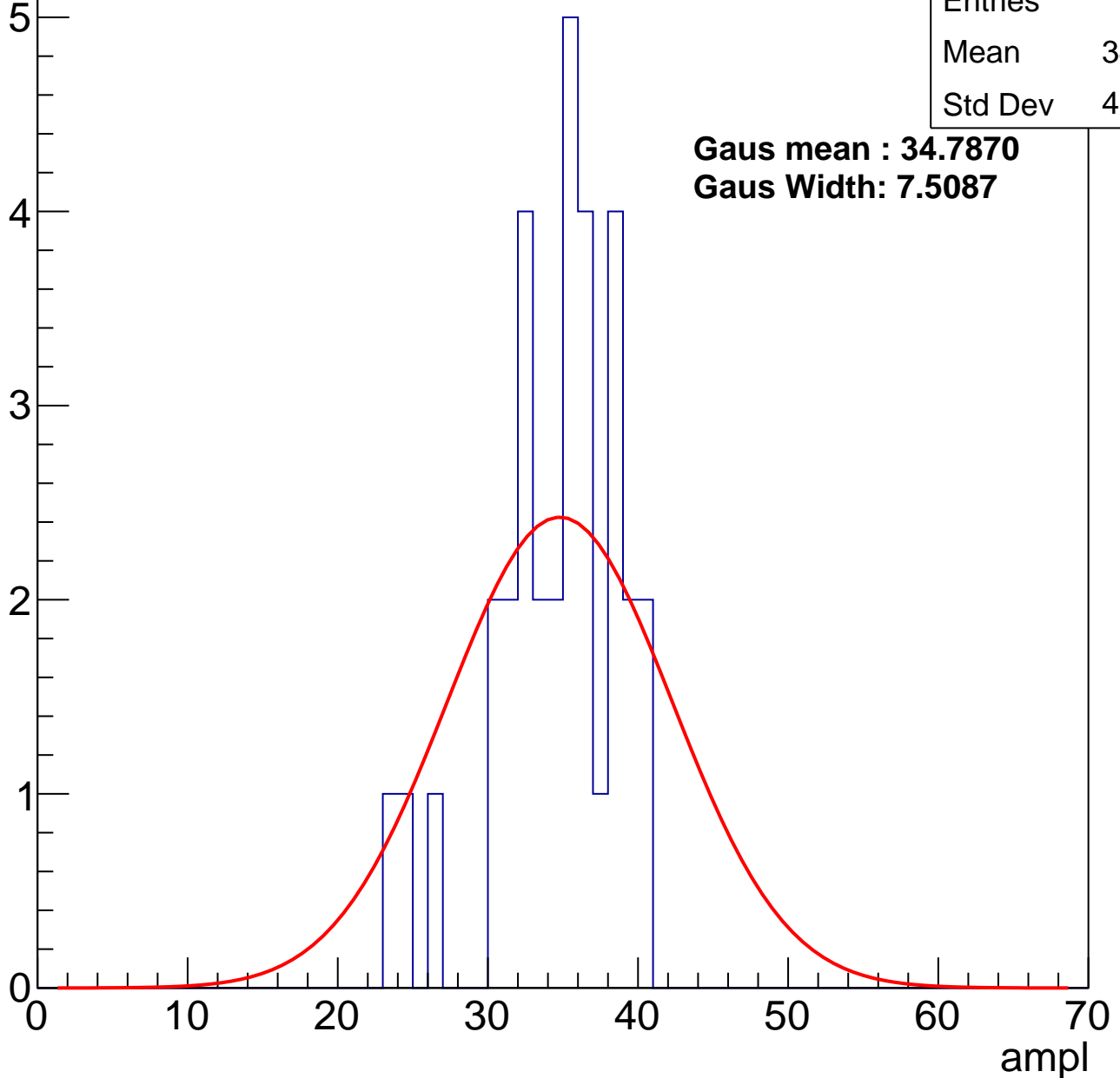
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	34.03
Std Dev	4.152

Gaus mean : 34.7870

Gaus Width: 7.5087



B1L103S, U15-ch86, adc2

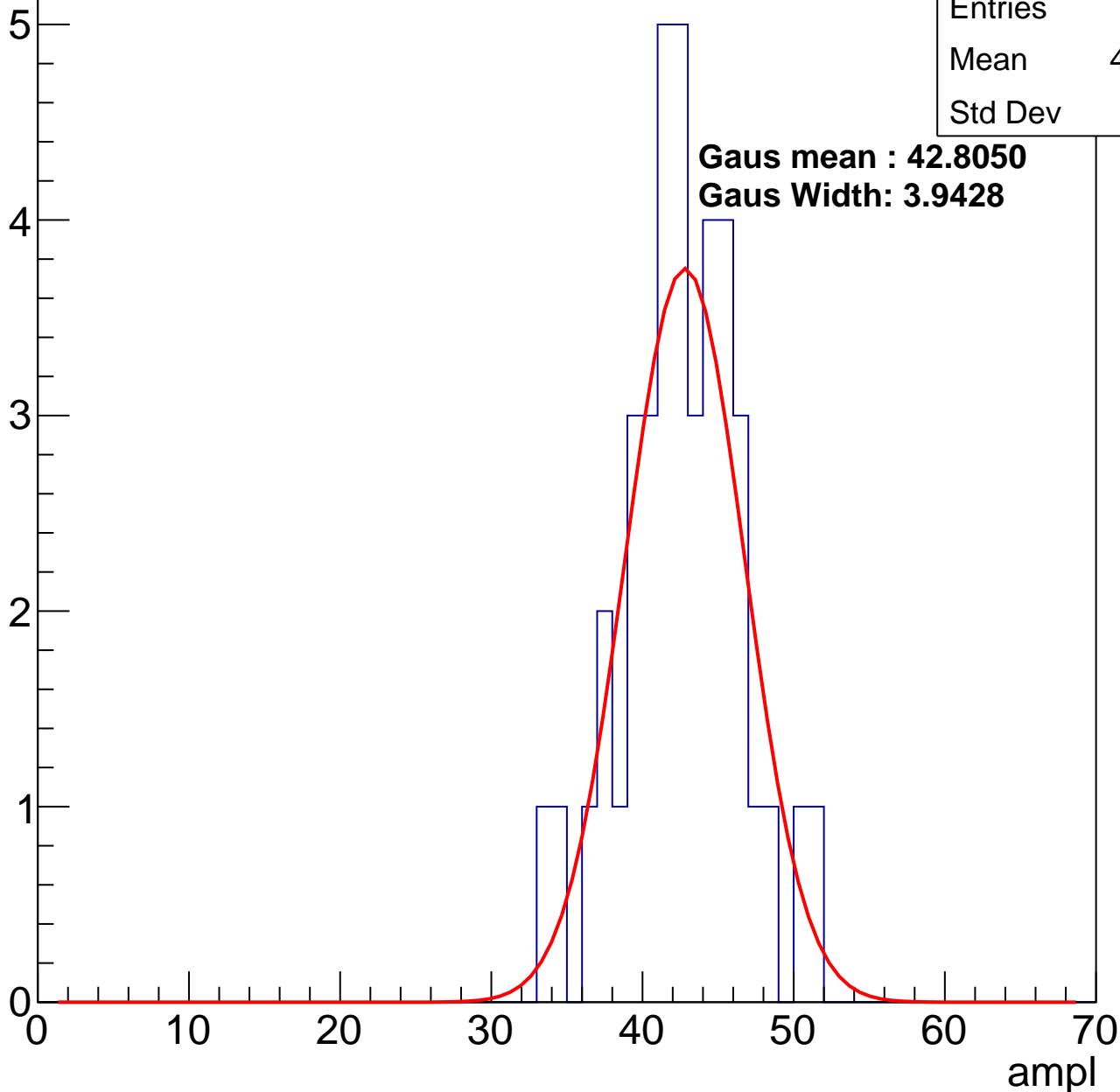
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	42.15
Std Dev	3.87

Gaus mean : 42.8050

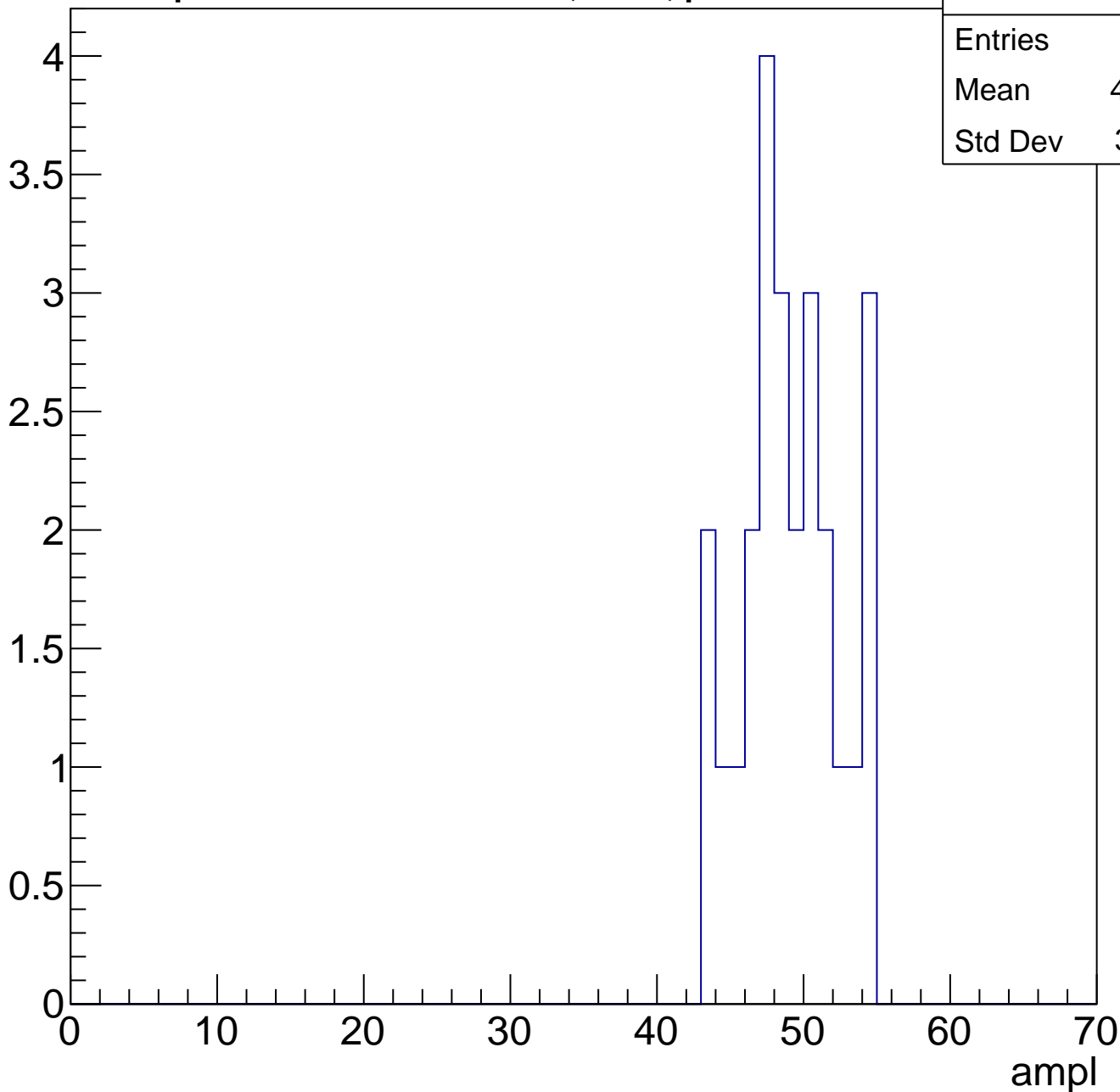
Gaus Width: 3.9428



B1L103S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	25
Mean	48.64
Std Dev	3.211

B1L103S, U15-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

5

4

3

2

1

0

Entries 25

Mean 54.4

Std Dev 3.382

ampl

0

10

20

30

40

50

60

70

B1L103S, U15-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

5

4

3

2

1

0

Entries

27

Mean

57.7

Std Dev

3.73

ampl

0

10

20

30

40

50

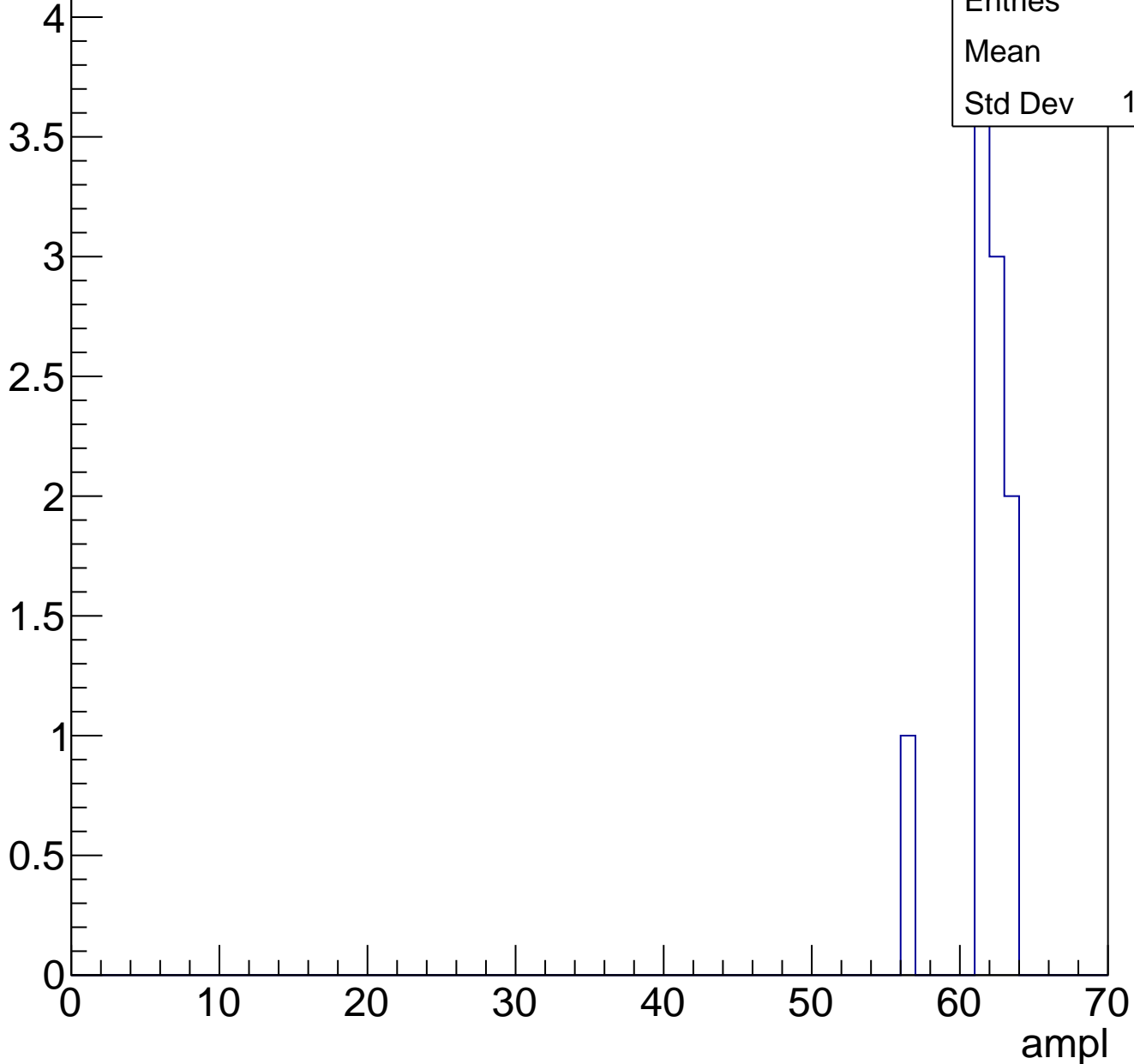
60

70

B1L103S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch87, adc0

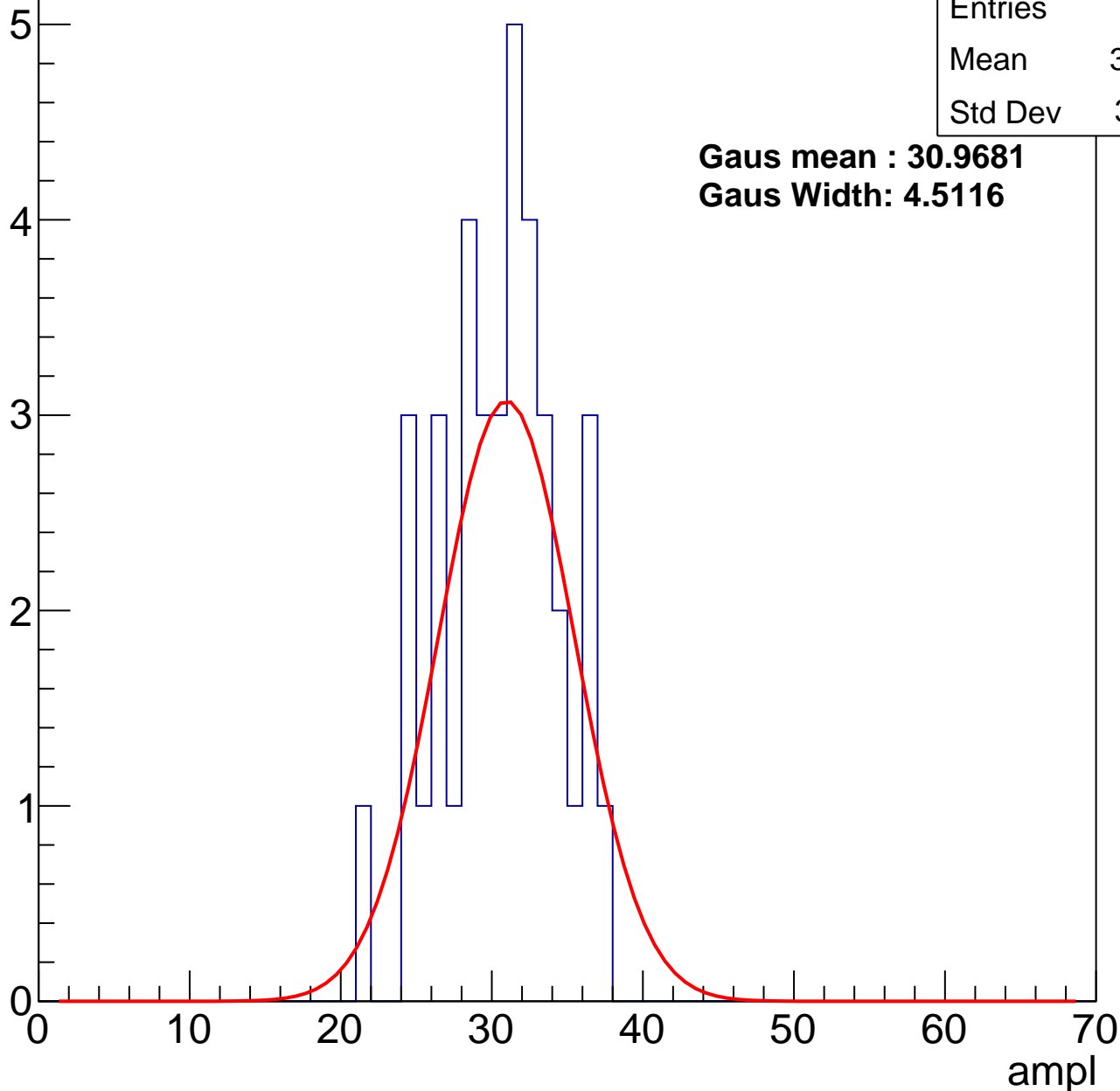
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	30.05
Std Dev	3.811

Gaus mean : 30.9681

Gaus Width: 4.5116



B1L103S, U15-ch87, adc1

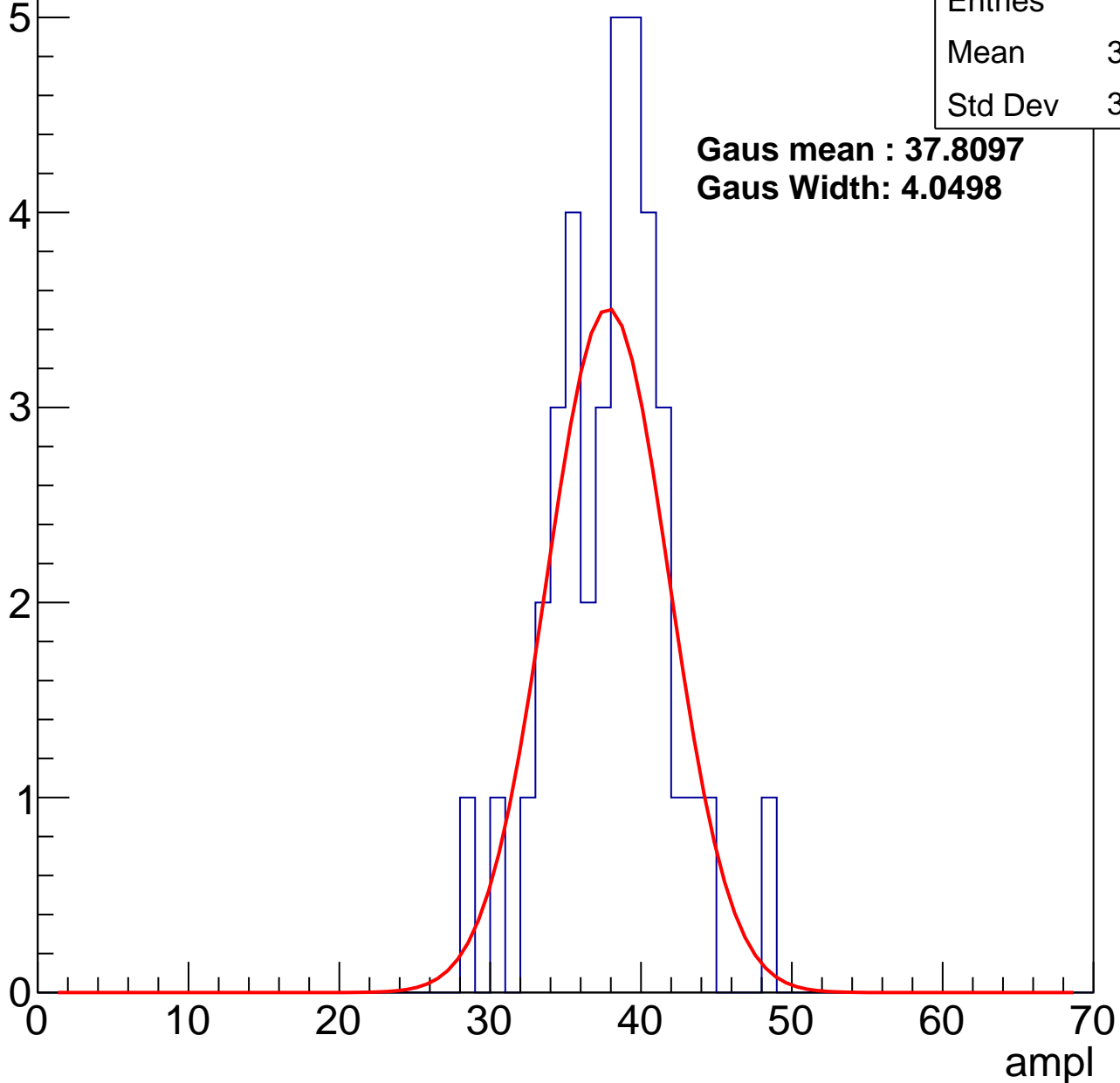
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	37.53
Std Dev	3.844

Gaus mean : 37.8097

Gaus Width: 4.0498



B1L103S, U15-ch87, adc2

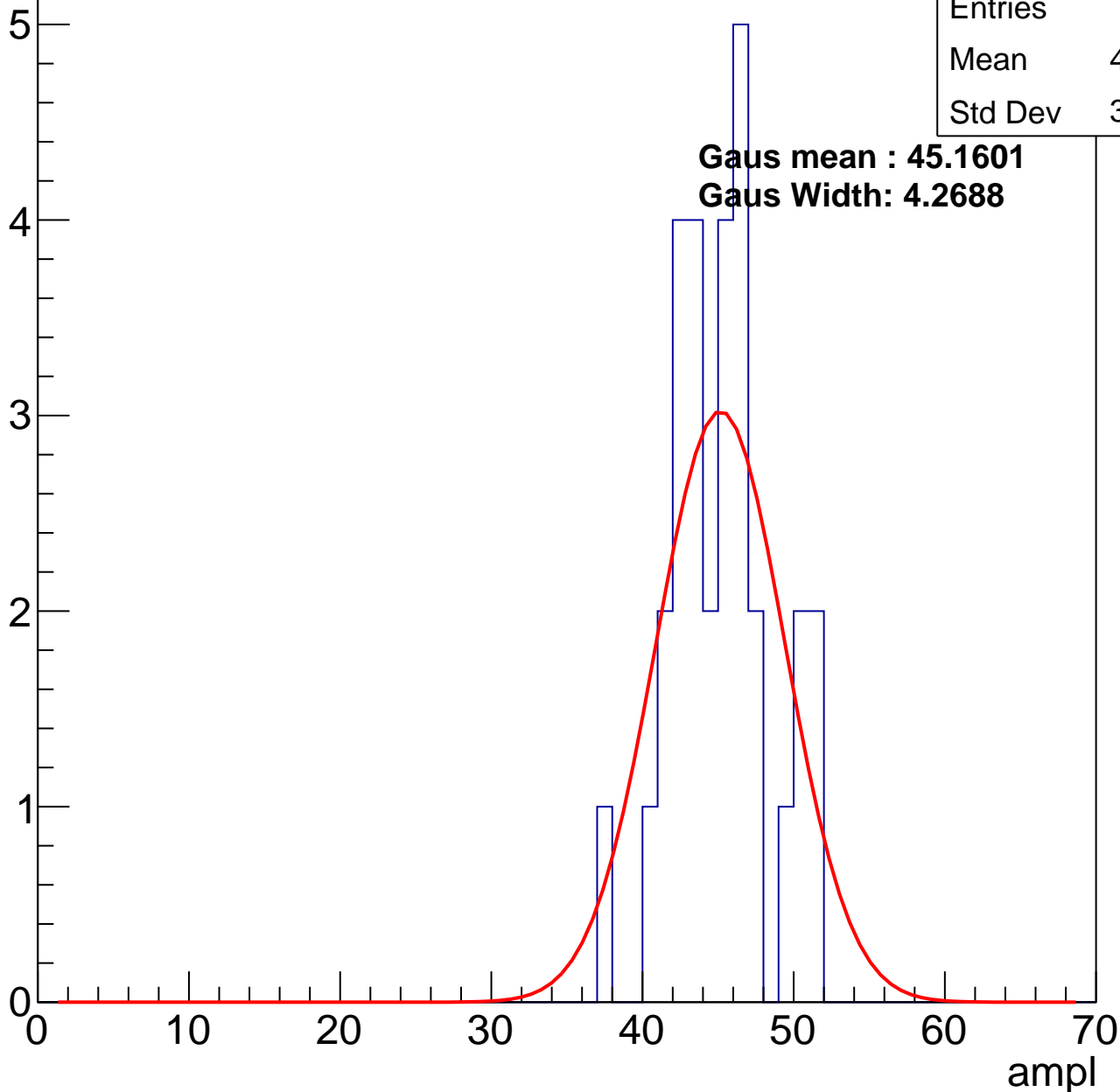
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	44.73
Std Dev	3.286

Gaus mean : 45.1601

Gaus Width: 4.2688

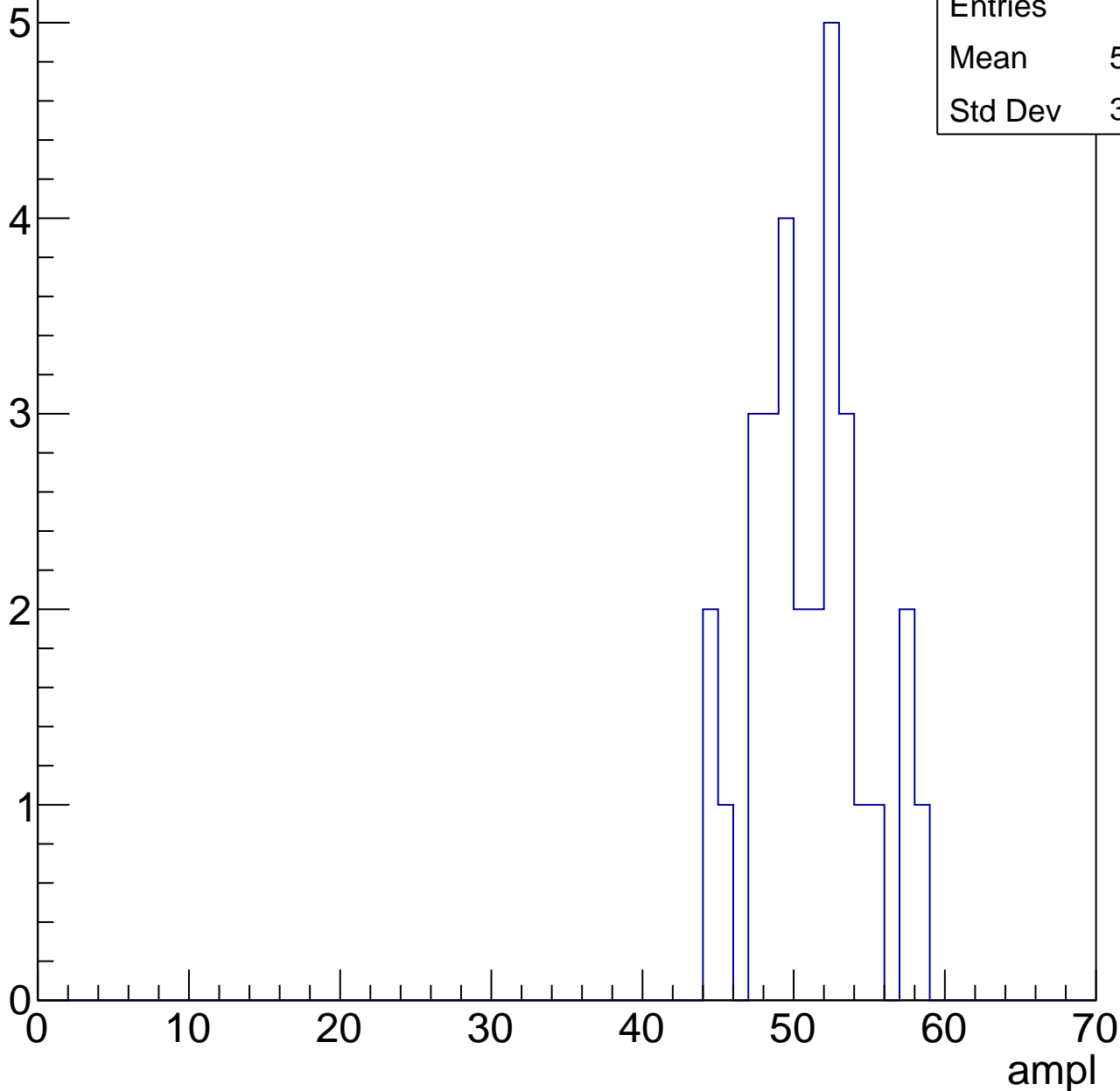


B1L103S, U15-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

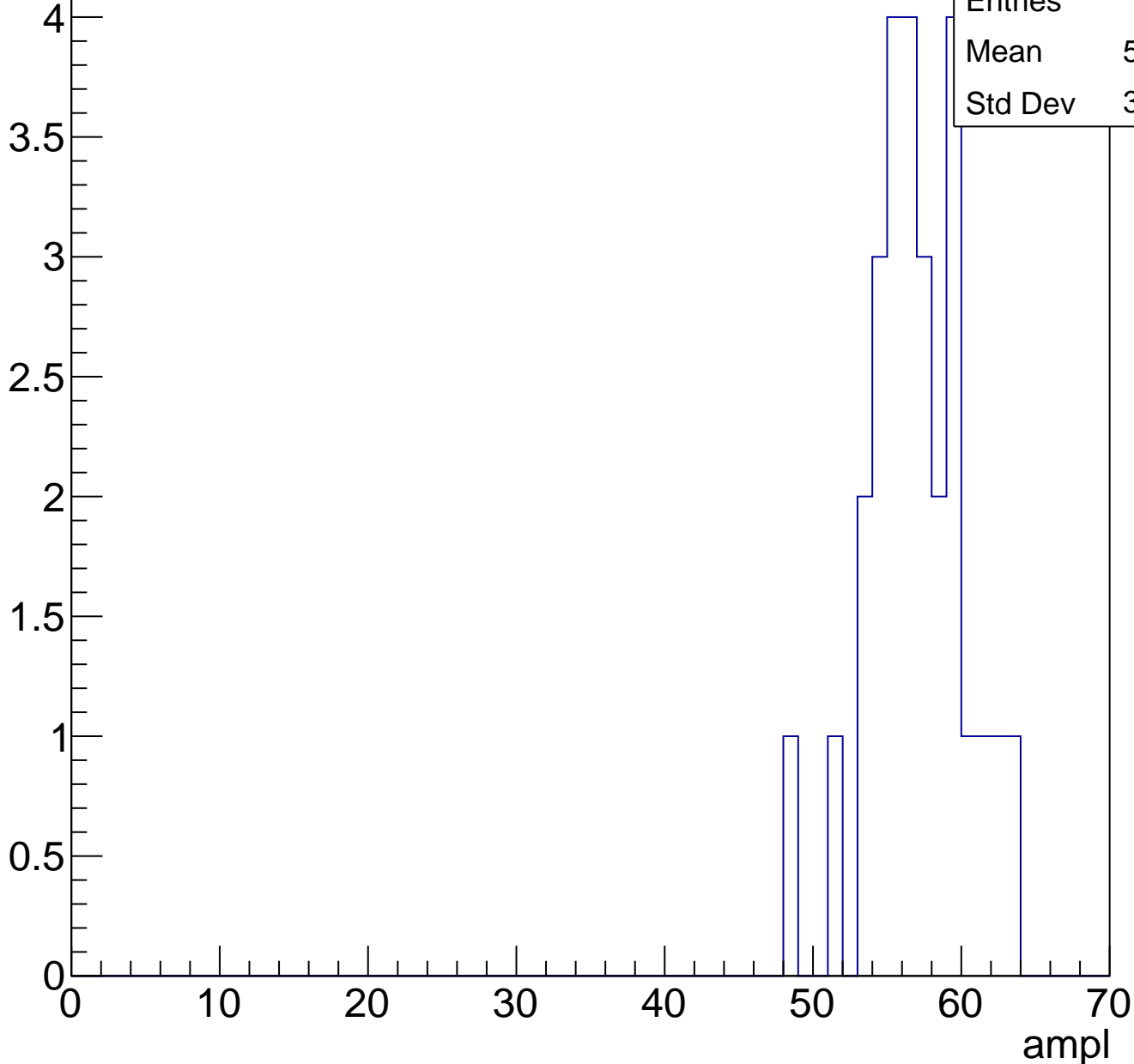
Entries	30
Mean	50.53
Std Dev	3.575



B1L103S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

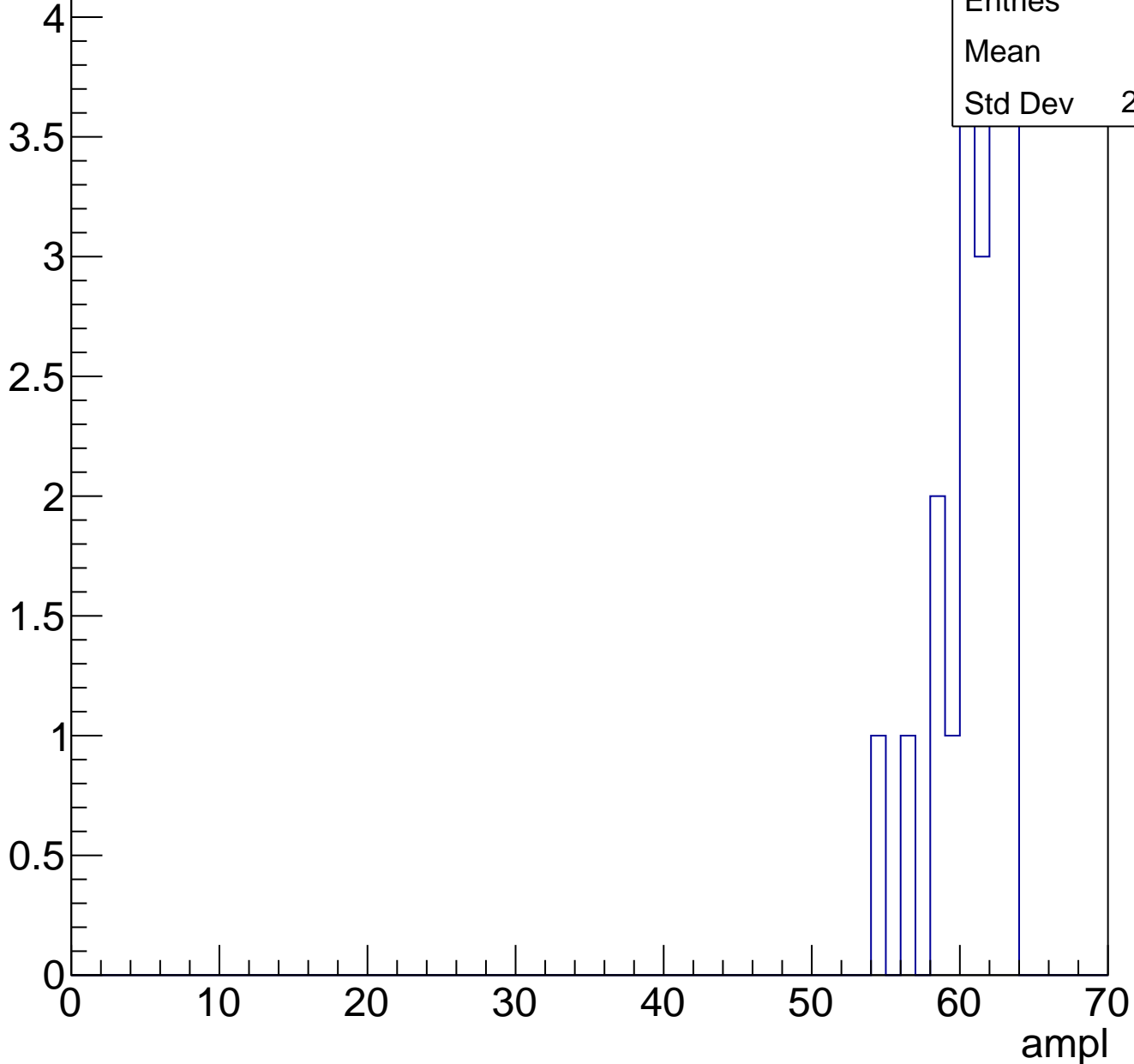
Entry



B1L103S, U15-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

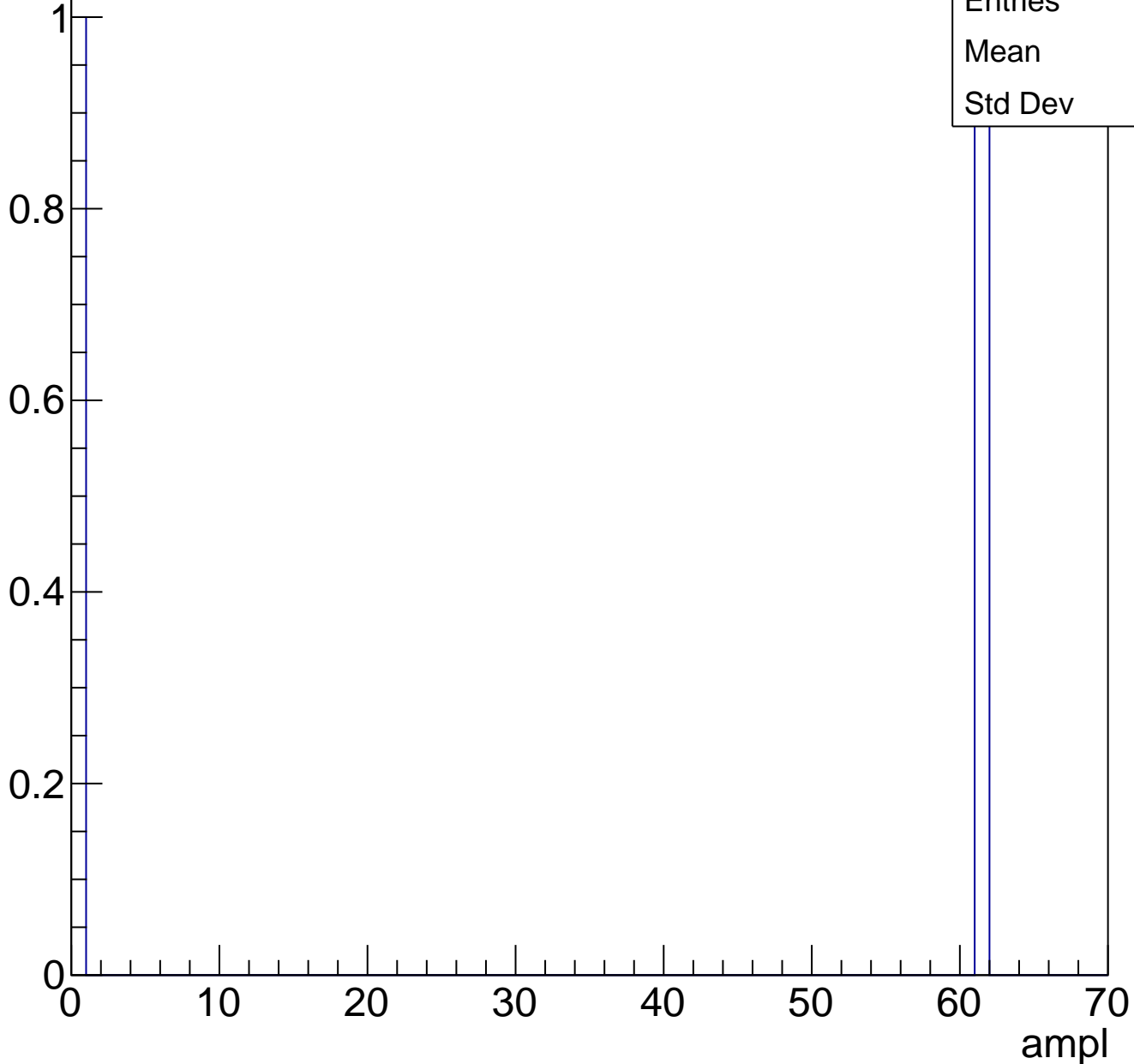


Entries	20
Mean	60.4
Std Dev	2.375

B1L103S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

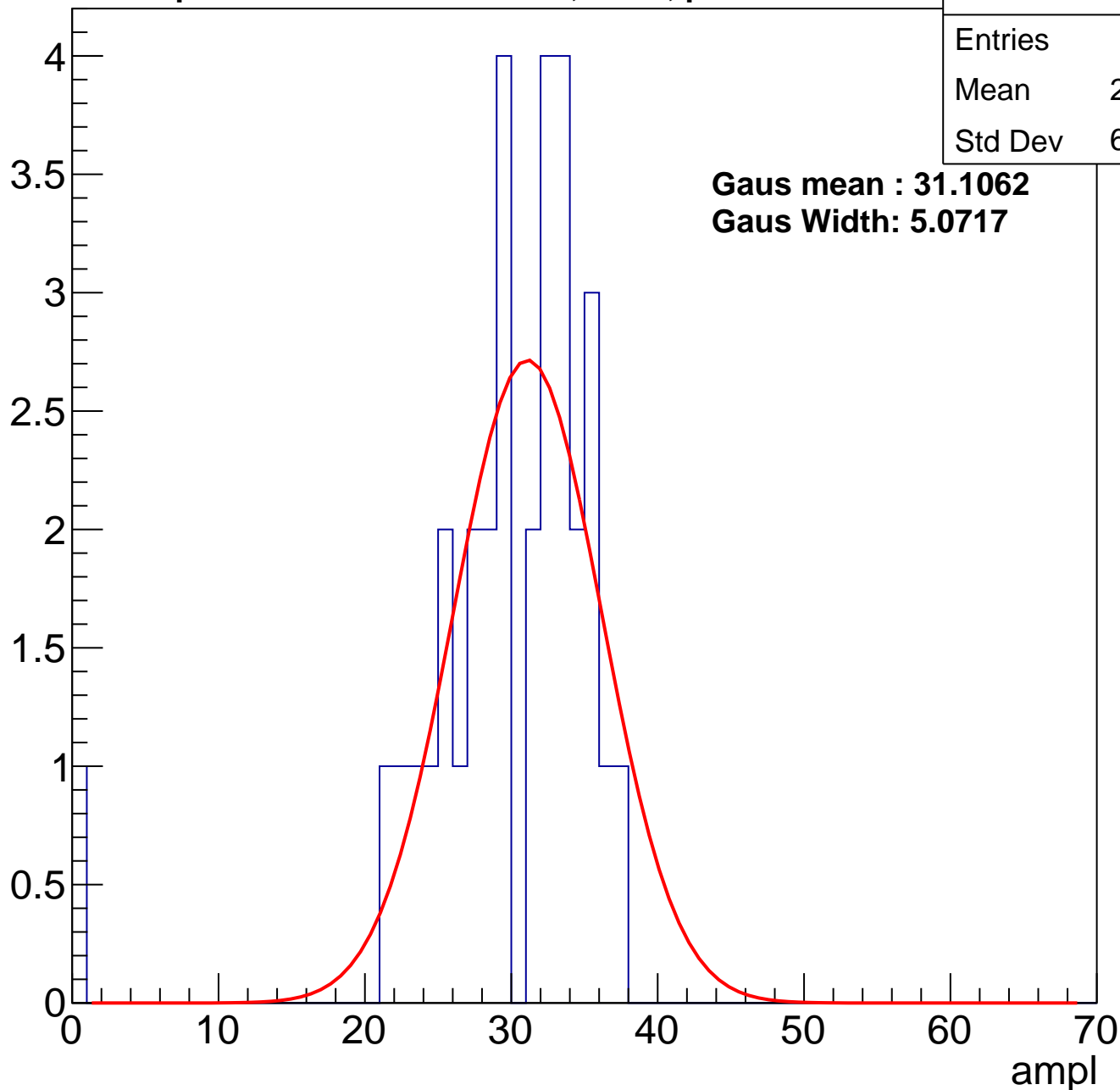


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch88, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



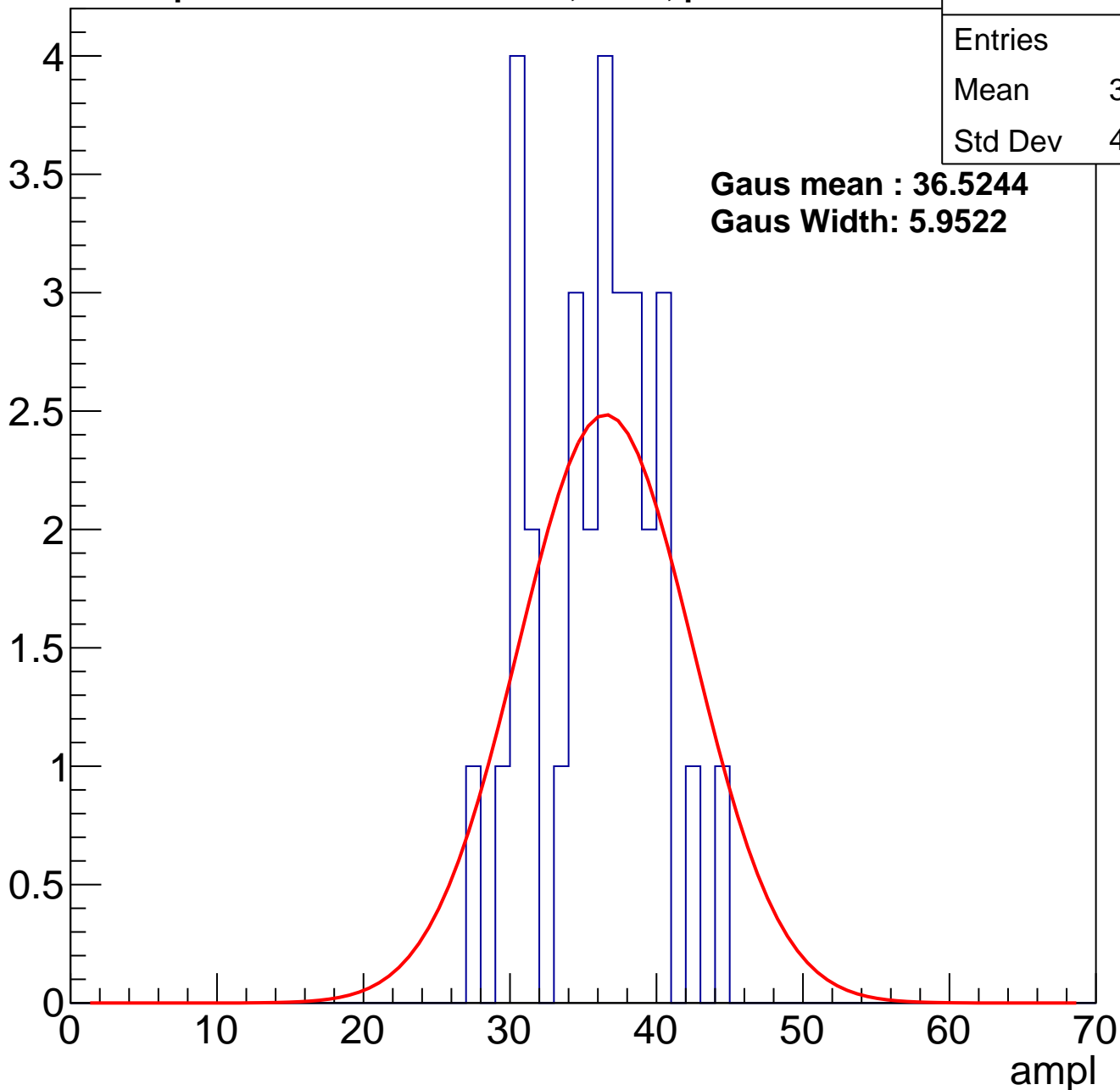
Entries	33
Mean	29.09
Std Dev	6.617

Gaus mean : 31.1062
Gaus Width: 5.0717

B1L103S, U15-ch88, adc1

calib_packv5_042523_0143.root, FC#7, port C2

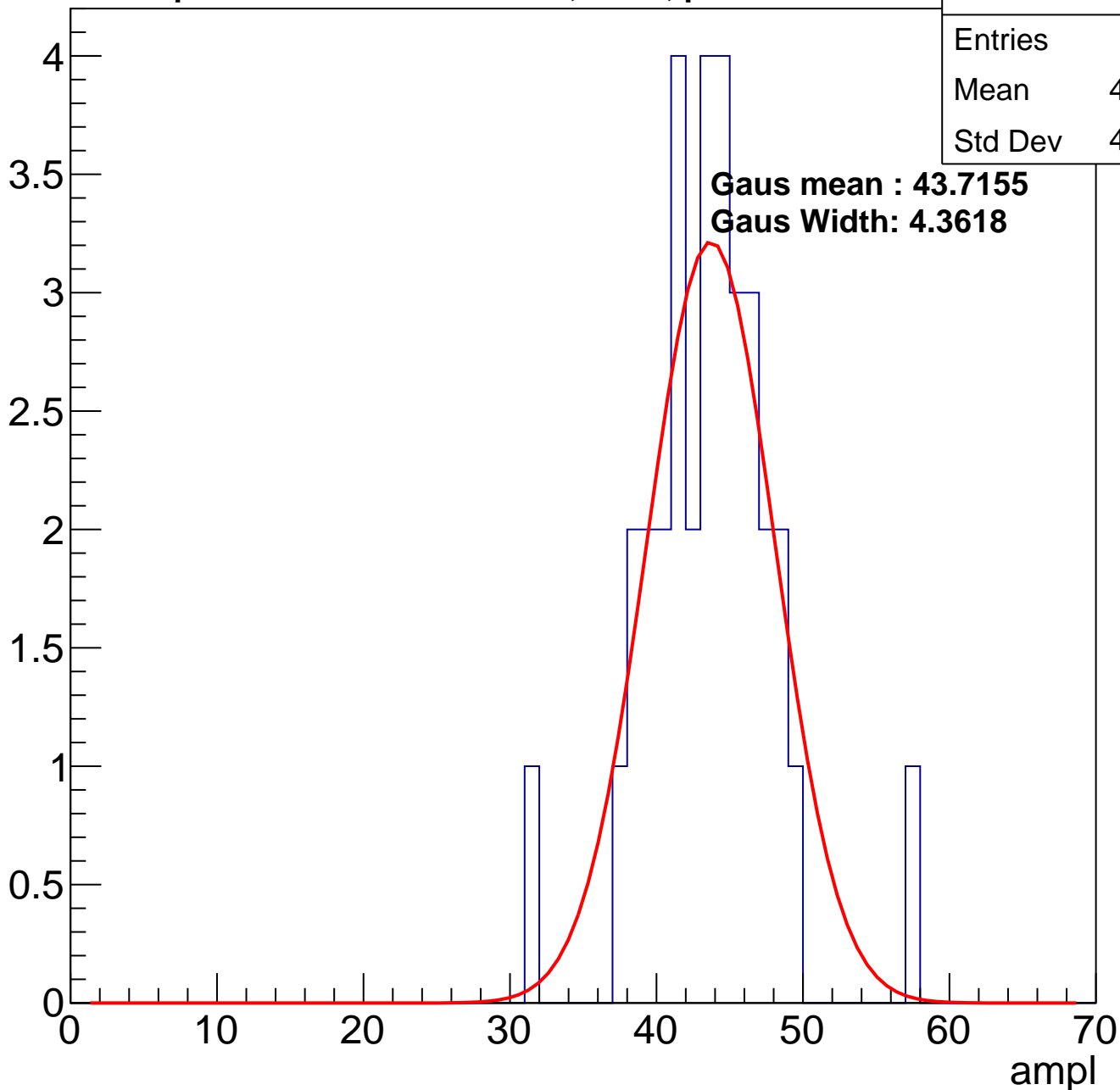
Entry



B1L103S, U15-ch88, adc2

calib_packv5_042523_0143.root, FC#7, port C2

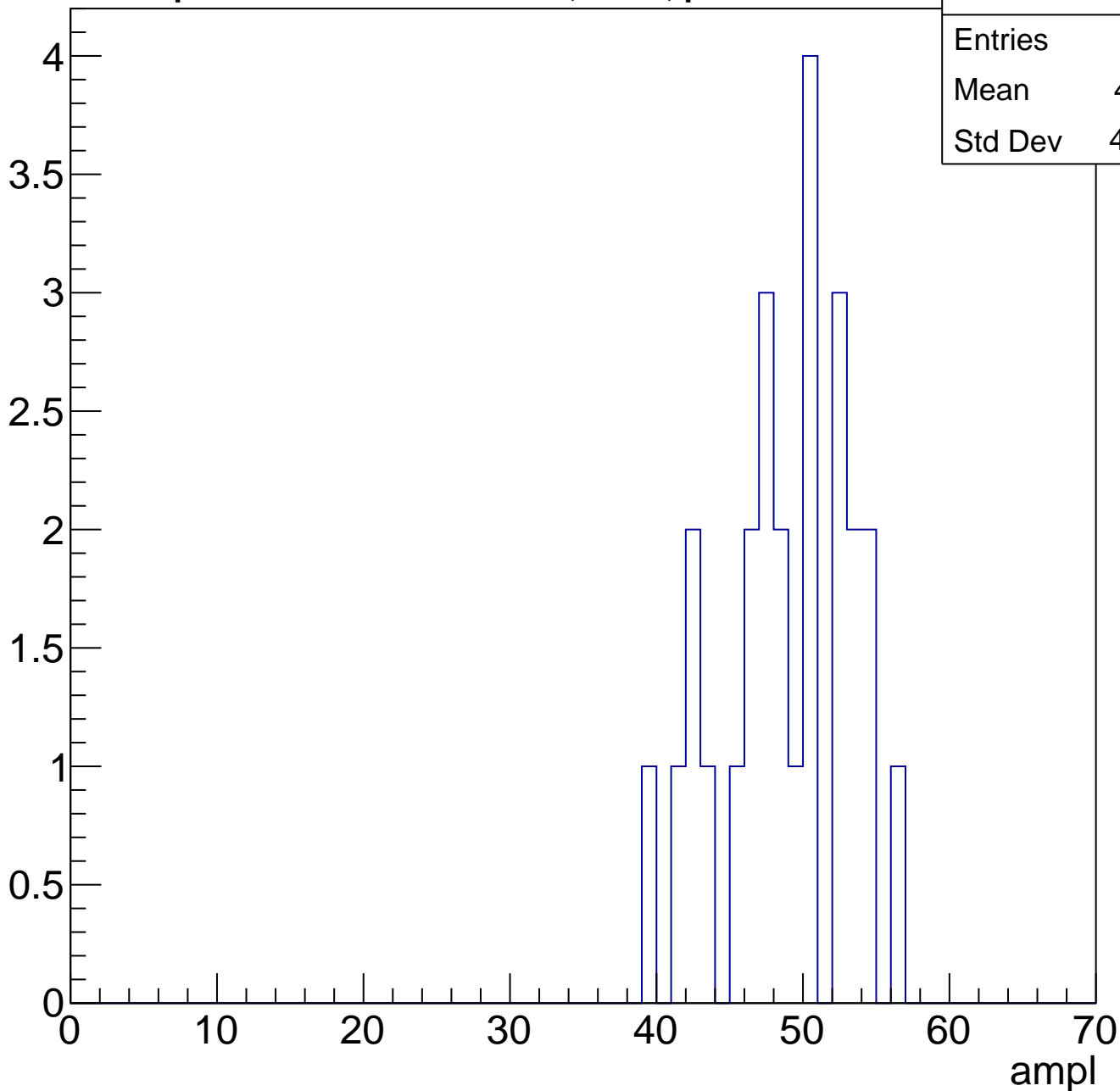
Entry



B1L103S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

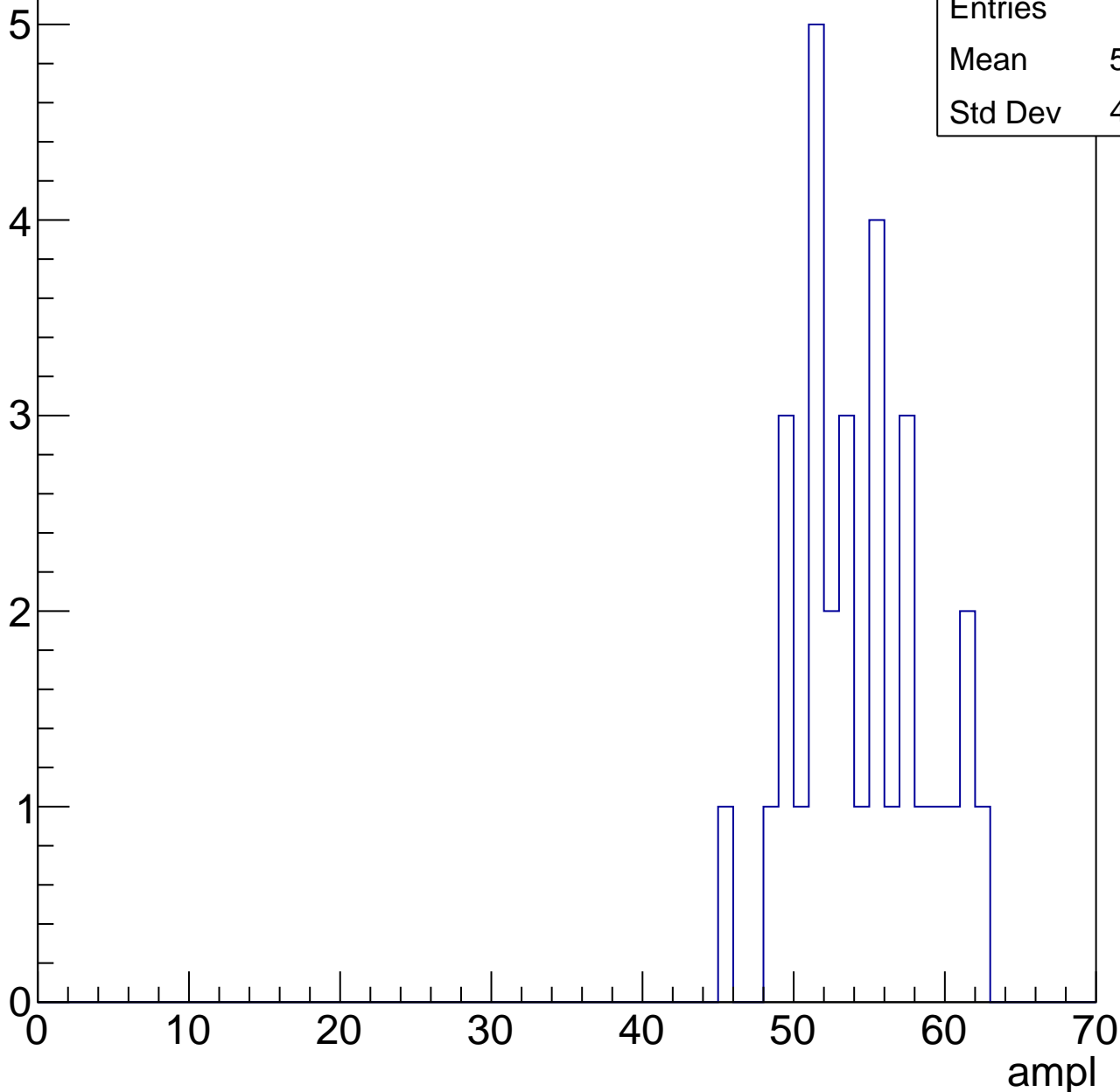


B1L103S, U15-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

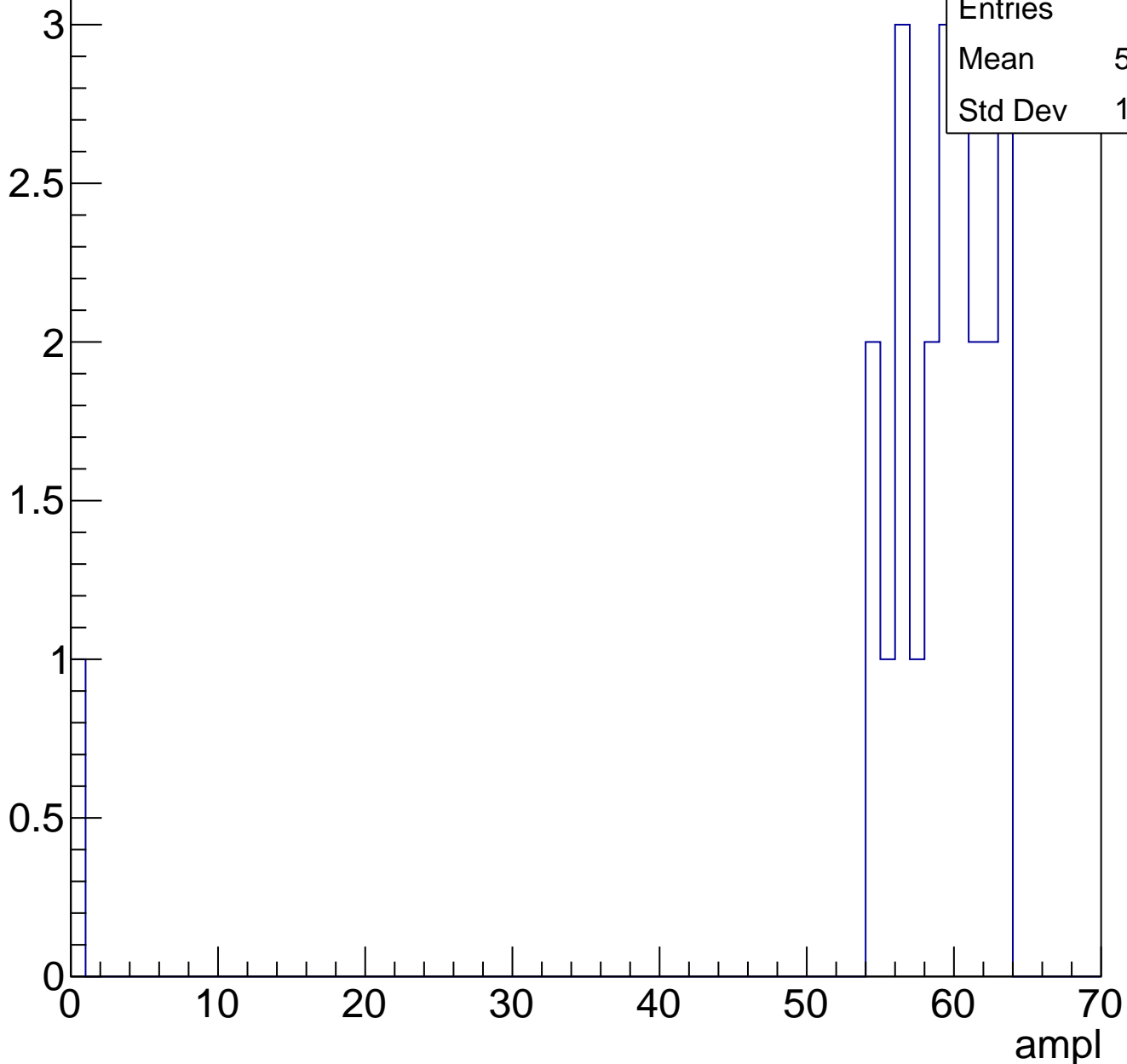
Entries	31
Mean	53.87
Std Dev	4.164



B1L103S, U15-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	23
Mean	56.35
Std Dev	12.33

B1L103S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

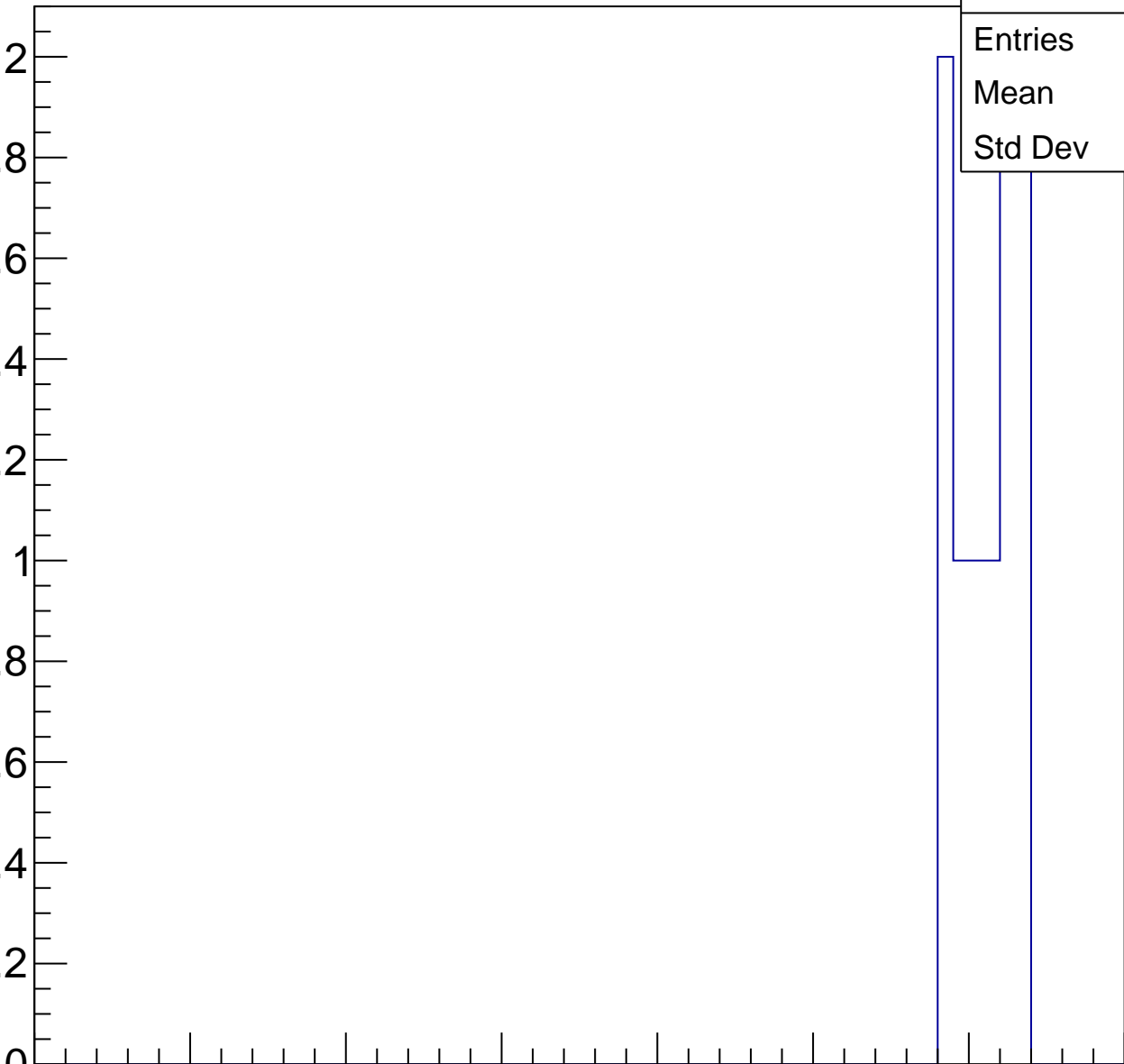
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.67
Std Dev	1.886

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch89, adc0

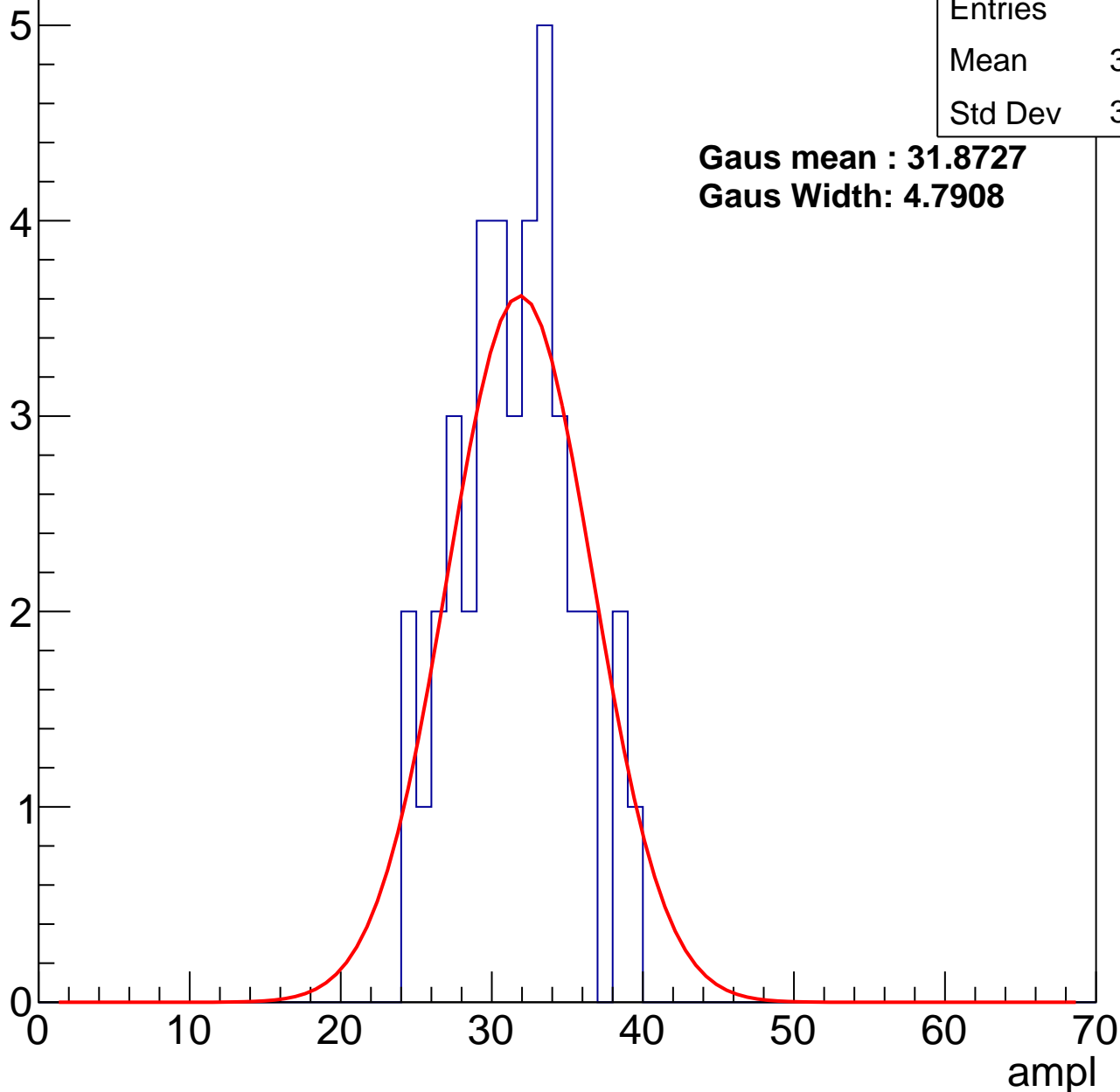
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	31.07
Std Dev	3.758

Gaus mean : 31.8727

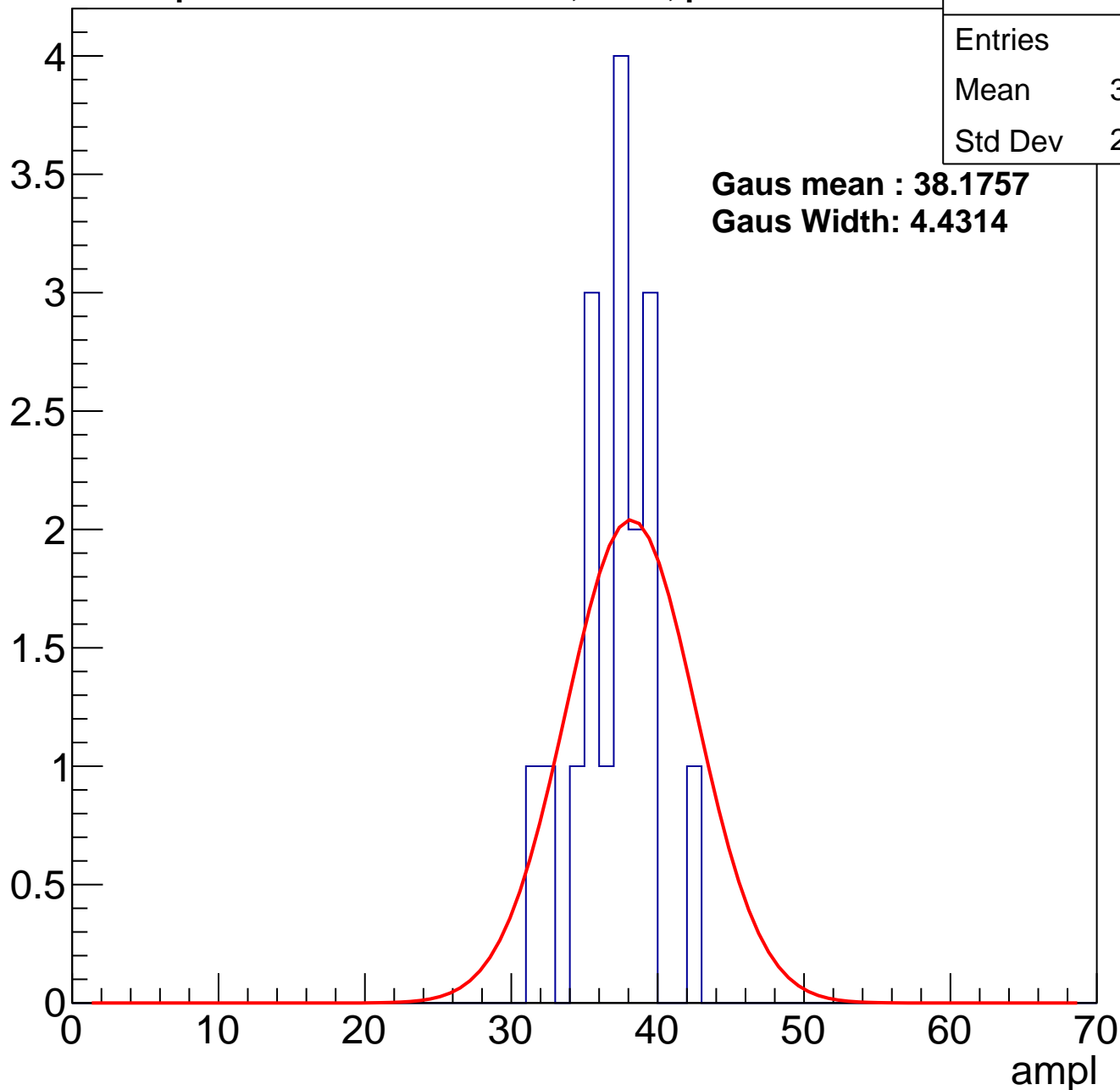
Gaus Width: 4.7908



B1L103S, U15-ch89, adc1

calib_packv5_042523_0143.root, FC#7, port C2

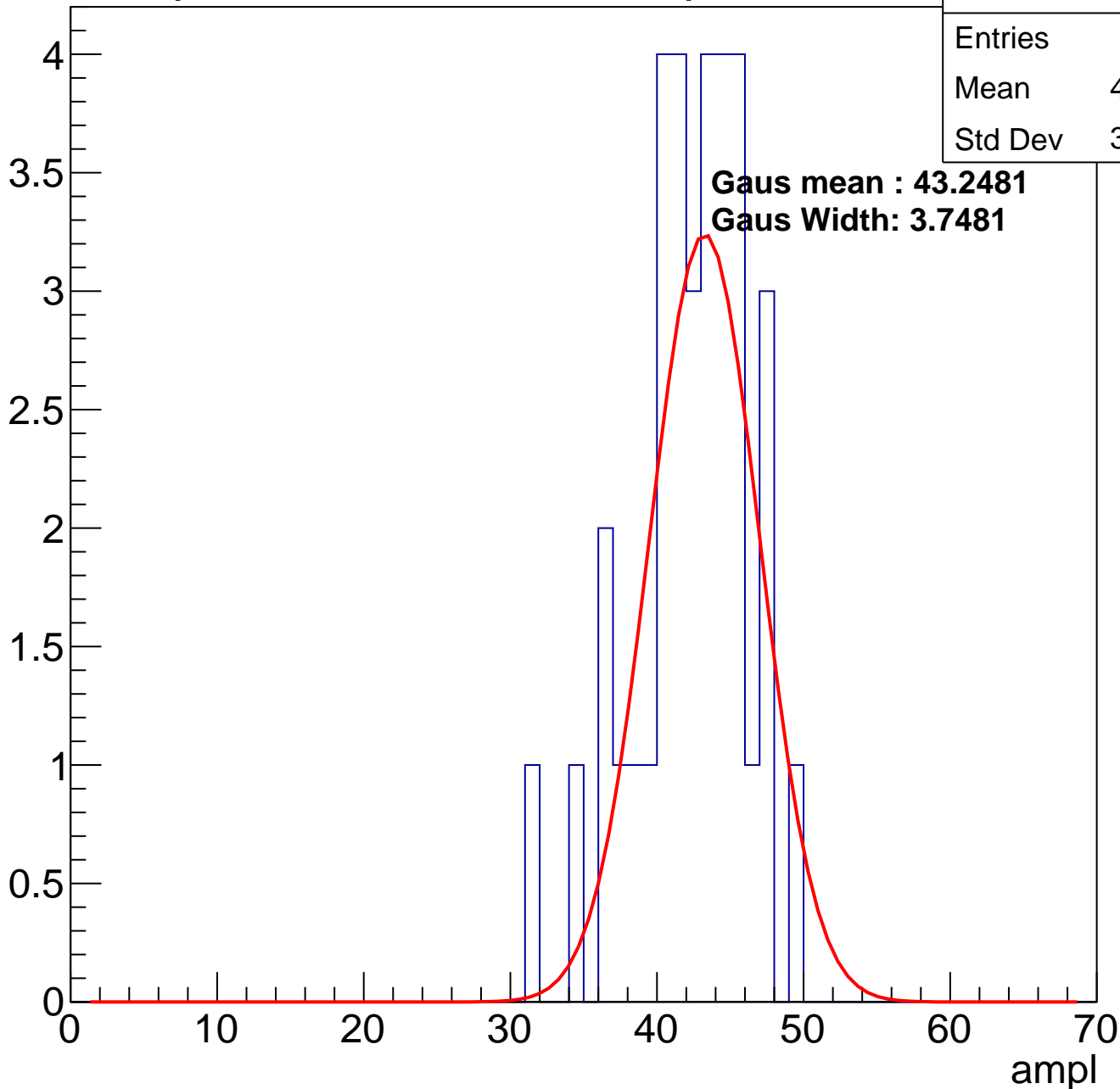
Entry



B1L103S, U15-ch89, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

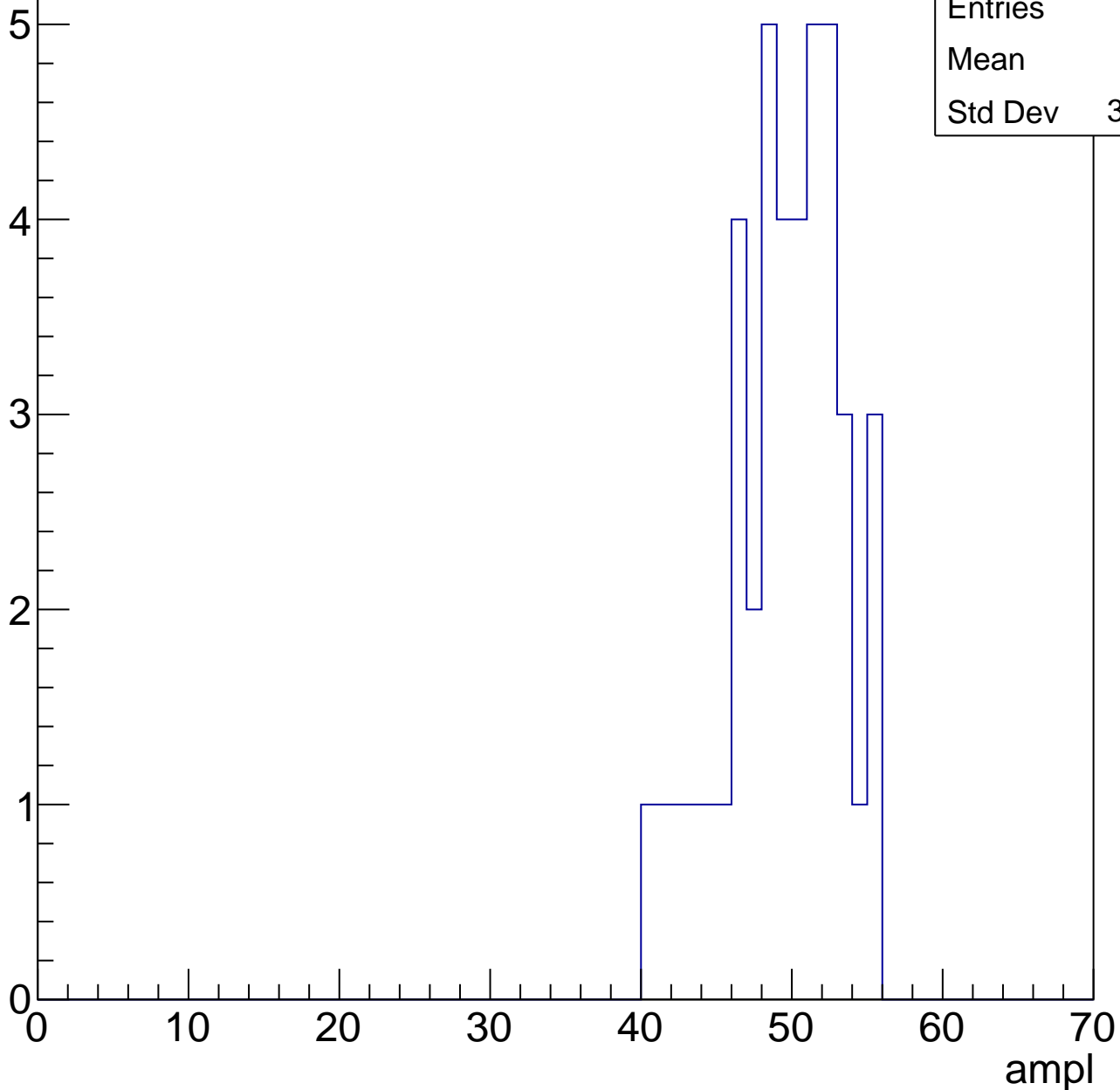


B1L103S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	49.1
Std Dev	3.683



B1L103S, U15-ch89, adc4

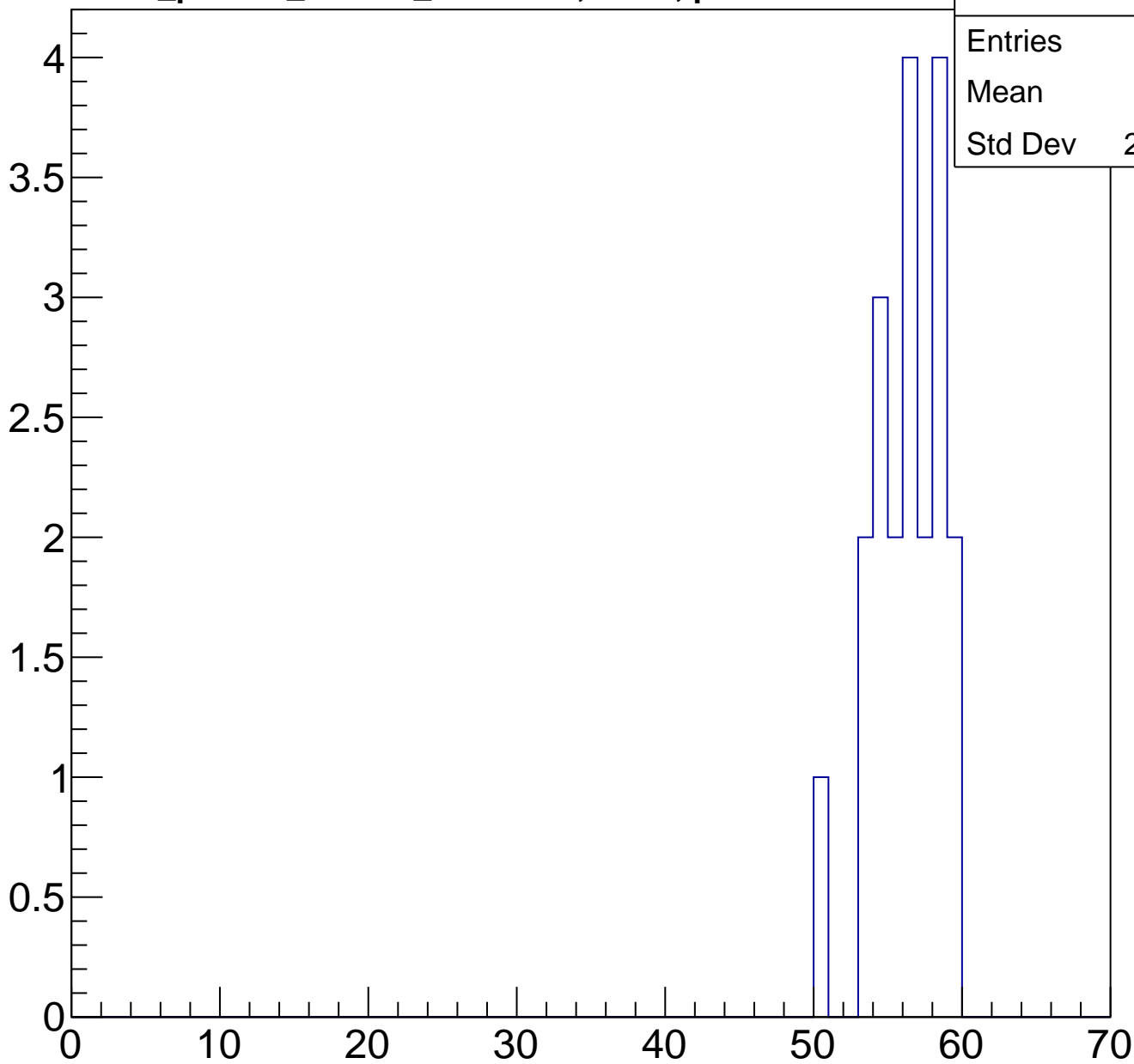
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	20
Mean	55.8
Std Dev	2.272

ampl

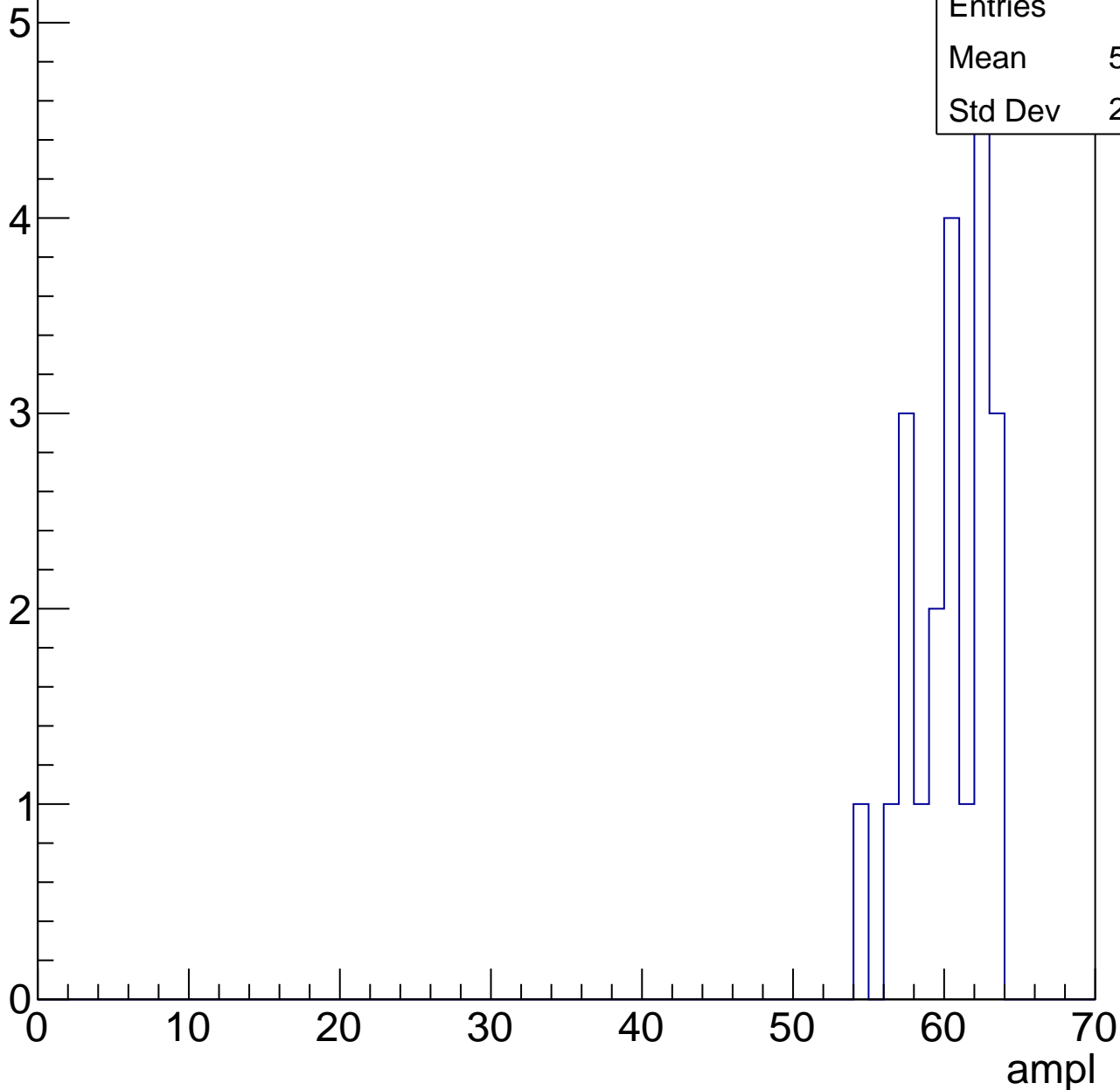


B1L103S, U15-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

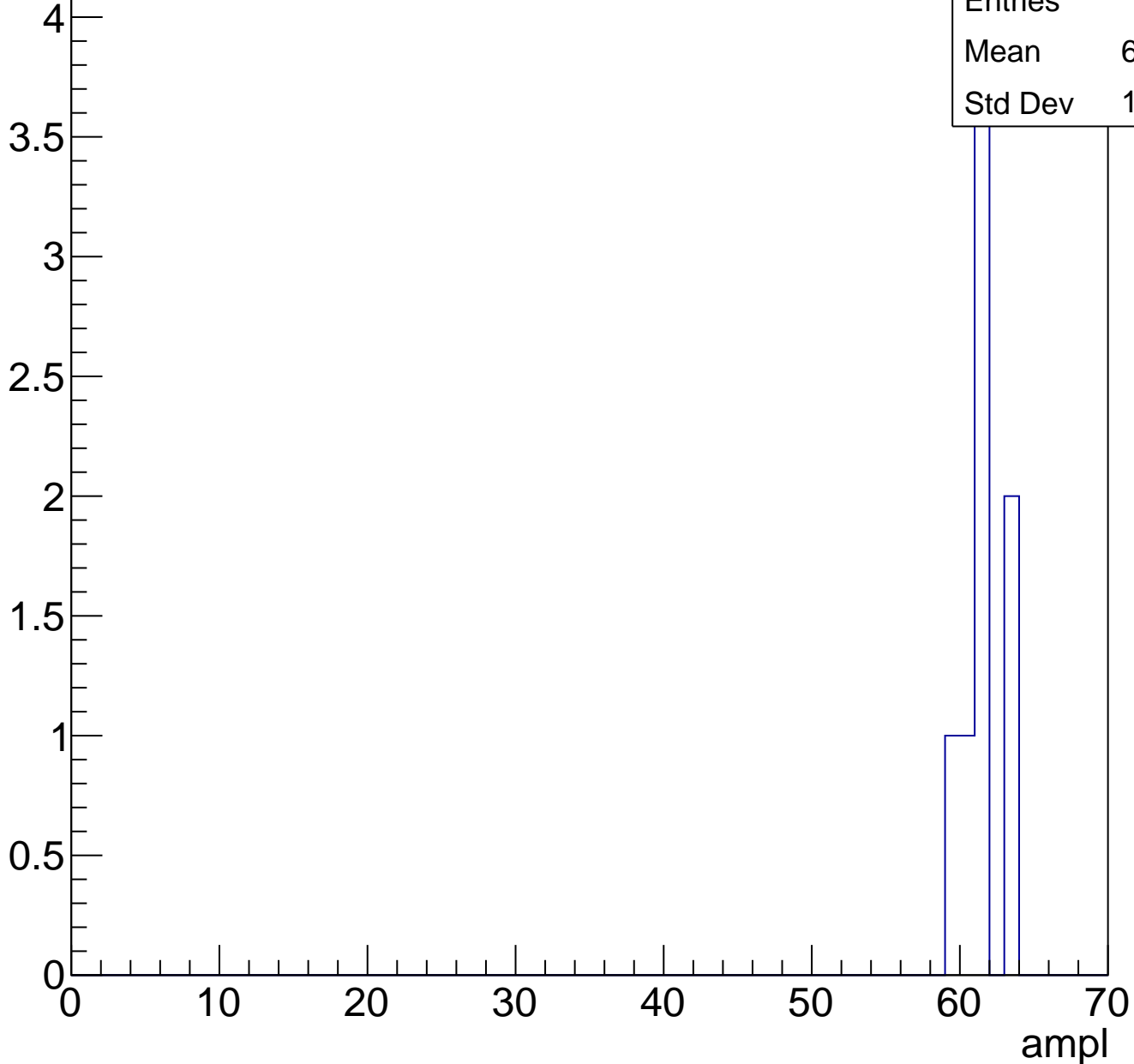
Entries	21
Mean	59.86
Std Dev	2.513



B1L103S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

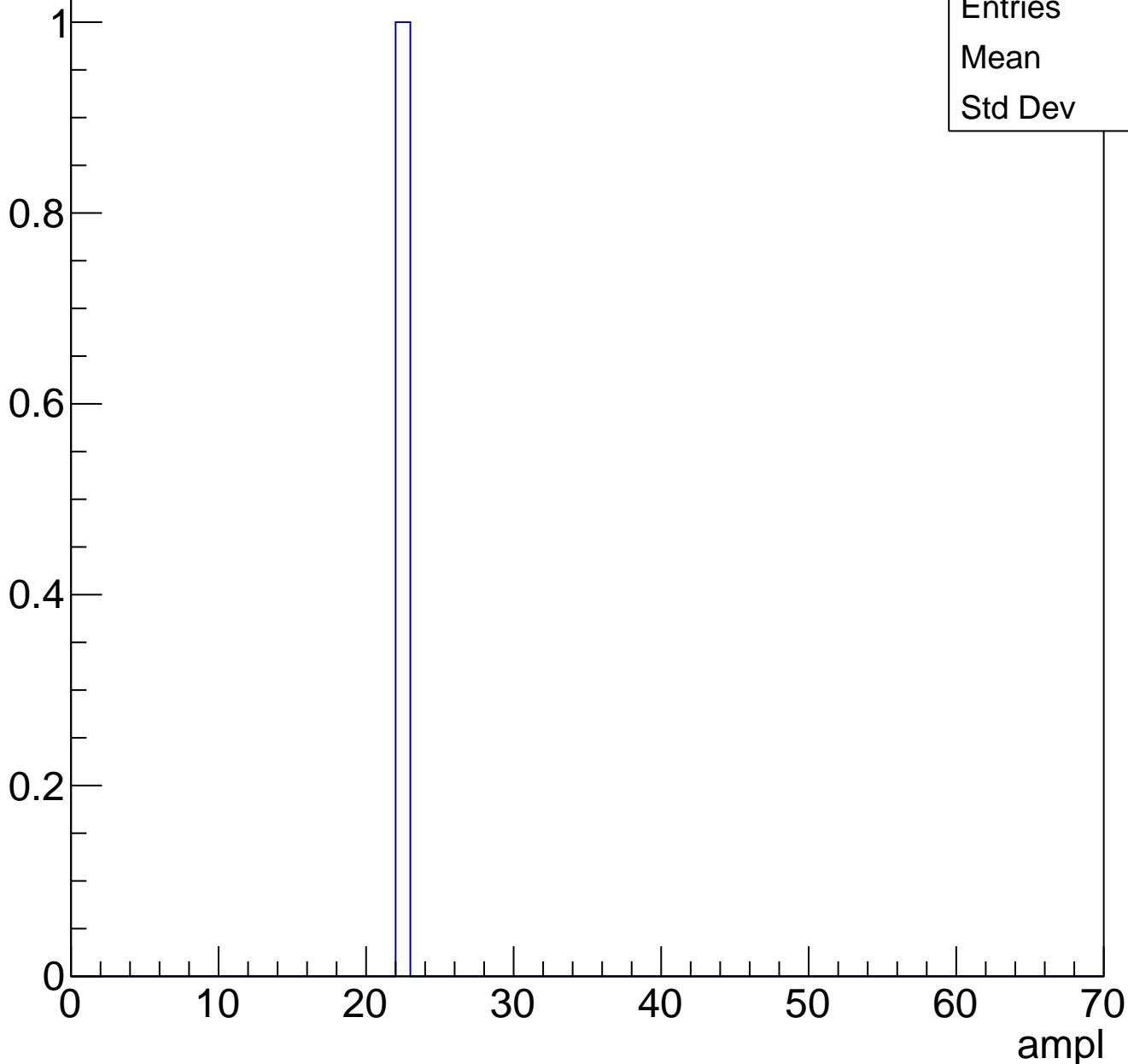


Entries	8
Mean	61.12
Std Dev	1.269

B1L103S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

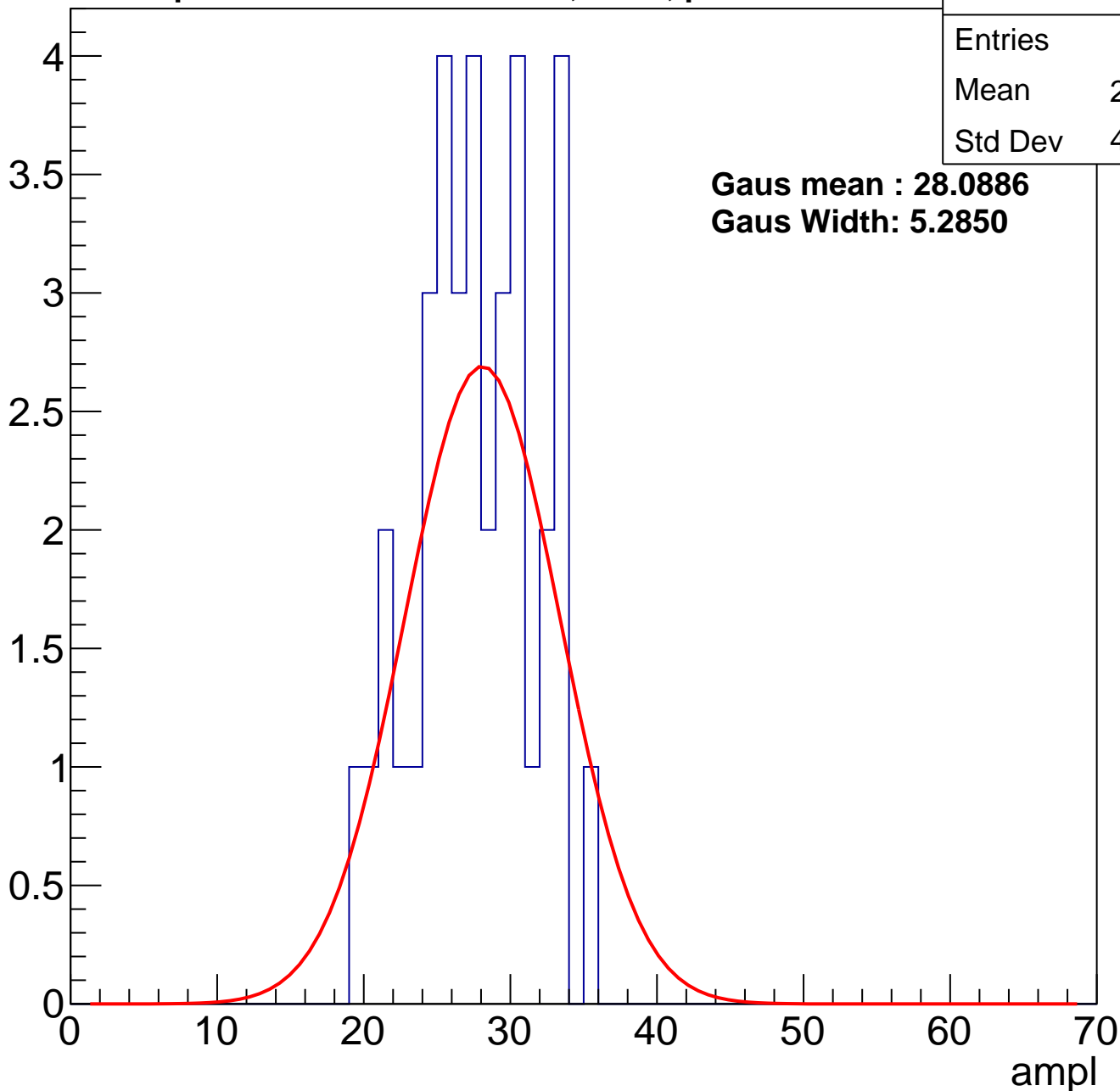
Entry



B1L103S, U15-ch90, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

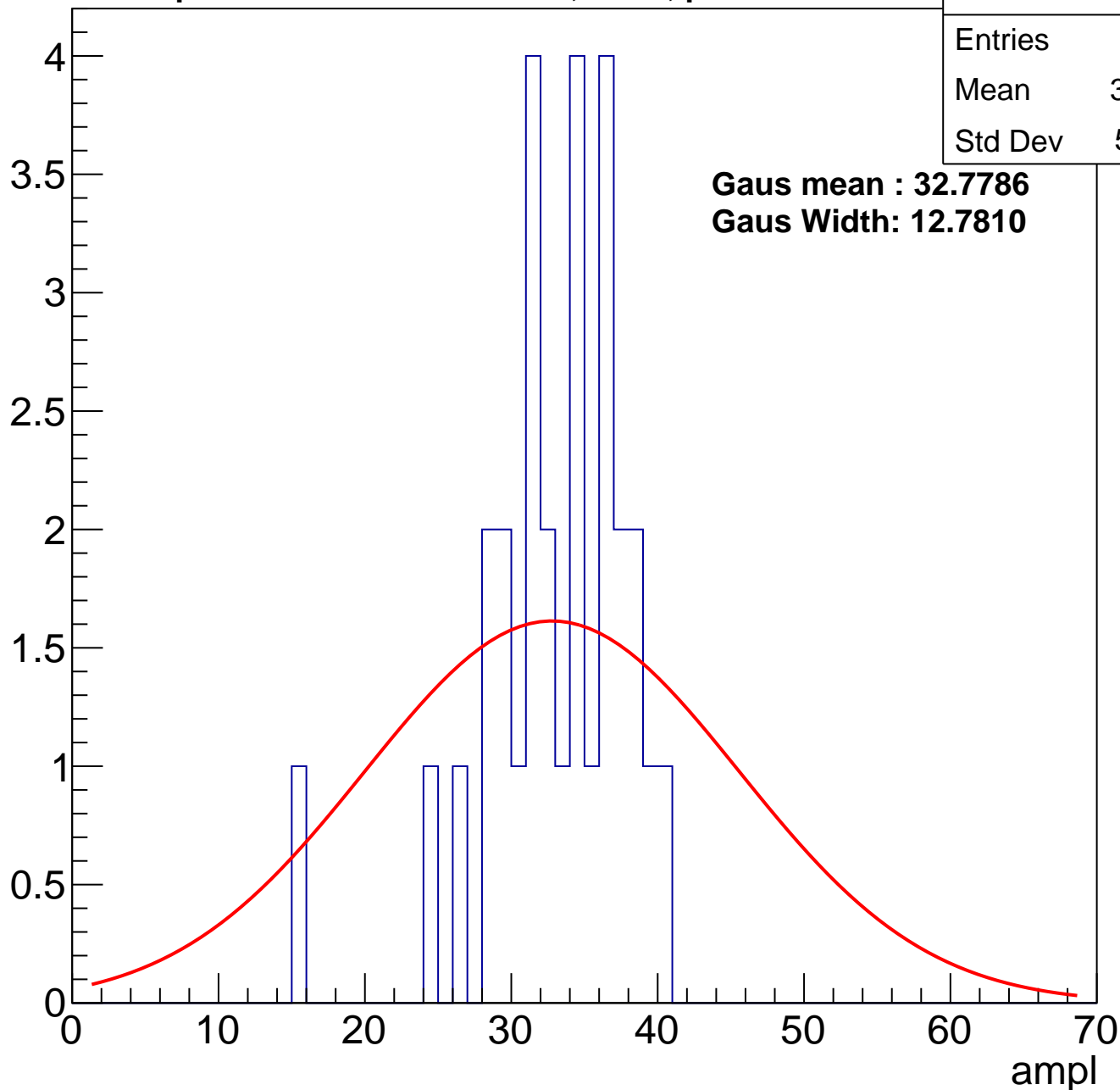


Entries	37
Mean	27.27
Std Dev	4.004

B1L103S, U15-ch90, adc1

calib_packv5_042523_0143.root, FC#7, port C2

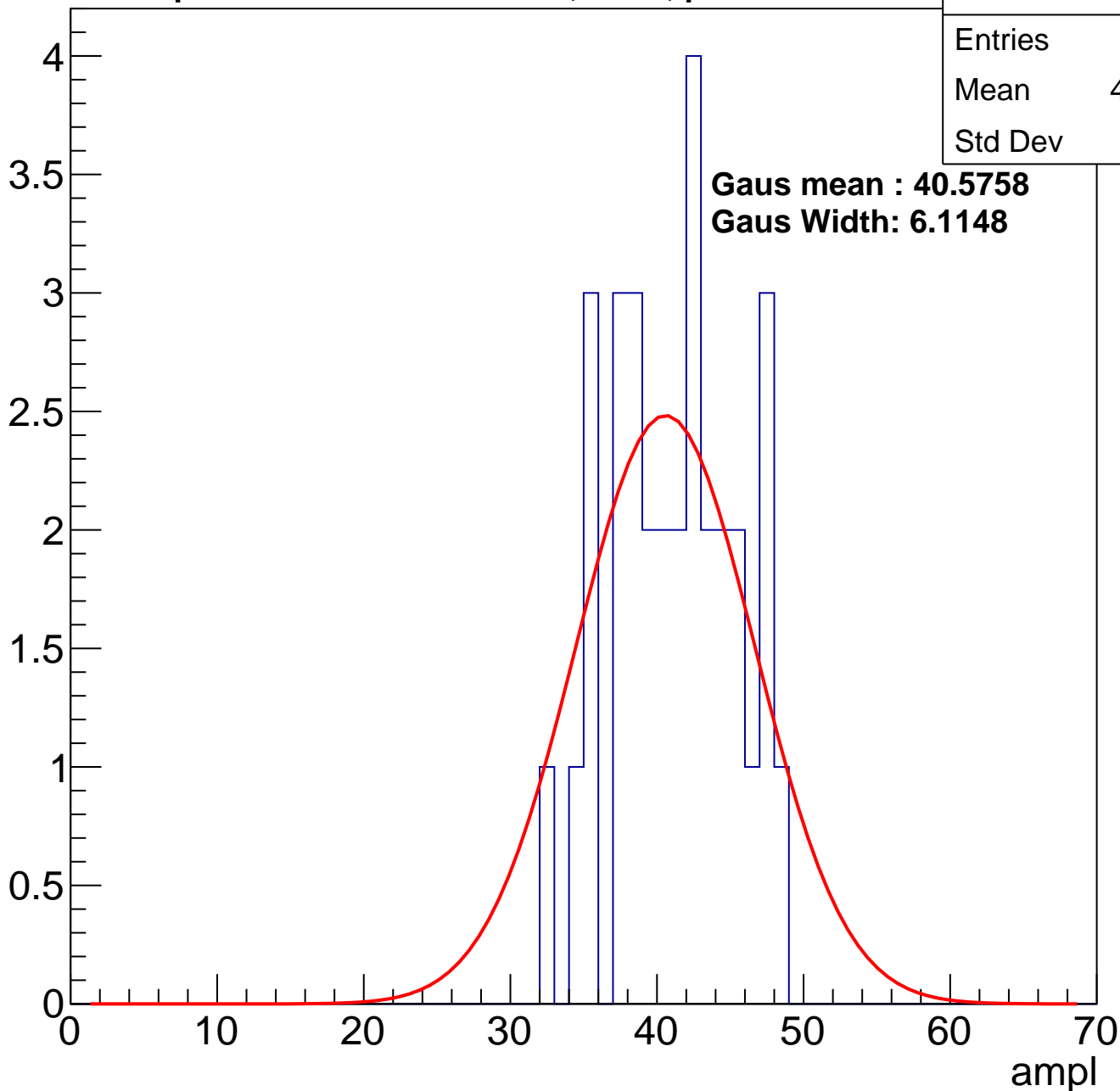
Entry



B1L103S, U15-ch90, adc2

calib_packv5_042523_0143.root, FC#7, port C2

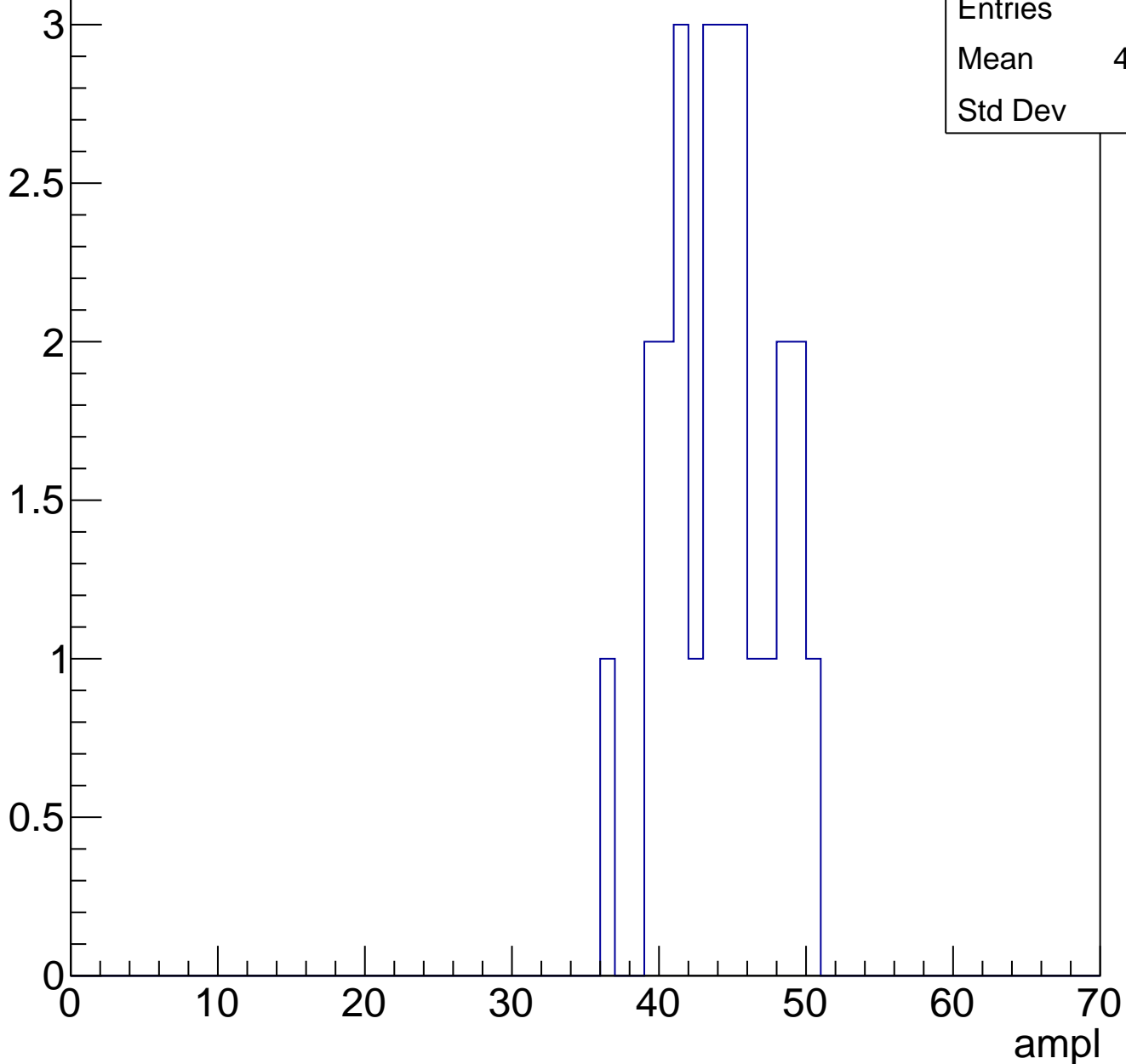
Entry



B1L103S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

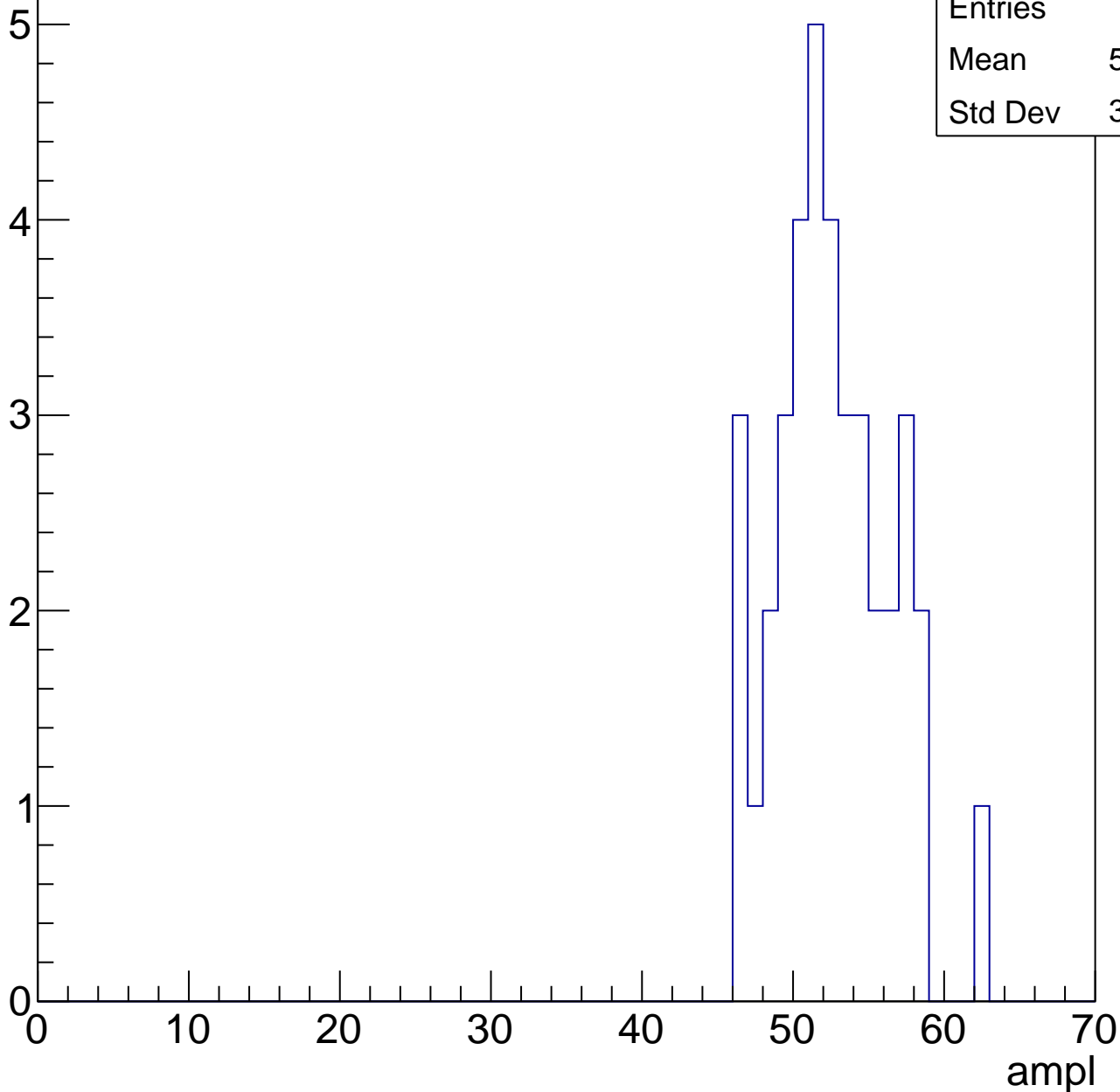


B1L103S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

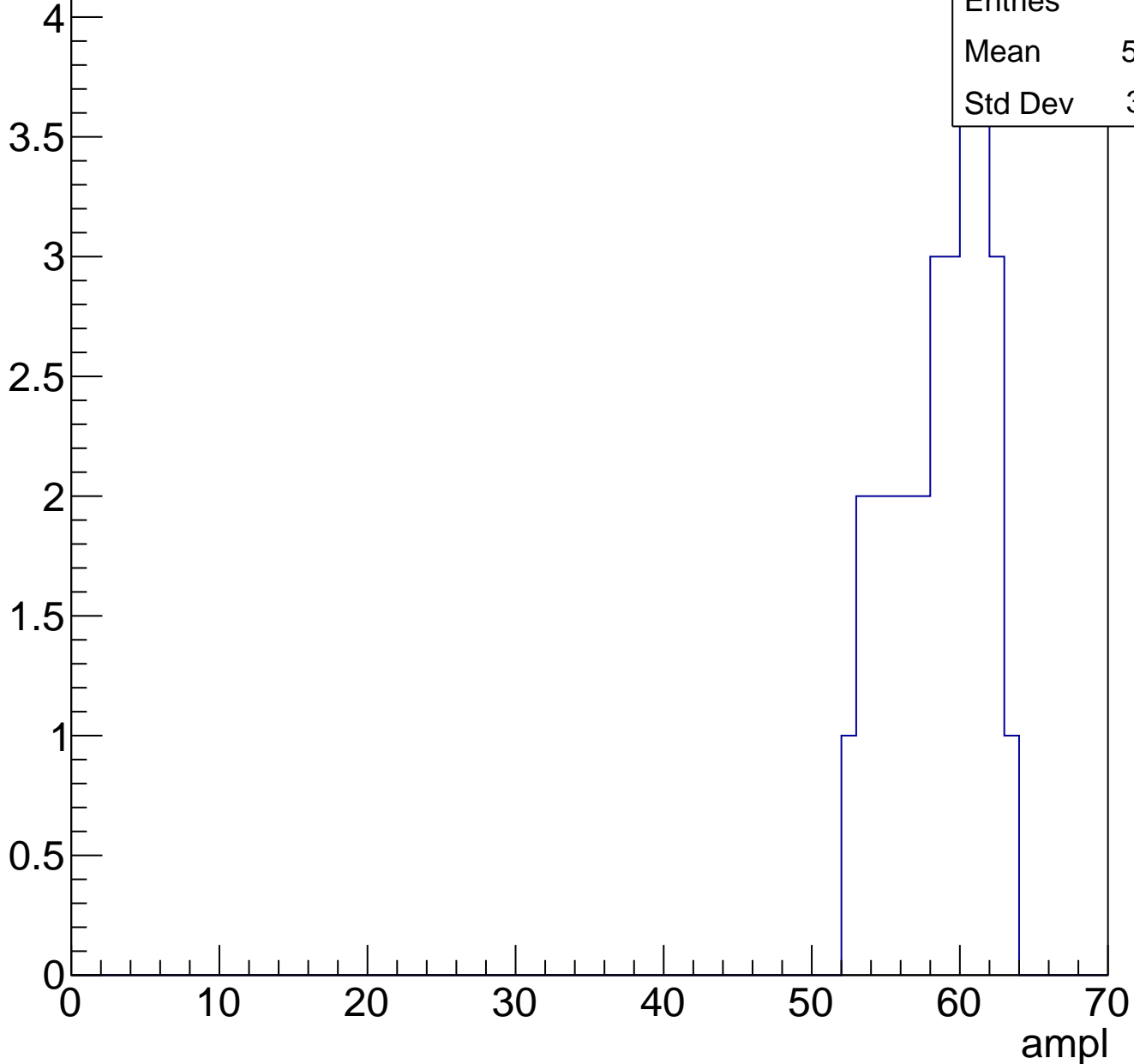
Entries	38
Mean	52.18
Std Dev	3.712



B1L103S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

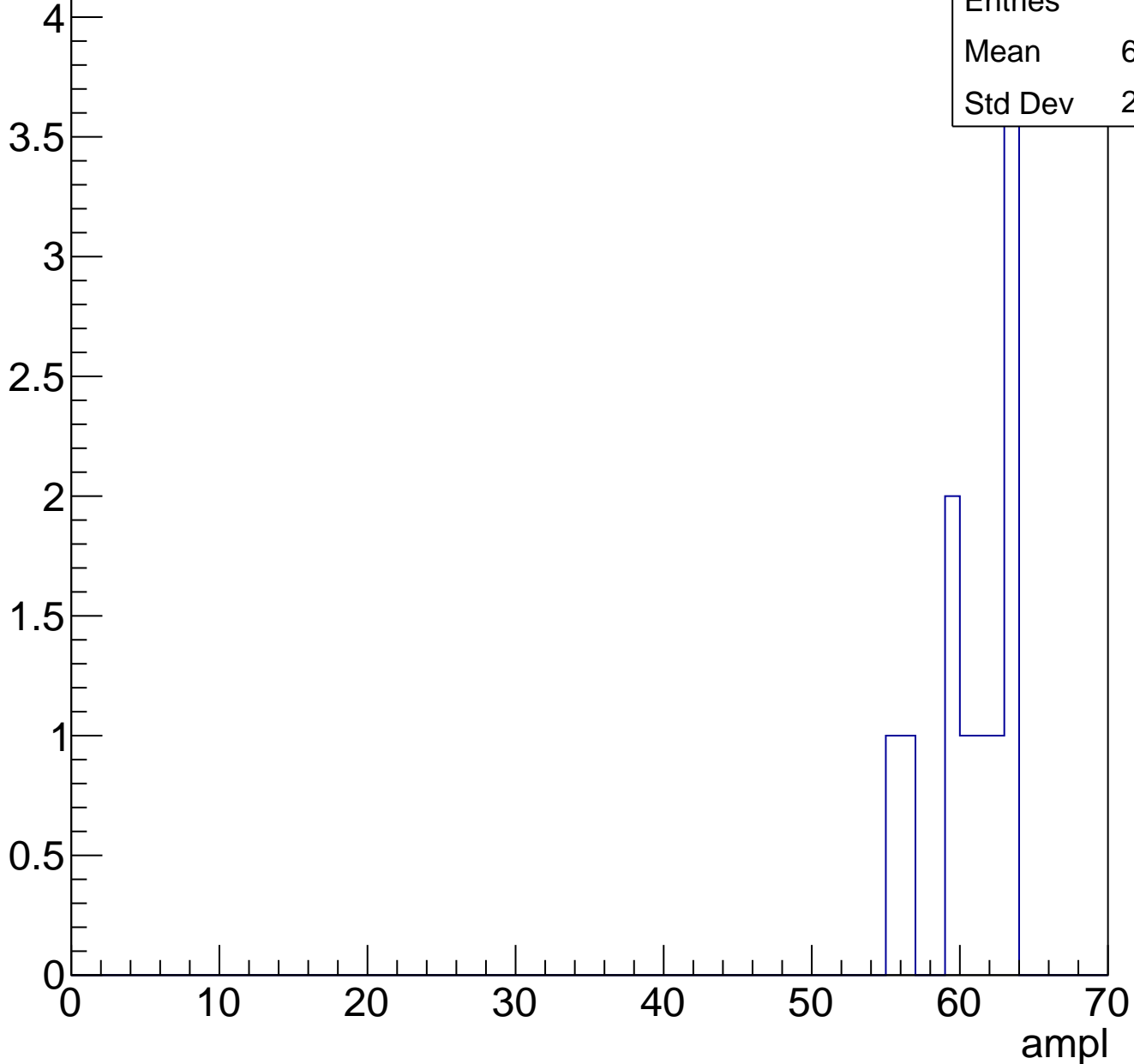
Entry



B1L103S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

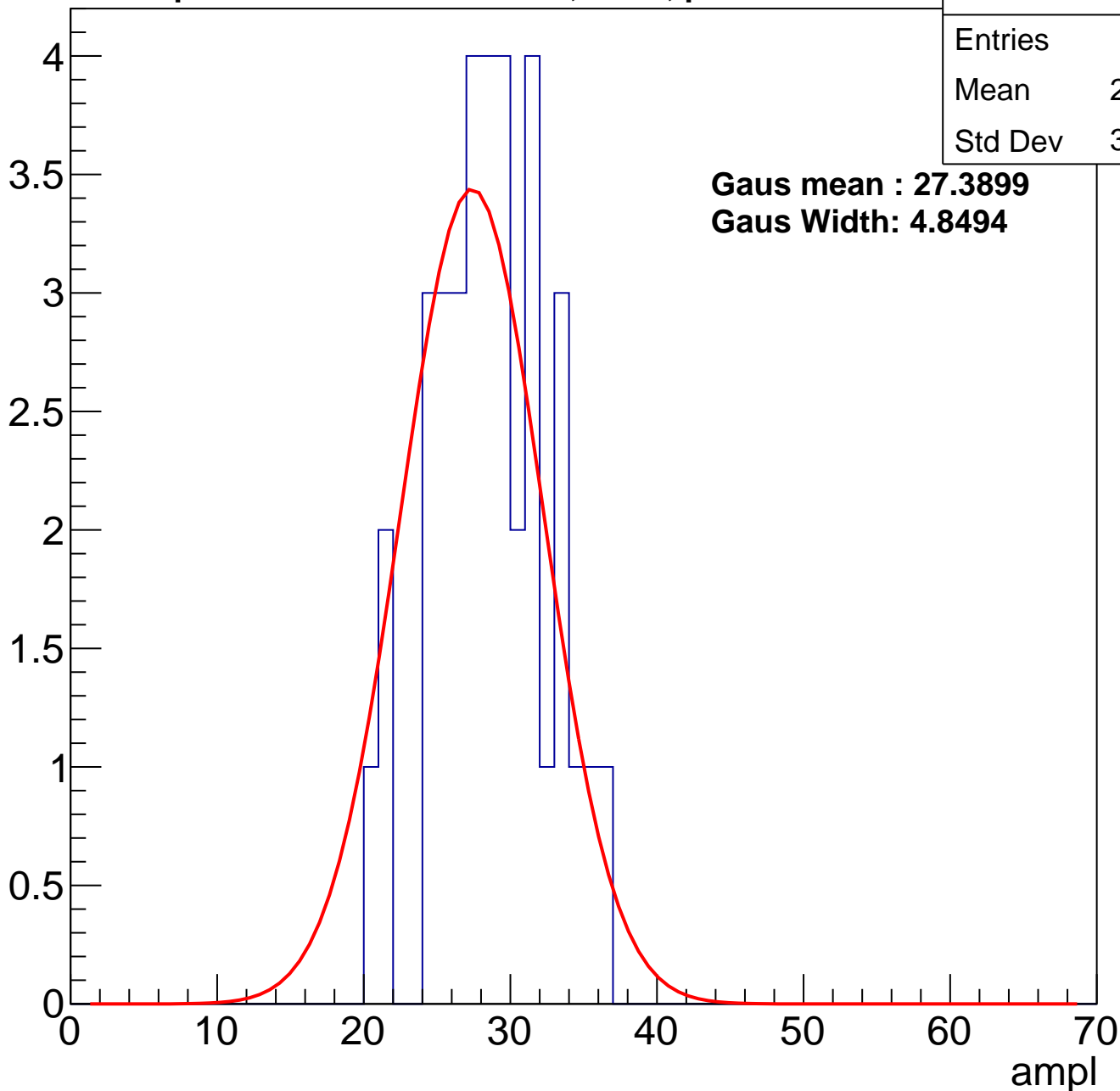


Entries	2
Mean	0
Std Dev	0

B1L103S, U15-ch91, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch91, adc1

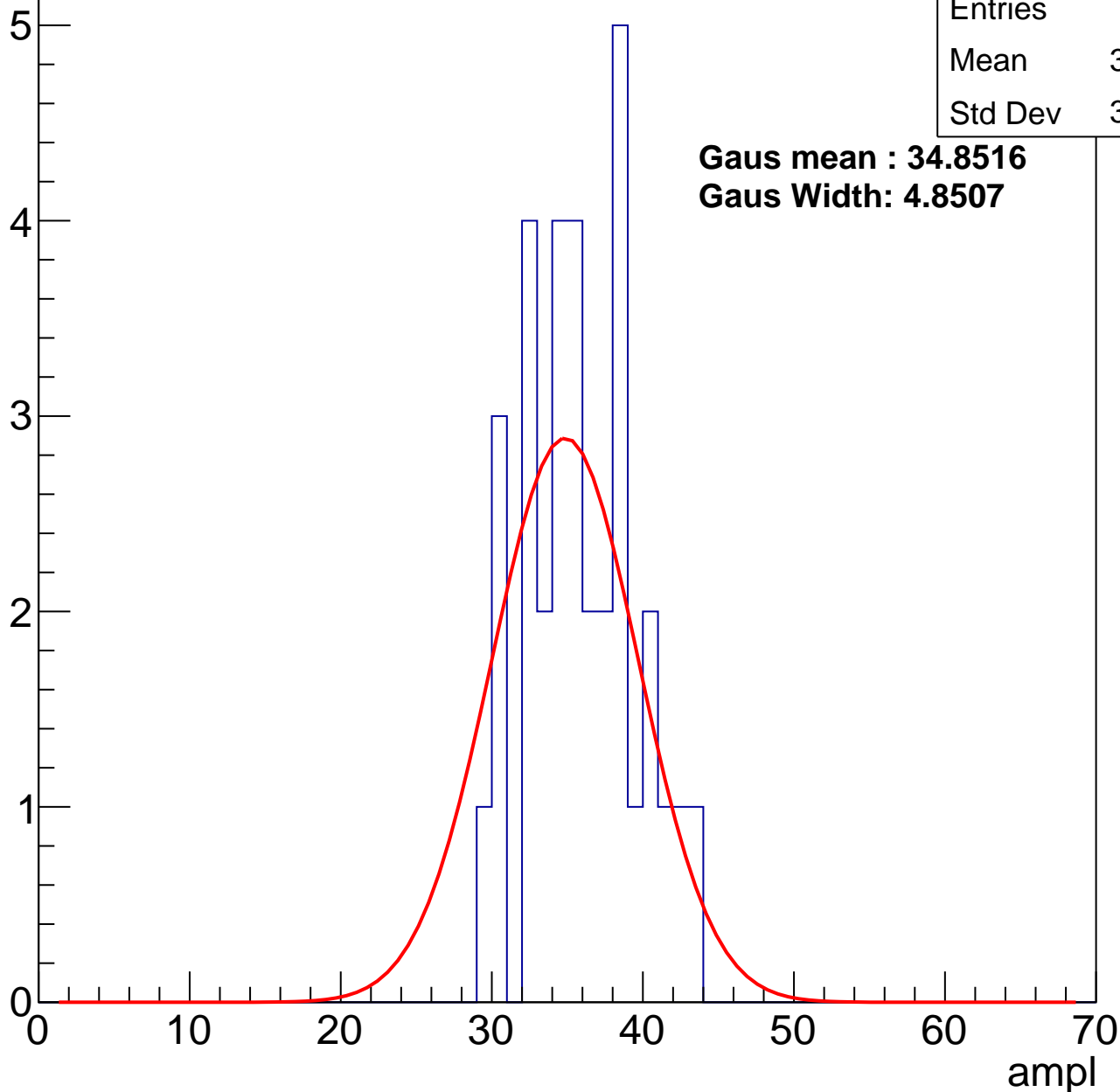
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	35.45
Std Dev	3.577

Gaus mean : 34.8516

Gaus Width: 4.8507



B1L103S, U15-ch91, adc2

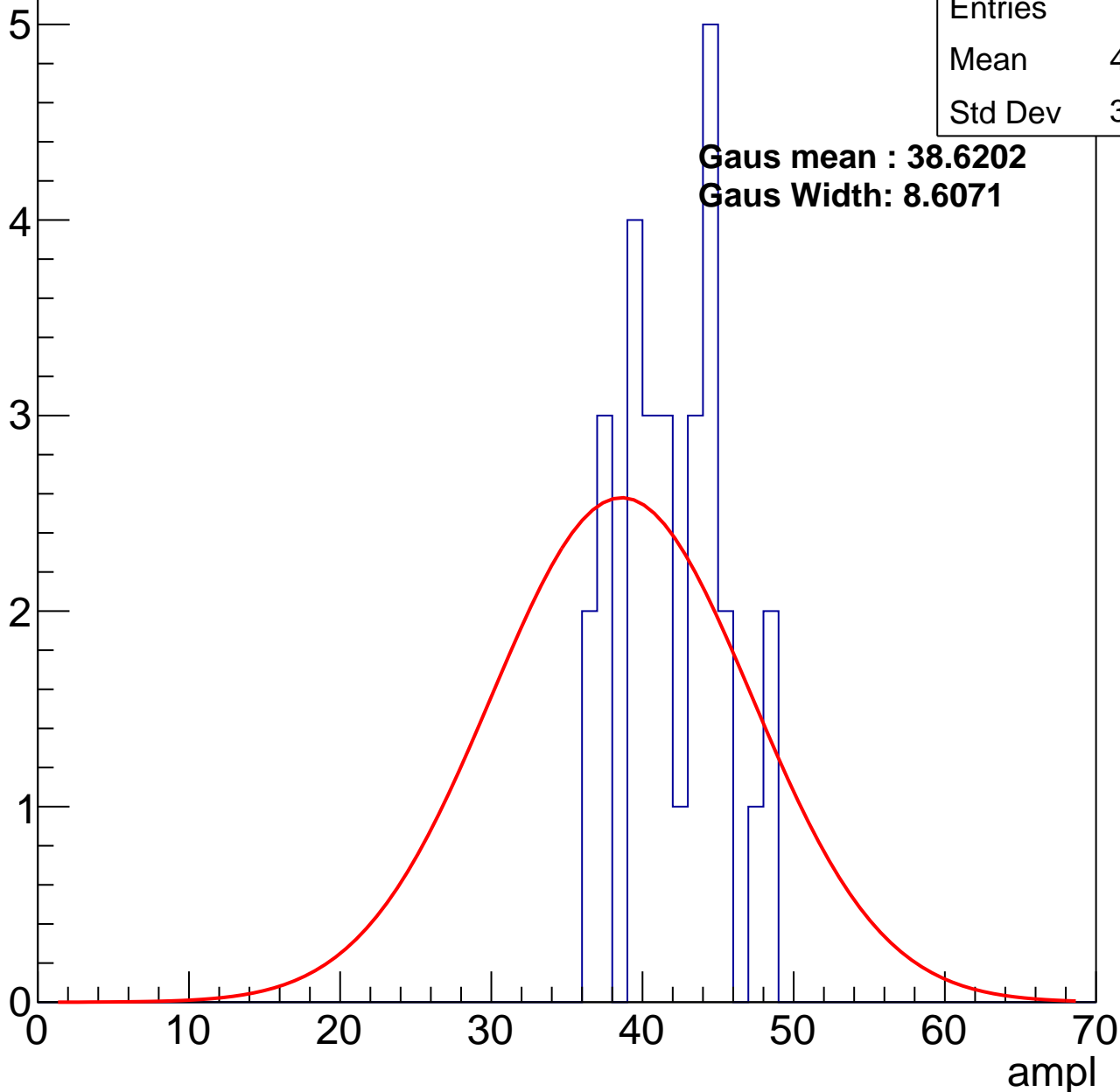
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	41.59
Std Dev	3.379

Gaus mean : 38.6202

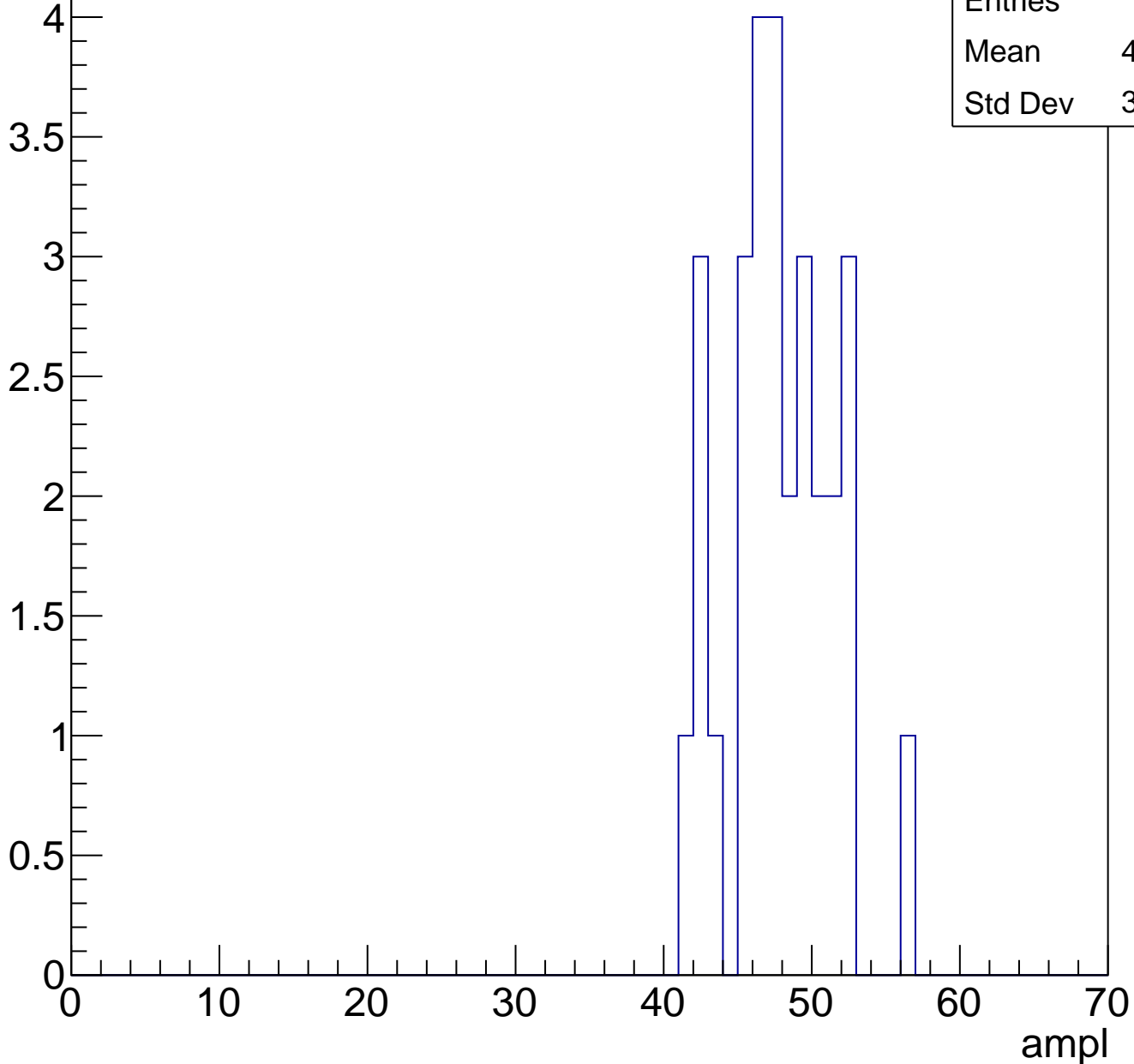
Gaus Width: 8.6071



B1L103S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



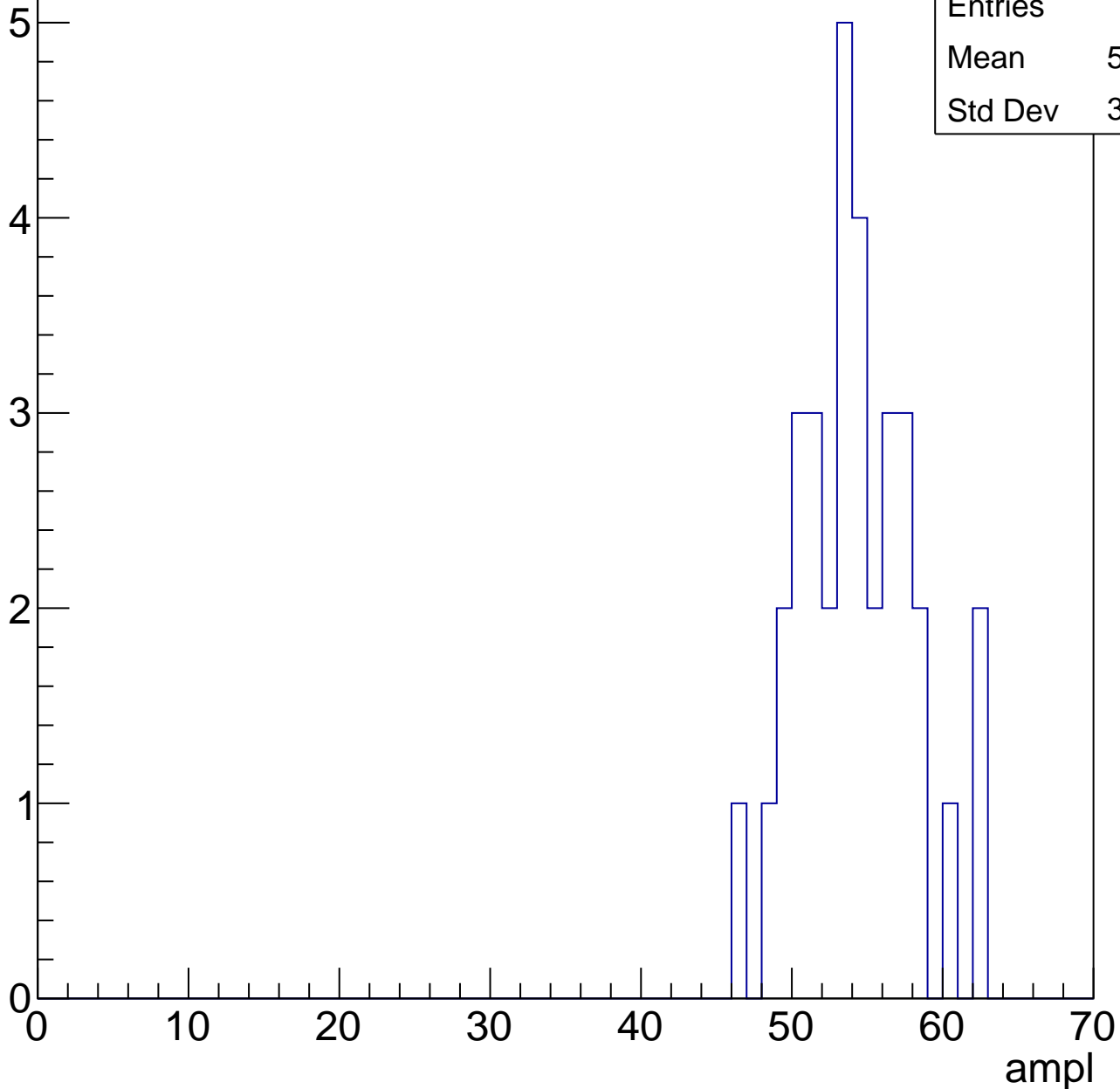
Entries	29
Mean	47.38
Std Dev	3.518

B1L103S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

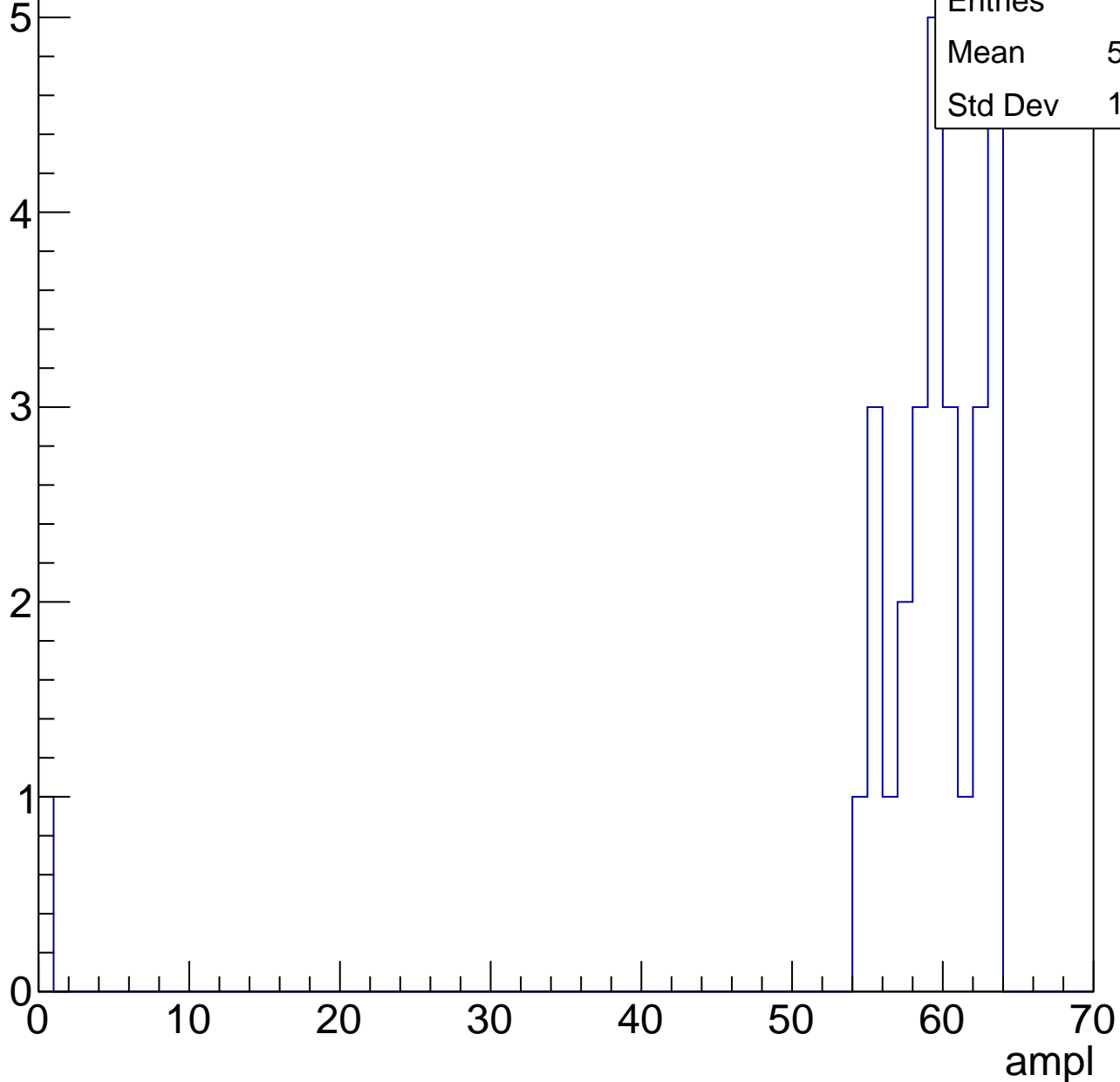
Entries	34
Mean	53.79
Std Dev	3.732



B1L103S, U15-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

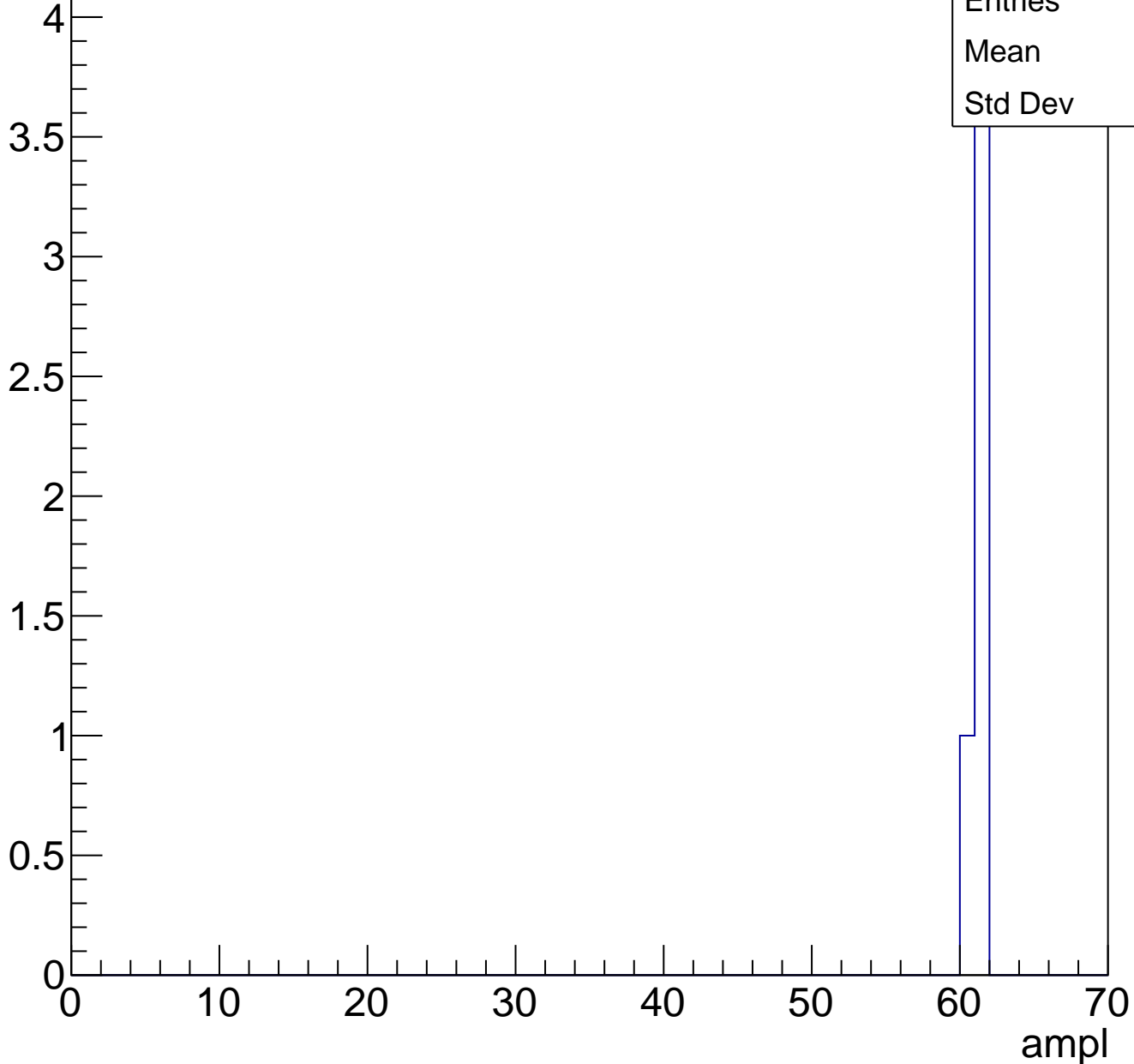
Entry



B1L103S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	60.8
Std Dev	0.4

B1L103S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

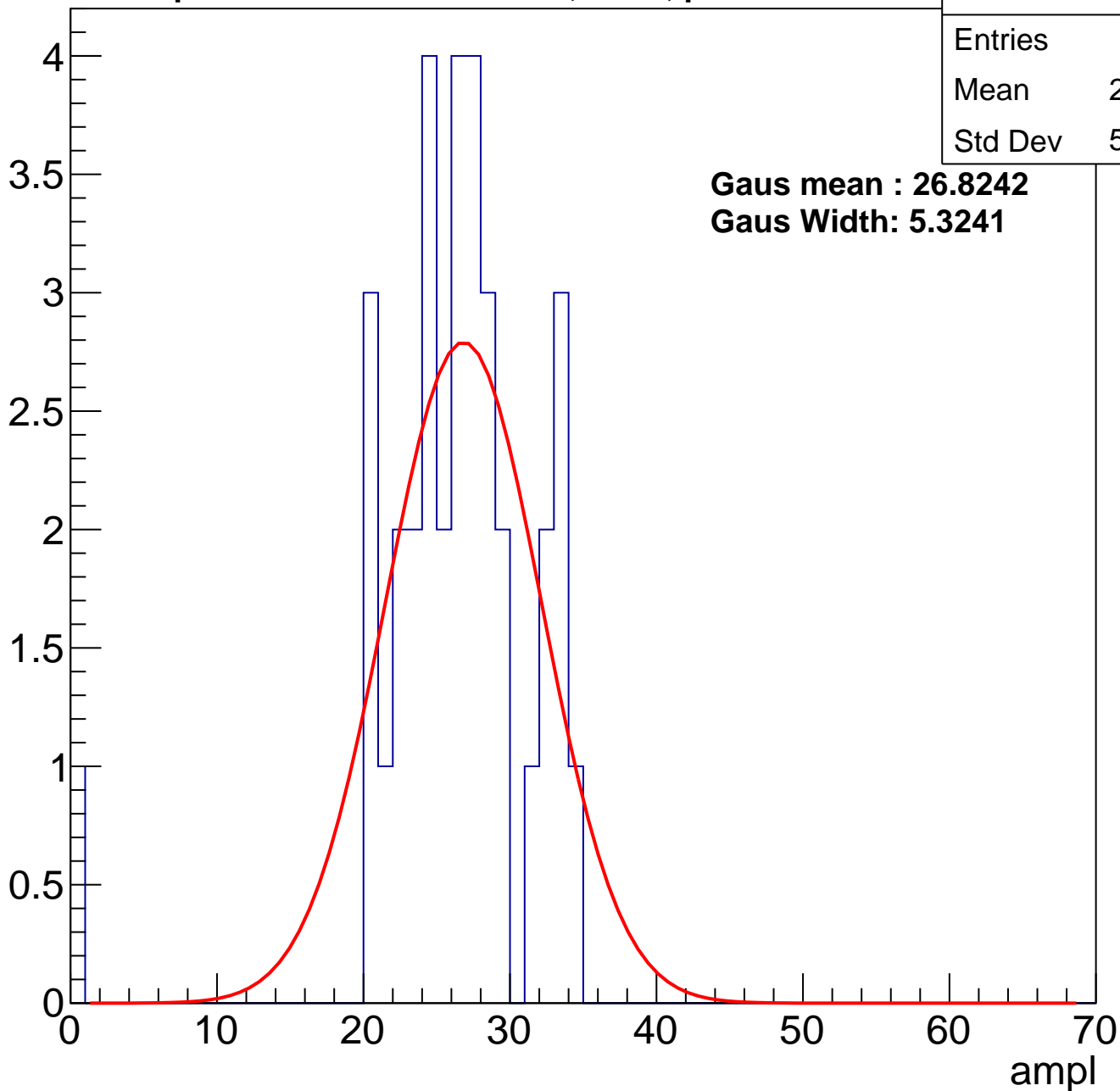


Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch92, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch92, adc1

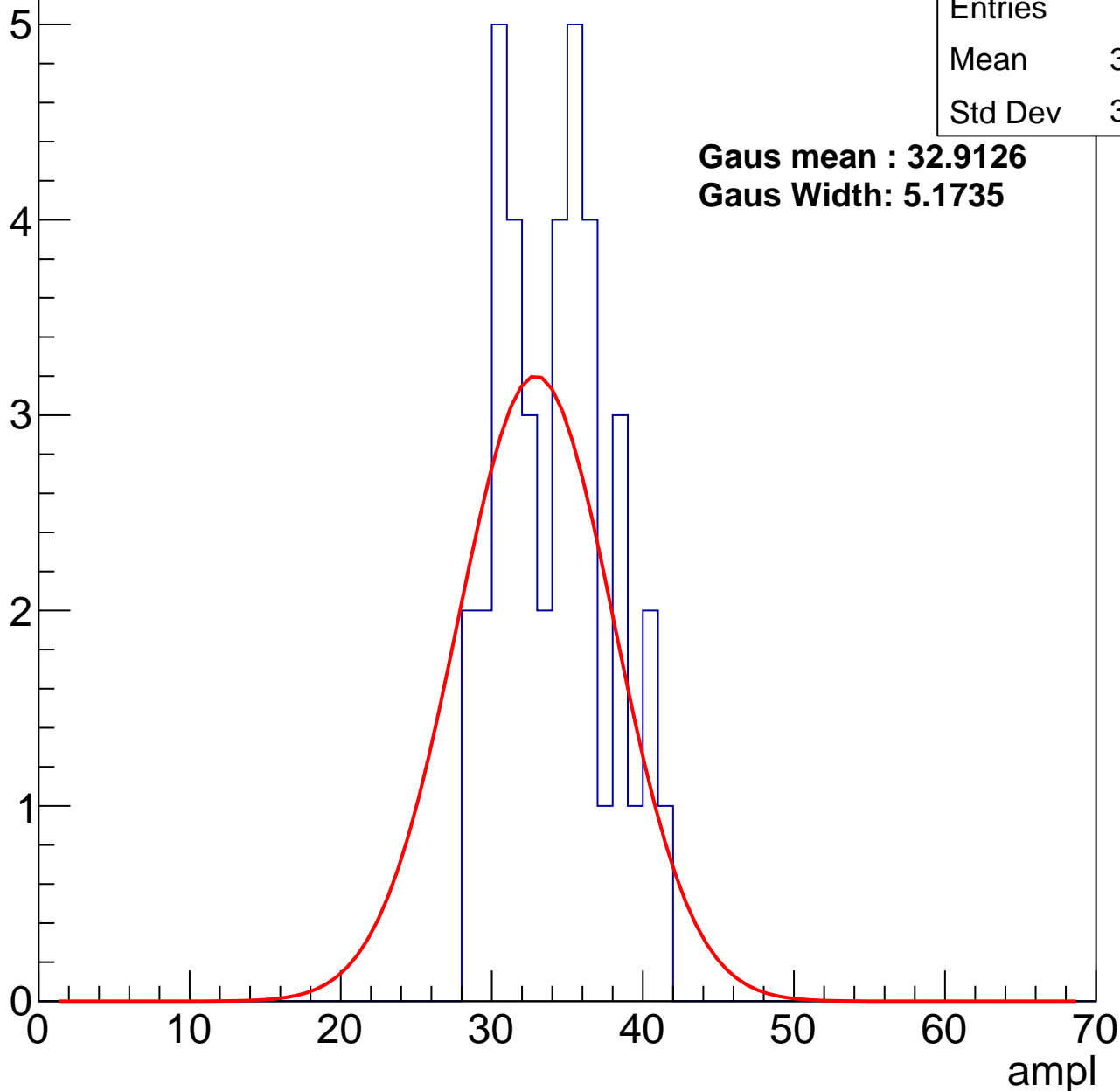
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	33.74
Std Dev	3.484

Gaus mean : 32.9126

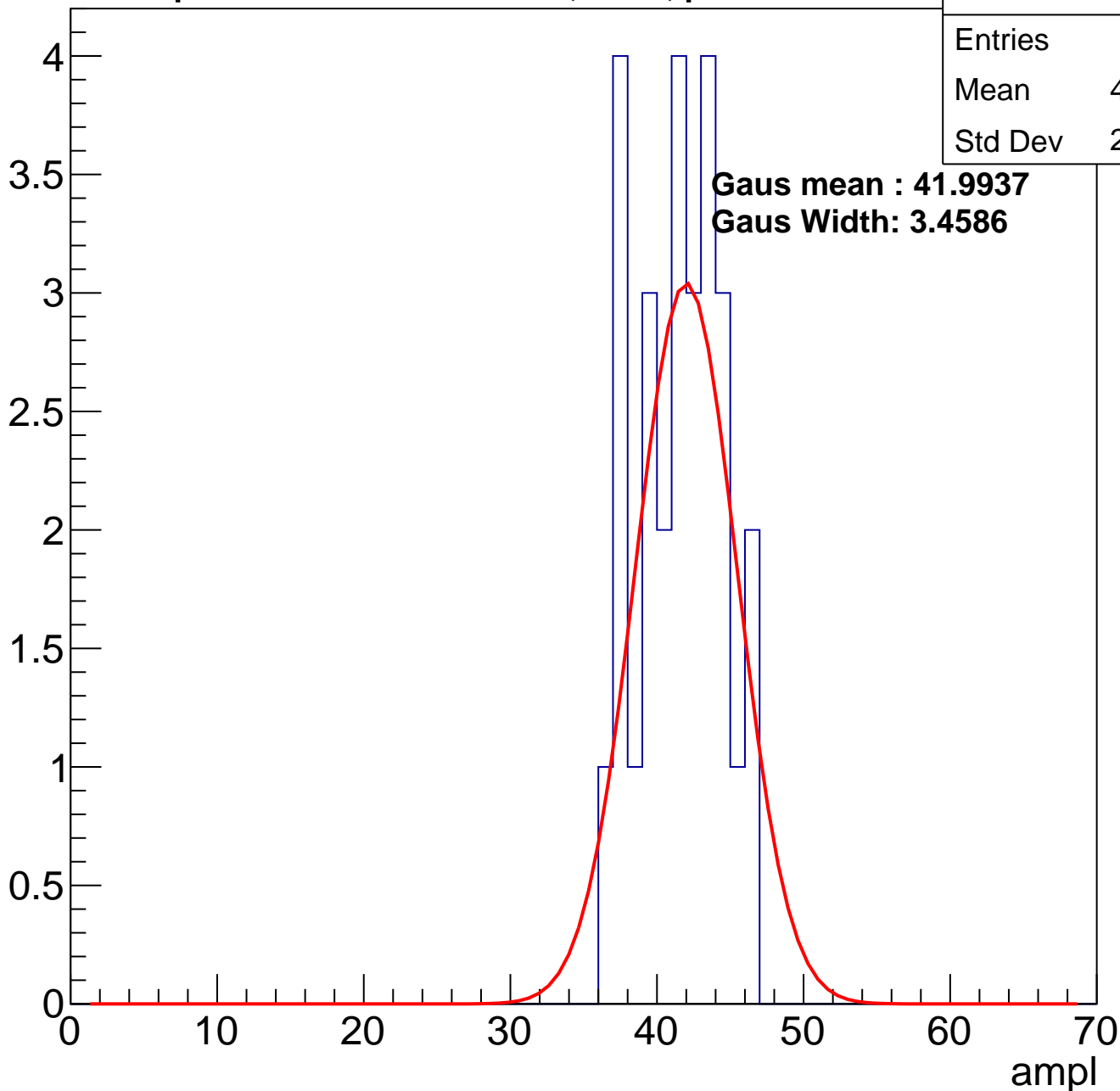
Gaus Width: 5.1735



B1L103S, U15-ch92, adc2

calib_packv5_042523_0143.root, FC#7, port C2

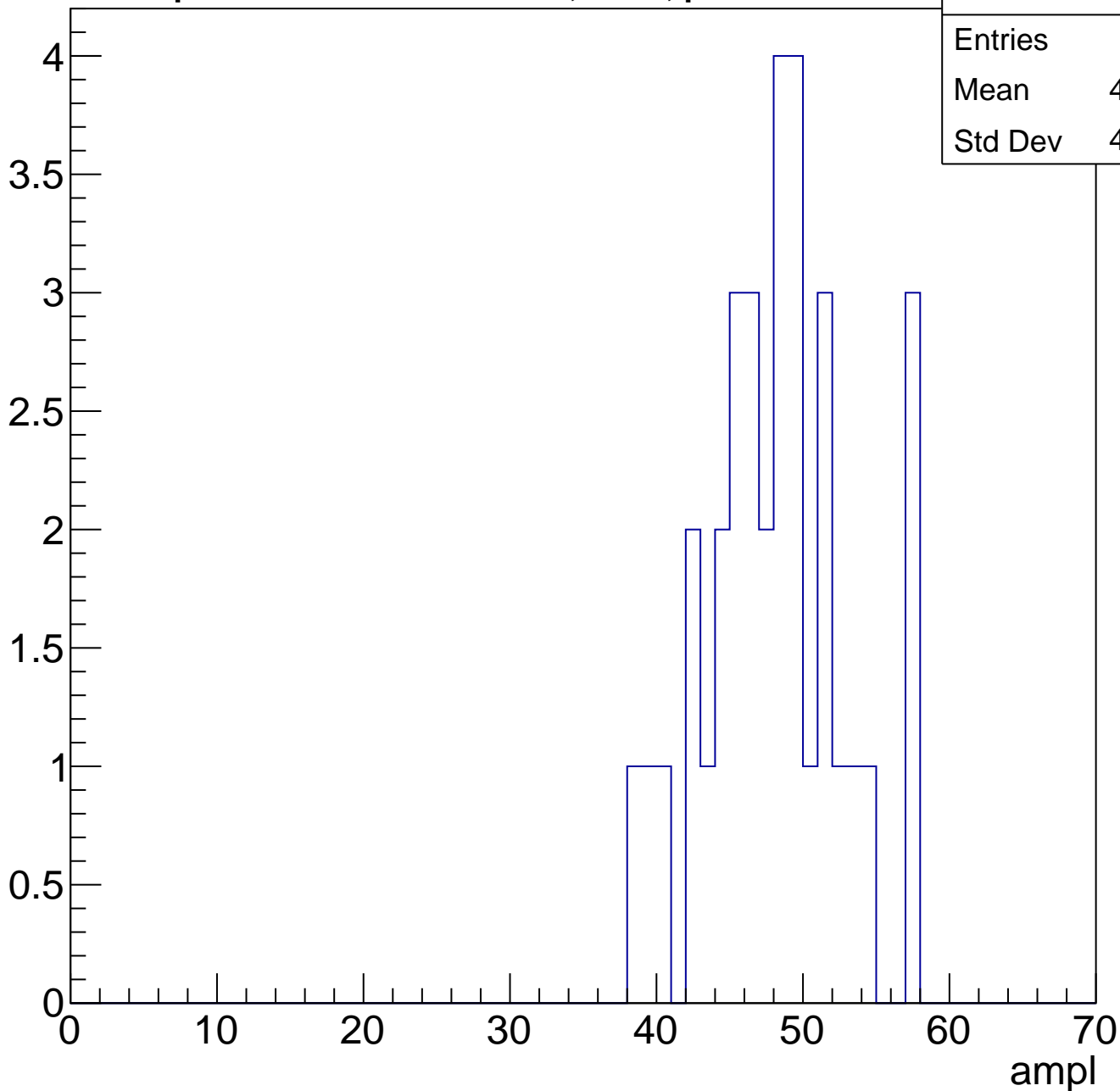
Entry



B1L103S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

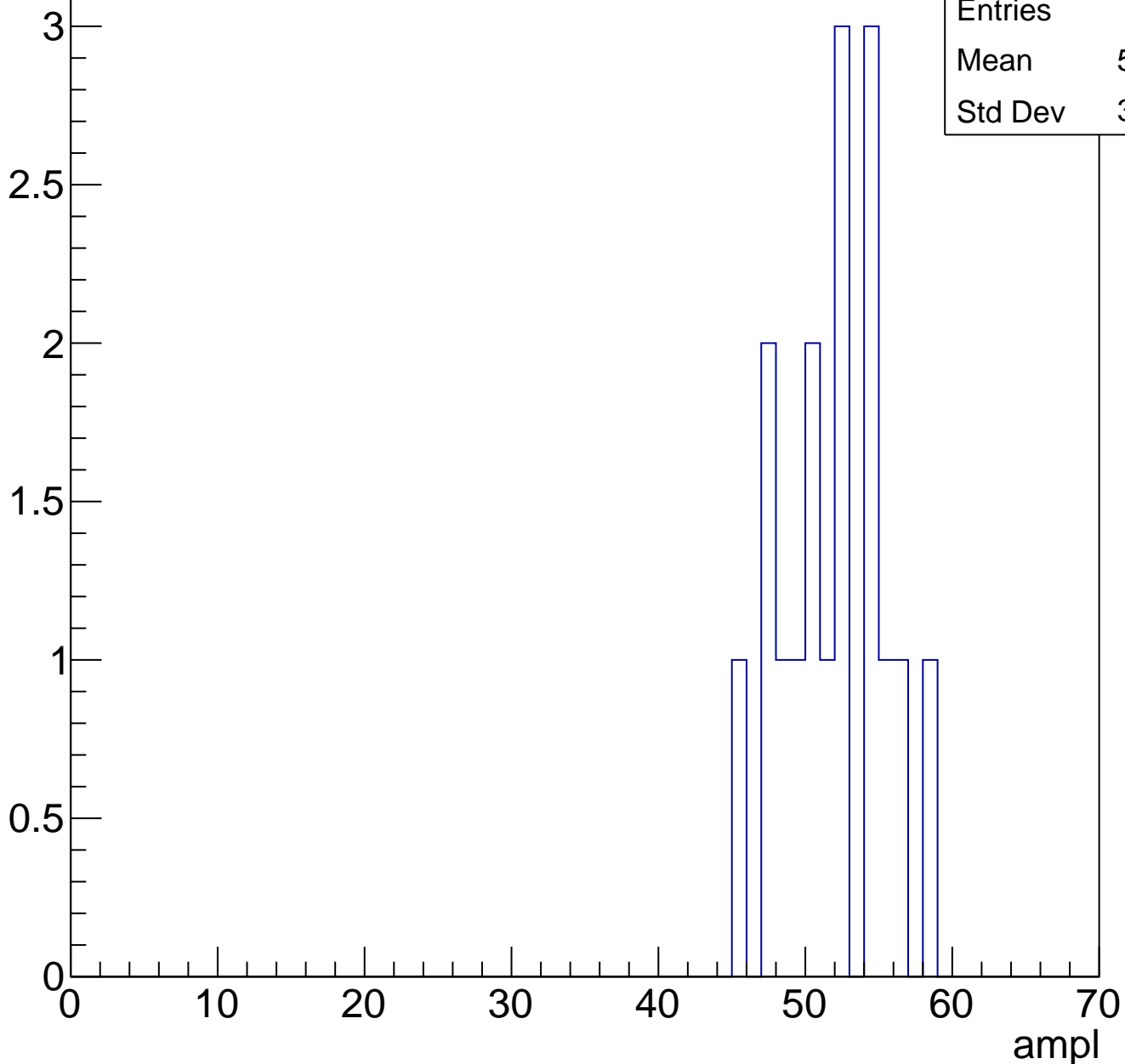
Entry



B1L103S, U15-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

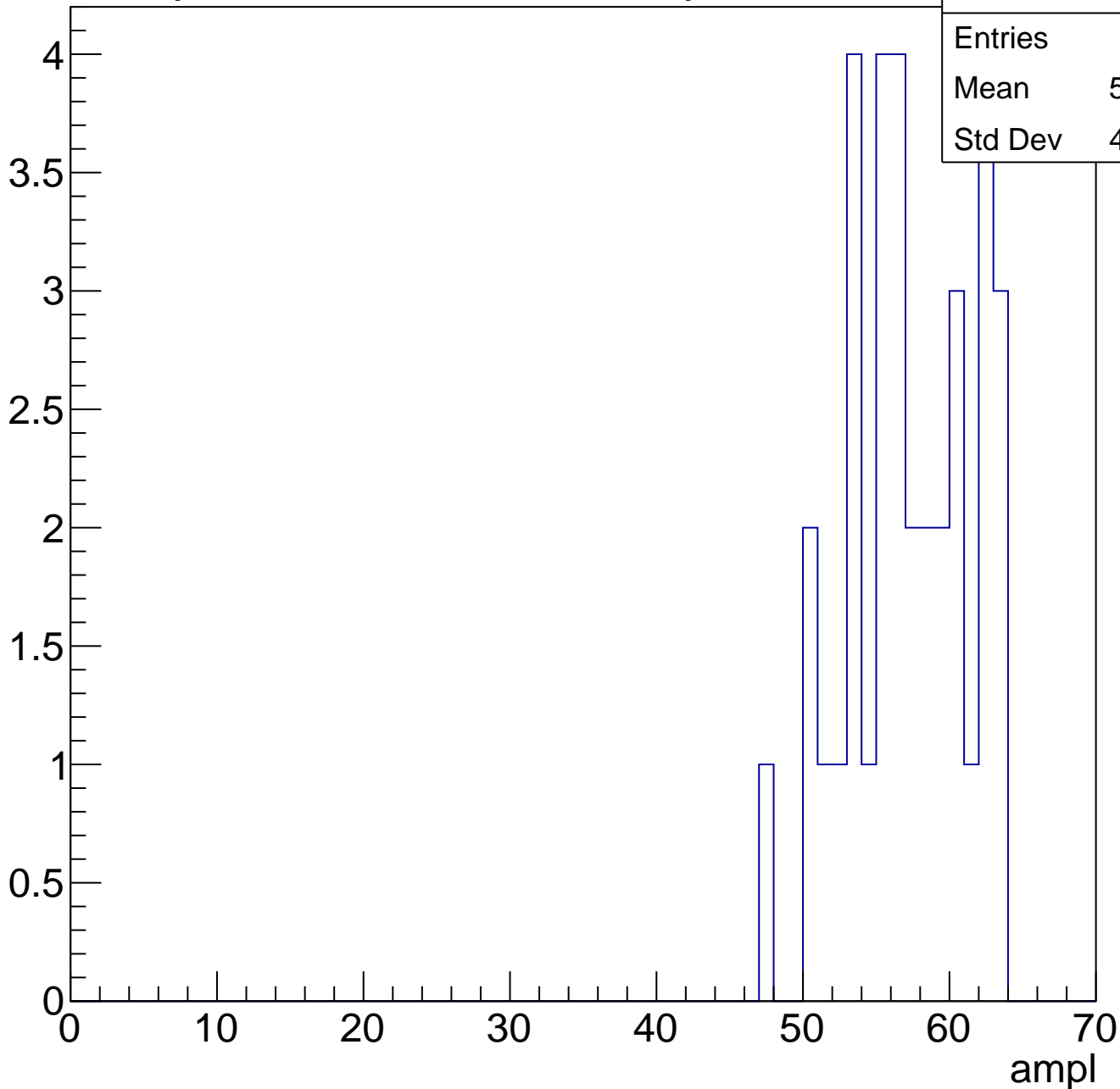
Entry



B1L103S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	35
Mean	56.74
Std Dev	4.177

B1L103S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

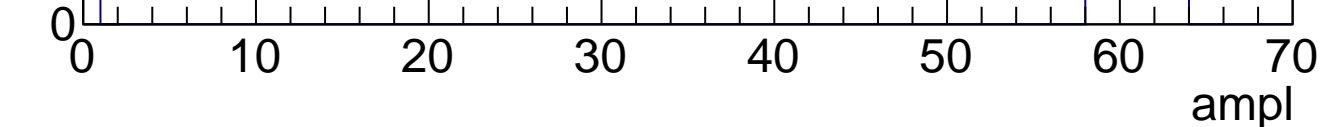
11

Mean

54.45

Std Dev

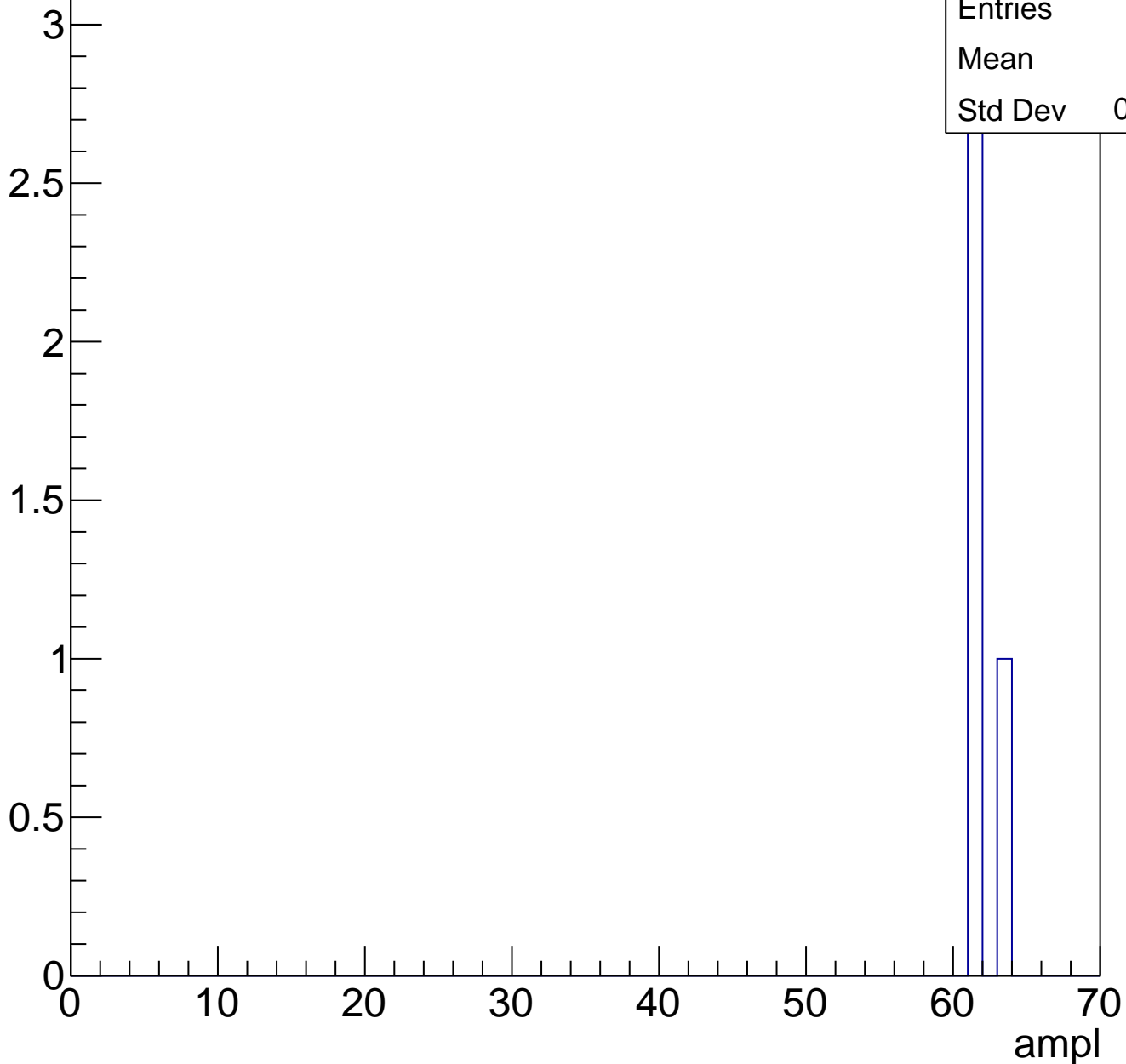
17.29



B1L103S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch93, adc0

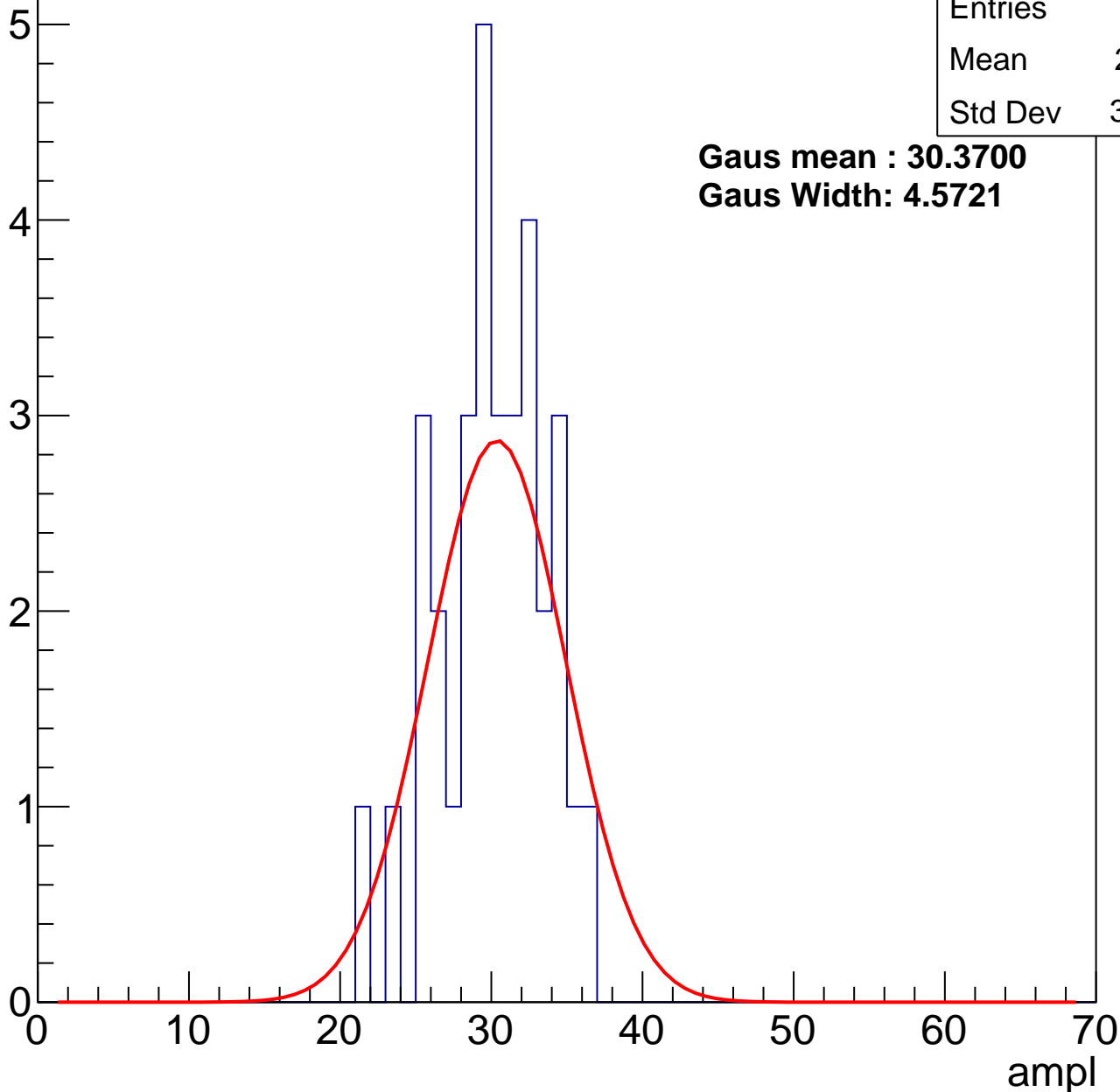
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	29.61
Std Dev	3.498

Gaus mean : 30.3700

Gaus Width: 4.5721



B1L103S, U15-ch93, adc1

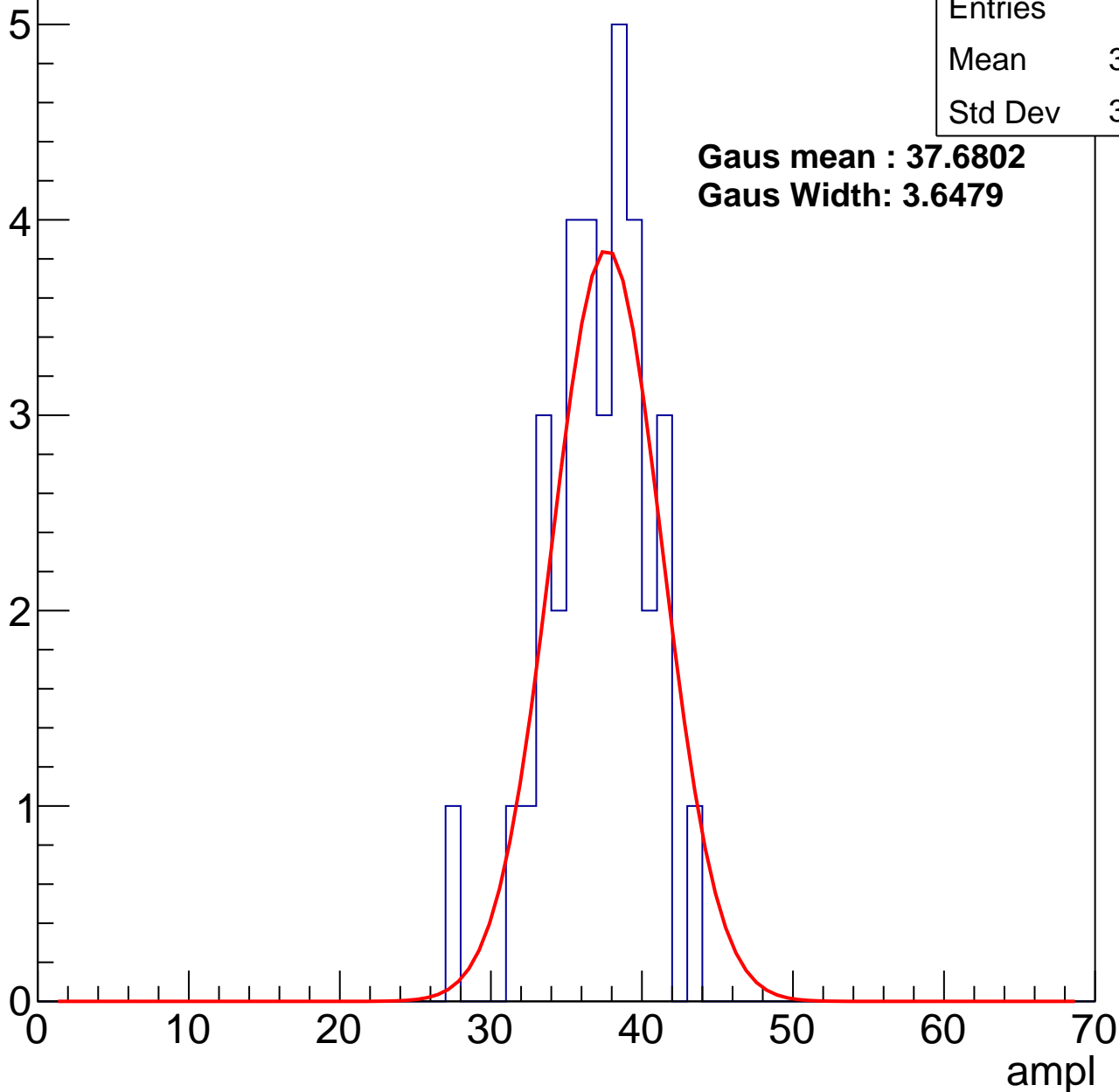
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	36.59
Std Dev	3.273

Gaus mean : 37.6802

Gaus Width: 3.6479



B1L103S, U15-ch93, adc2

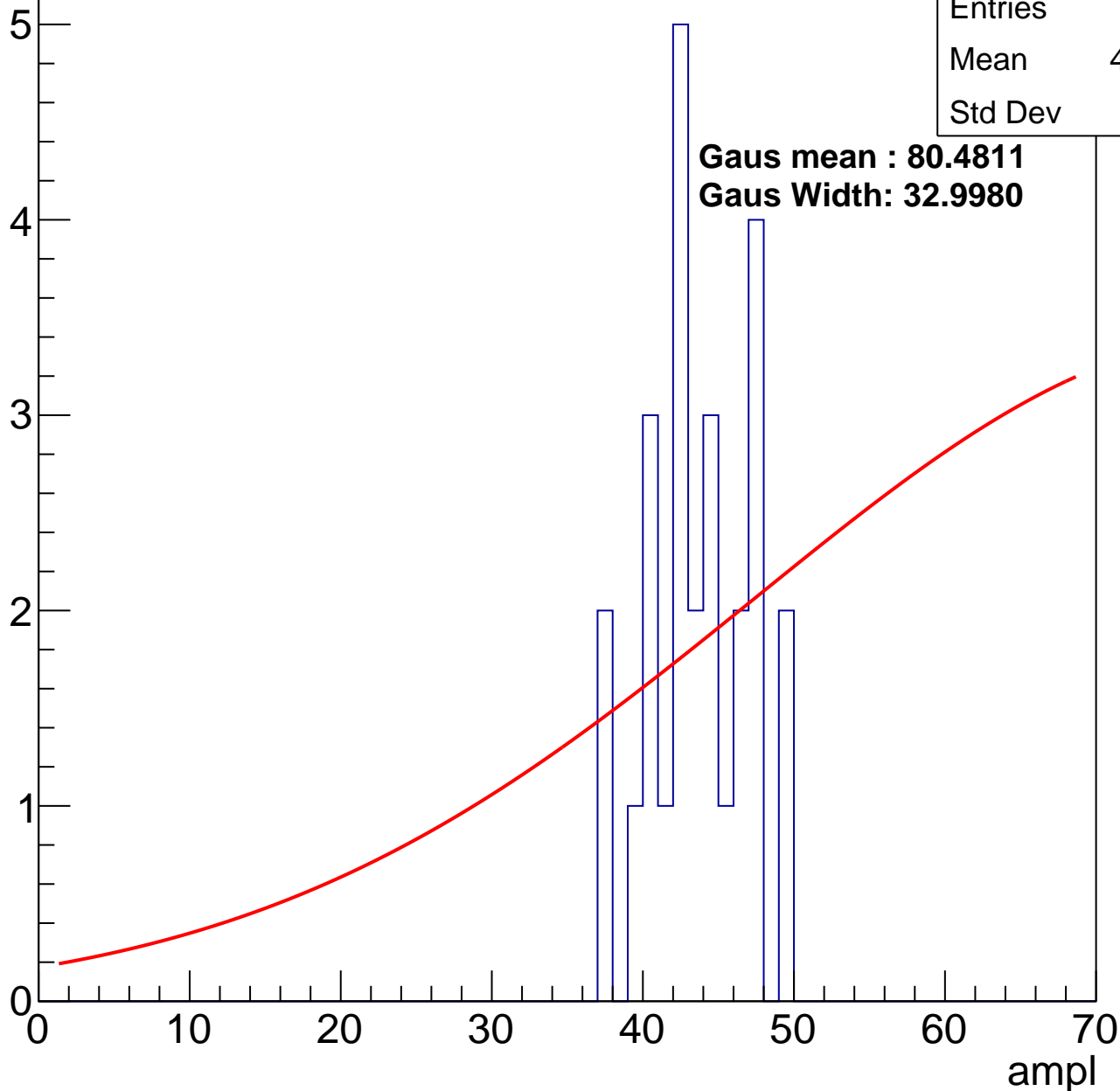
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	43.27
Std Dev	3.3

Gaus mean : 80.4811

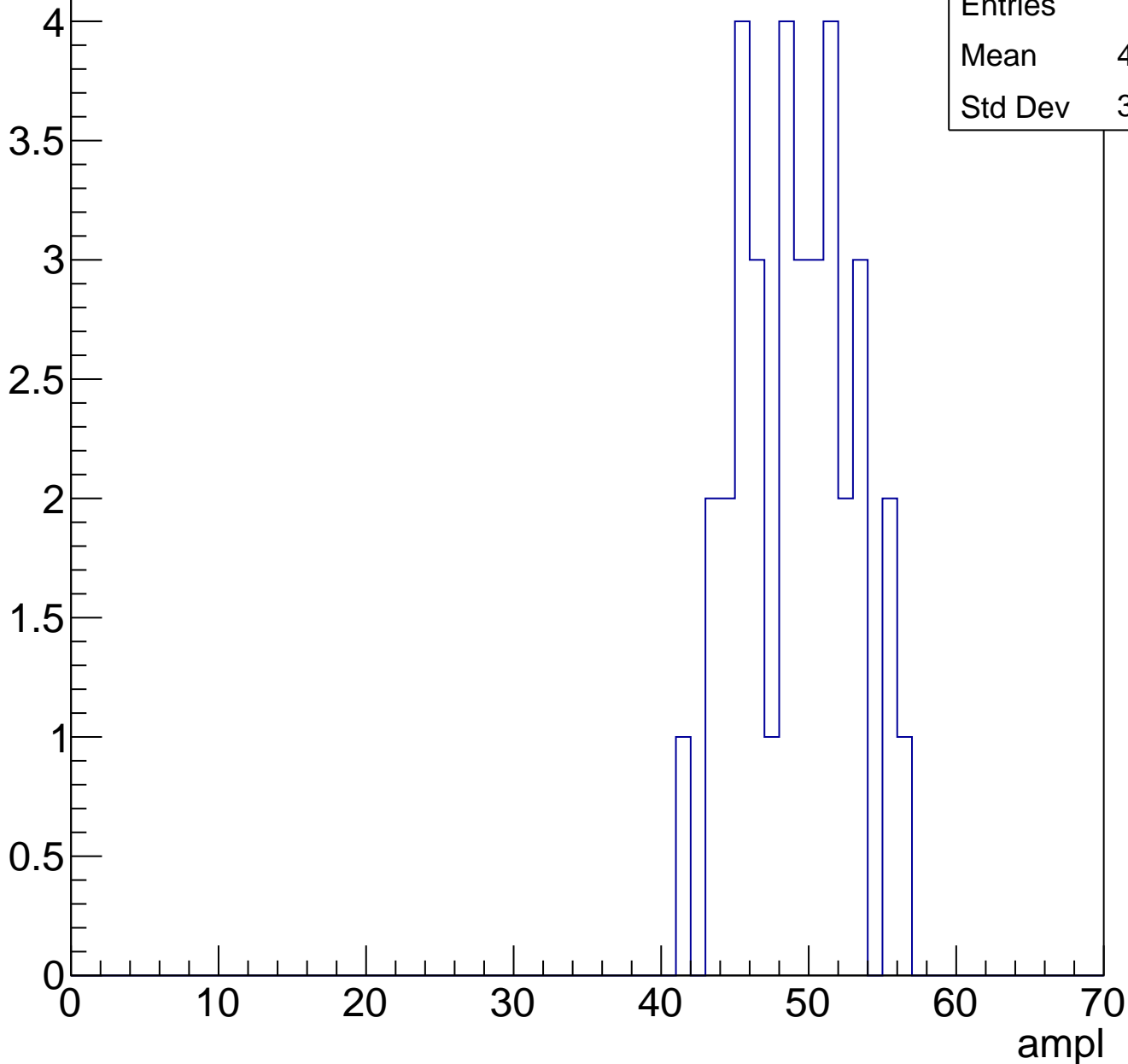
Gaus Width: 32.9980



B1L103S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

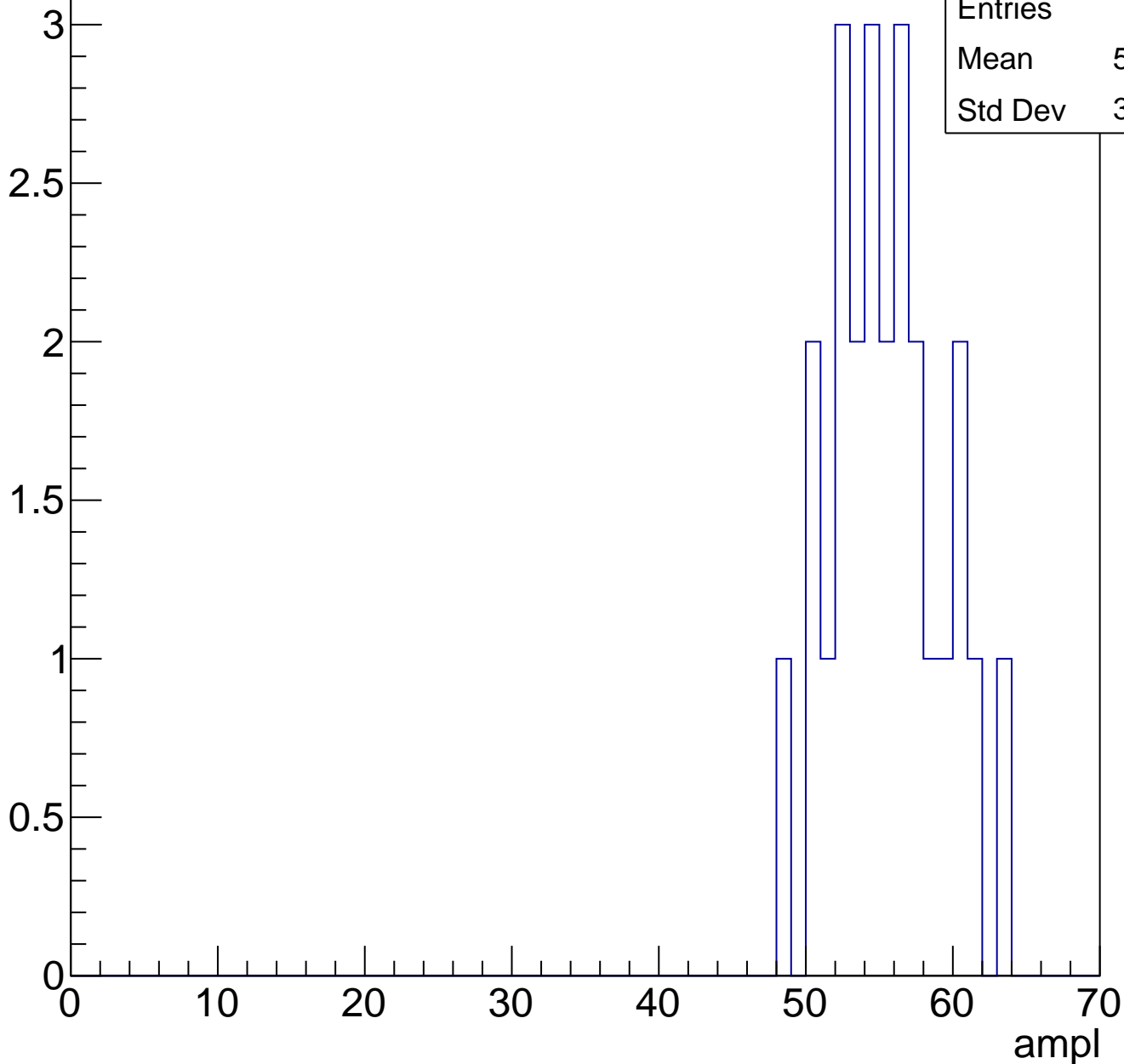
Entry



B1L103S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

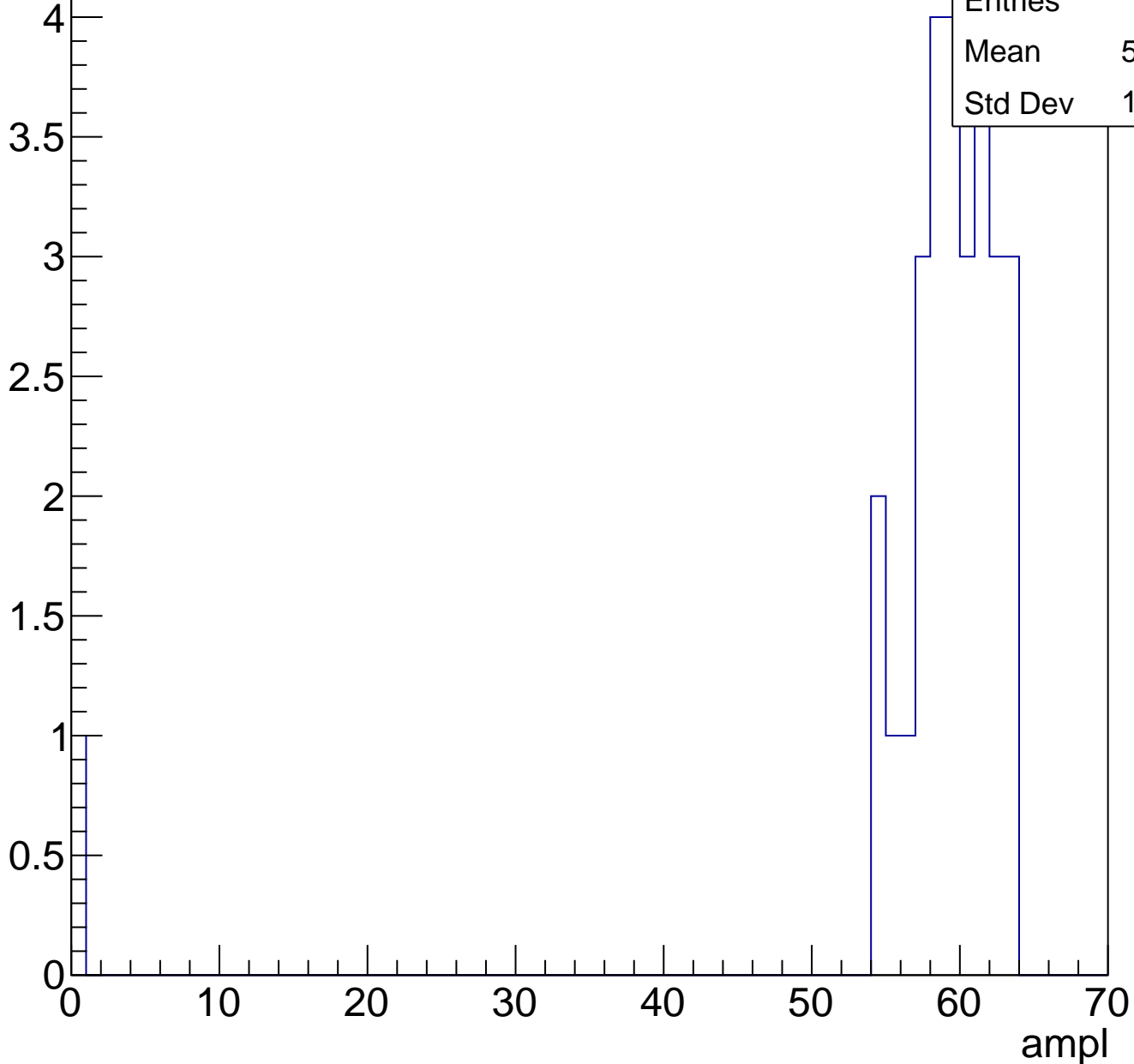
Entry



B1L103S, U15-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch94, adc0

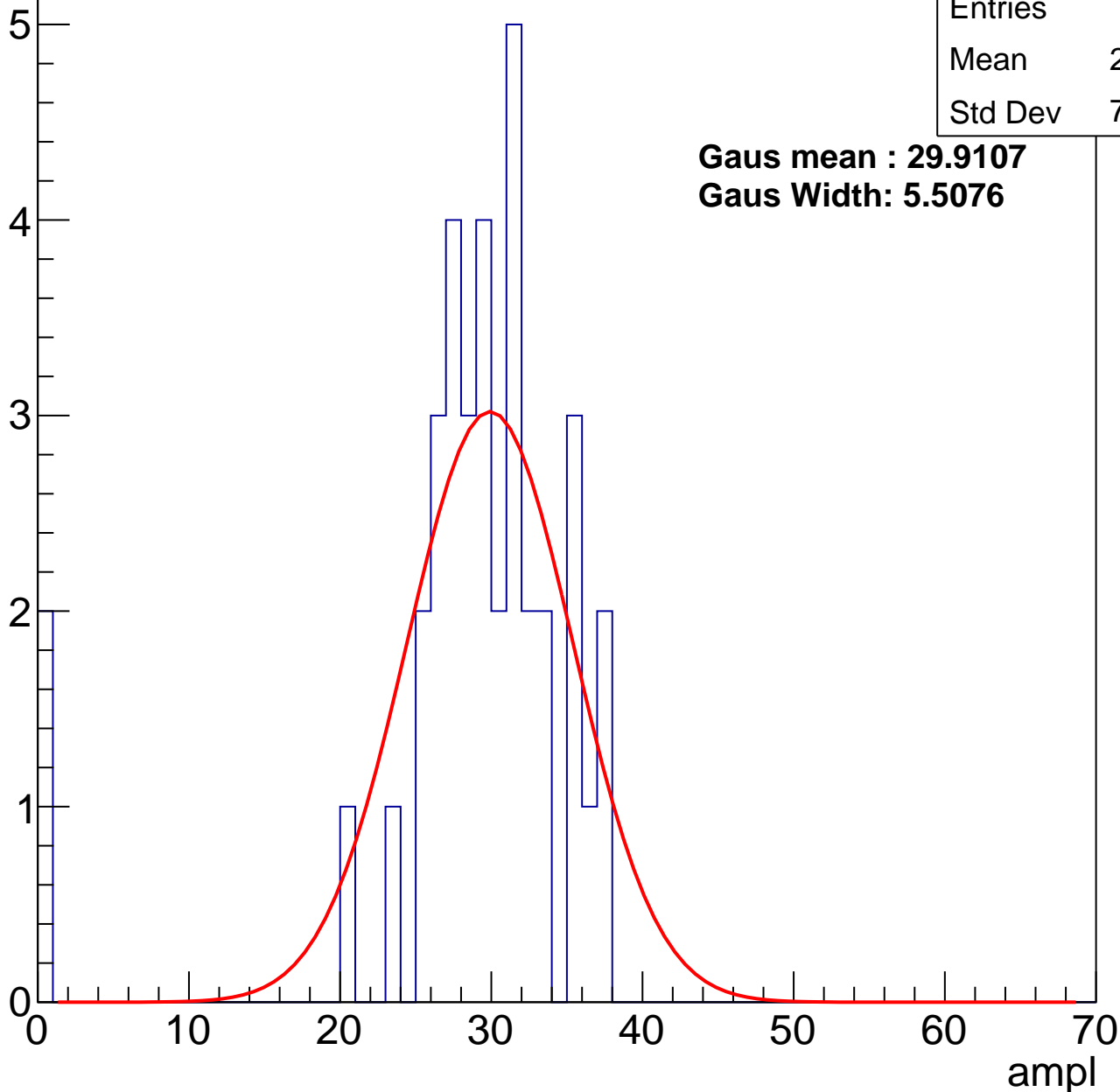
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	28.08
Std Dev	7.716

Gaus mean : 29.9107

Gaus Width: 5.5076



B1L103S, U15-ch94, adc1

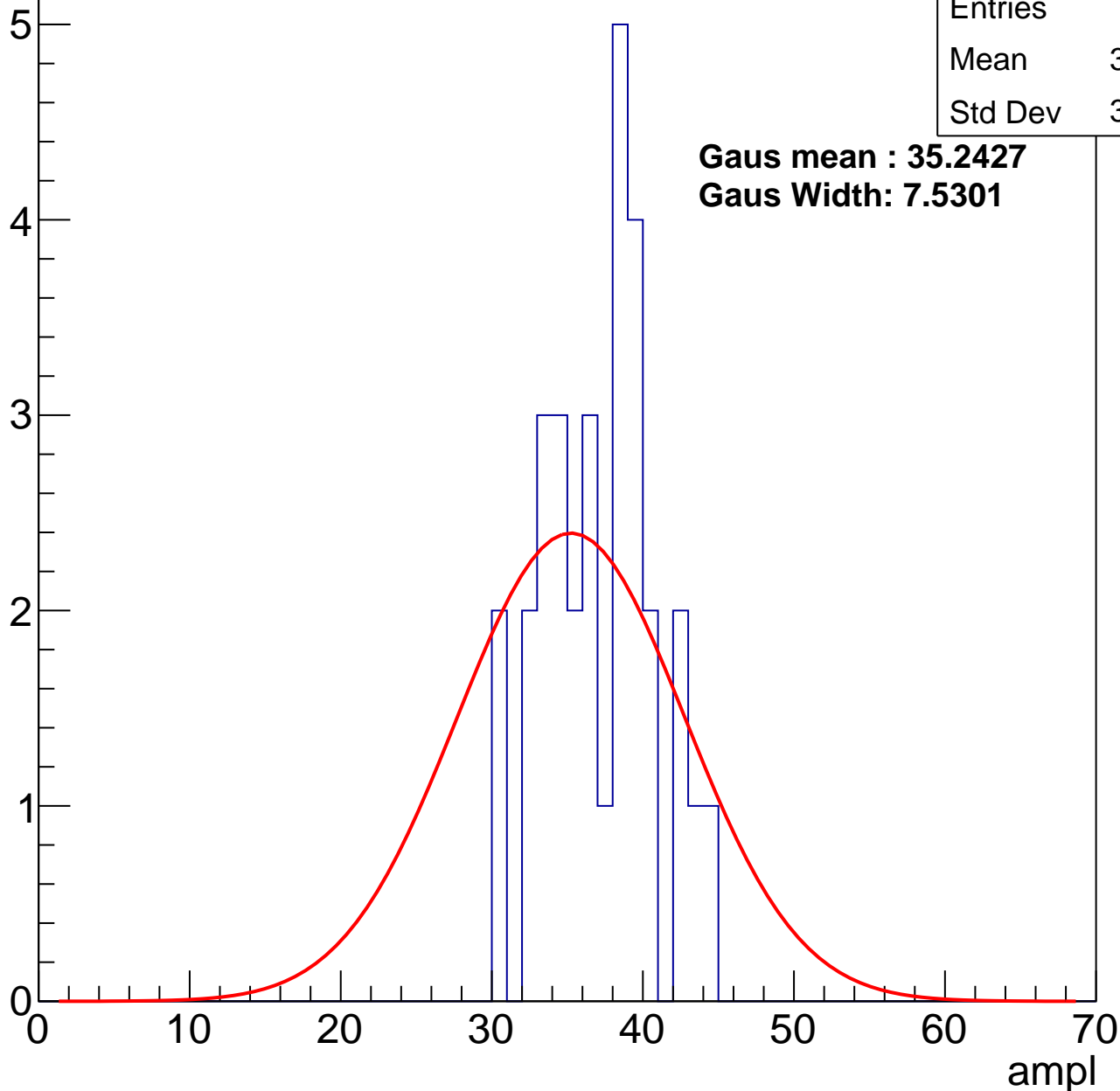
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	36.68
Std Dev	3.613

Gaus mean : 35.2427

Gaus Width: 7.5301



B1L103S, U15-ch94, adc2

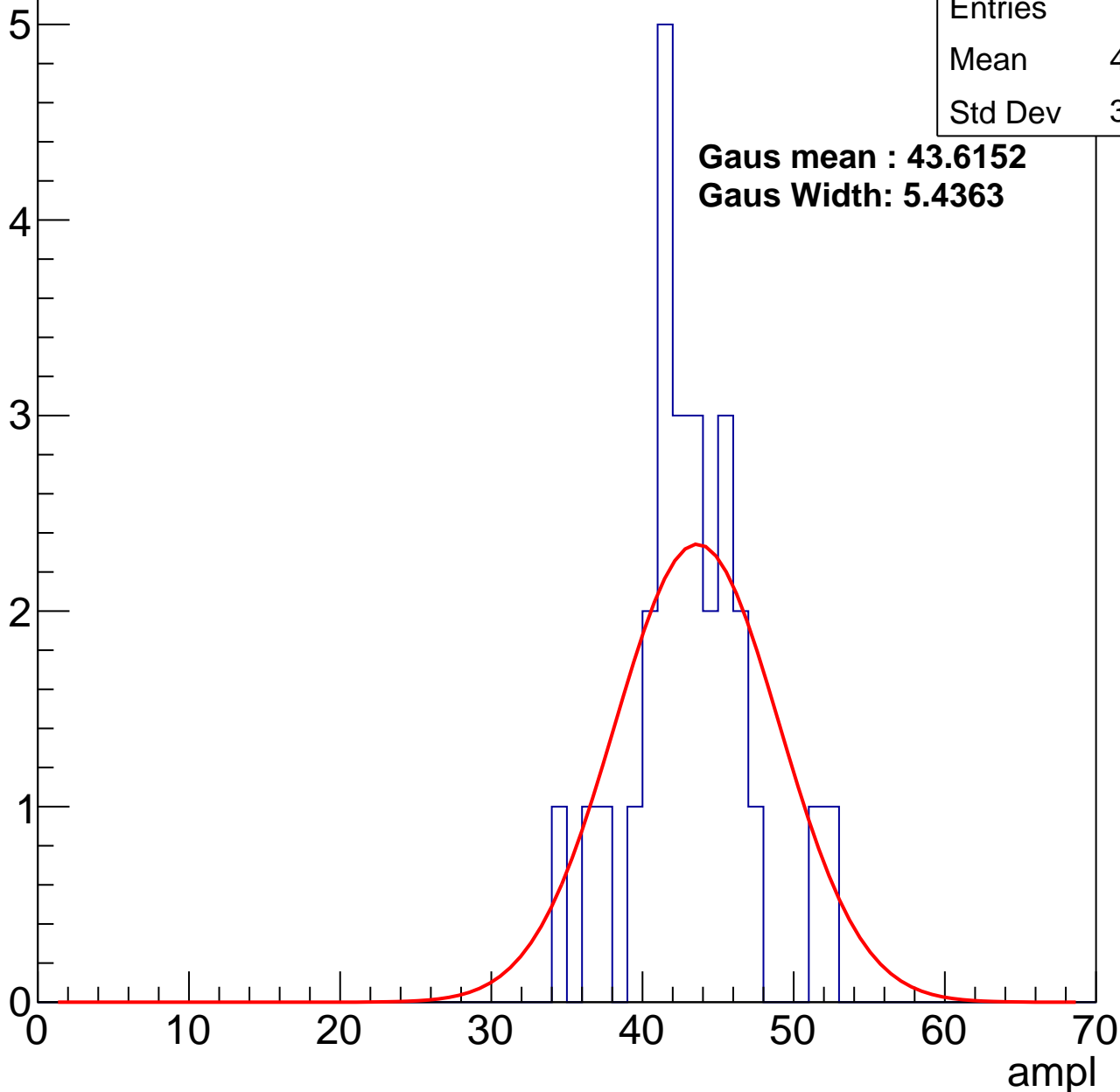
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	42.63
Std Dev	3.908

Gaus mean : 43.6152

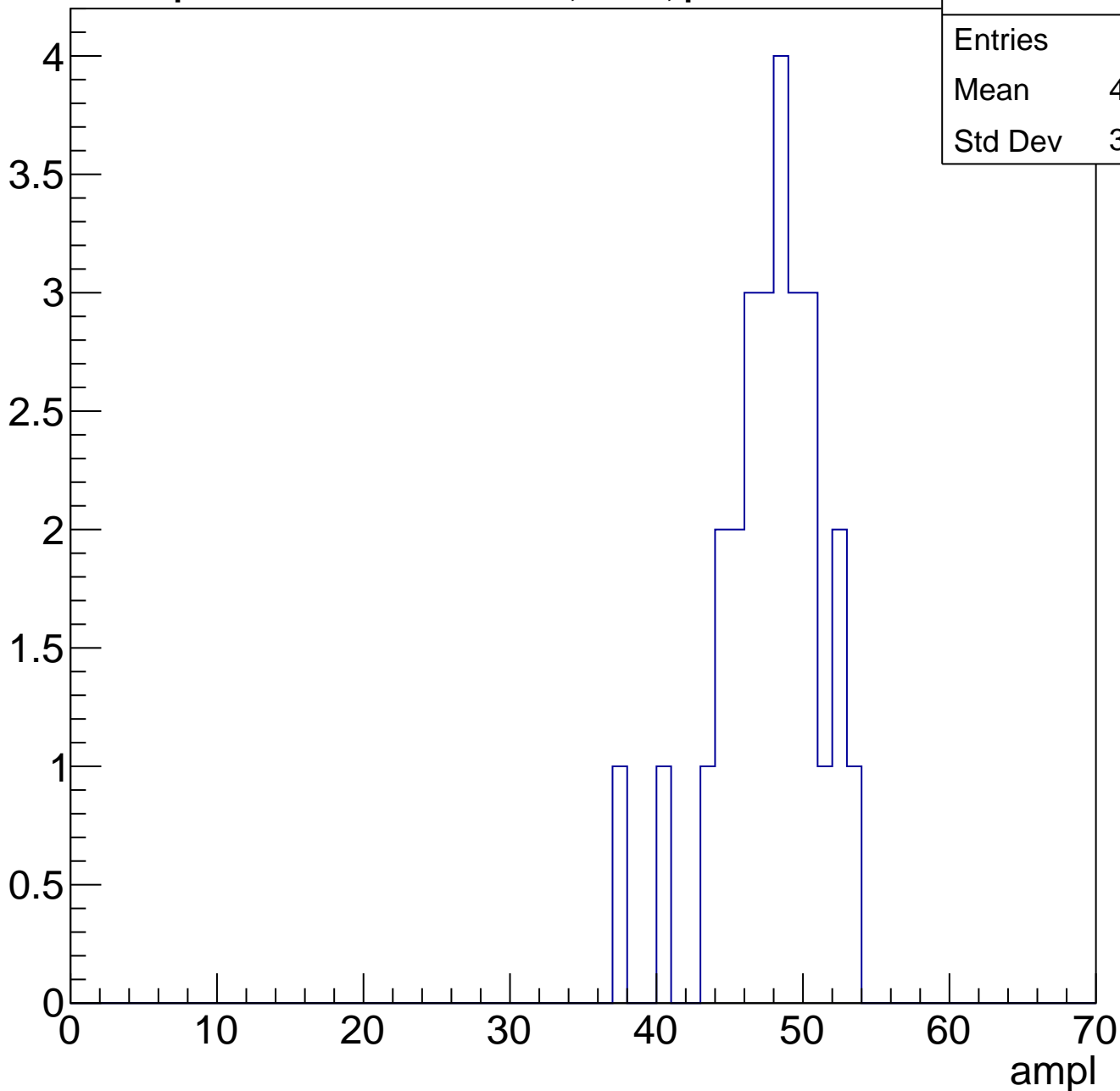
Gaus Width: 5.4363



B1L103S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



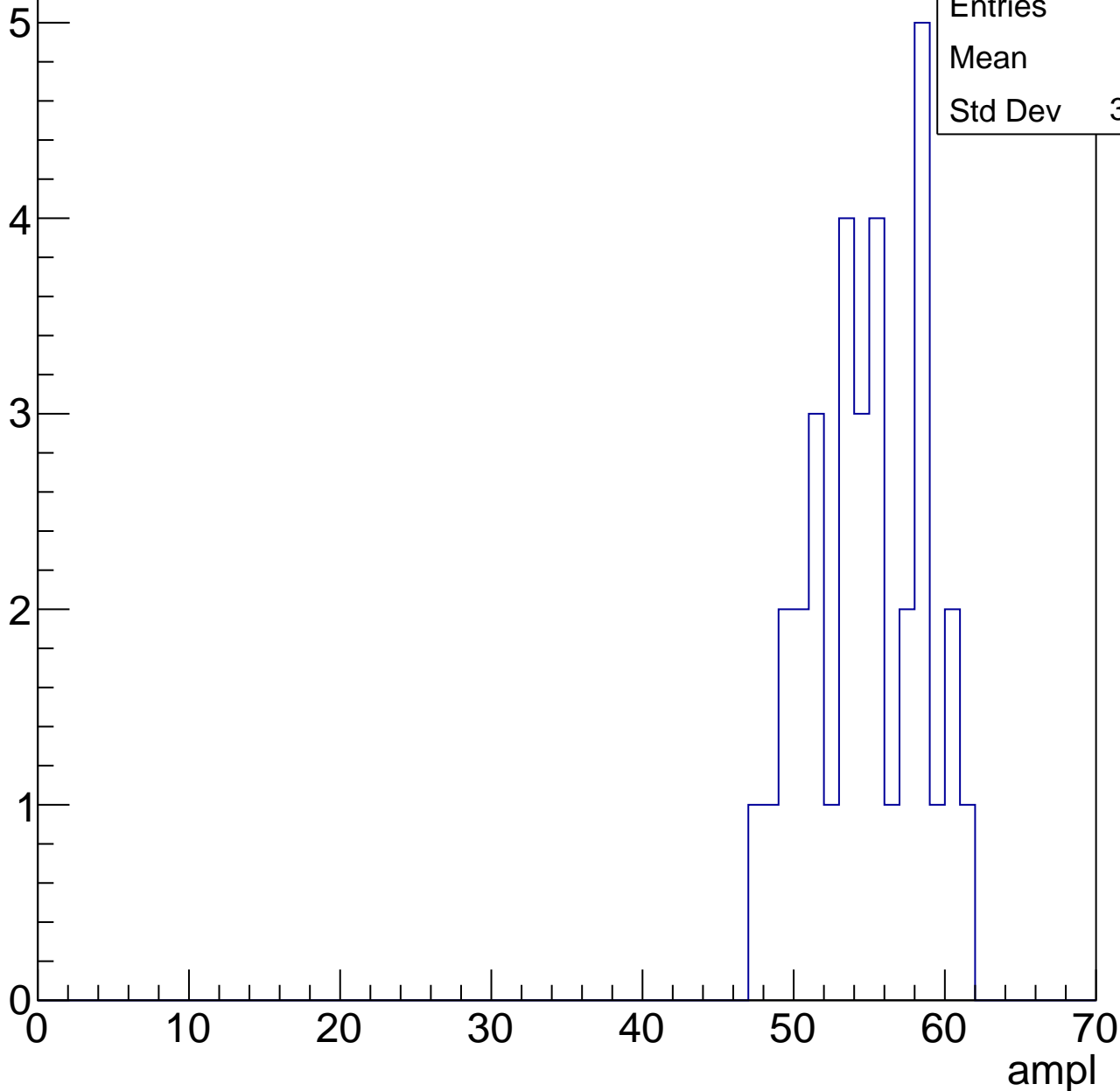
Entries	27
Mean	47.19
Std Dev	3.539

B1L103S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

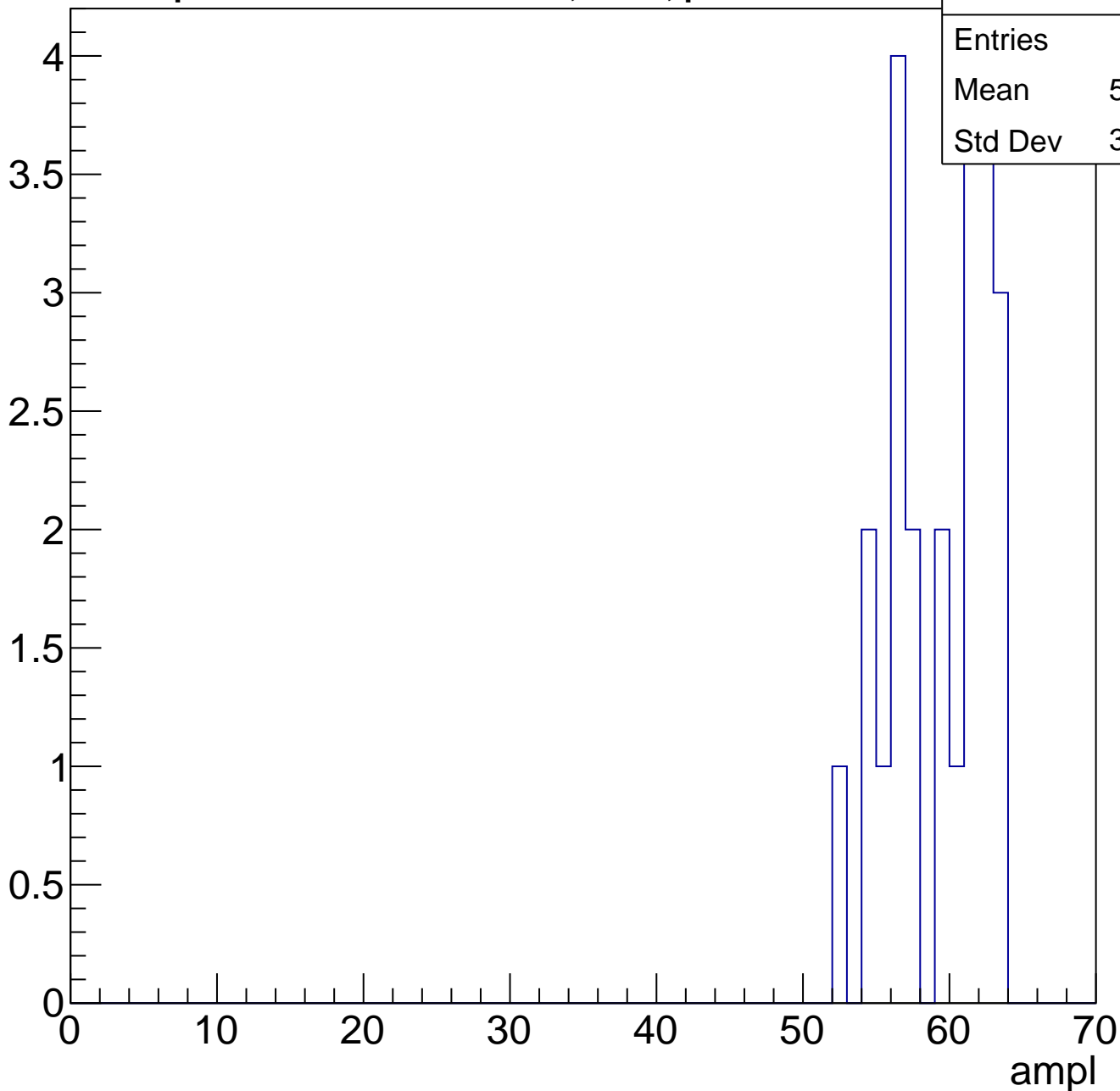
Entries	33
Mean	54.3
Std Dev	3.697



B1L103S, U15-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	24
Mean	58.83
Std Dev	3.287

B1L103S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

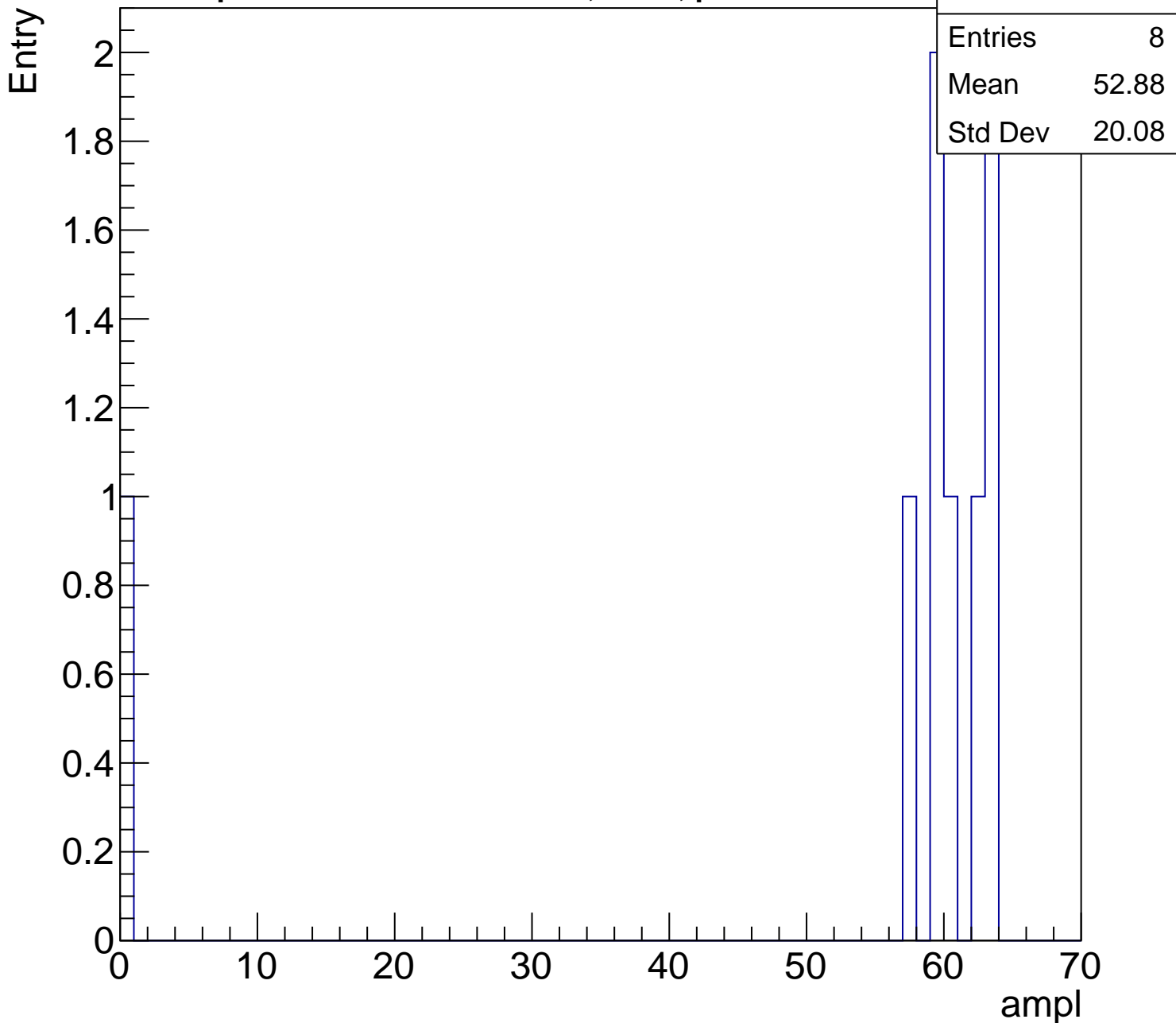
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	52.88
Std Dev	20.08

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch95, adc0

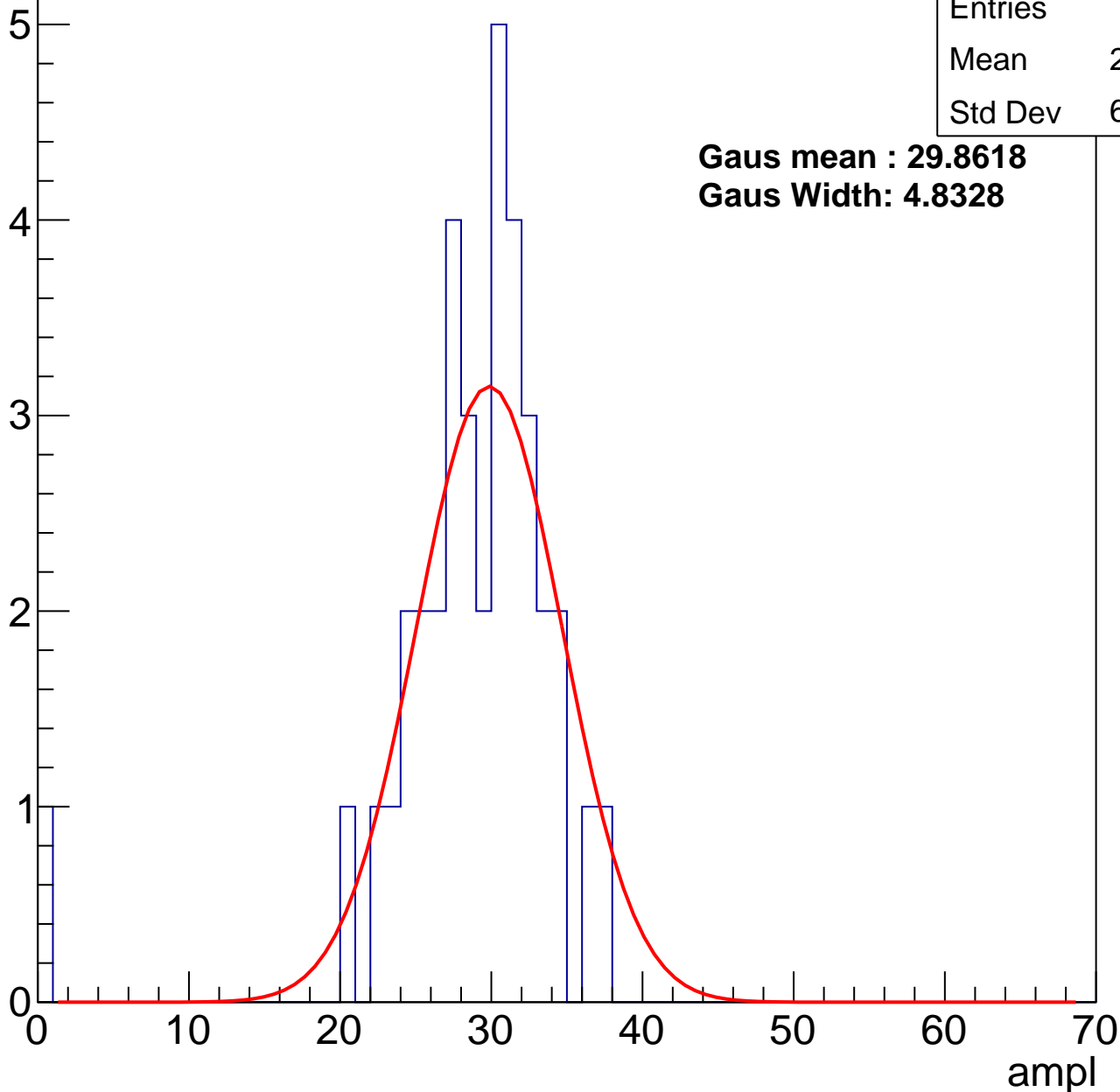
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	28.16
Std Dev	6.025

Gaus mean : 29.8618

Gaus Width: 4.8328



B1L103S, U15-ch95, adc1

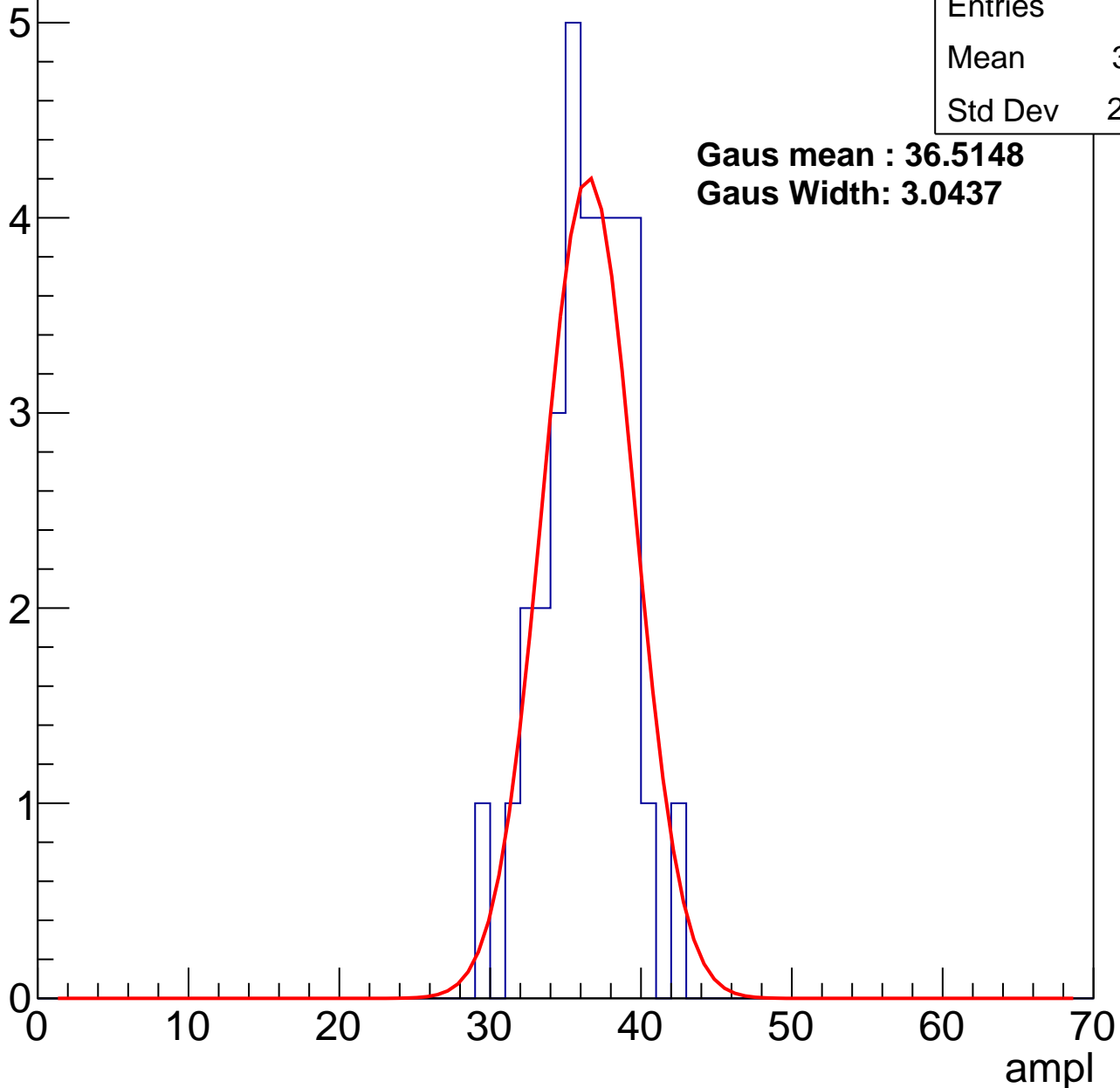
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	35.91
Std Dev	2.788

Gaus mean : 36.5148

Gaus Width: 3.0437



B1L103S, U15-ch95, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

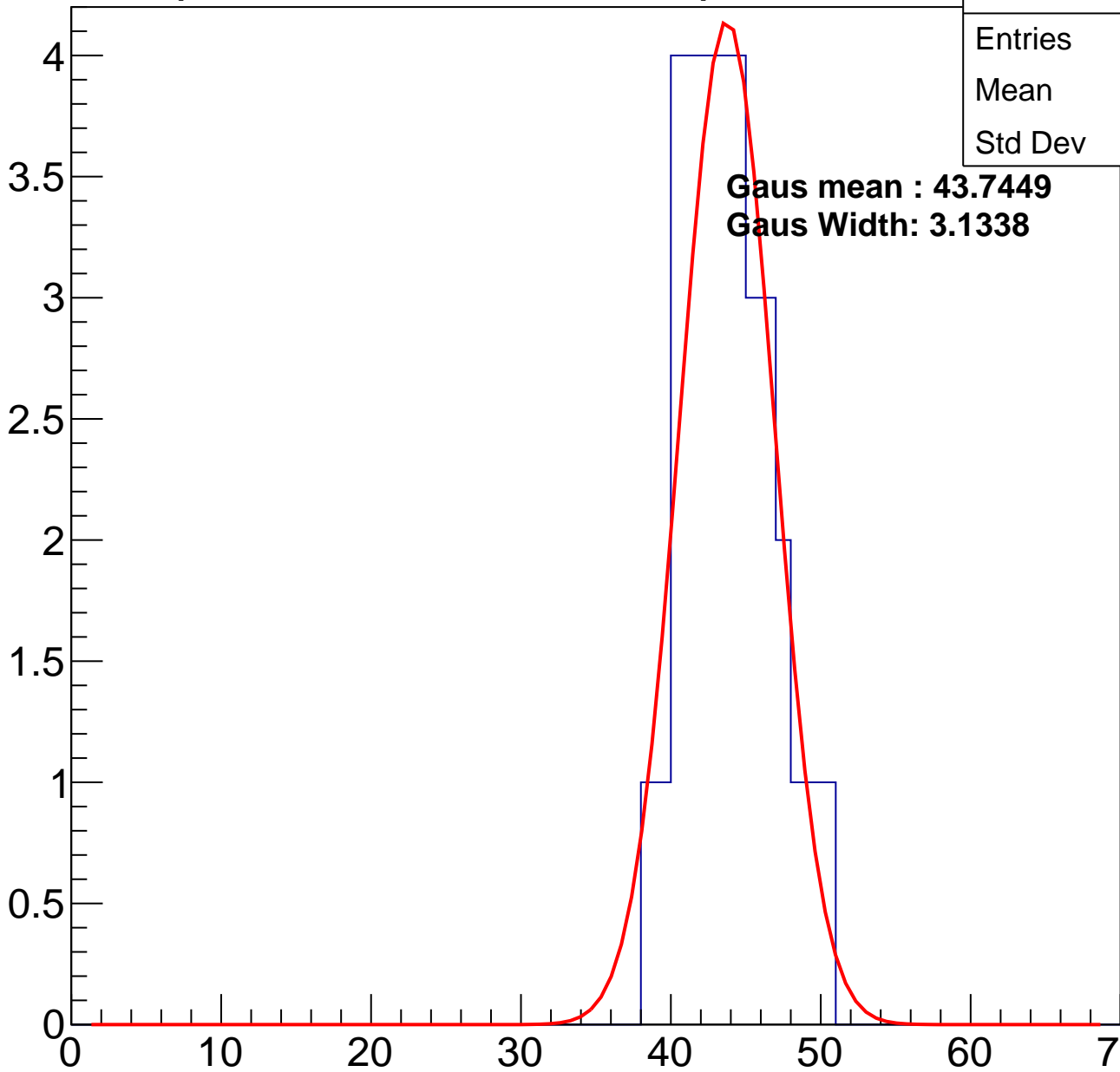
Entries	33
Mean	43.36
Std Dev	2.901

Gaus mean : 43.7449

Gaus Width: 3.1338

0 10 20 30 40 50 60 70

ampl

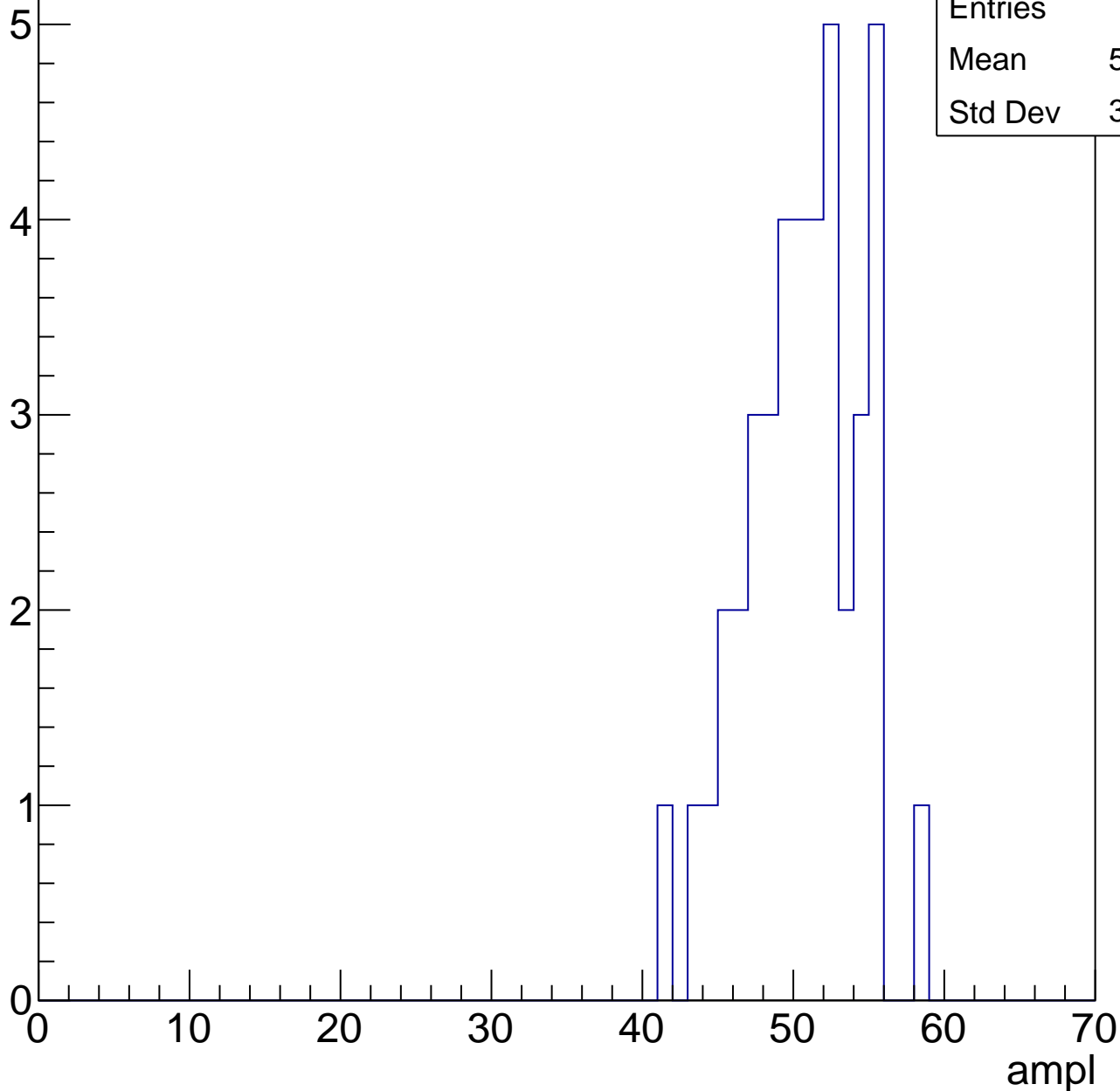


B1L103S, U15-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	50.15
Std Dev	3.726

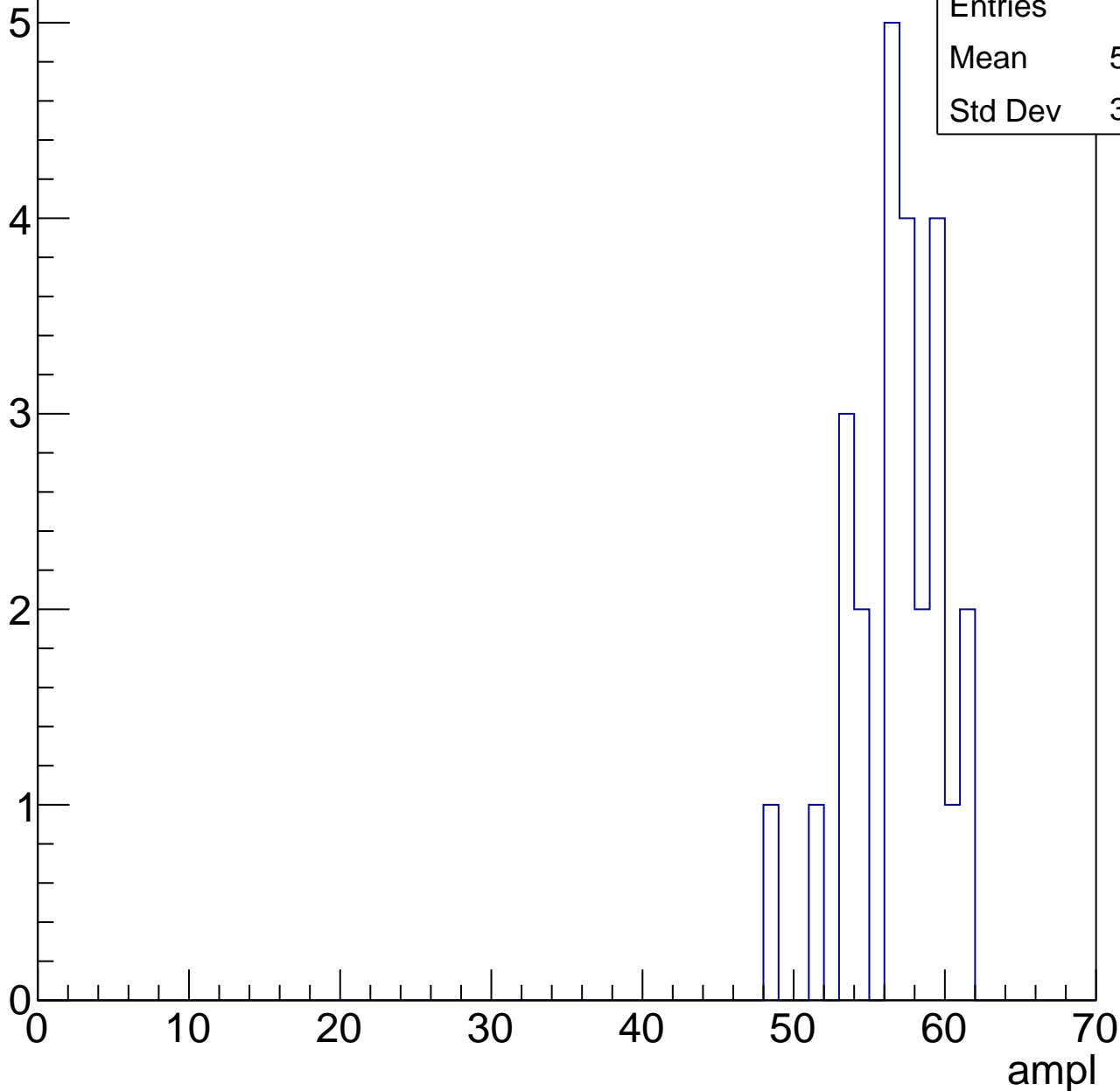


B1L103S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

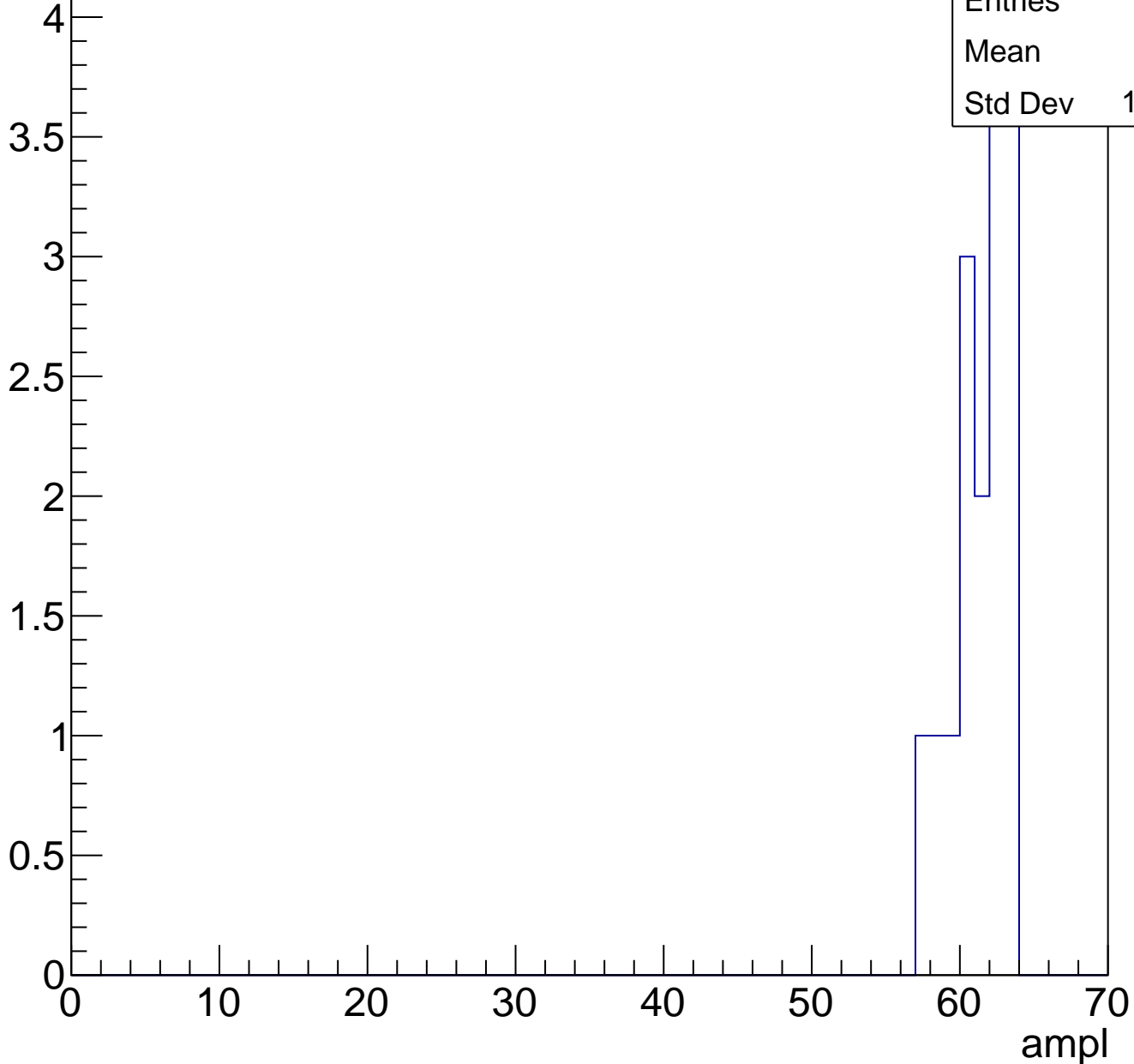
Entries	25
Mean	56.32
Std Dev	3.069



B1L103S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

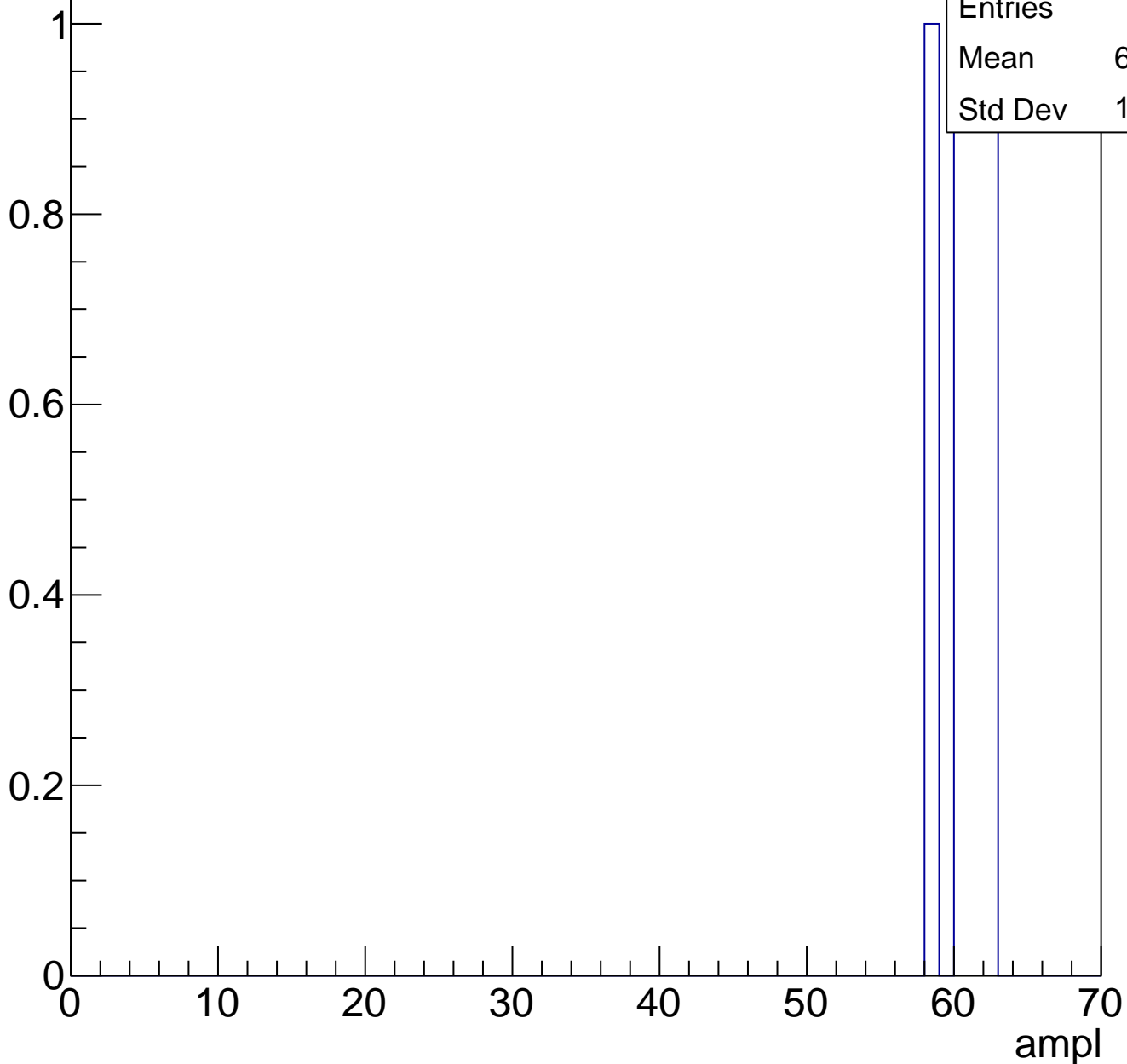
Entry



B1L103S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

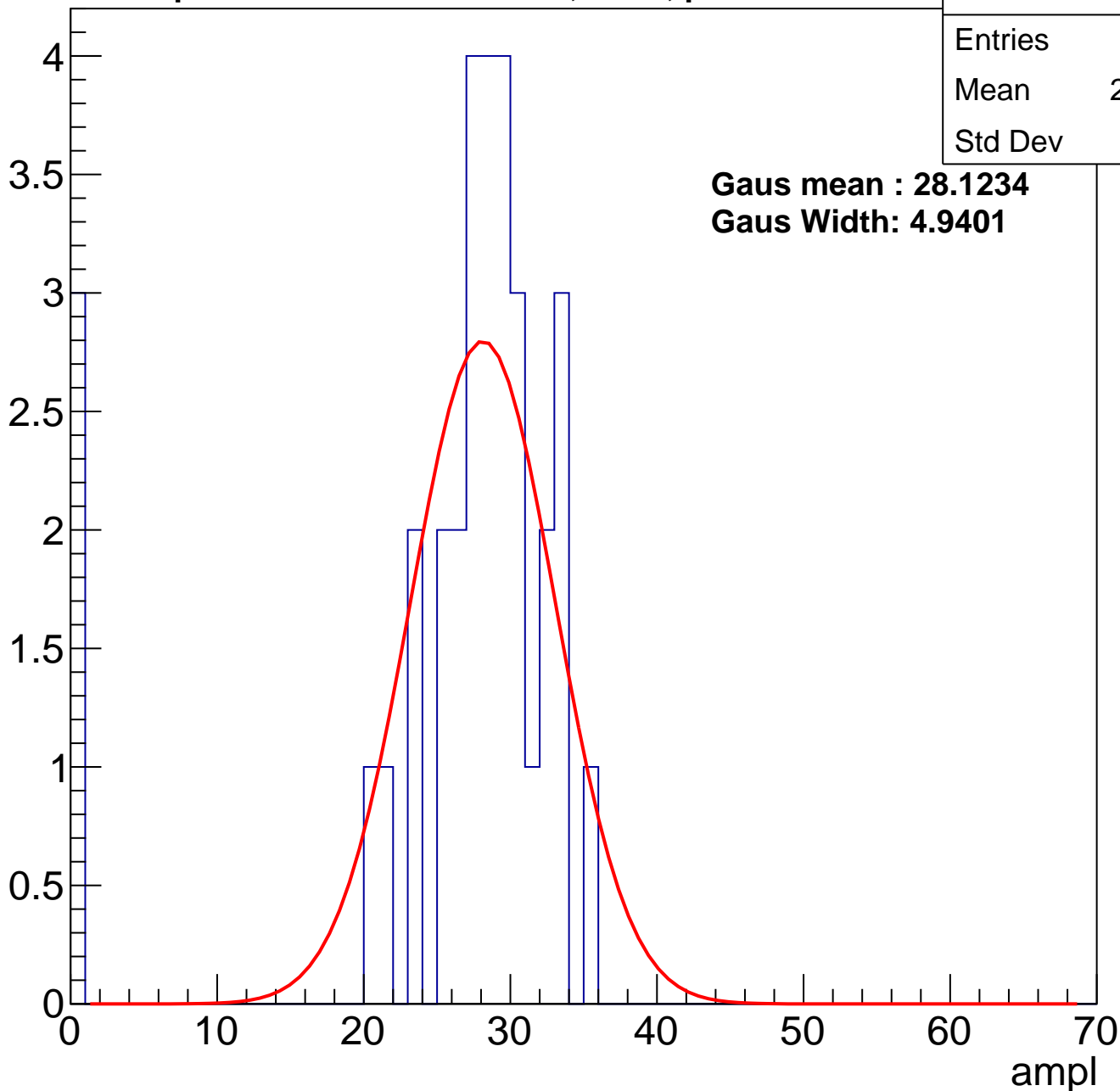
Entry



B1L103S, U15-ch96, adc0

calib_packv5_042523_0143.root, FC#7, port C2

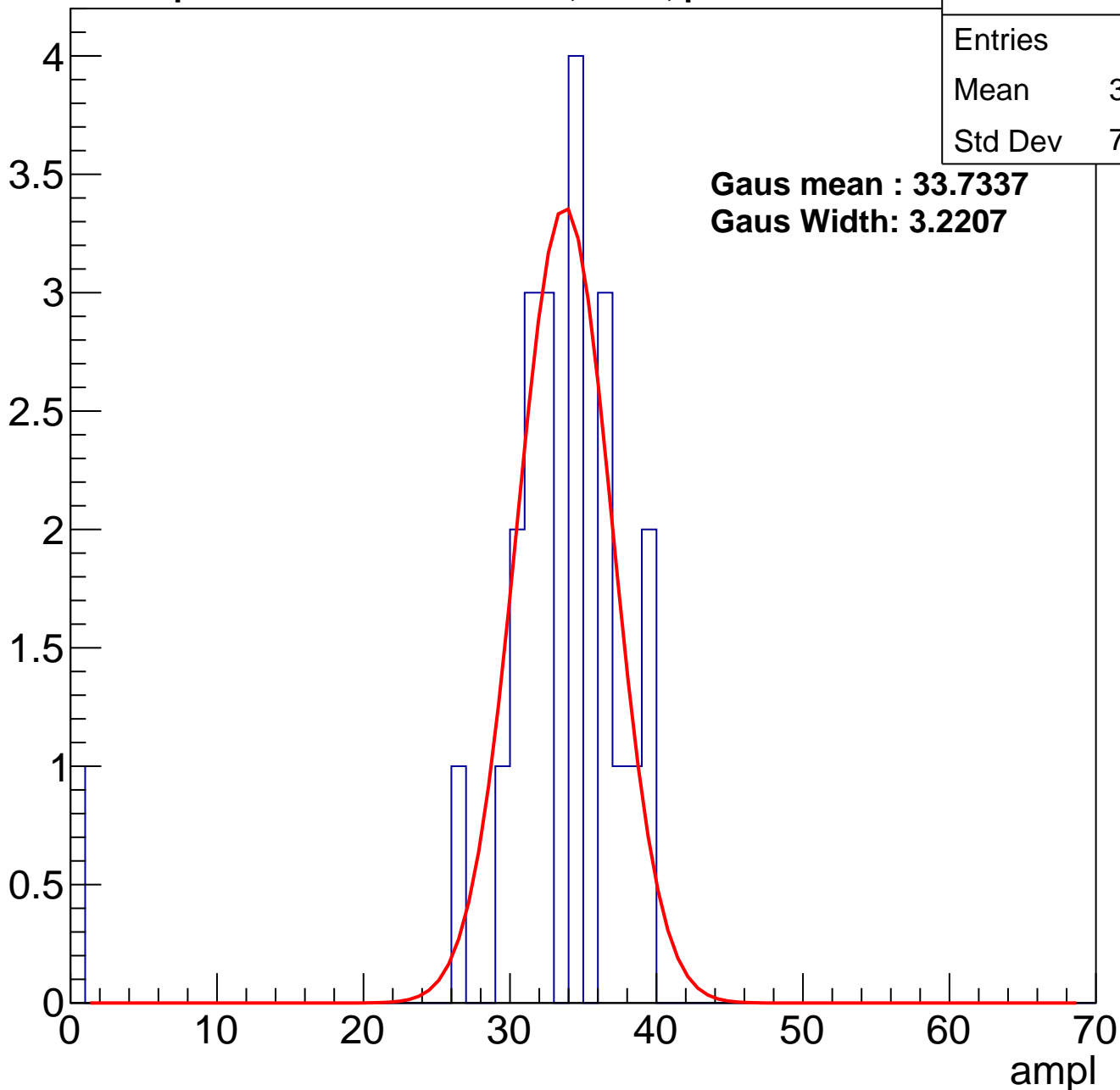
Entry



B1L103S, U15-ch96, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch96, adc2

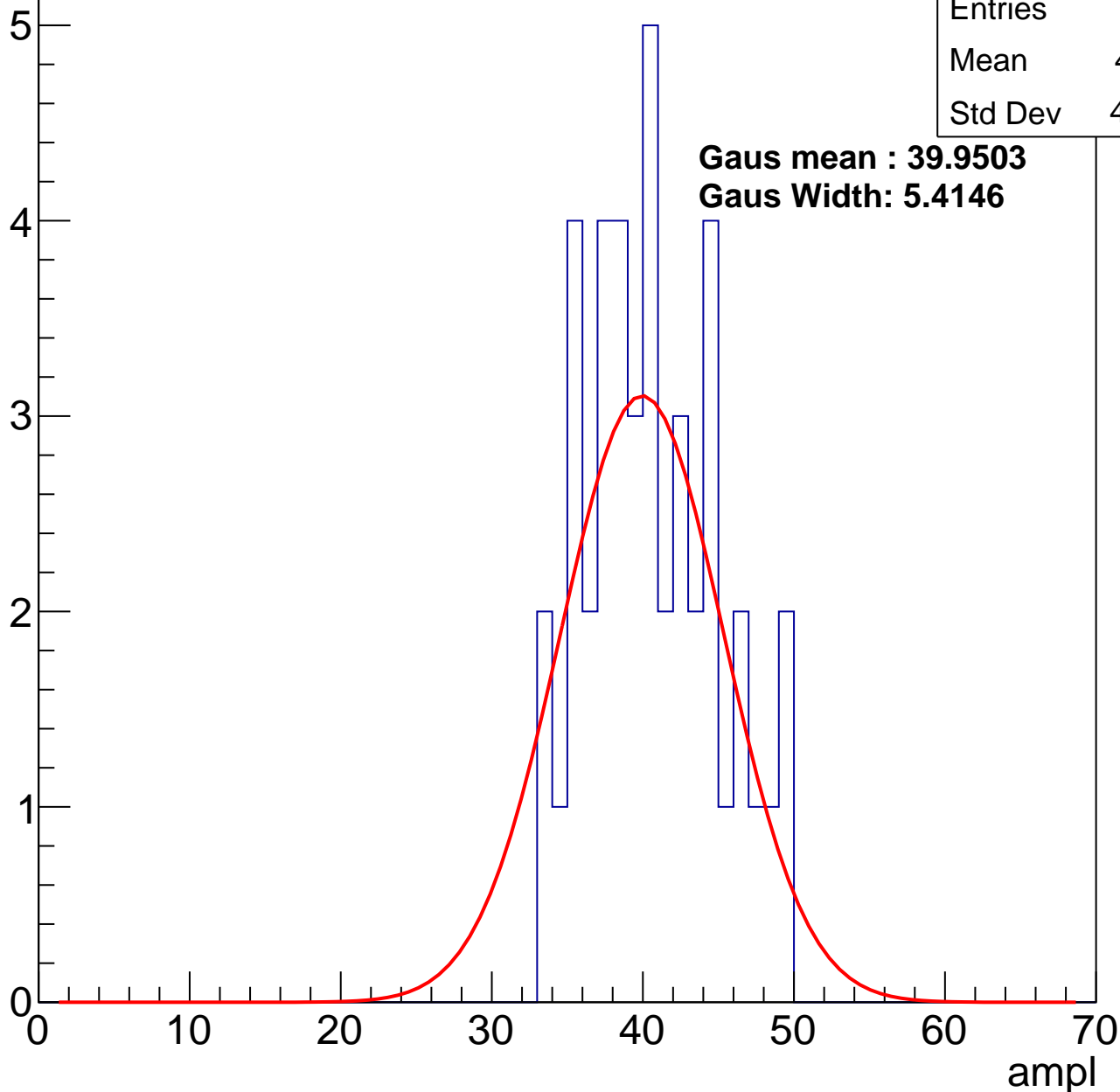
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	40.21
Std Dev	4.278

Gaus mean : 39.9503

Gaus Width: 5.4146



B1L103S, U15-ch96, adc3

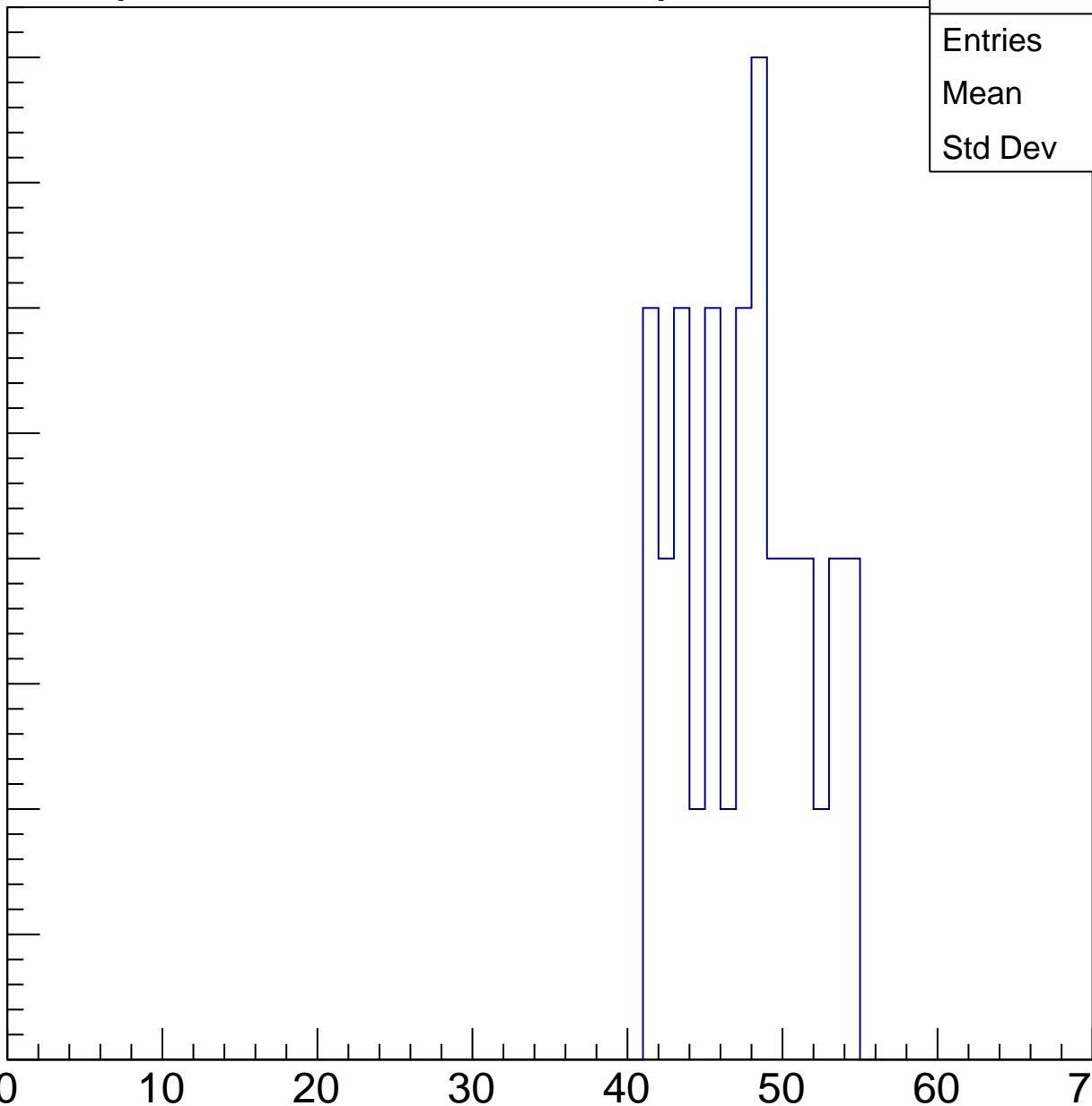
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	31
Mean	47.1
Std Dev	3.954

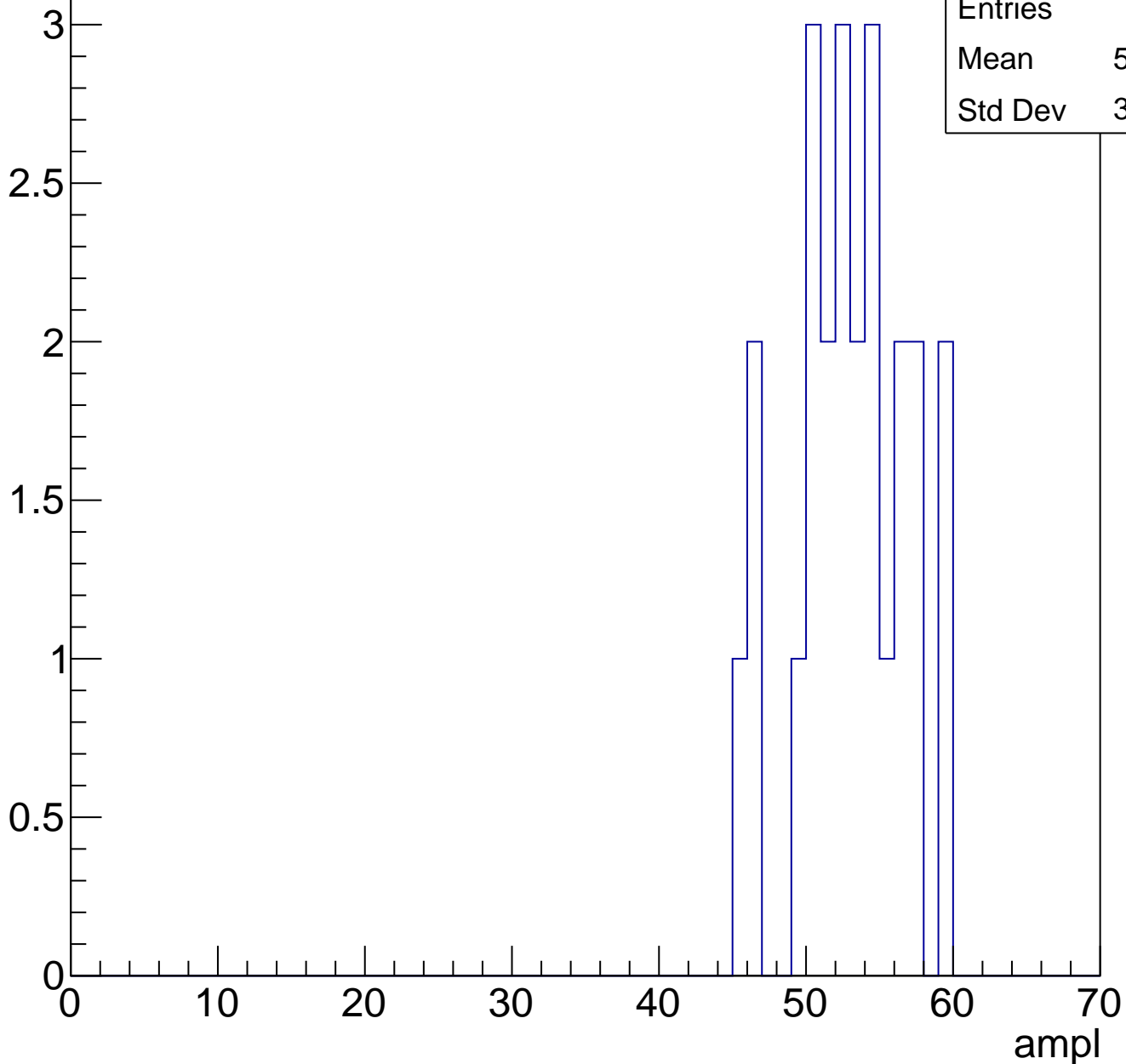
ampl



B1L103S, U15-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

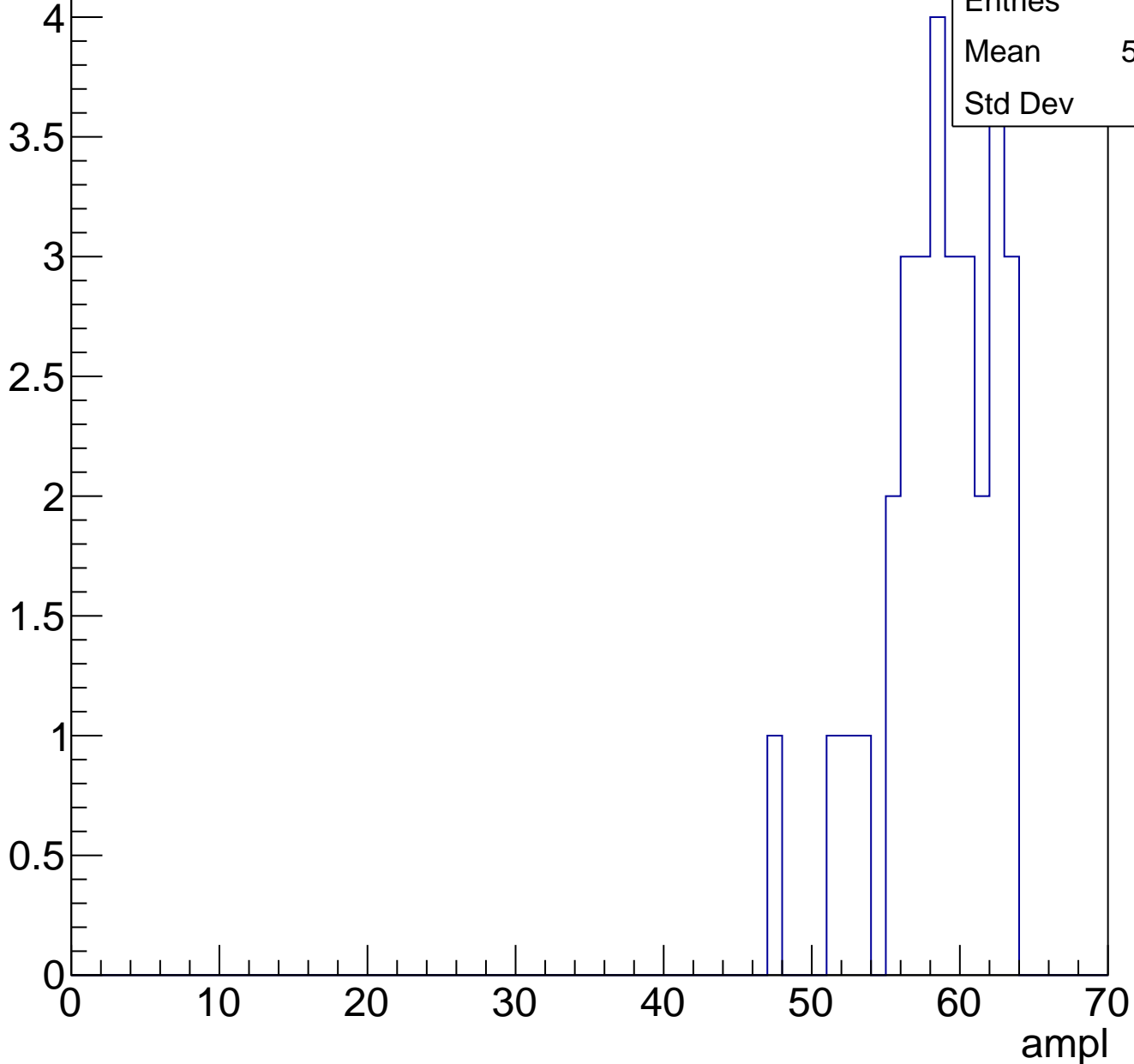
Entry



B1L103S, U15-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

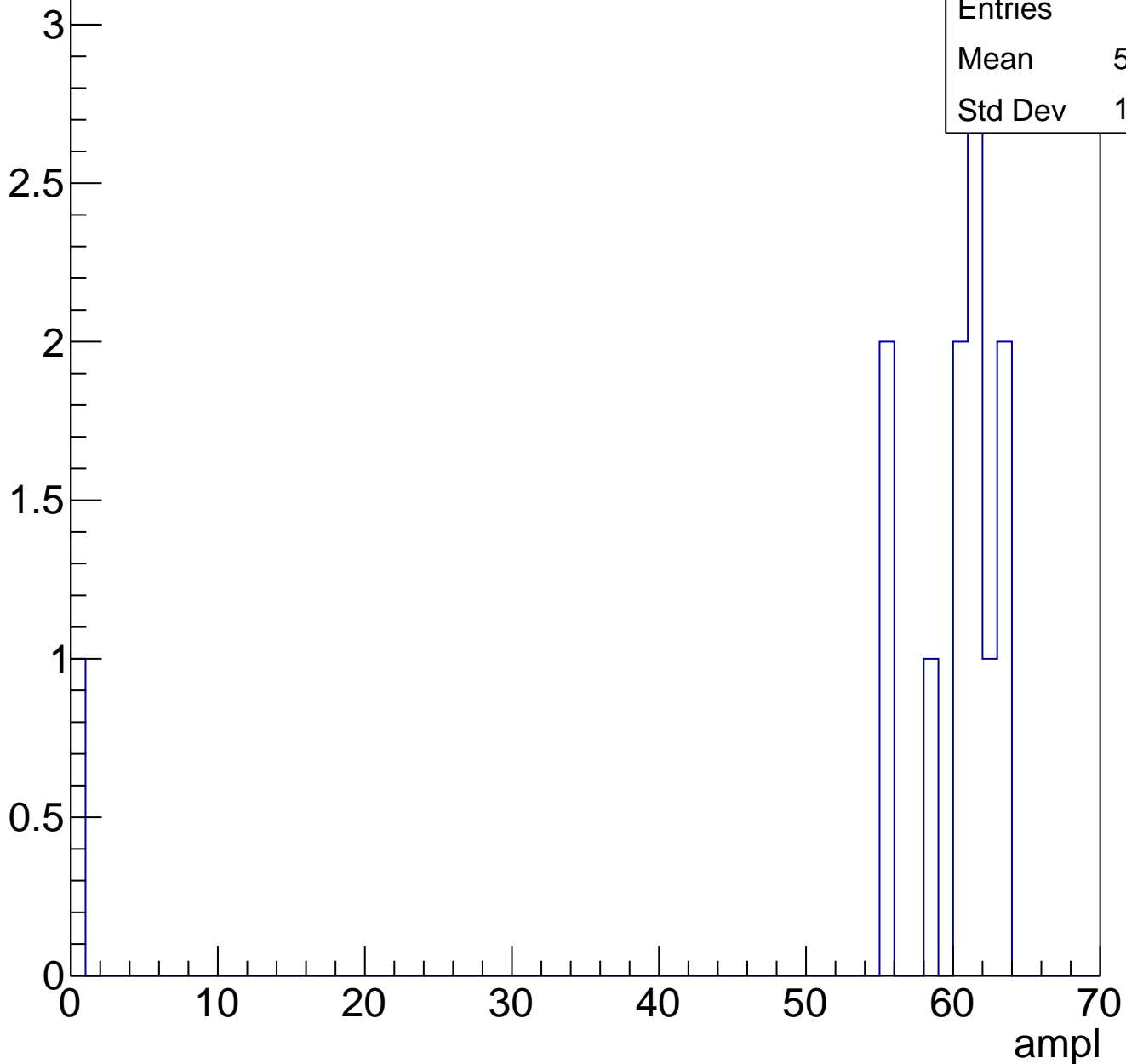
Entry



B1L103S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch97, adc0

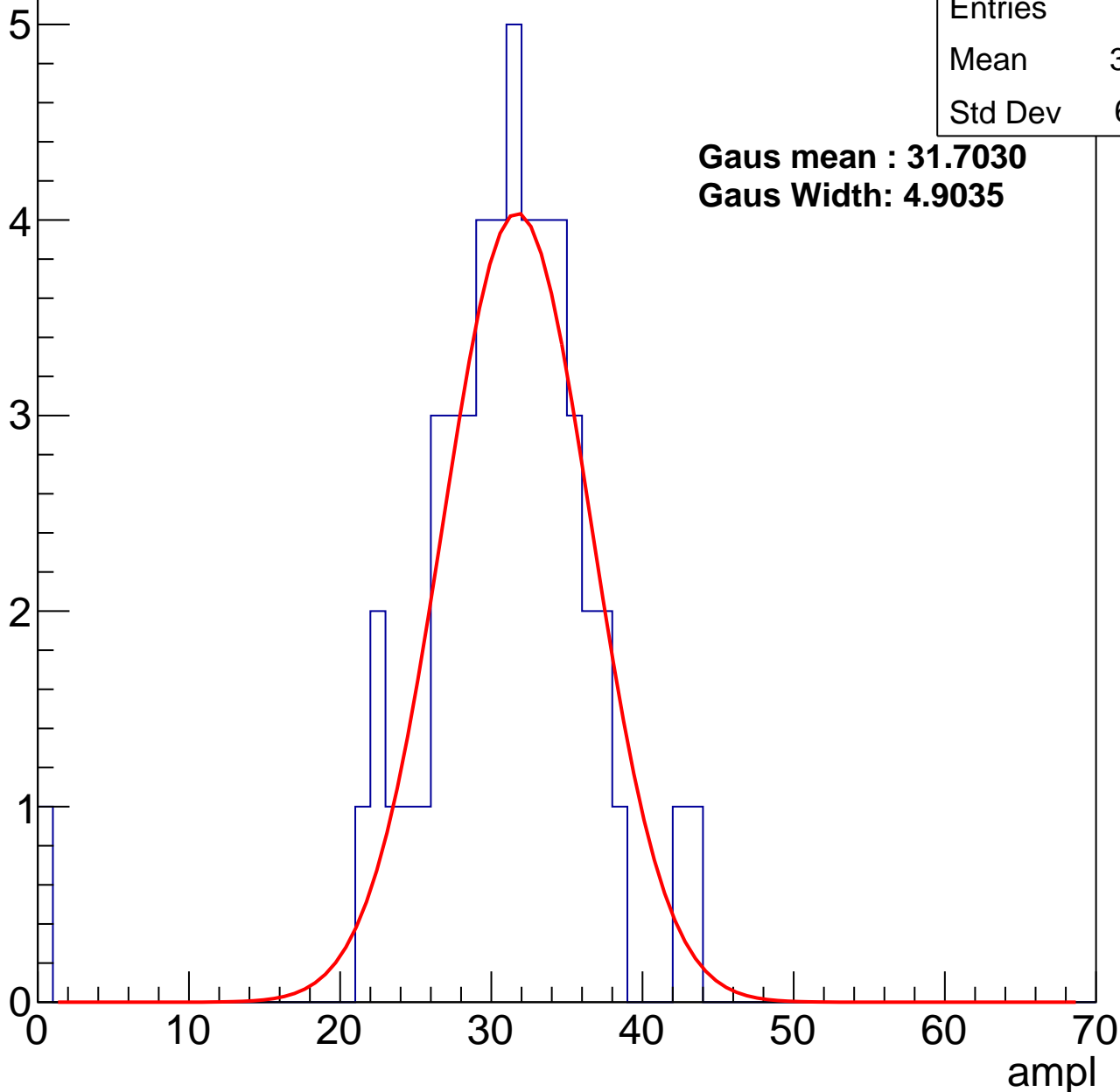
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	30.22
Std Dev	6.341

Gaus mean : 31.7030

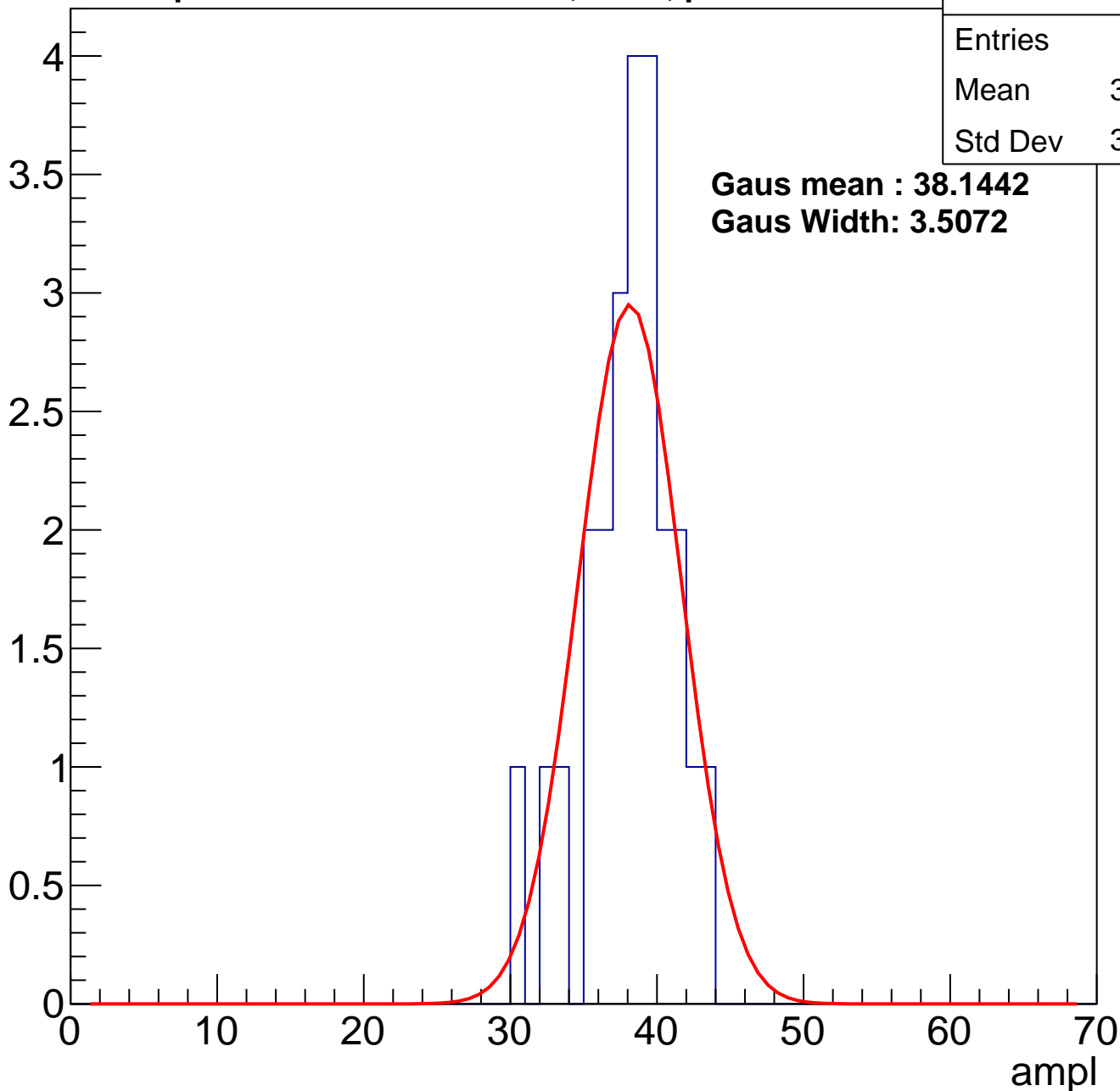
Gaus Width: 4.9035



B1L103S, U15-ch97, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch97, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

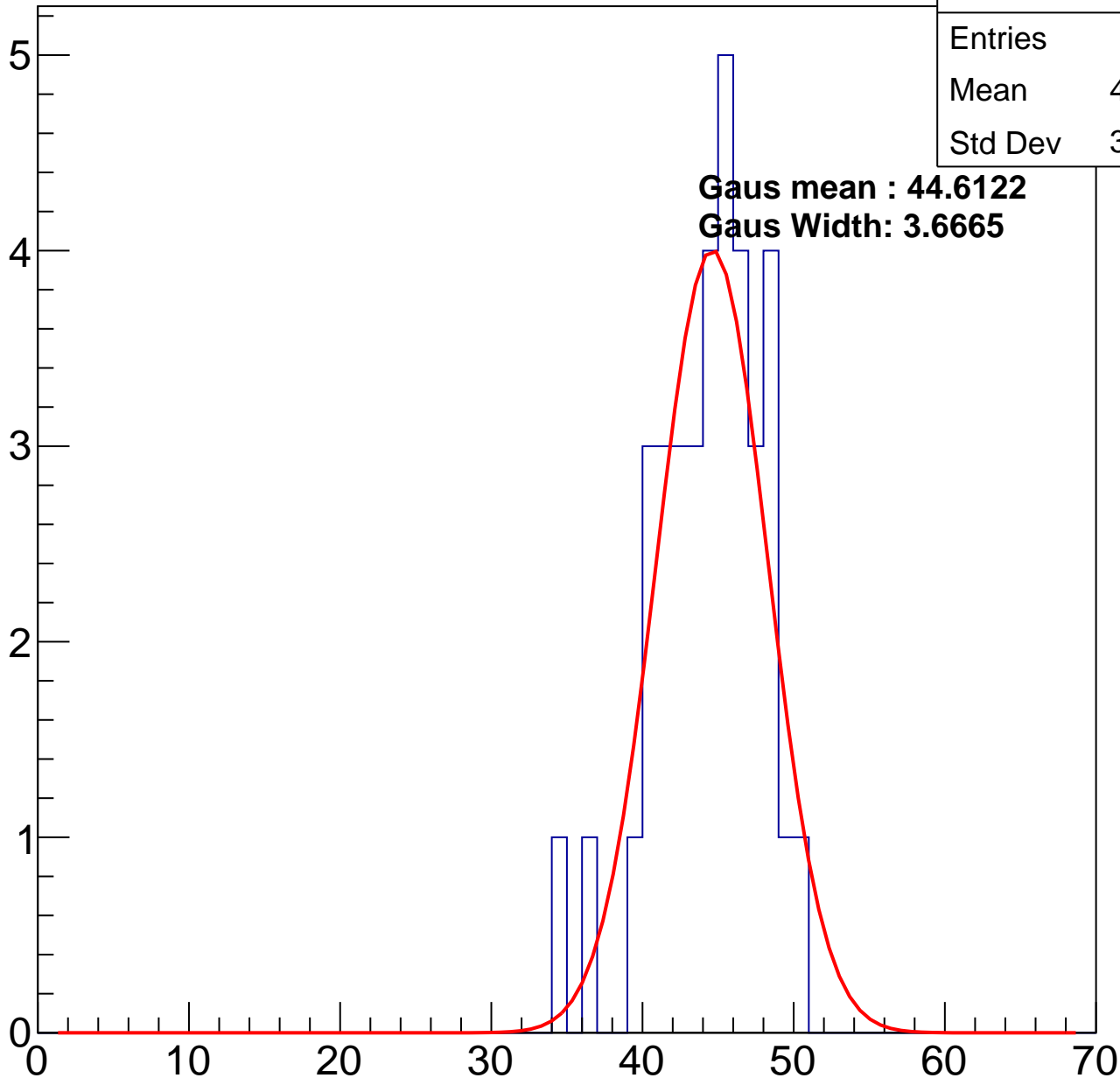
Entries	37
Mean	43.89
Std Dev	3.493

Gaus mean : 44.6122

Gaus Width: 3.6665

5
4
3
2
1
0

ampl

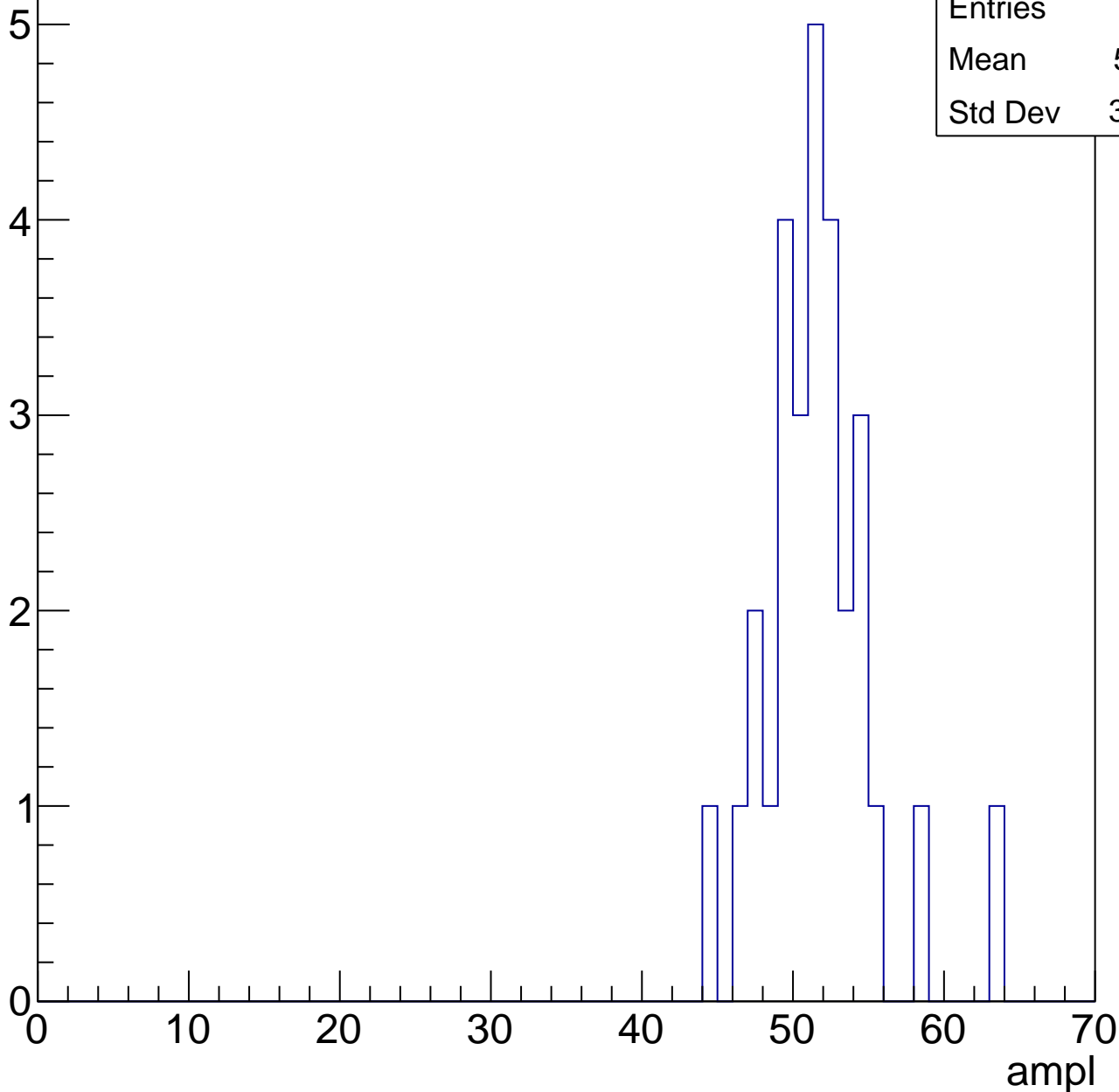


B1L103S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

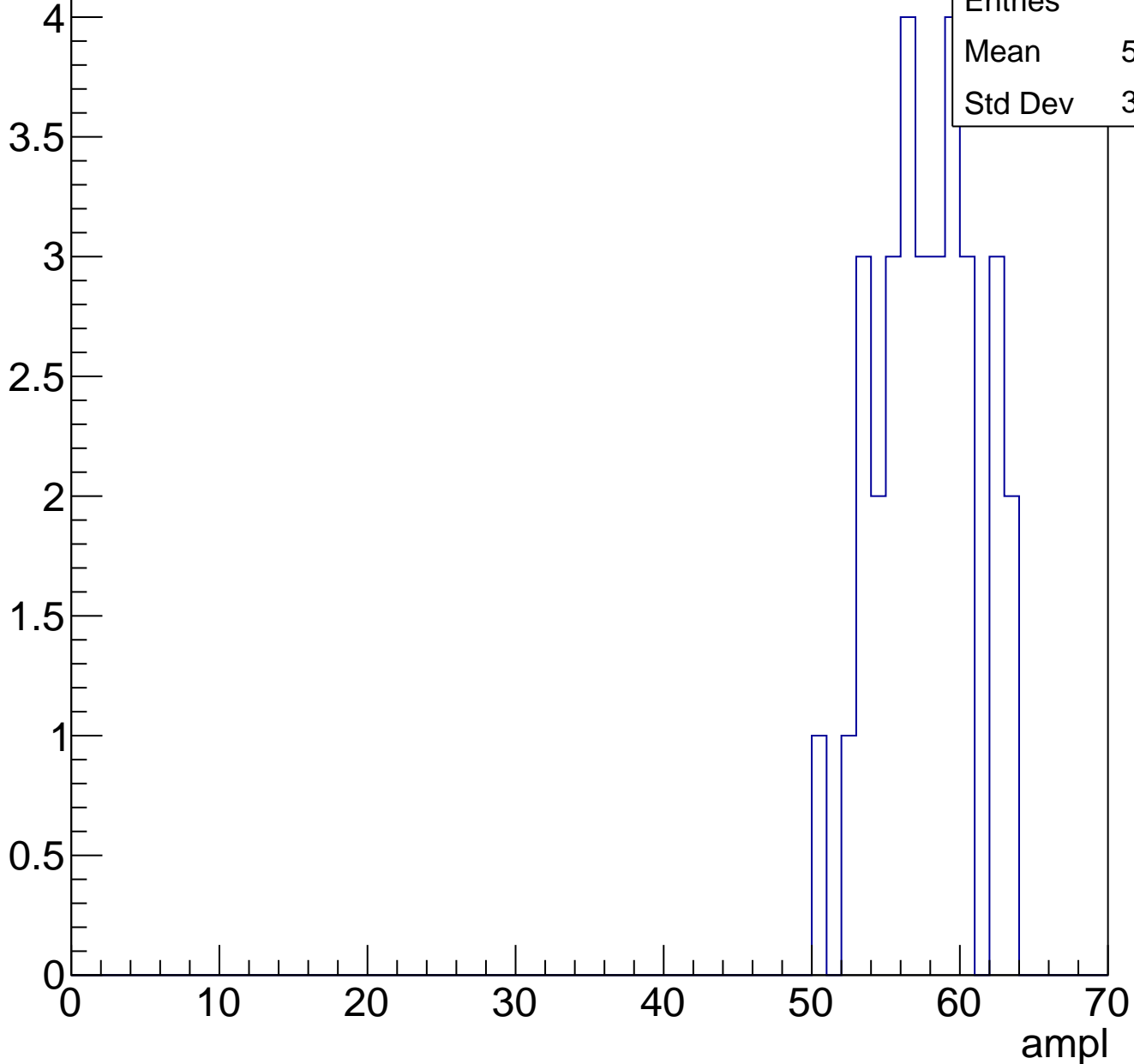
Entries	29
Mean	51.21
Std Dev	3.623



B1L103S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



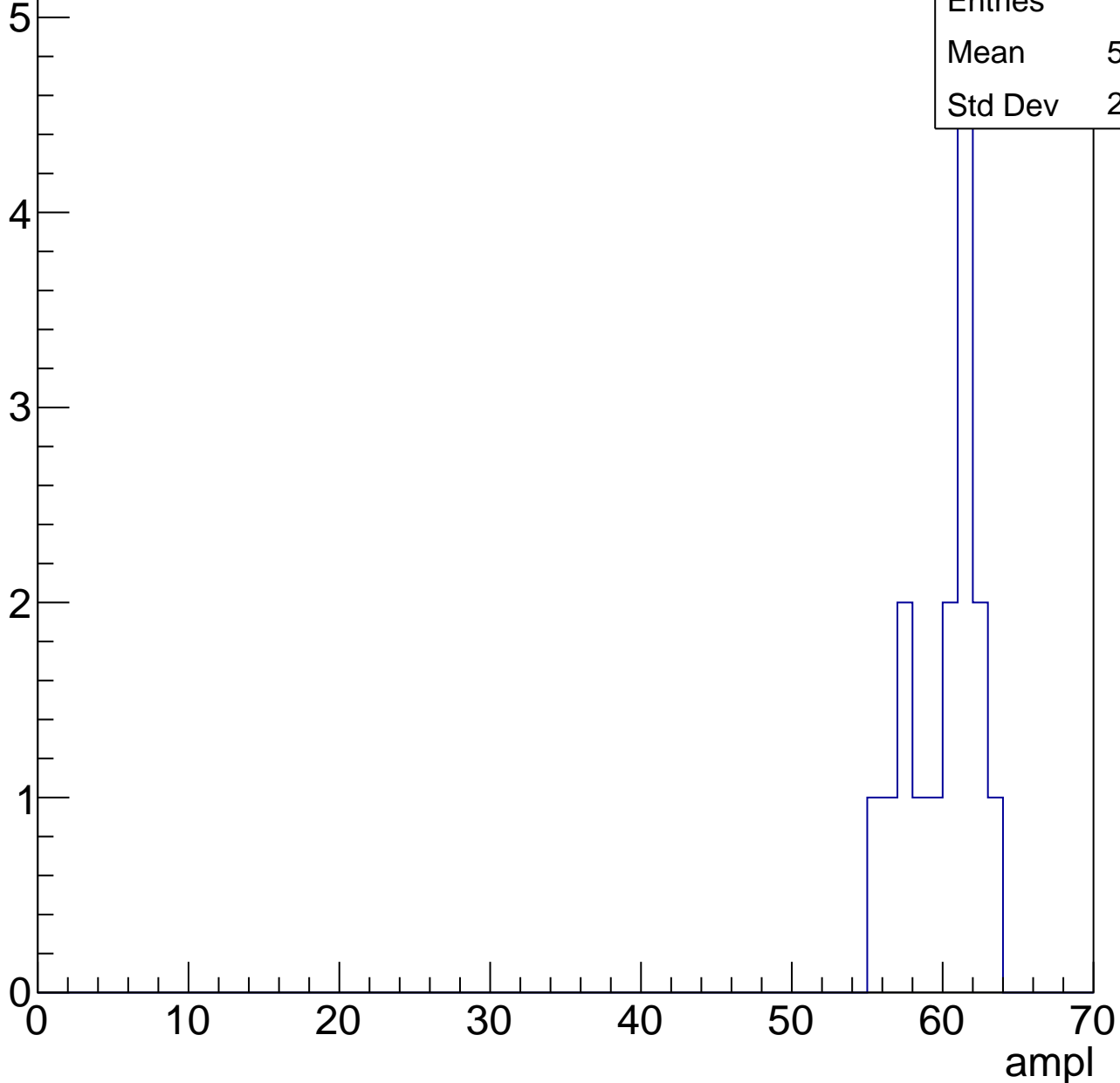
Entries	32
Mean	57.22
Std Dev	3.295

B1L103S, U15-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	59.62
Std Dev	2.288



B1L103S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch98, adc0

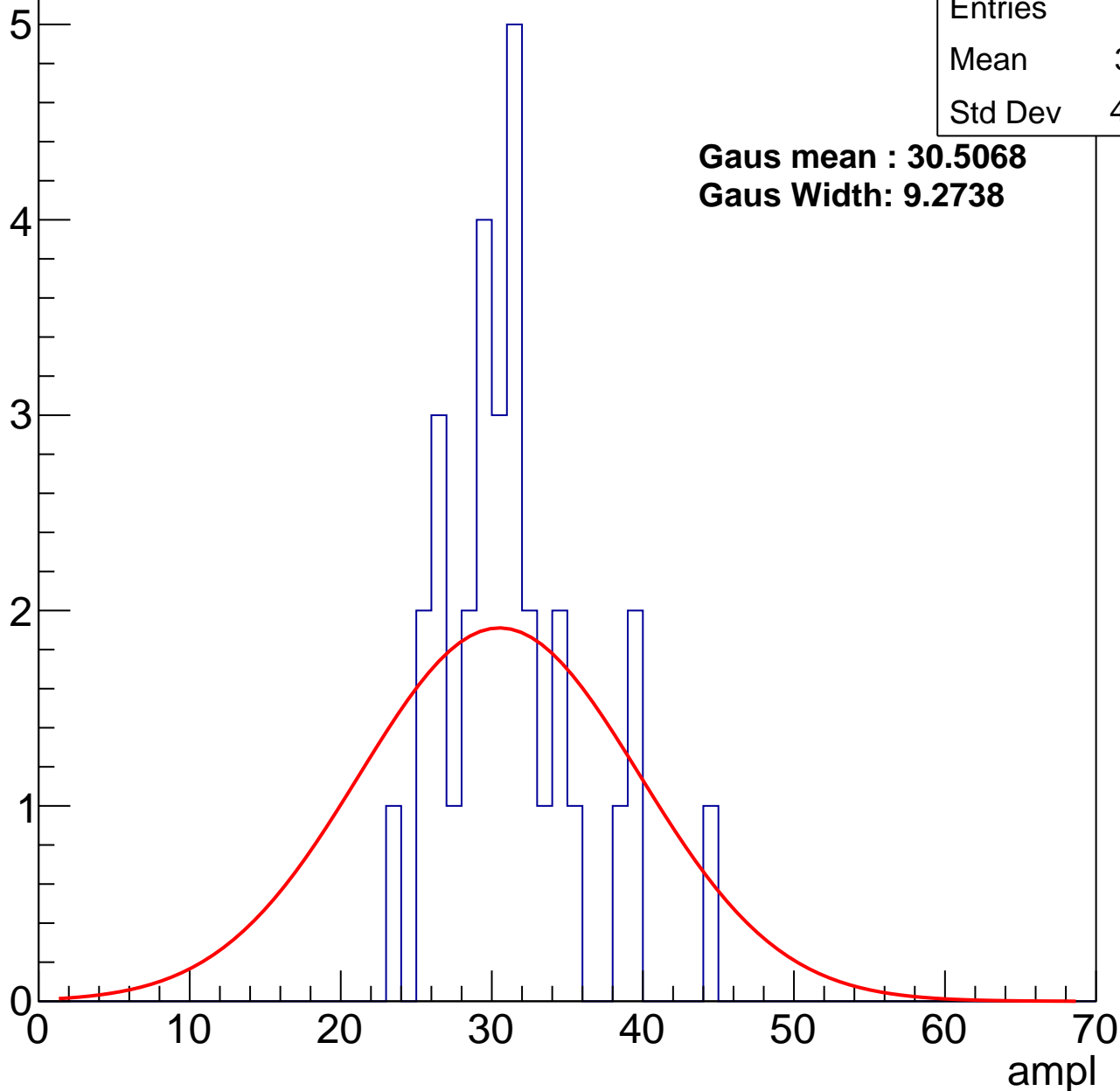
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	30.81
Std Dev	4.575

Gaus mean : 30.5068

Gaus Width: 9.2738



B1L103S, U15-ch98, adc1

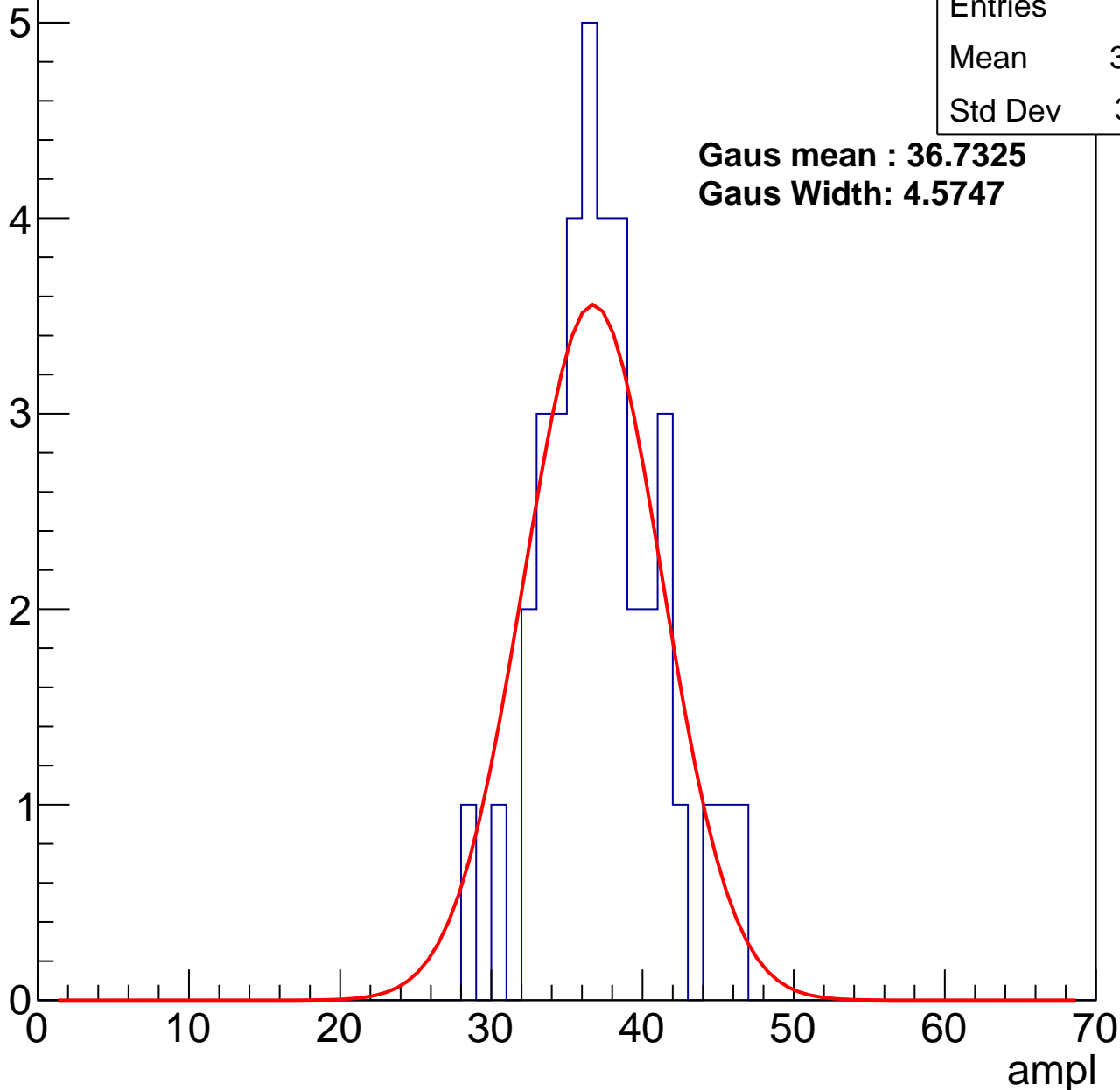
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	36.87
Std Dev	3.881

Gaus mean : 36.7325

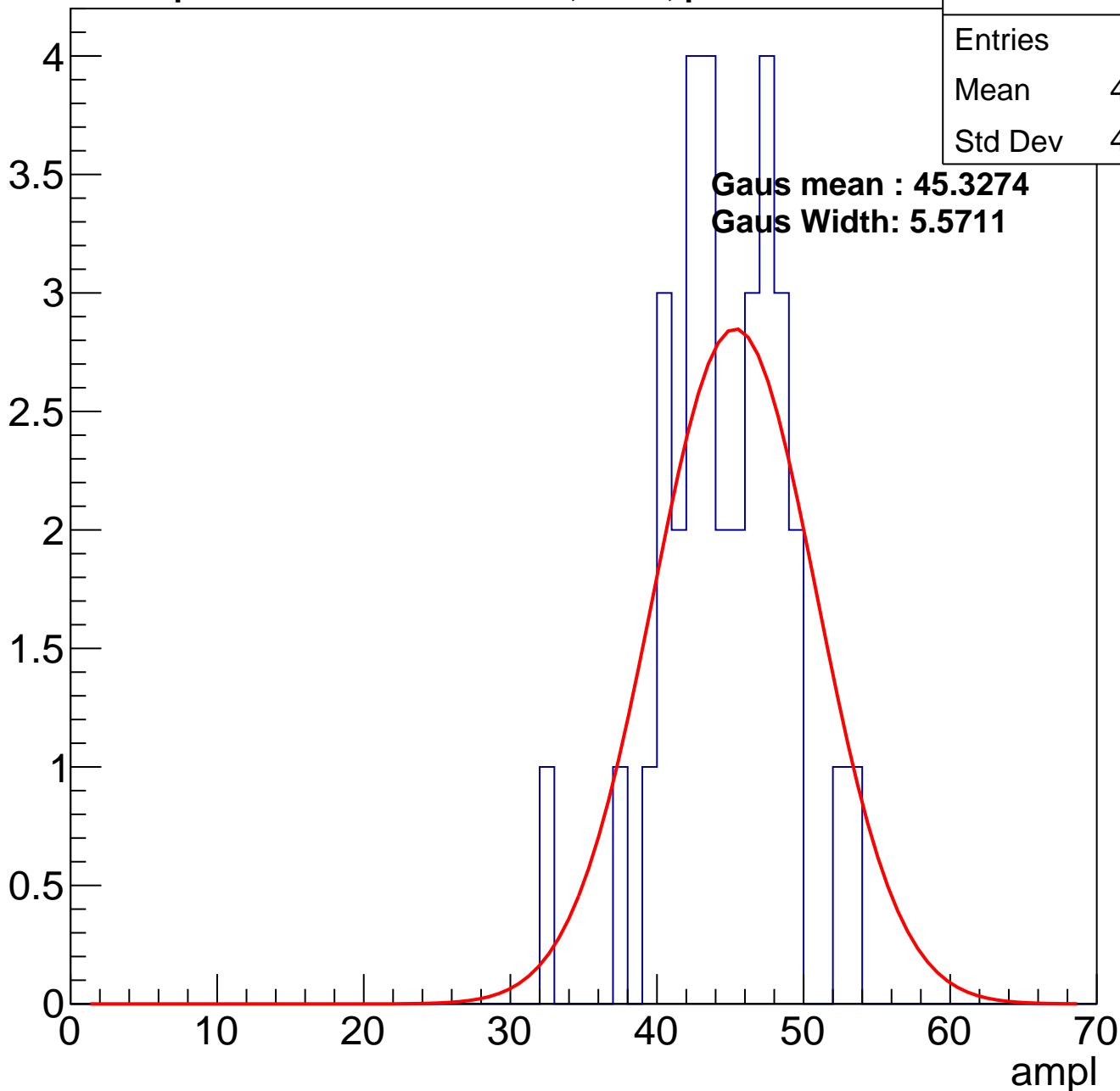
Gaus Width: 4.5747



B1L103S, U15-ch98, adc2

calib_packv5_042523_0143.root, FC#7, port C2

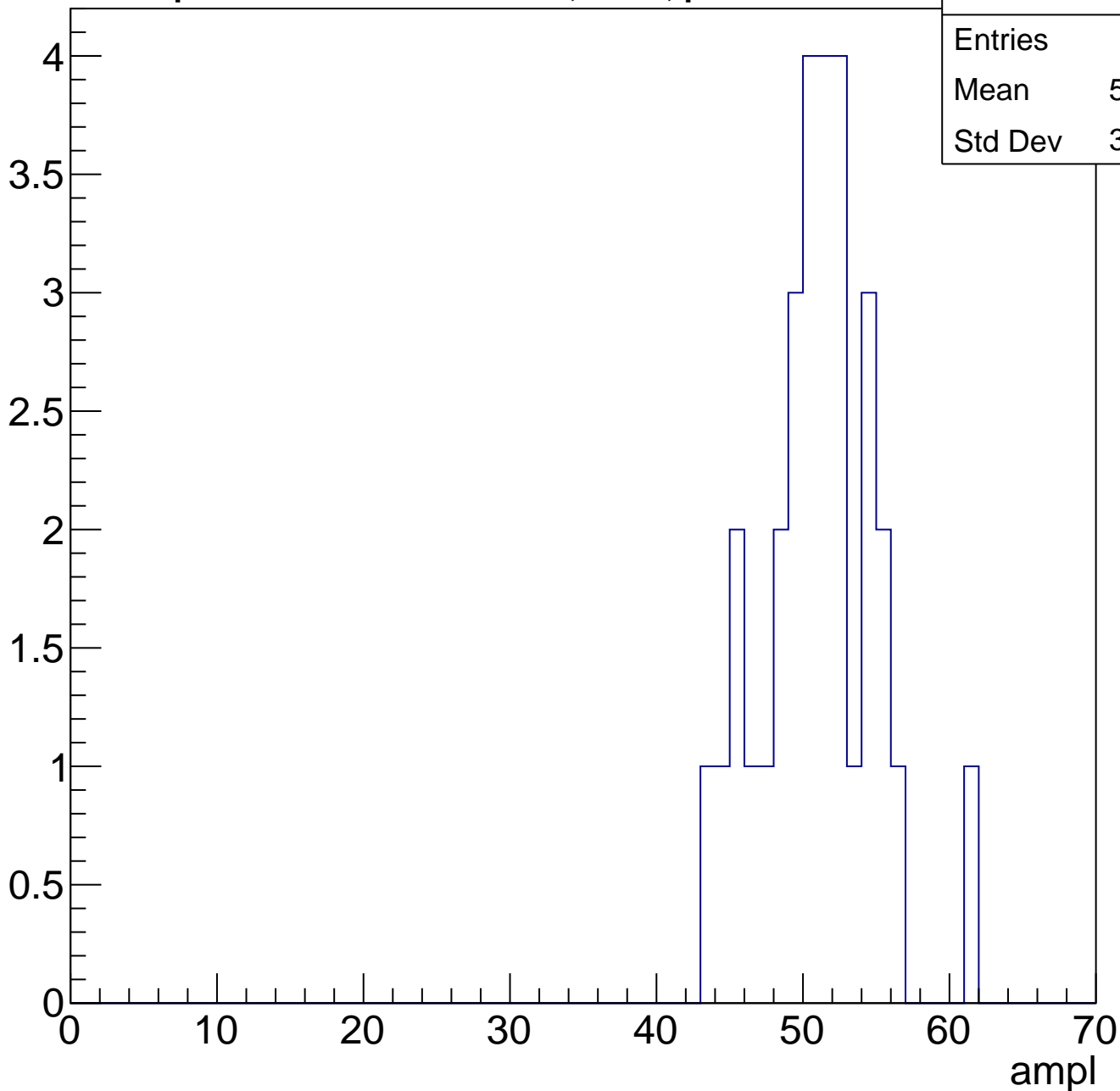
Entry



B1L103S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

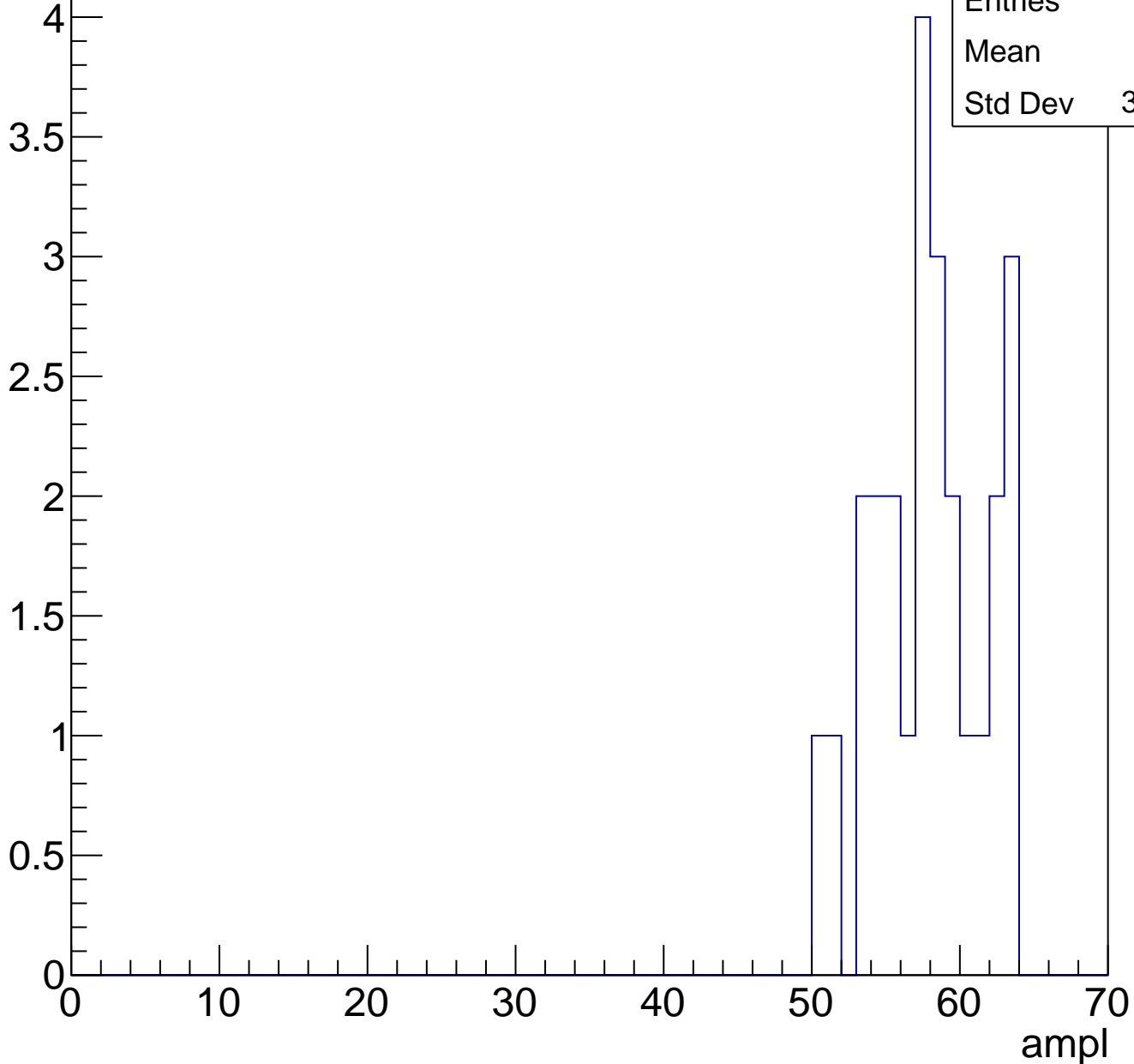
Entry



B1L103S, U15-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

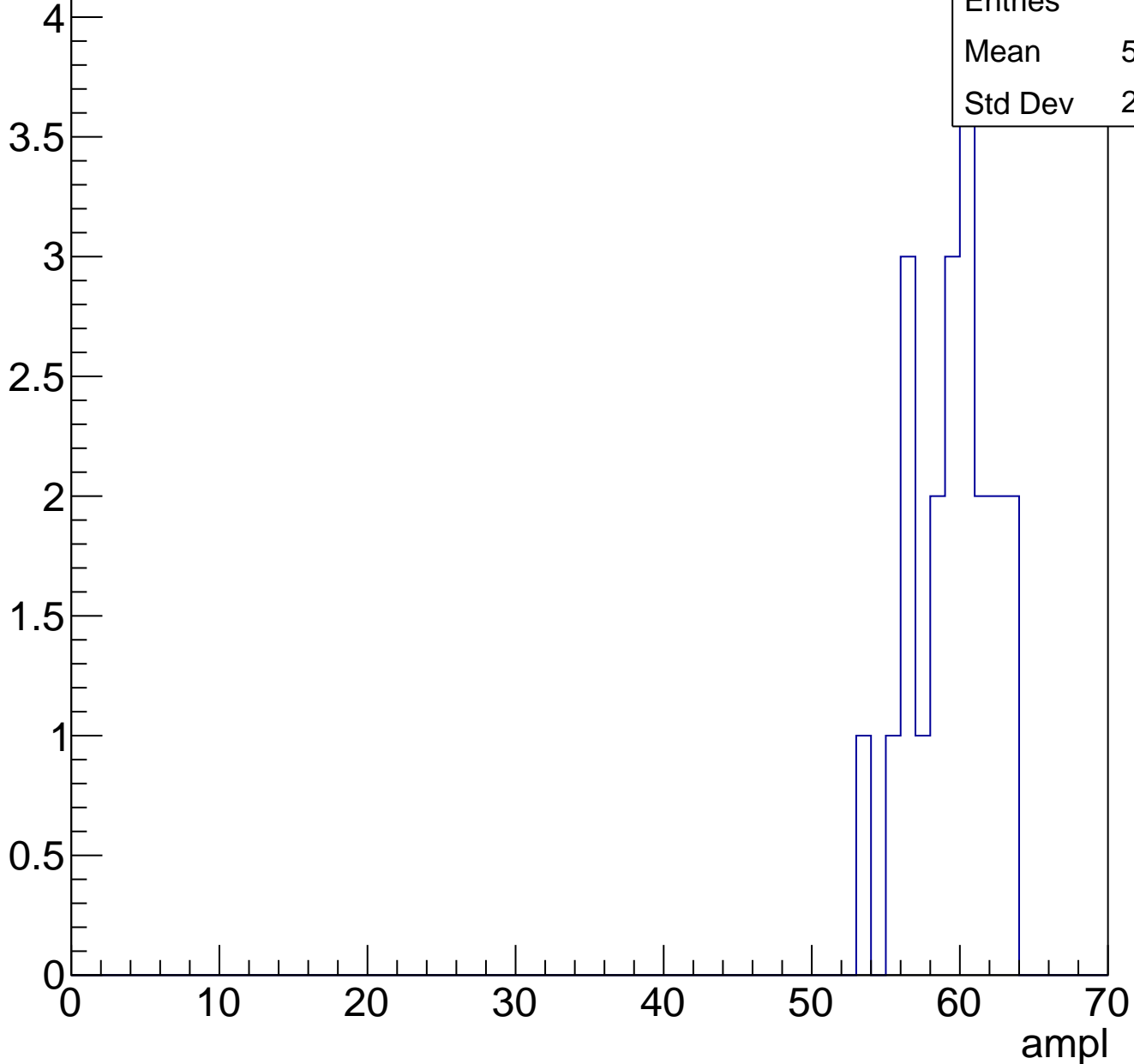
Entry



B1L103S, U15-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

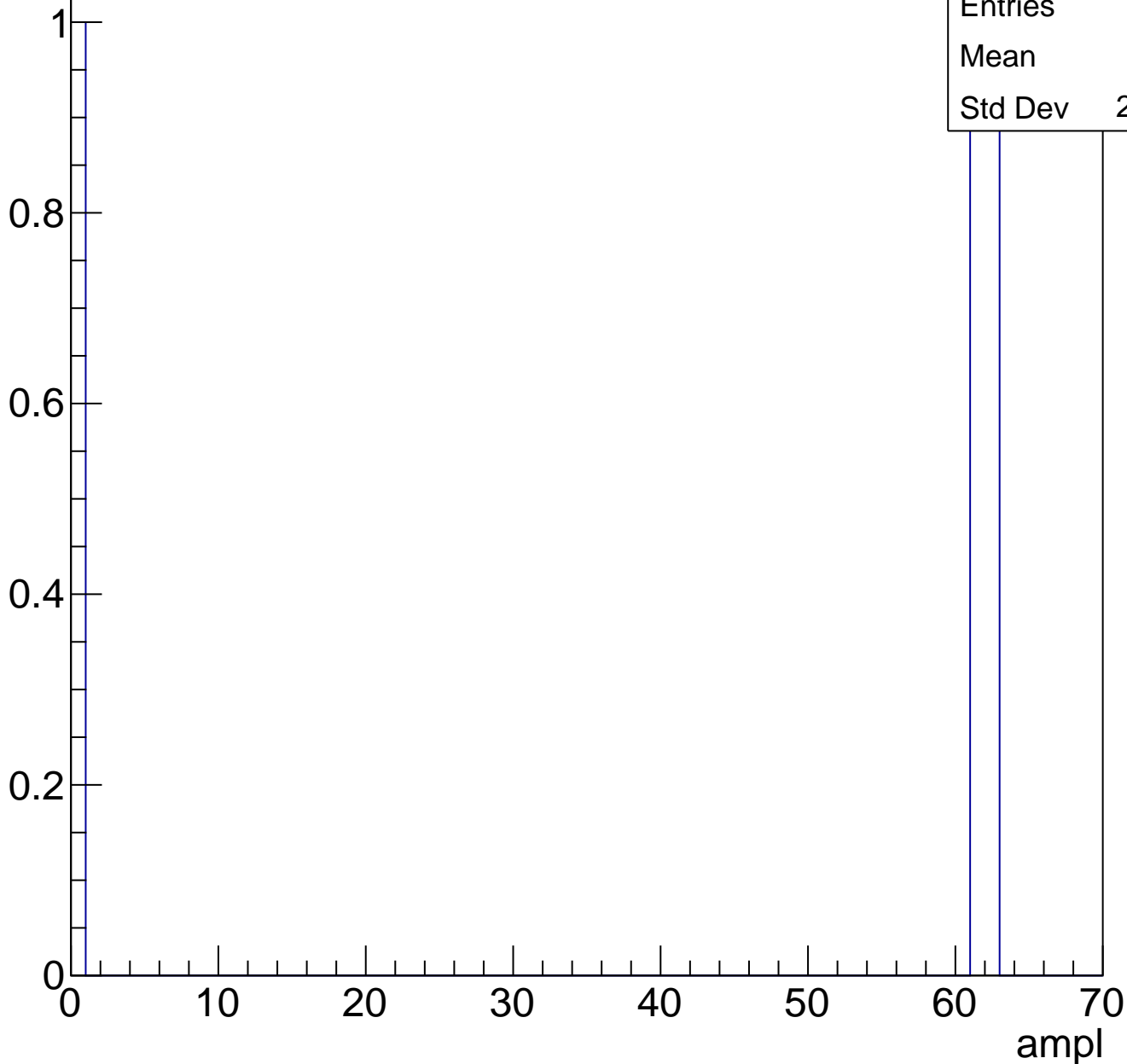
Entry



B1L103S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch99, adc0

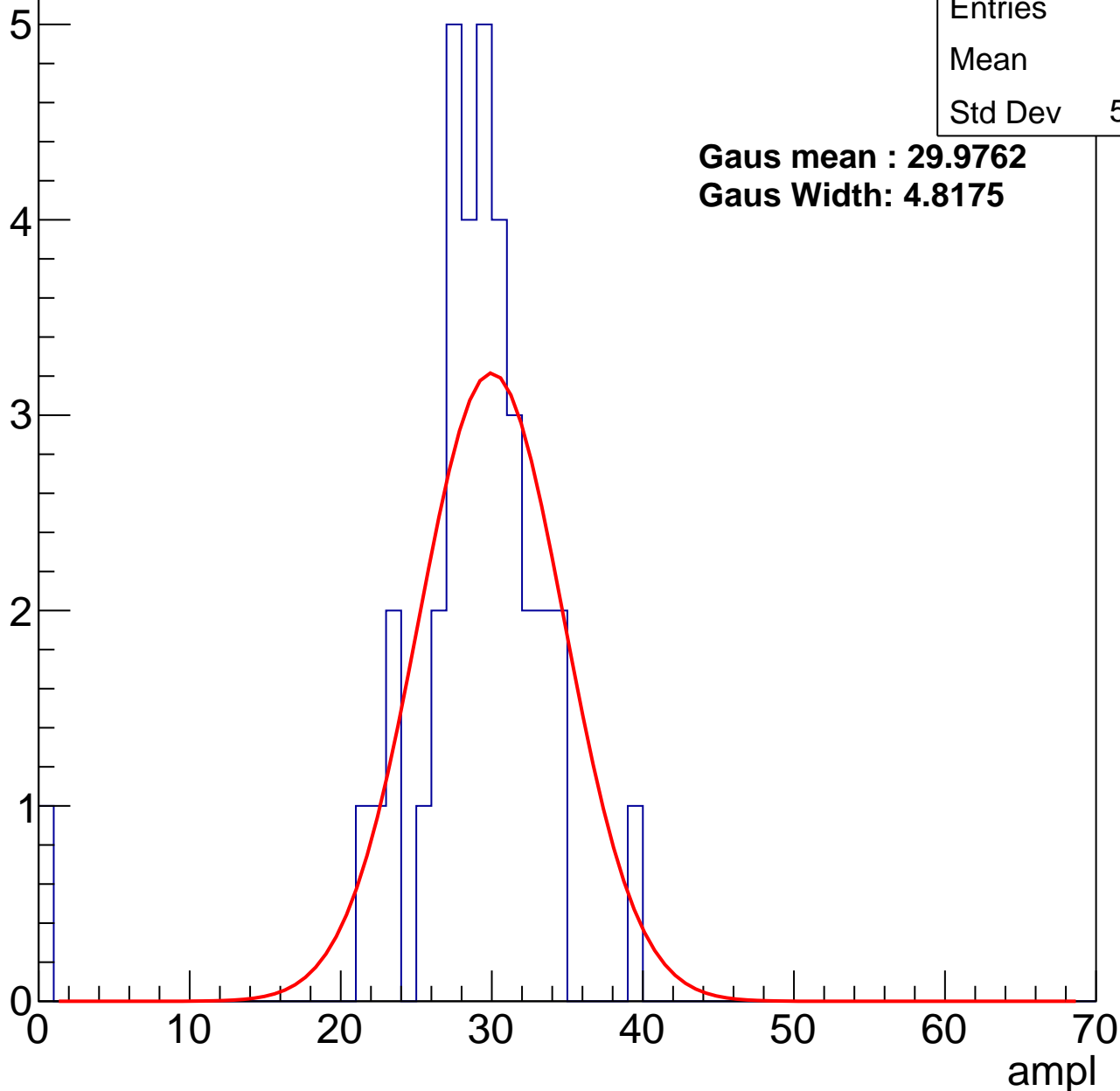
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	28
Std Dev	5.925

Gaus mean : 29.9762

Gaus Width: 4.8175



B1L103S, U15-ch99, adc1

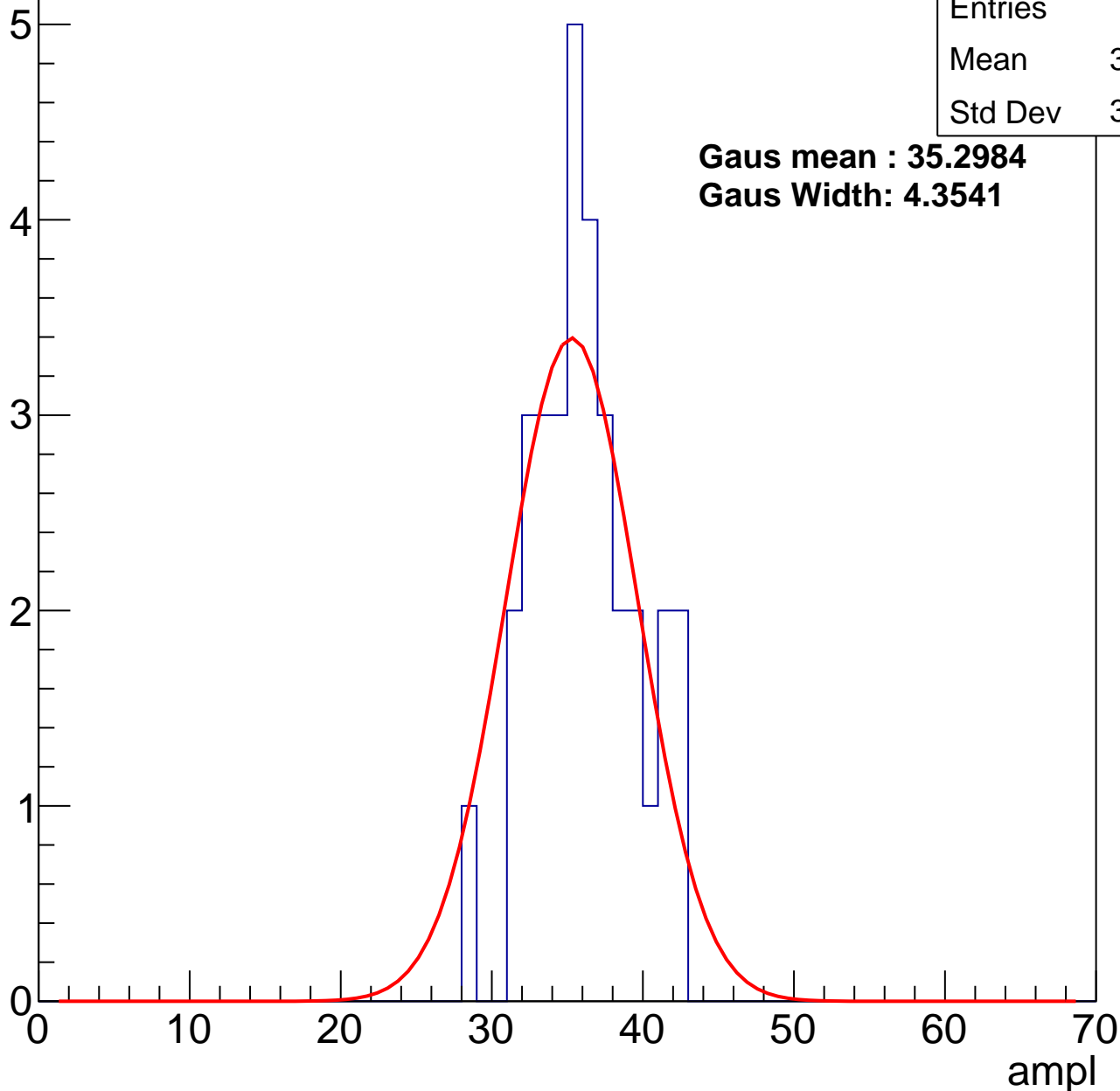
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	35.67
Std Dev	3.345

Gaus mean : 35.2984

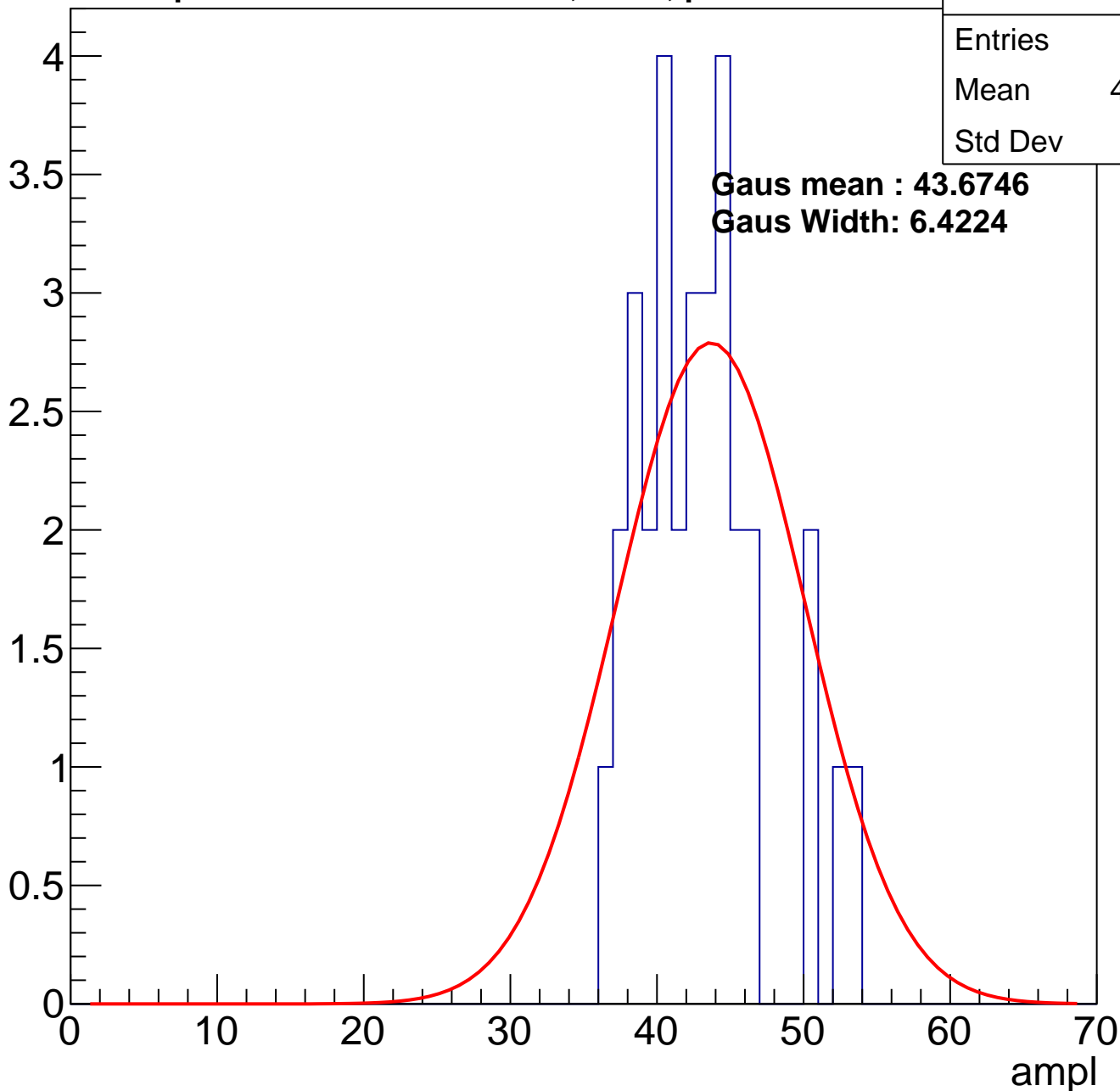
Gaus Width: 4.3541



B1L103S, U15-ch99, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

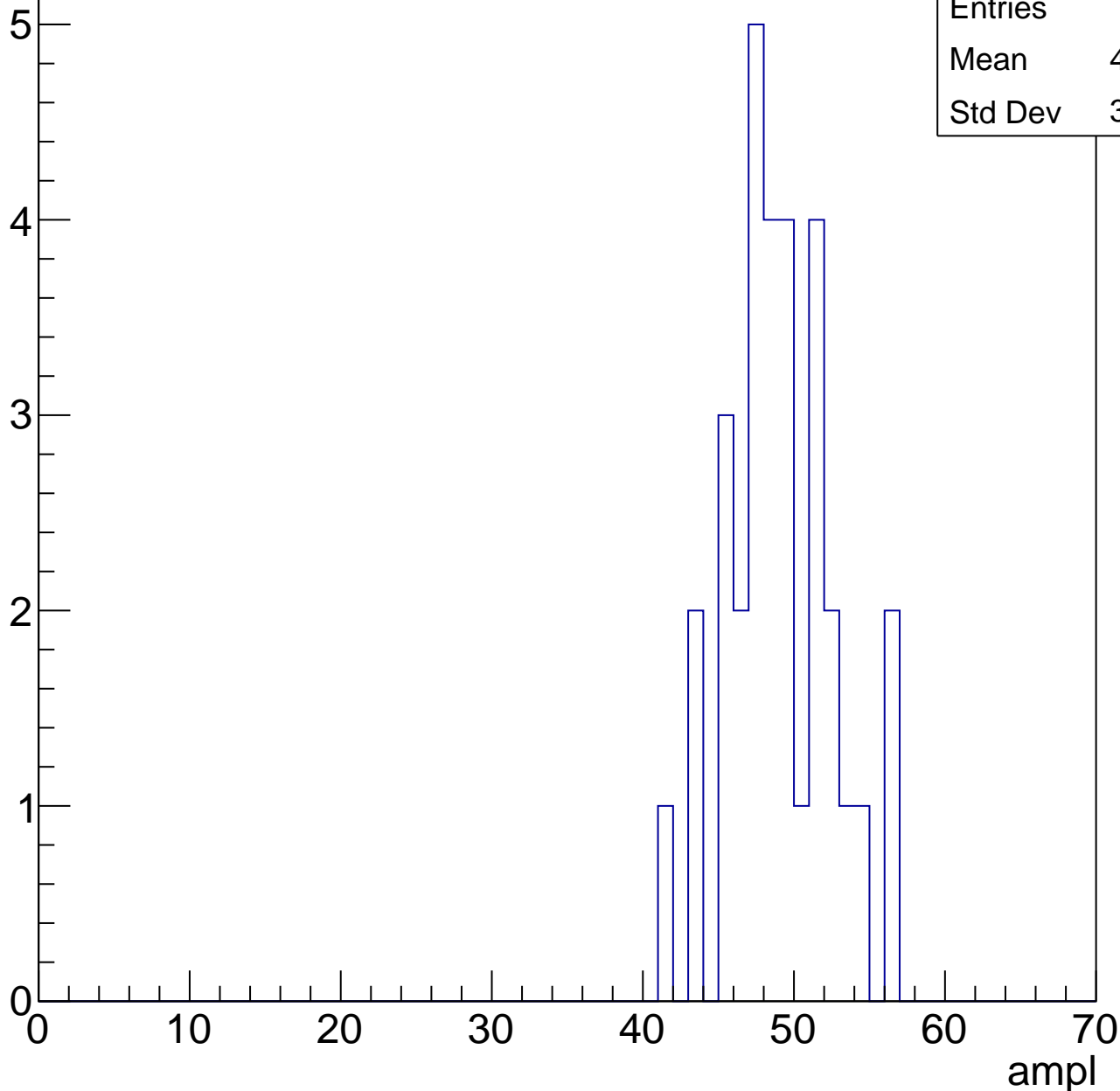


B1L103S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	48.56
Std Dev	3.508

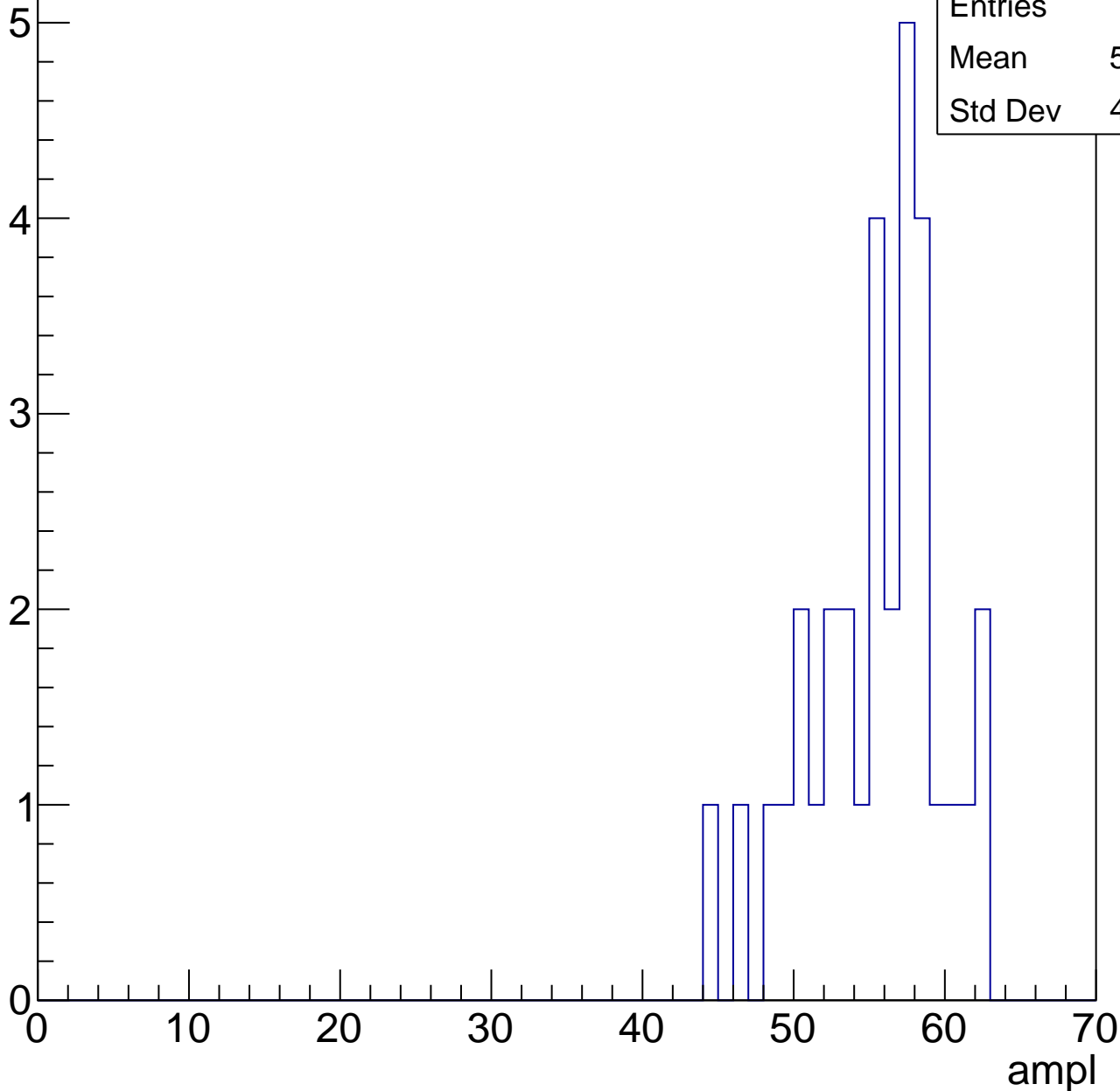


B1L103S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

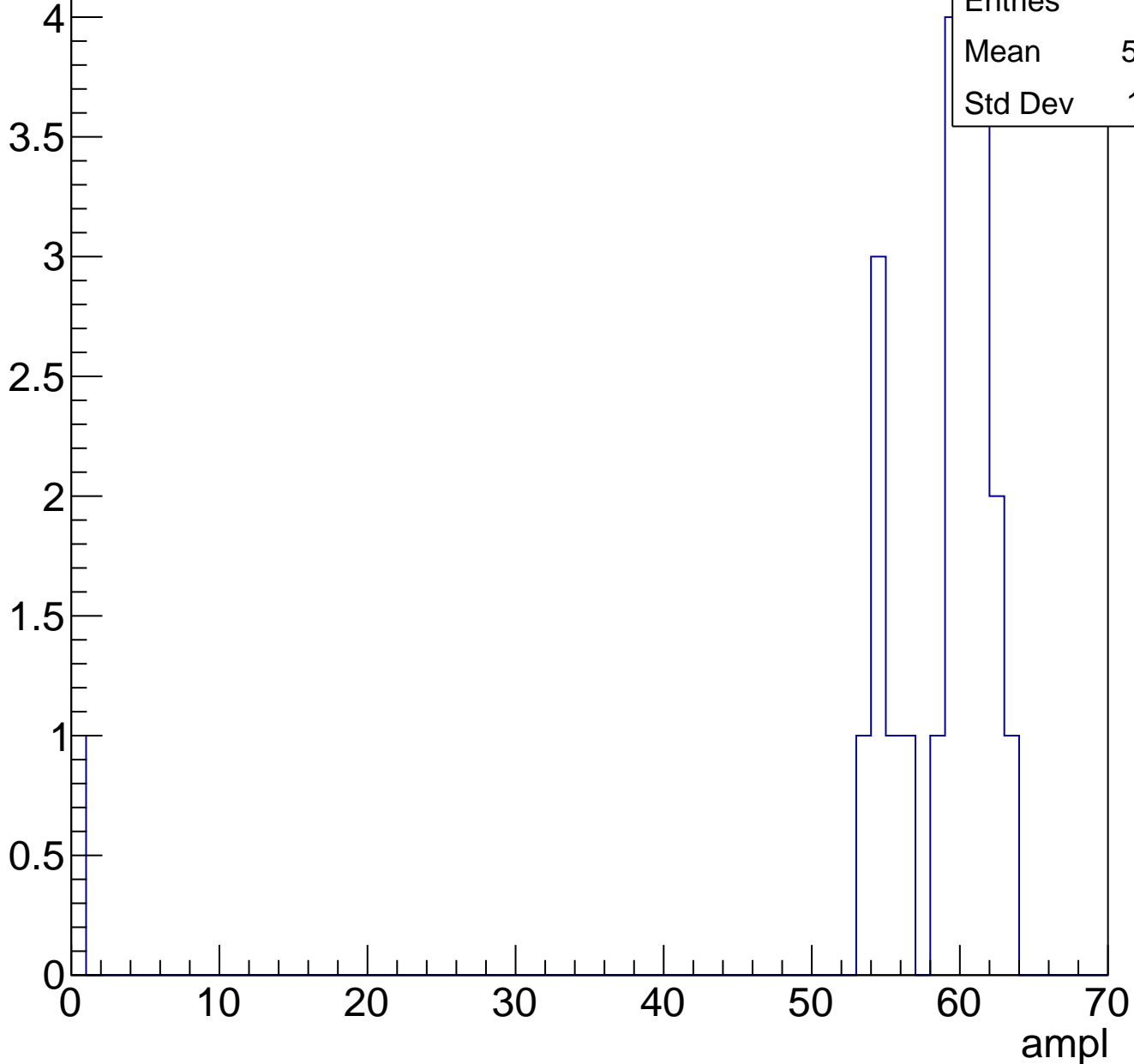
Entries	32
Mean	54.84
Std Dev	4.374



B1L103S, U15-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch100, adc0

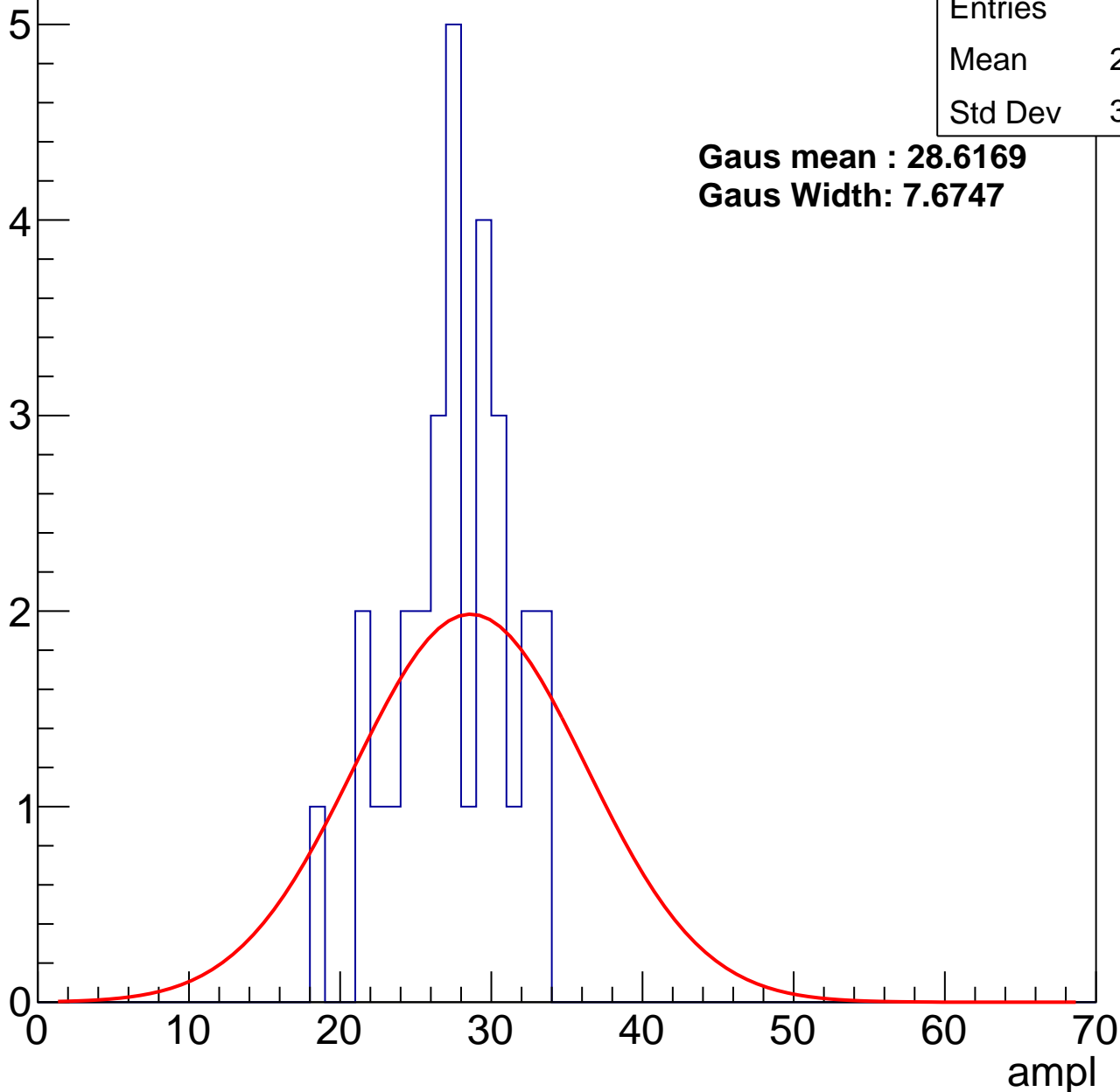
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	30
Mean	27.03
Std Dev	3.674

Gaus mean : 28.6169

Gaus Width: 7.6747



B1L103S, U15-ch100, adc1

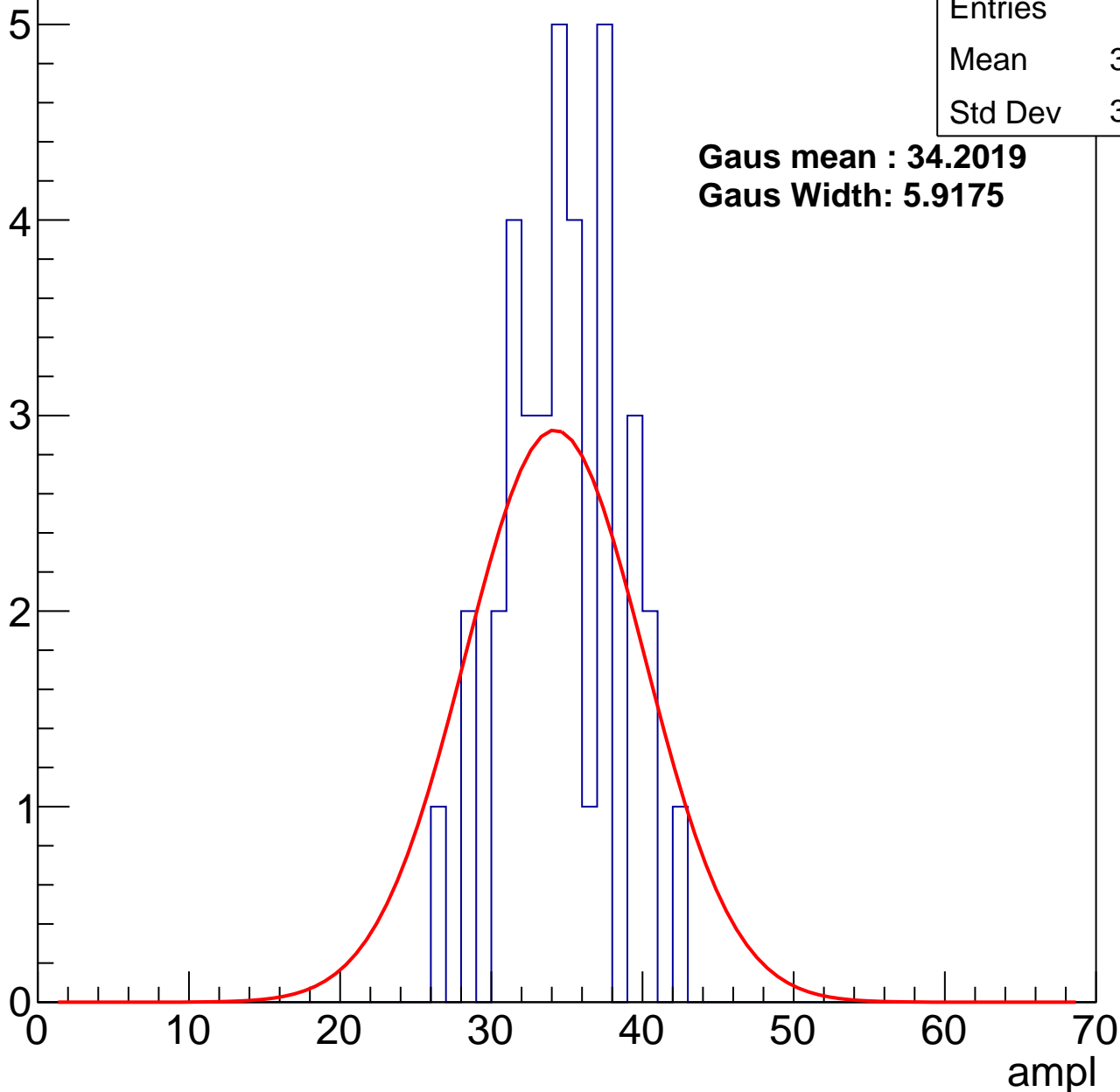
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	34.19
Std Dev	3.658

Gaus mean : 34.2019

Gaus Width: 5.9175



B1L103S, U15-ch100, adc2

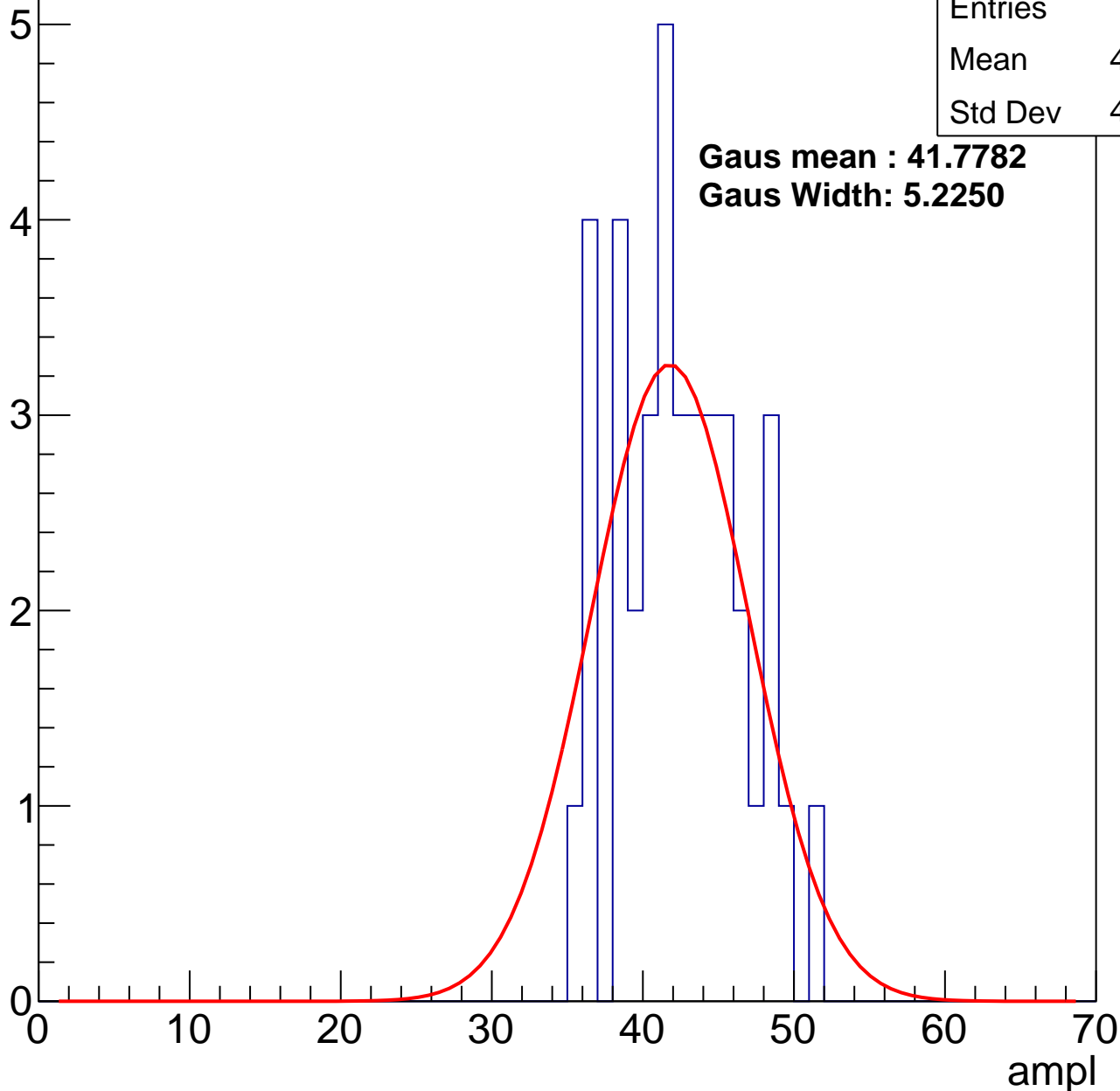
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	42.03
Std Dev	4.016

Gaus mean : 41.7782

Gaus Width: 5.2250



B1L103S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	33
Mean	48.52
Std Dev	4.272

ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch100, adc4

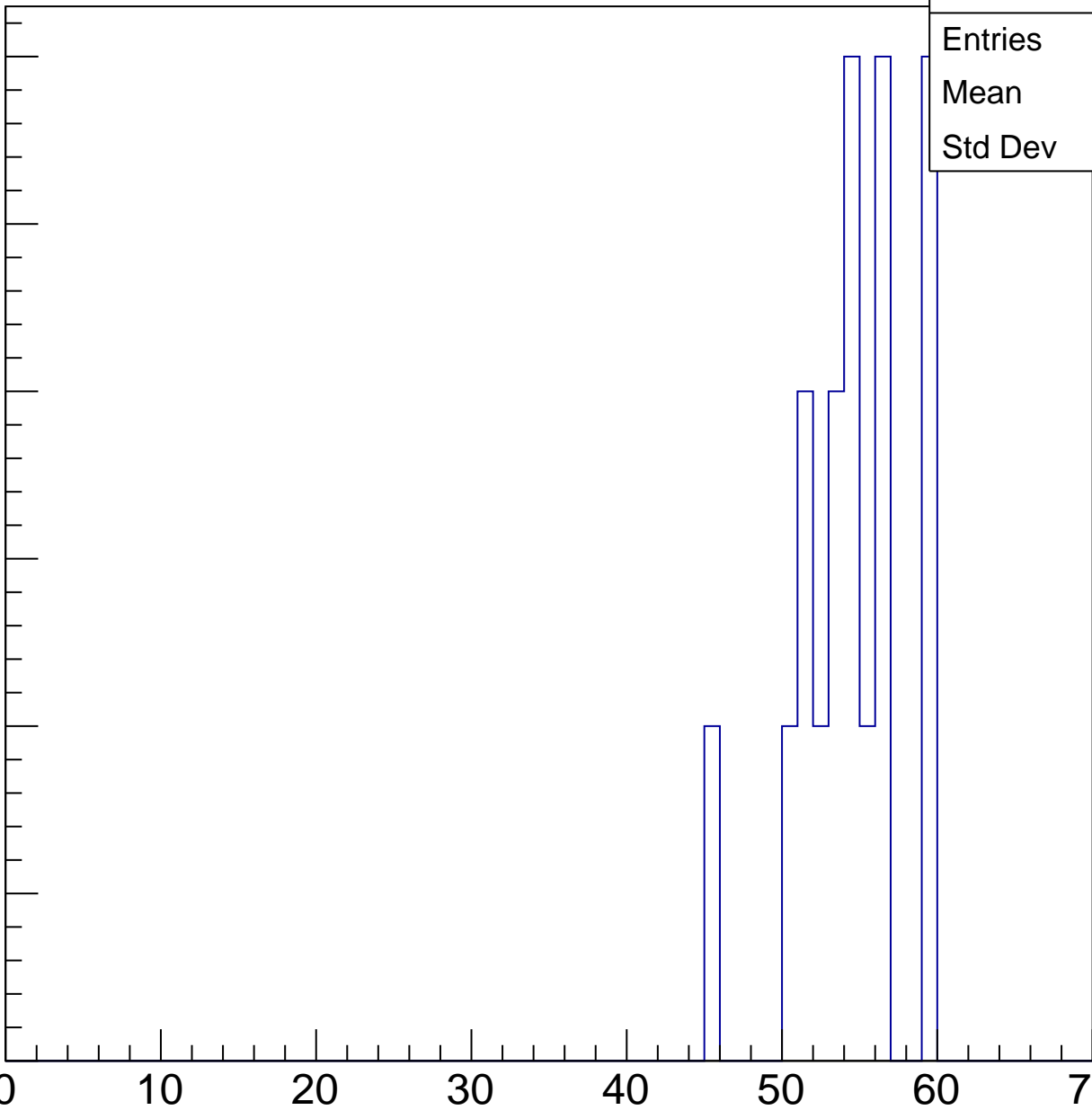
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	17
Mean	53.94
Std Dev	3.506

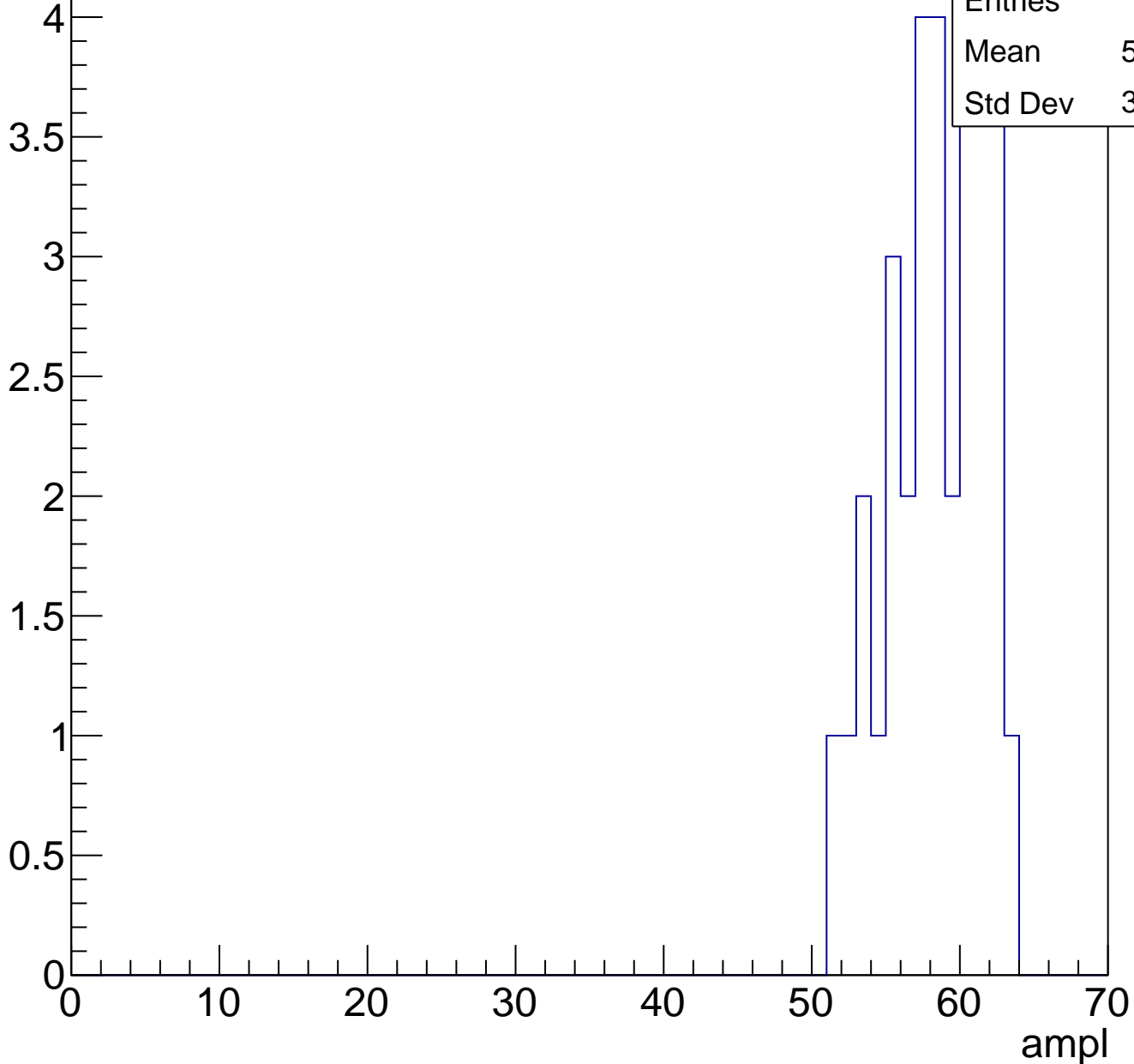
ampl



B1L103S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch101, adc0

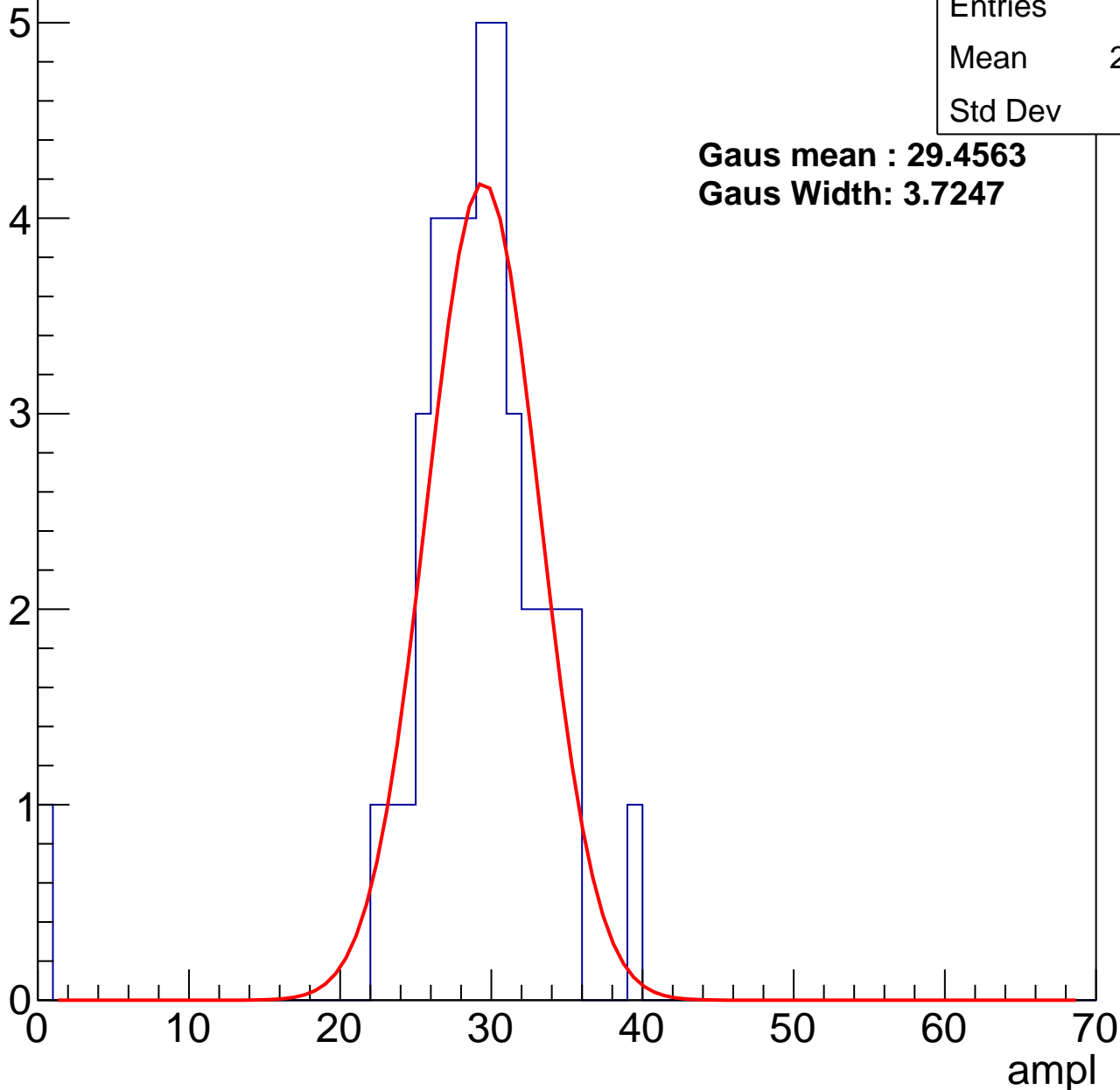
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	28.37
Std Dev	5.69

Gaus mean : 29.4563

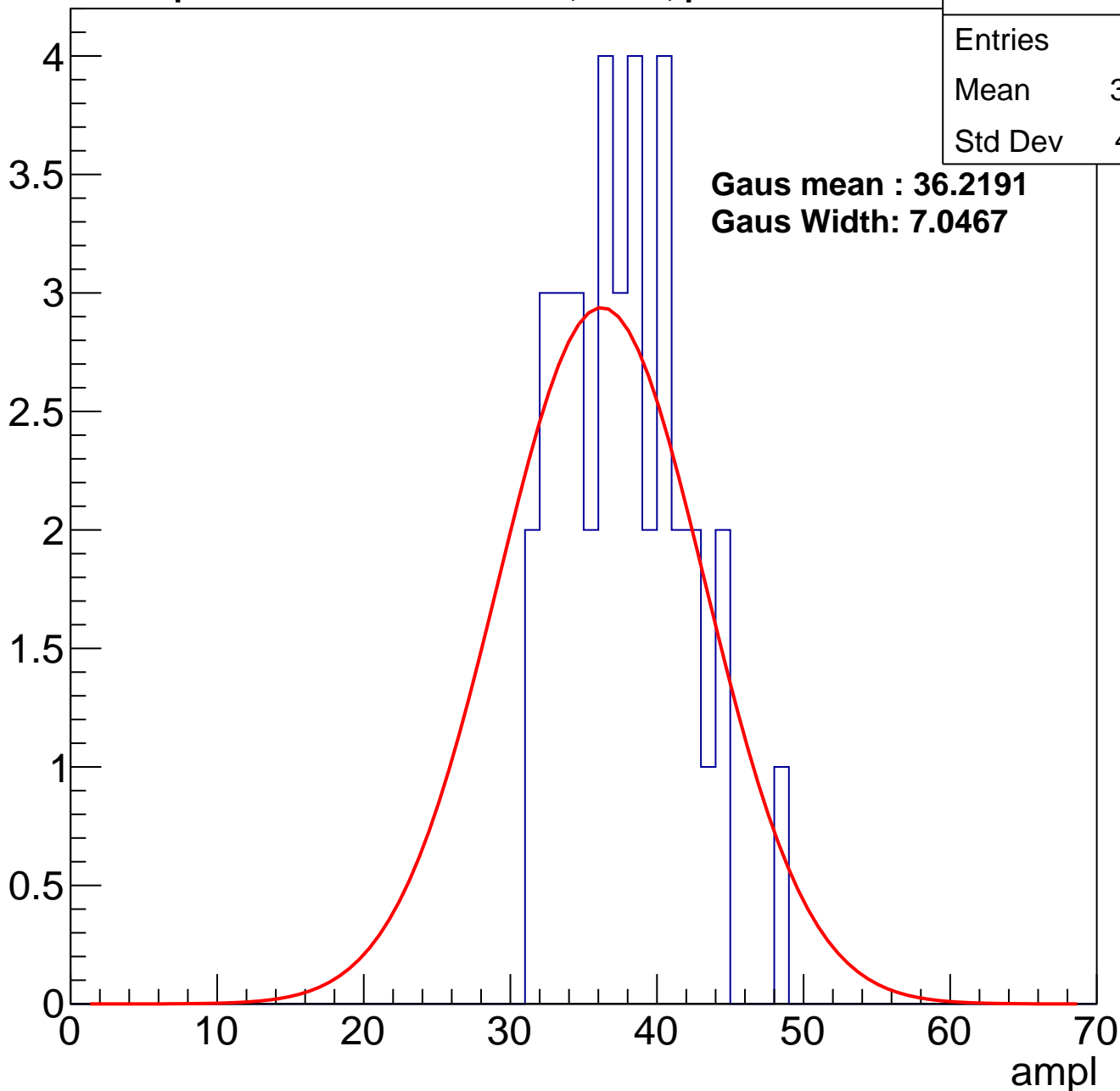
Gaus Width: 3.7247



B1L103S, U15-ch101, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch101, adc2

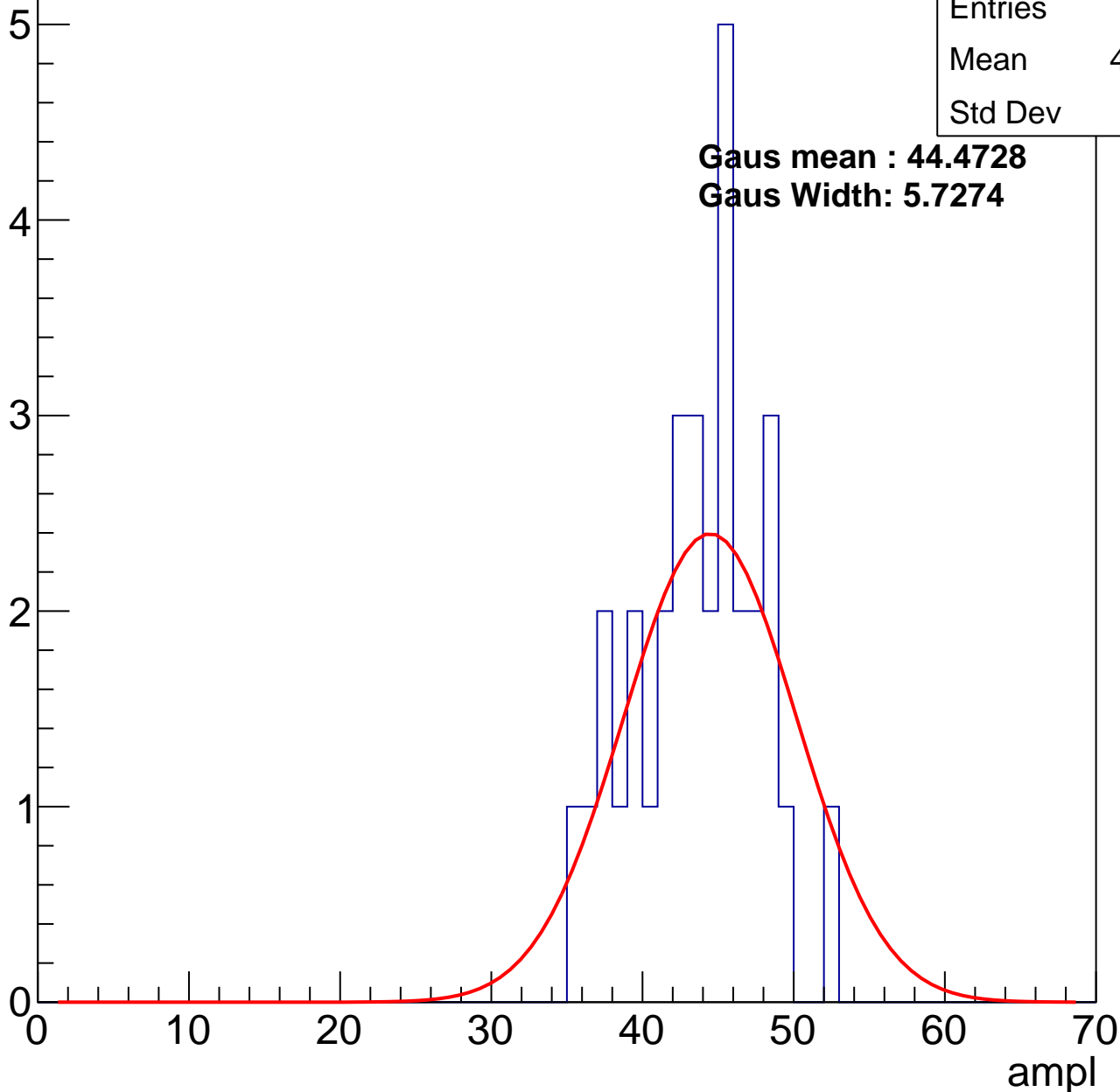
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	43.19
Std Dev	4.05

Gaus mean : 44.4728

Gaus Width: 5.7274

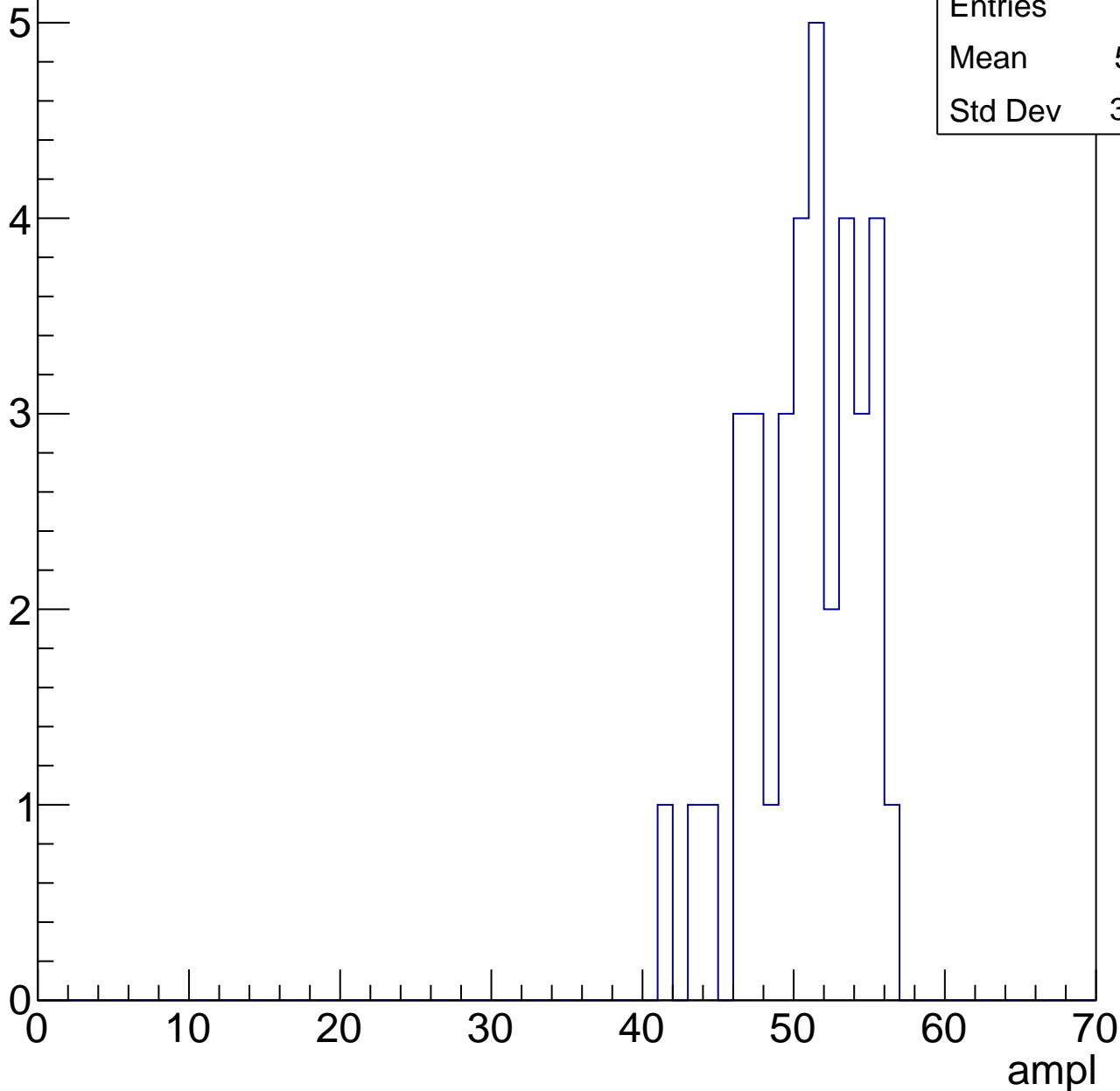


B1L103S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

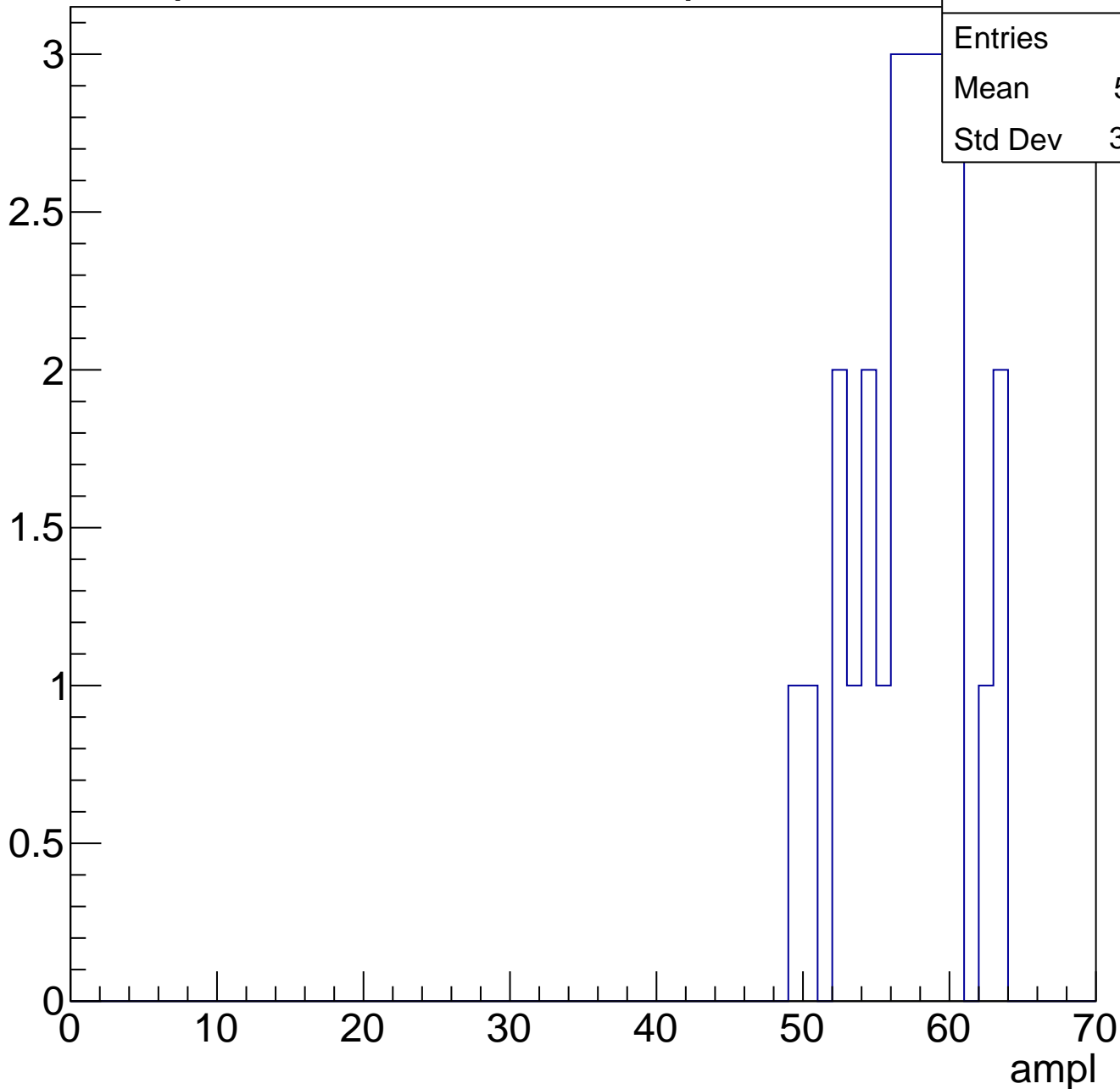
Entries	36
Mean	50.31
Std Dev	3.642



B1L103S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

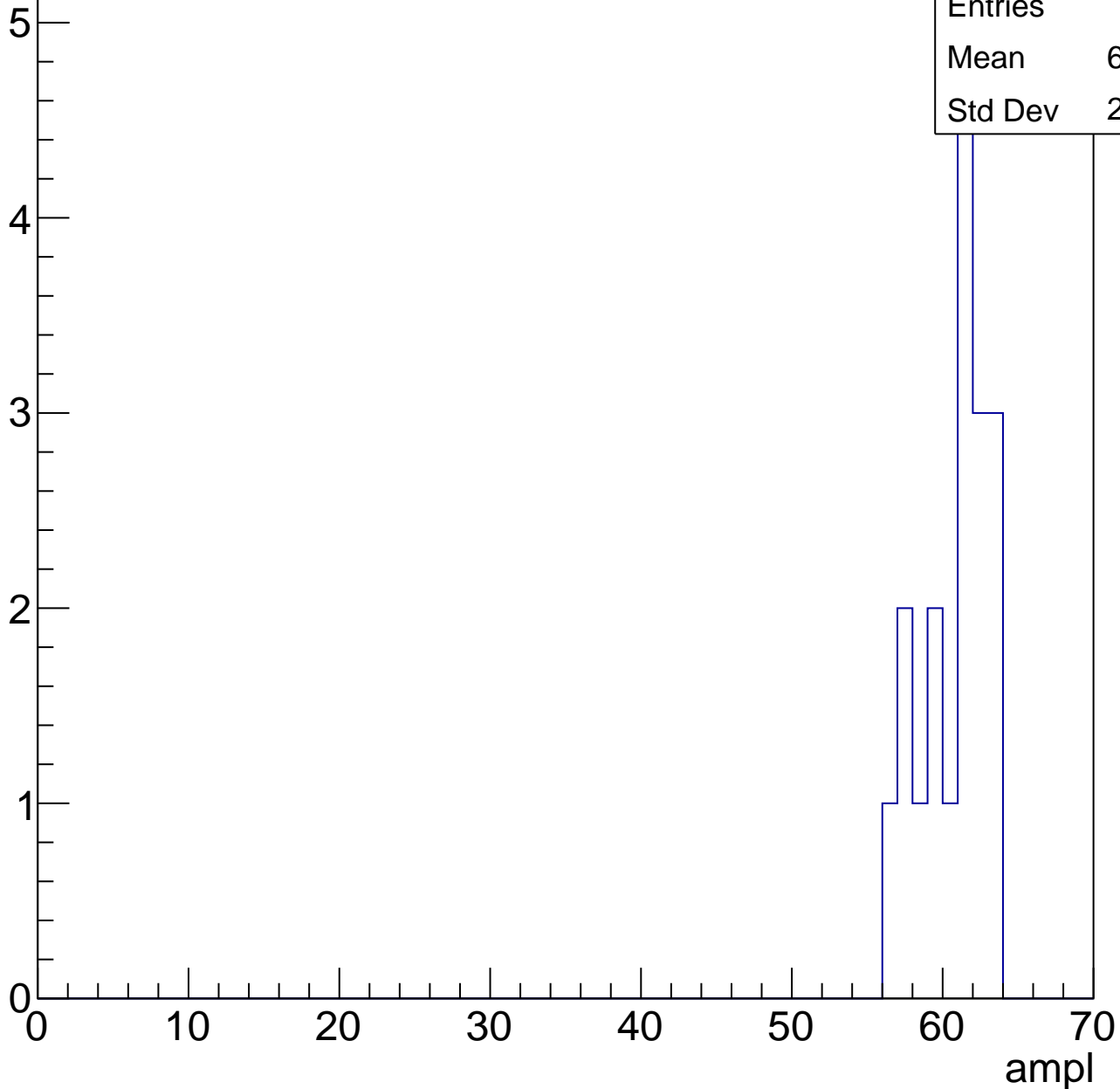


B1L103S, U15-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

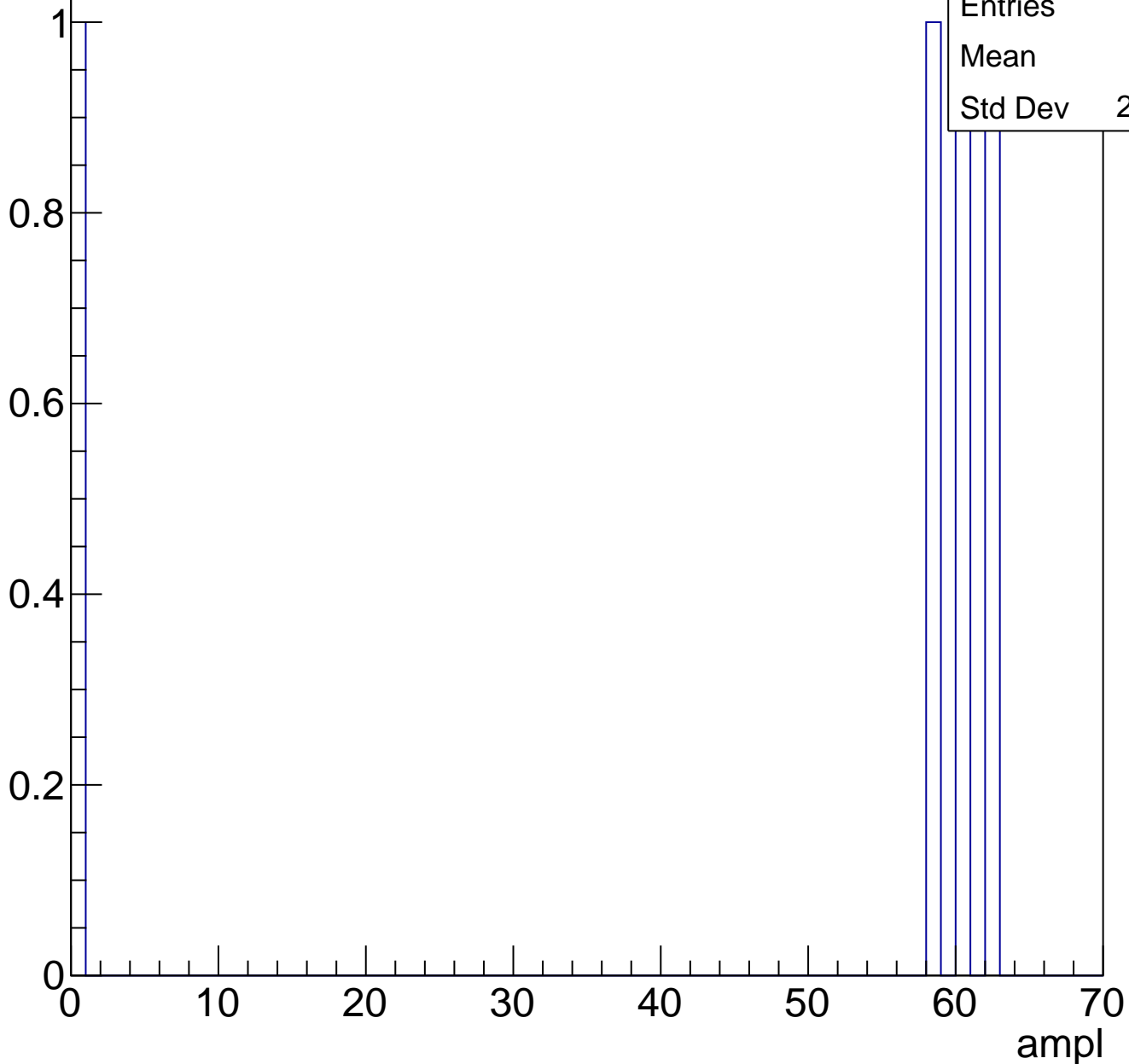
Entries	18
Mean	60.33
Std Dev	2.134



B1L103S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	45
Std Dev	26.02

B1L103S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

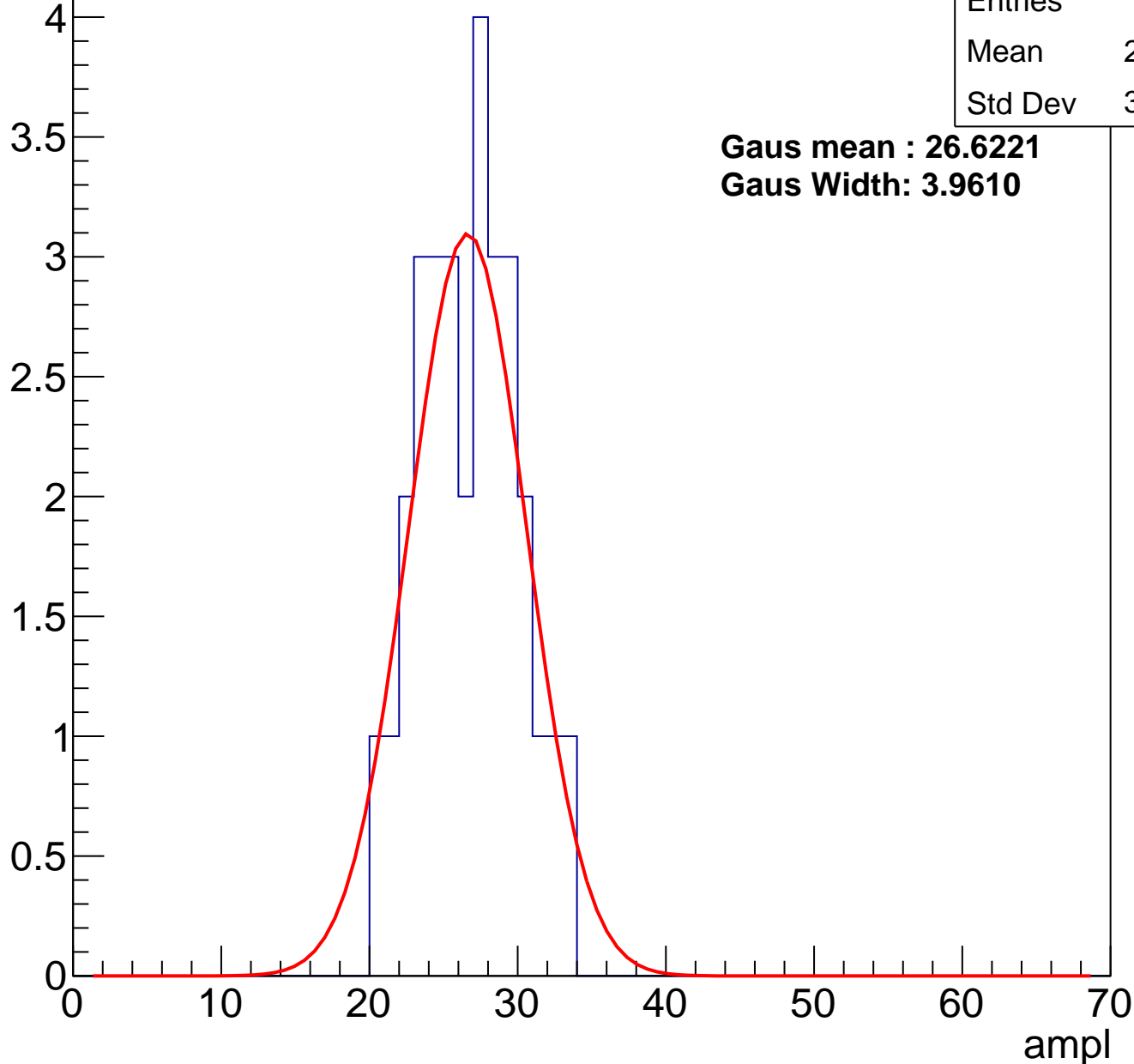
Std Dev

0

B1L103S, U15-ch102, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch102, adc1

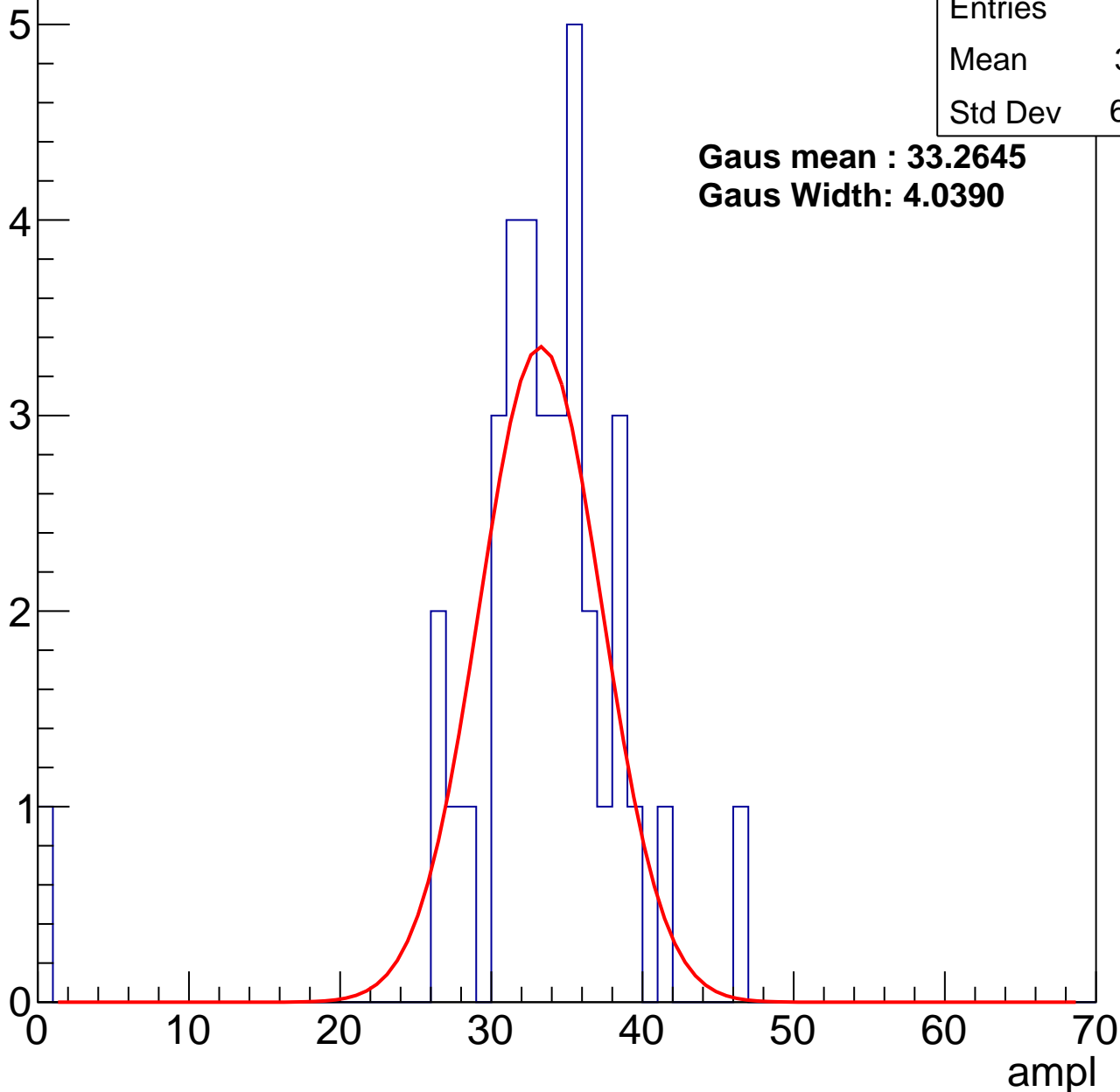
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	32.61
Std Dev	6.853

Gaus mean : 33.2645

Gaus Width: 4.0390



B1L103S, U15-ch102, adc2

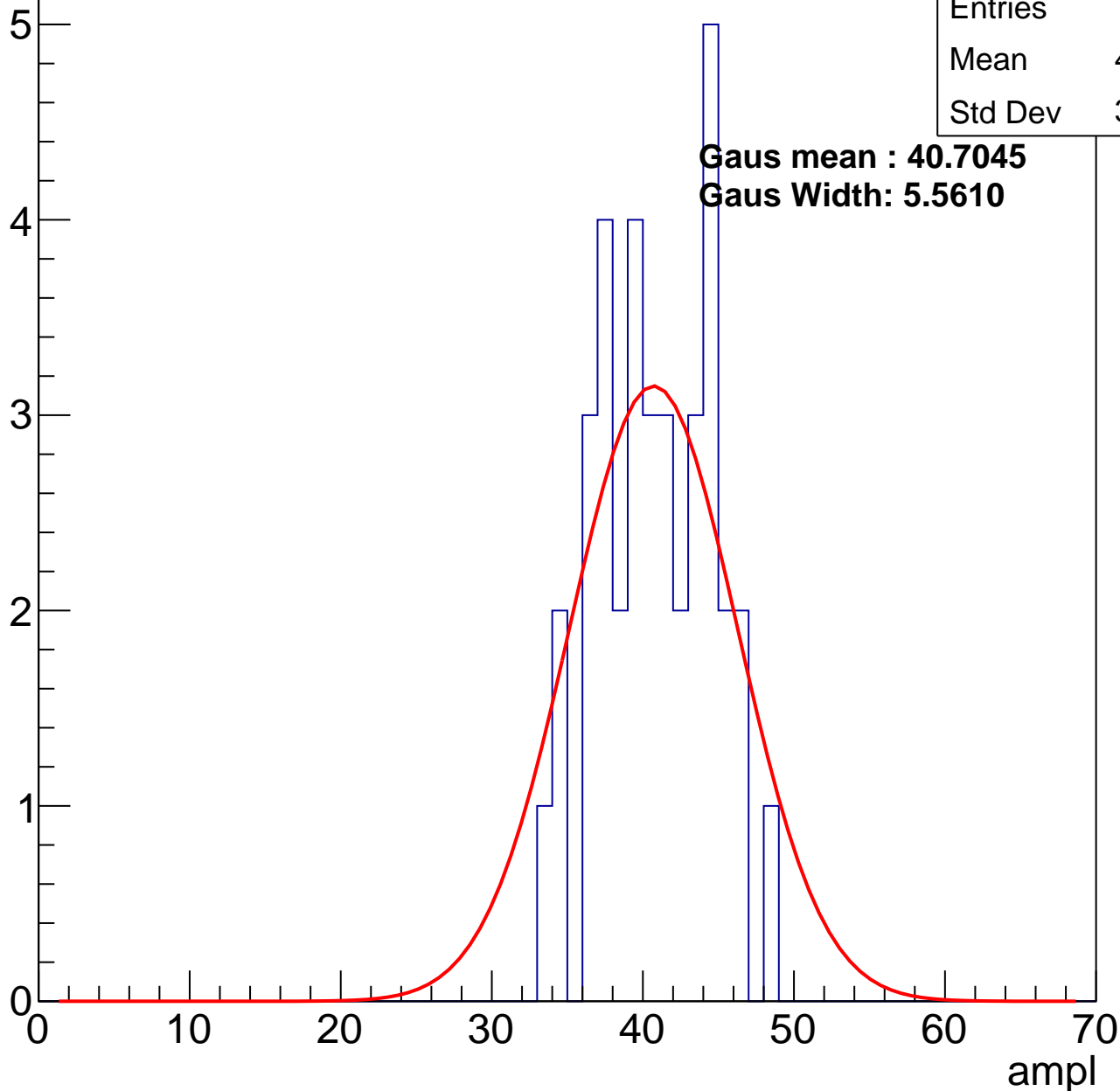
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	40.41
Std Dev	3.731

Gaus mean : 40.7045

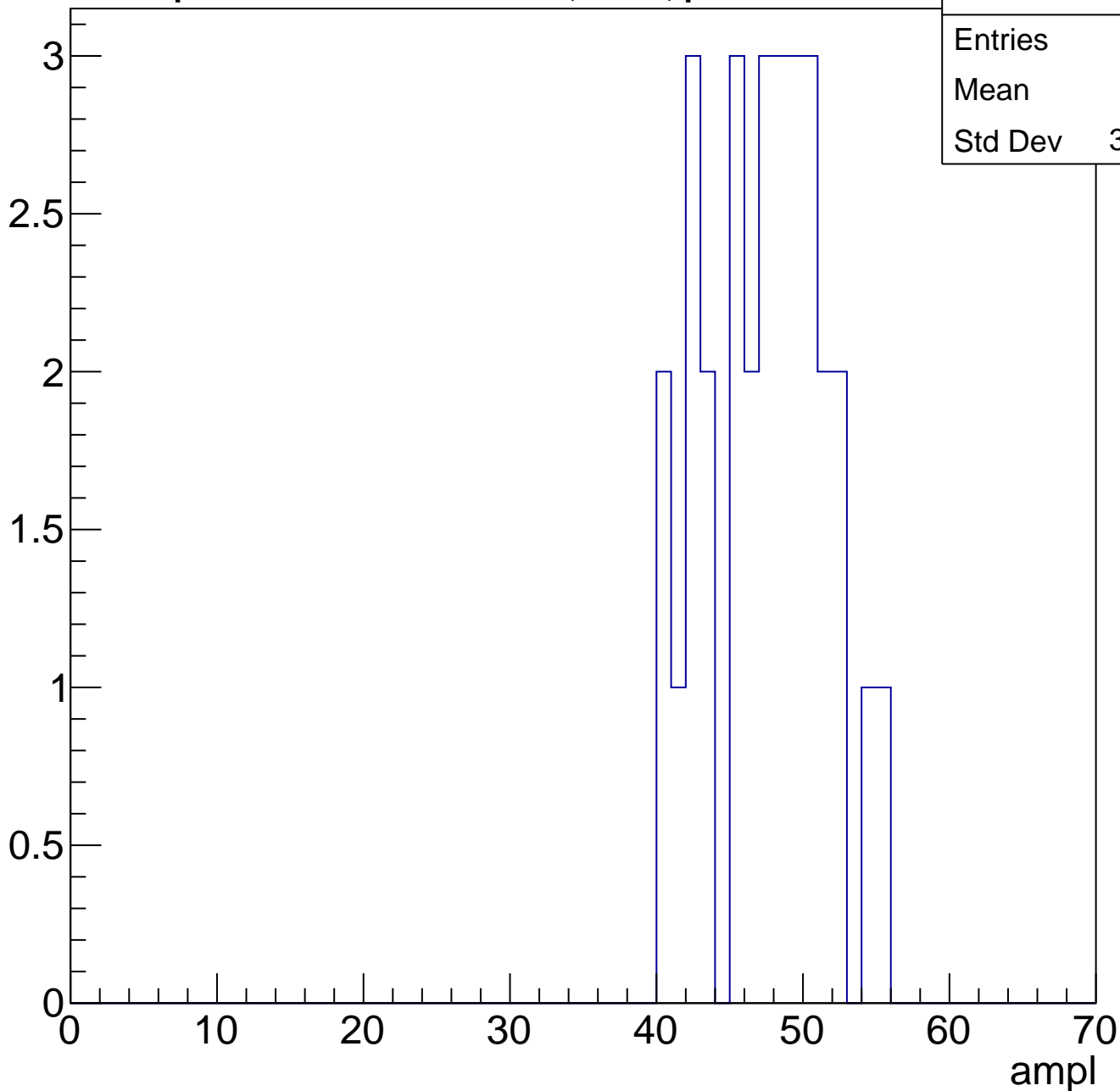
Gaus Width: 5.5610



B1L103S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

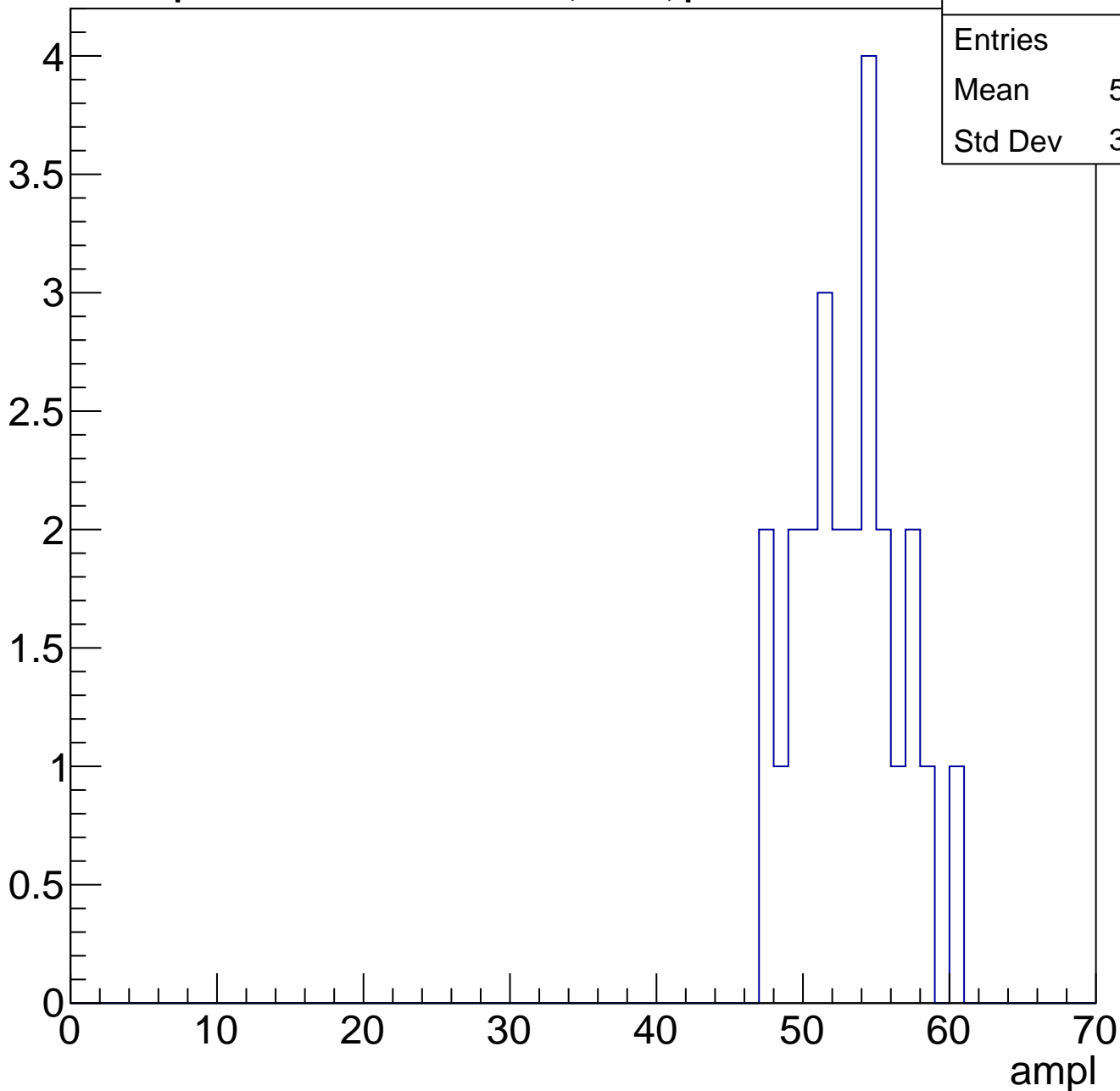


Entries	31
Mean	47
Std Dev	3.984

B1L103S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

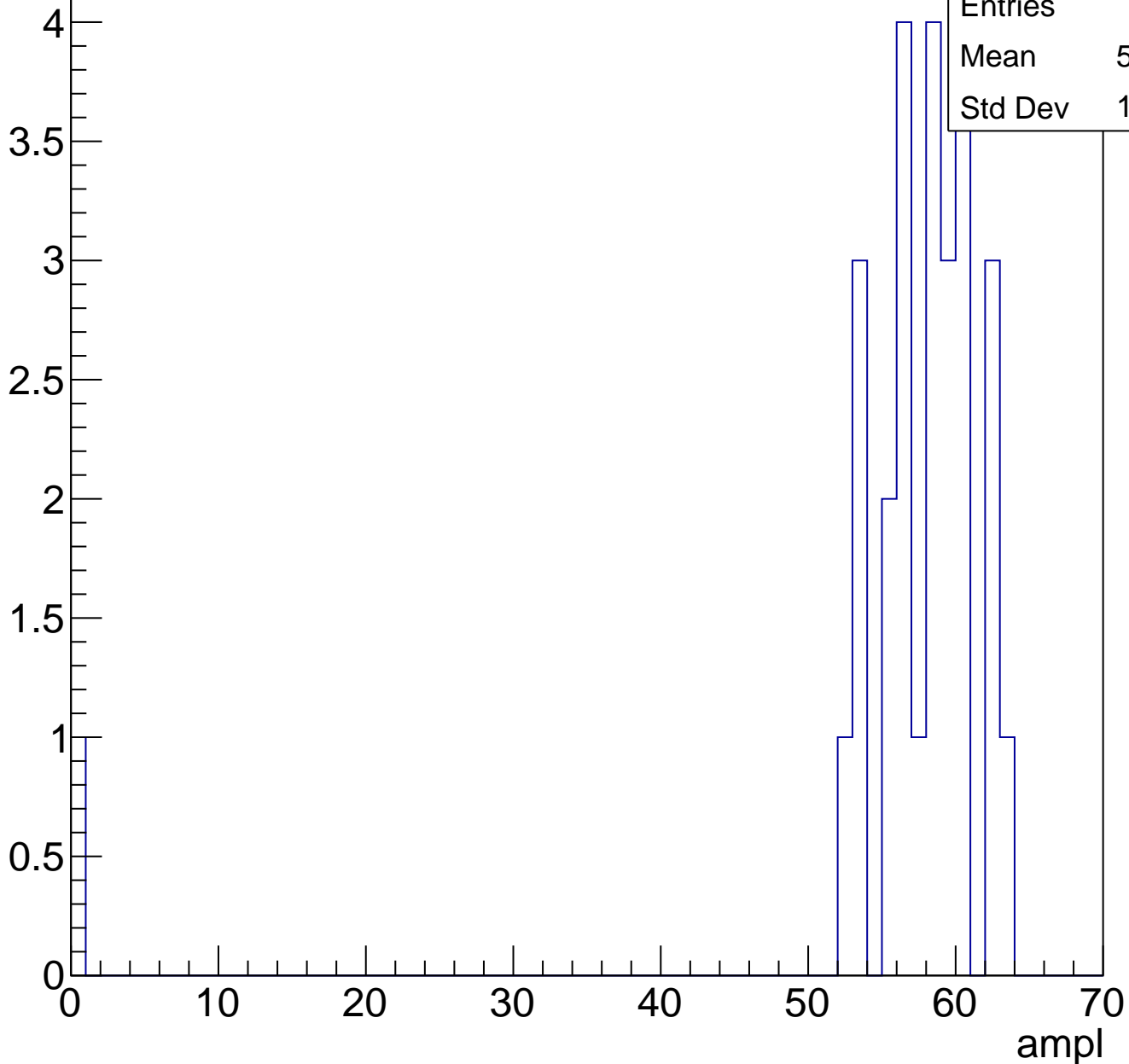
Entry



B1L103S, U15-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

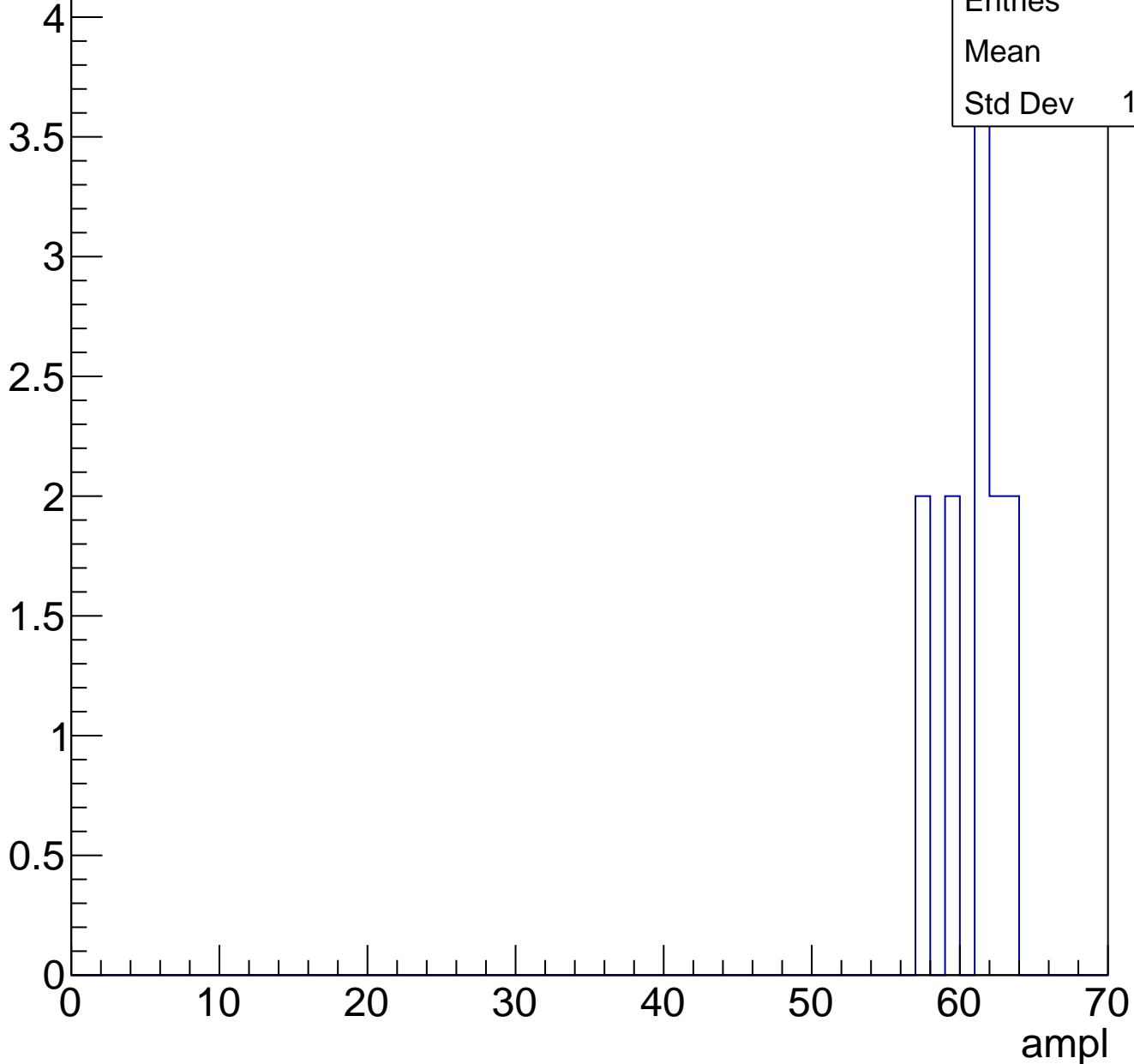
Entry



B1L103S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

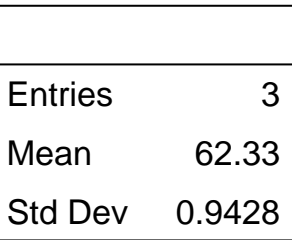
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

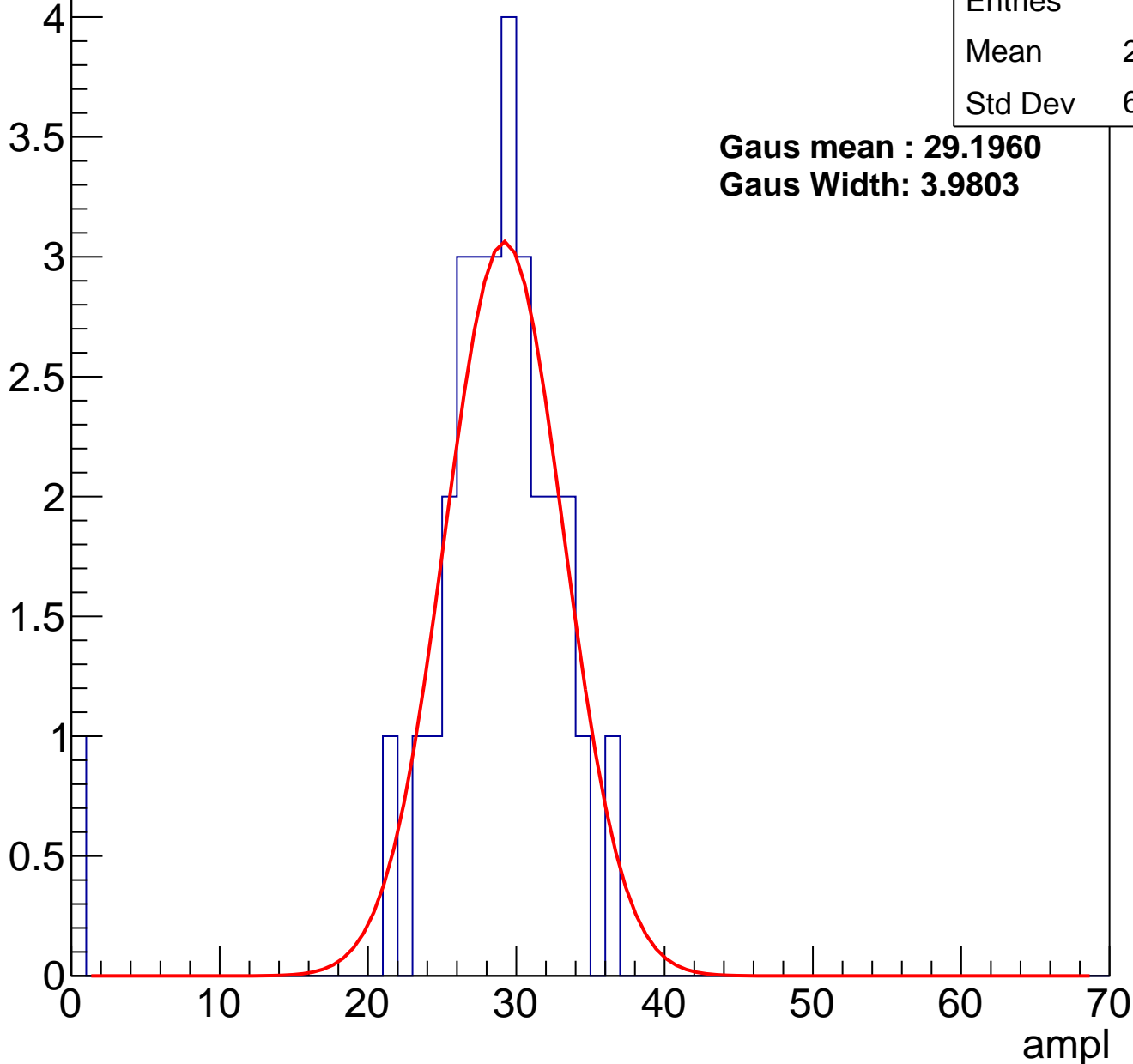
ampl



B1L103S, U15-ch103, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch103, adc1

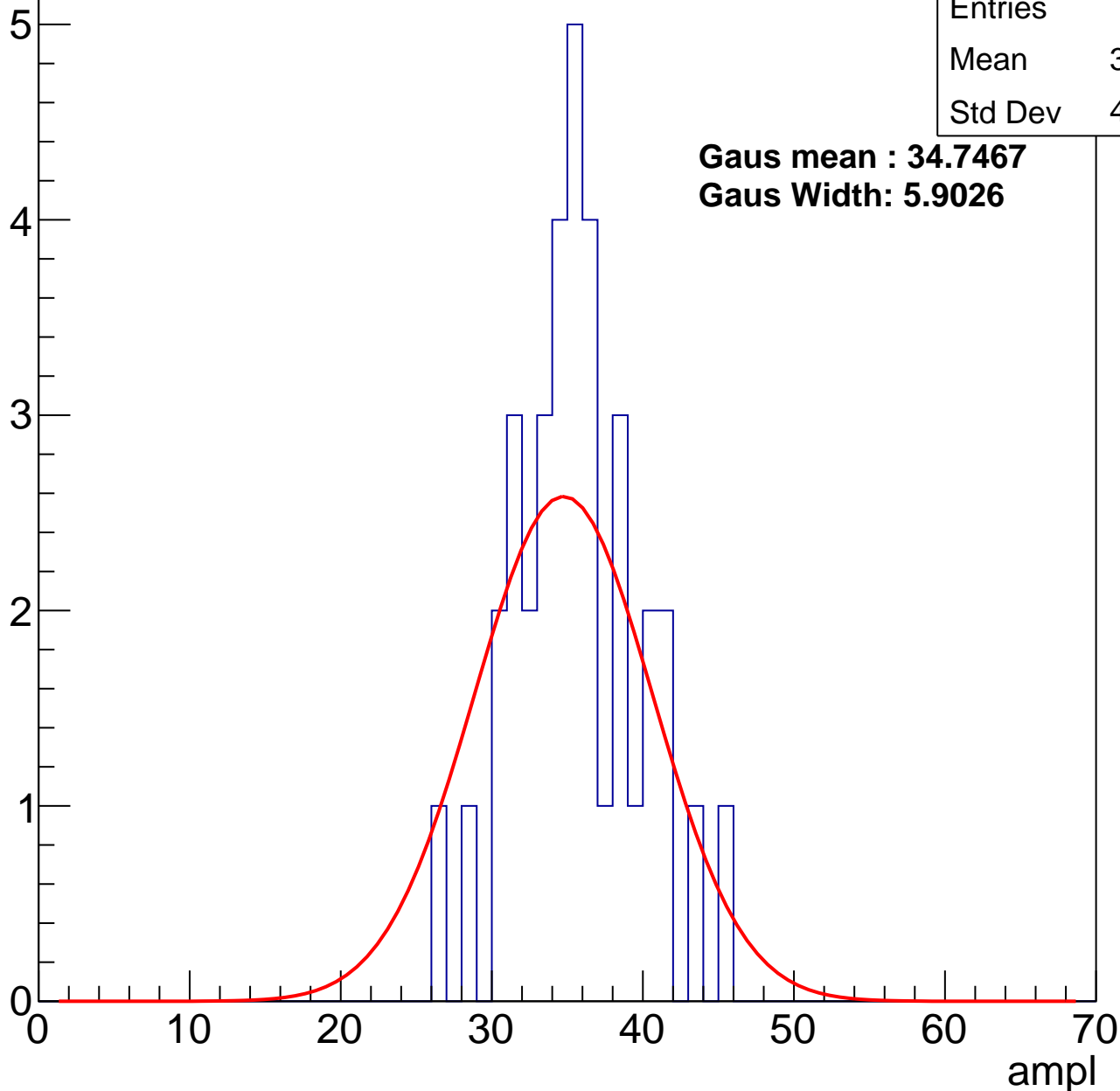
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	35.14
Std Dev	4.077

Gaus mean : 34.7467

Gaus Width: 5.9026



B1L103S, U15-ch103, adc2

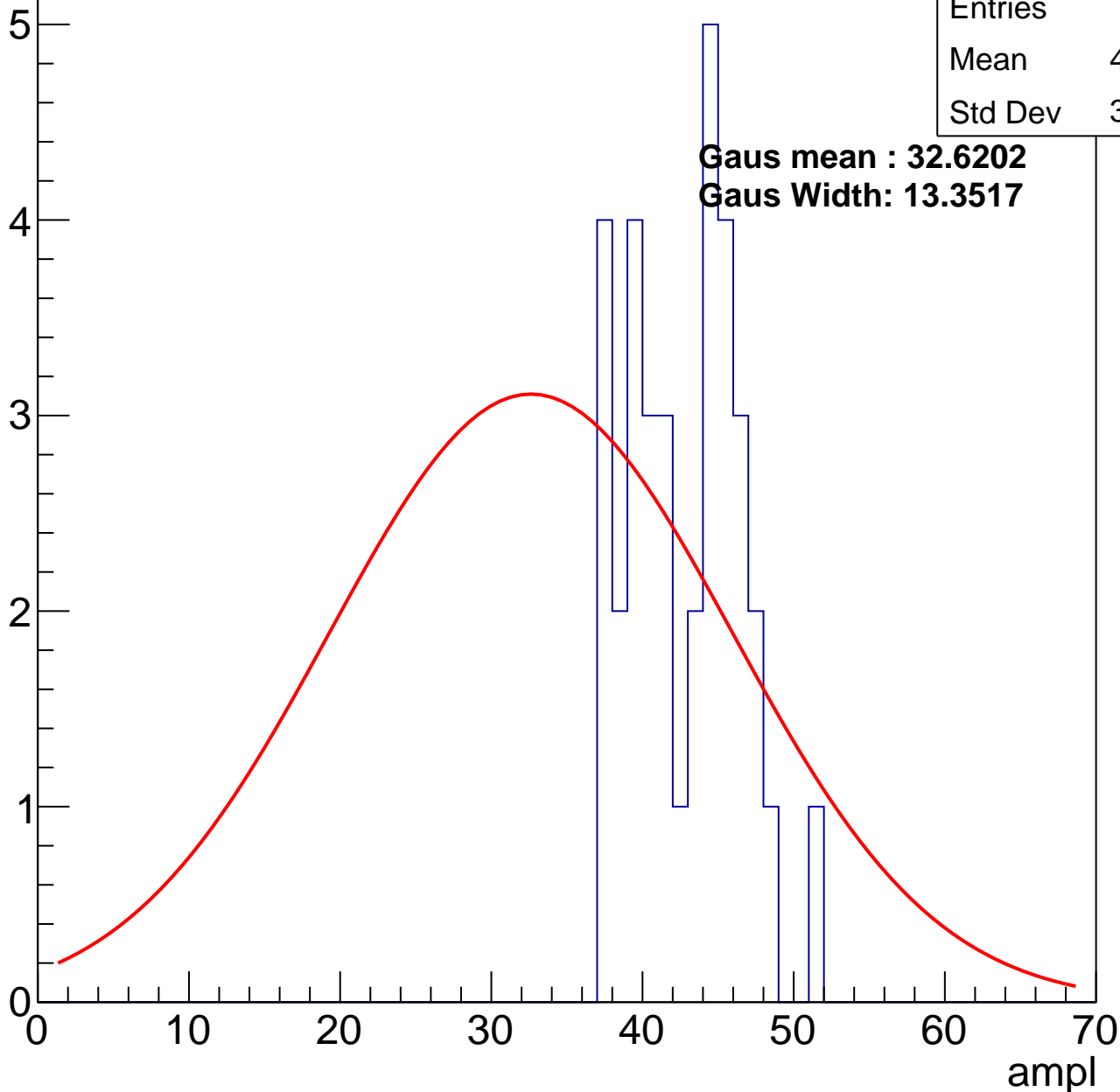
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	42.34
Std Dev	3.593

Gaus mean : 32.6202

Gaus Width: 13.3517

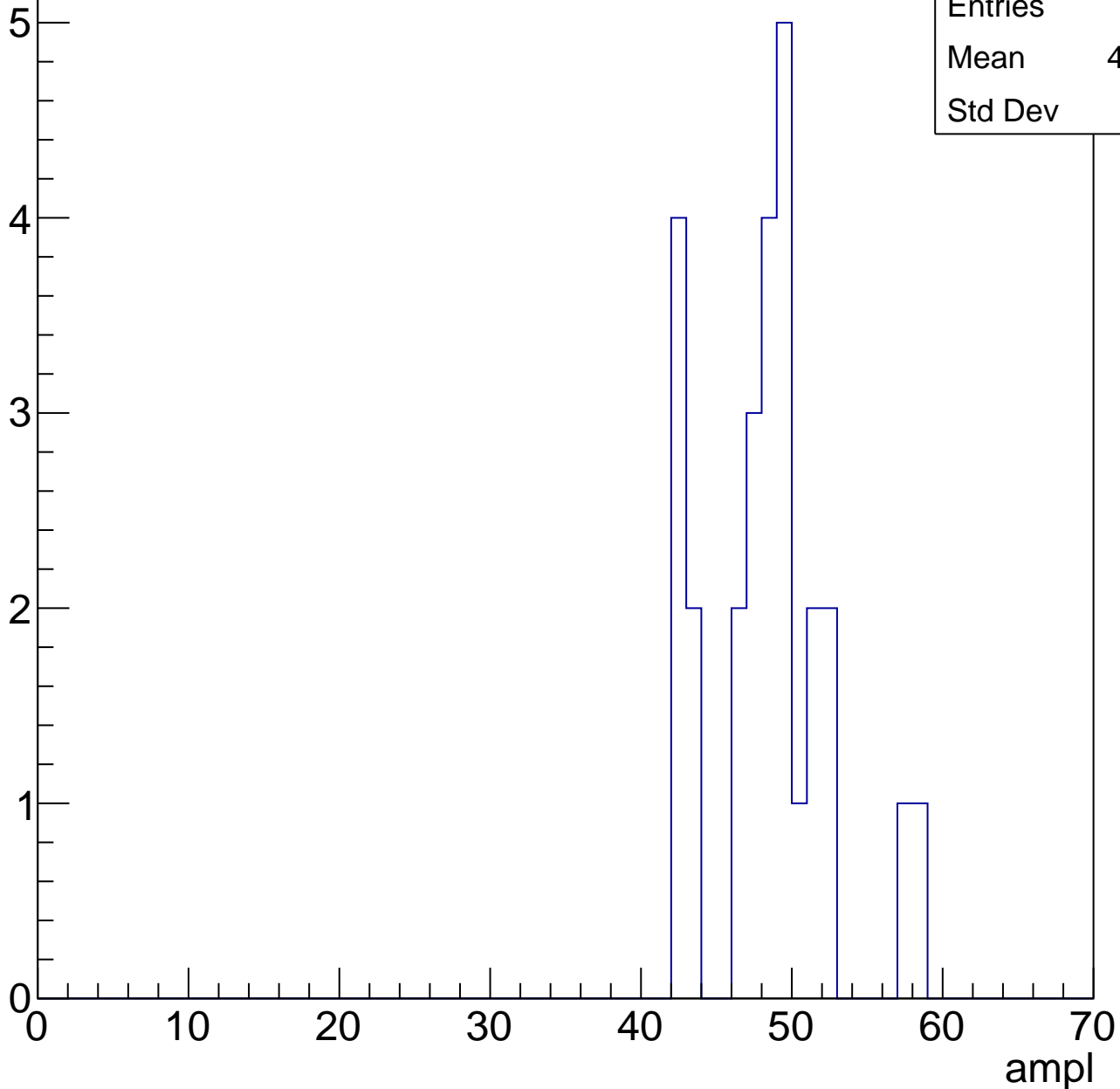


B1L103S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

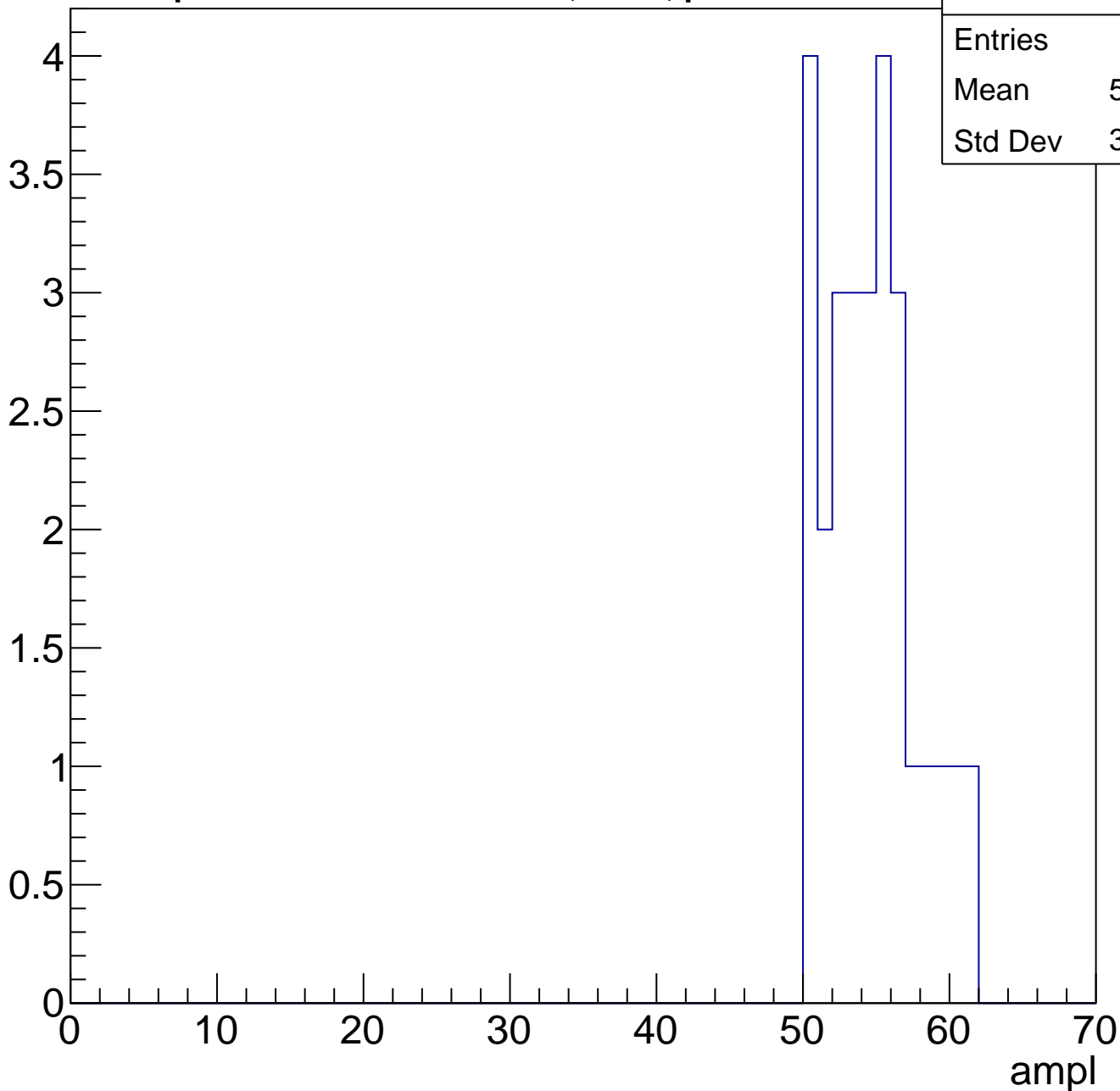
Entries	27
Mean	47.96
Std Dev	4.06



B1L103S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

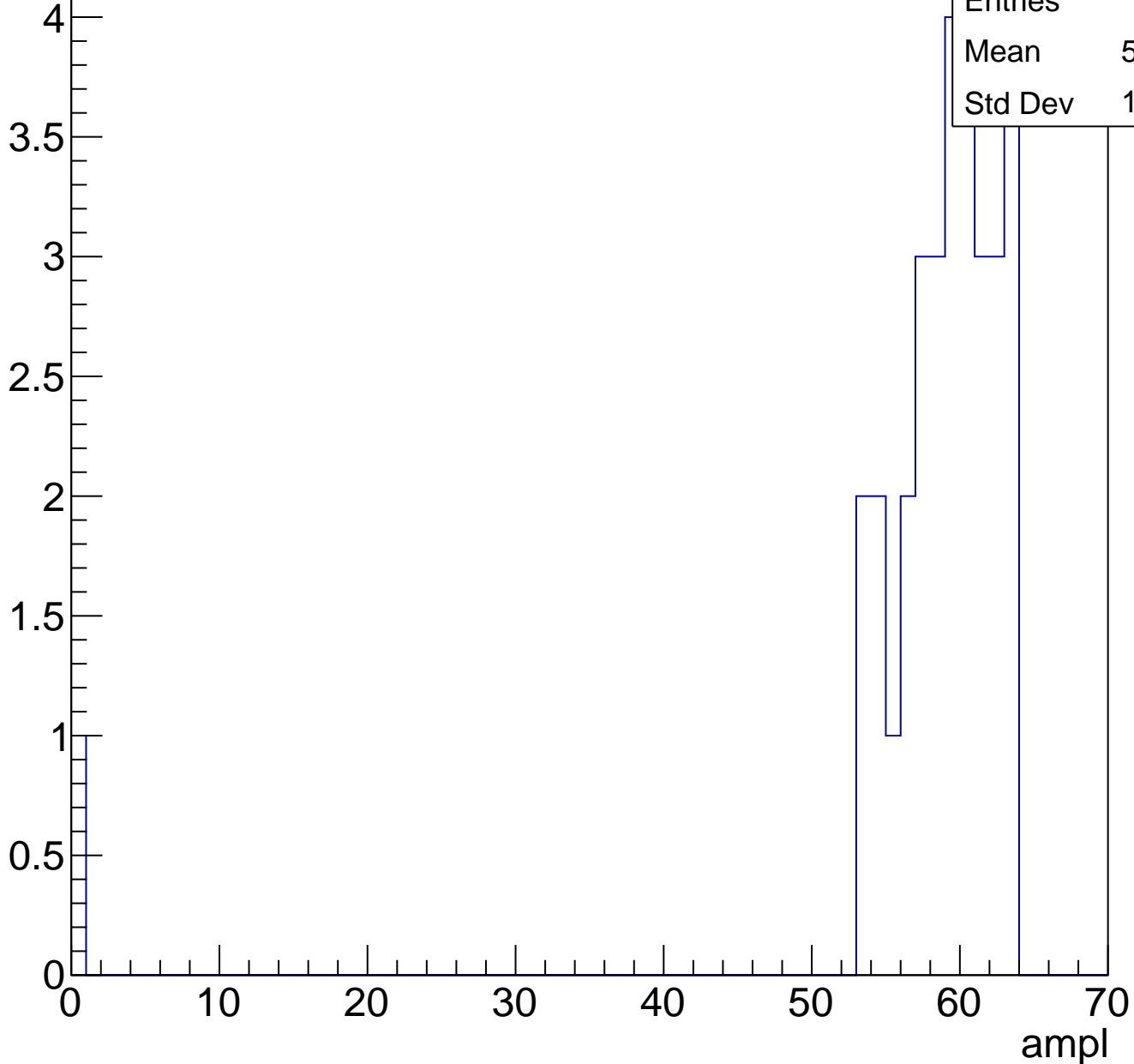


Entries	27
Mean	54.15
Std Dev	3.027

B1L103S, U15-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	62
Std Dev	0.7071

B1L103S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch104, adc0

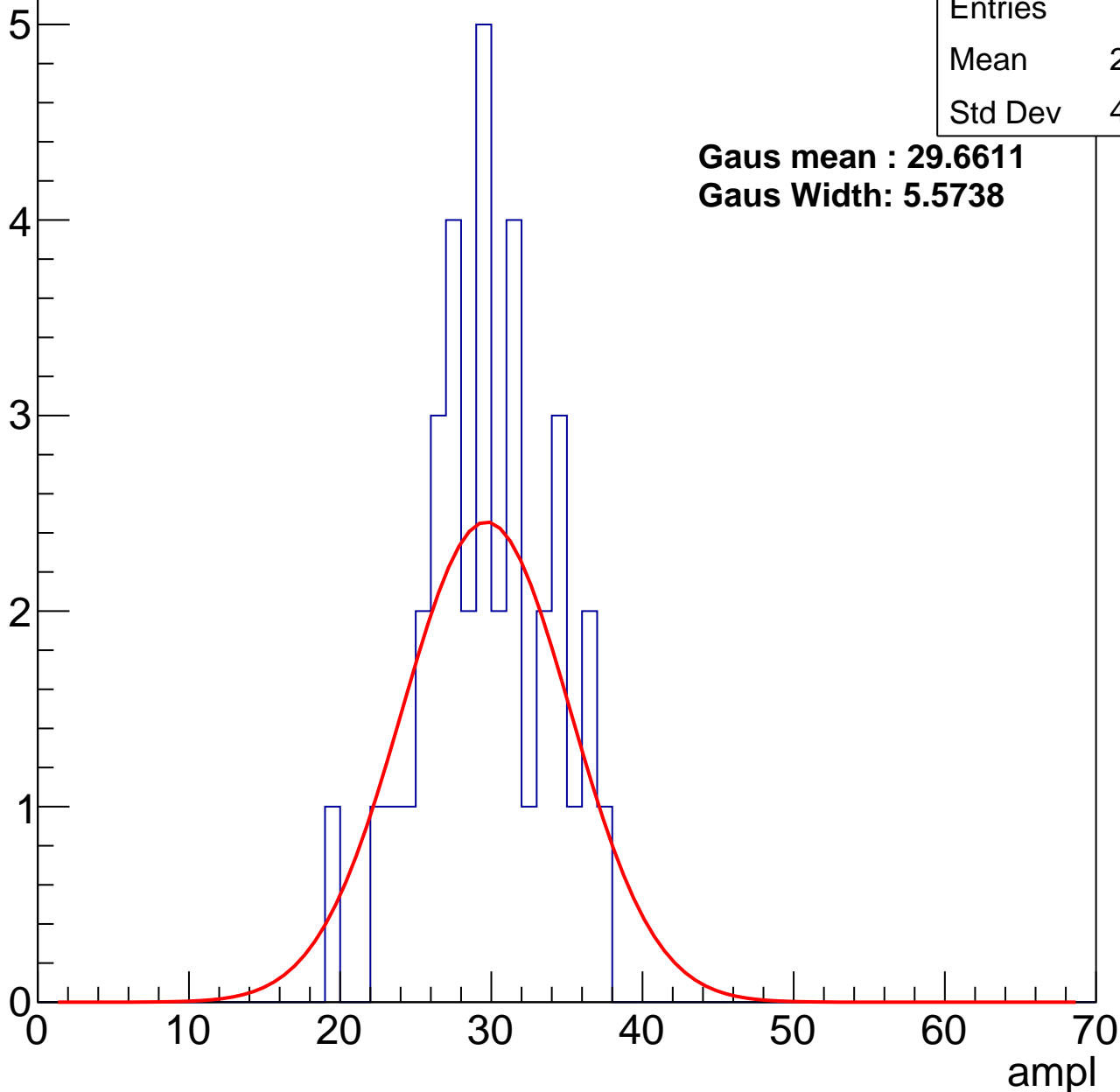
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	29.25
Std Dev	4.139

Gaus mean : 29.6611

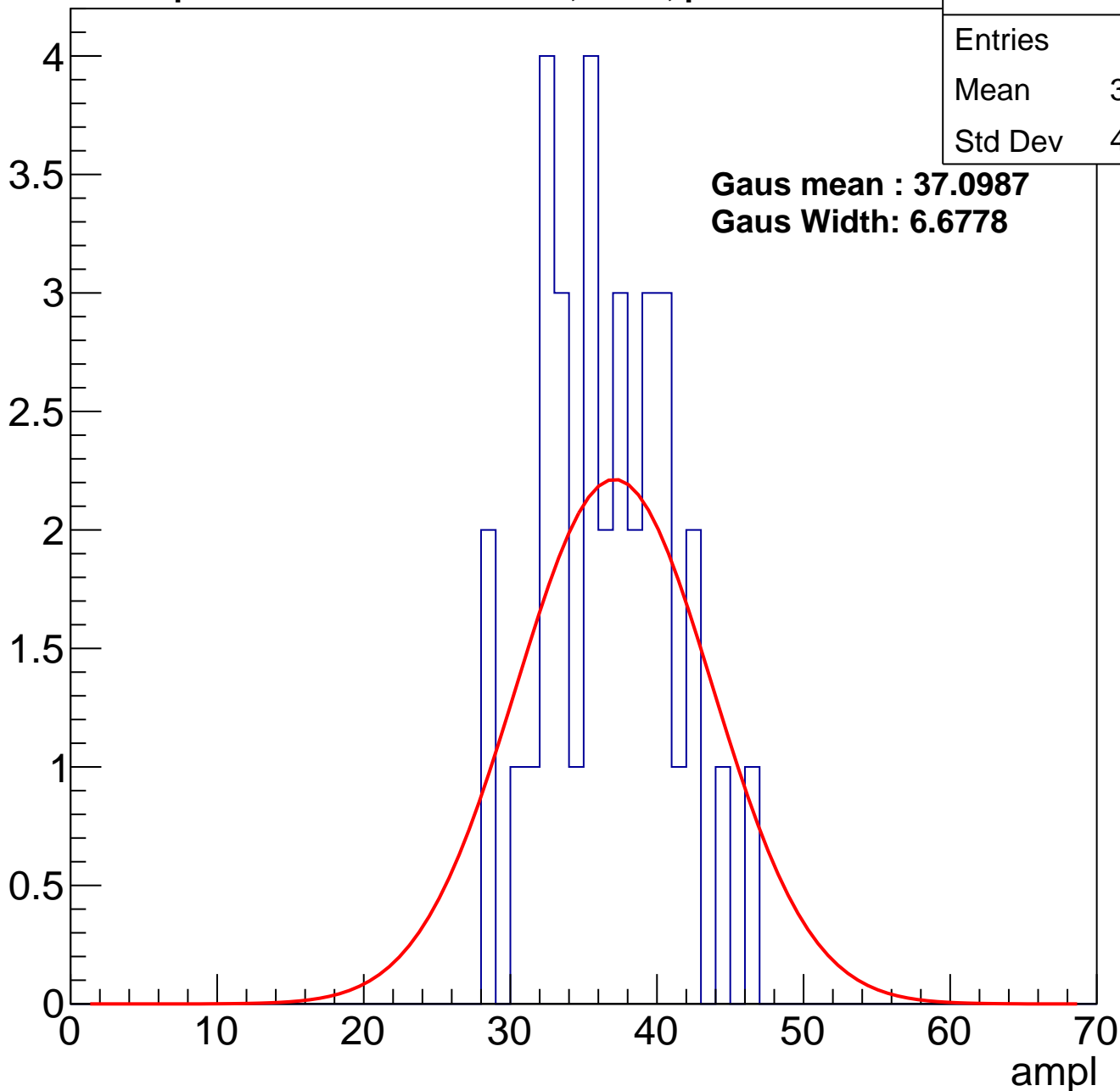
Gaus Width: 5.5738



B1L103S, U15-ch104, adc1

calib_packv5_042523_0143.root, FC#7, port C2

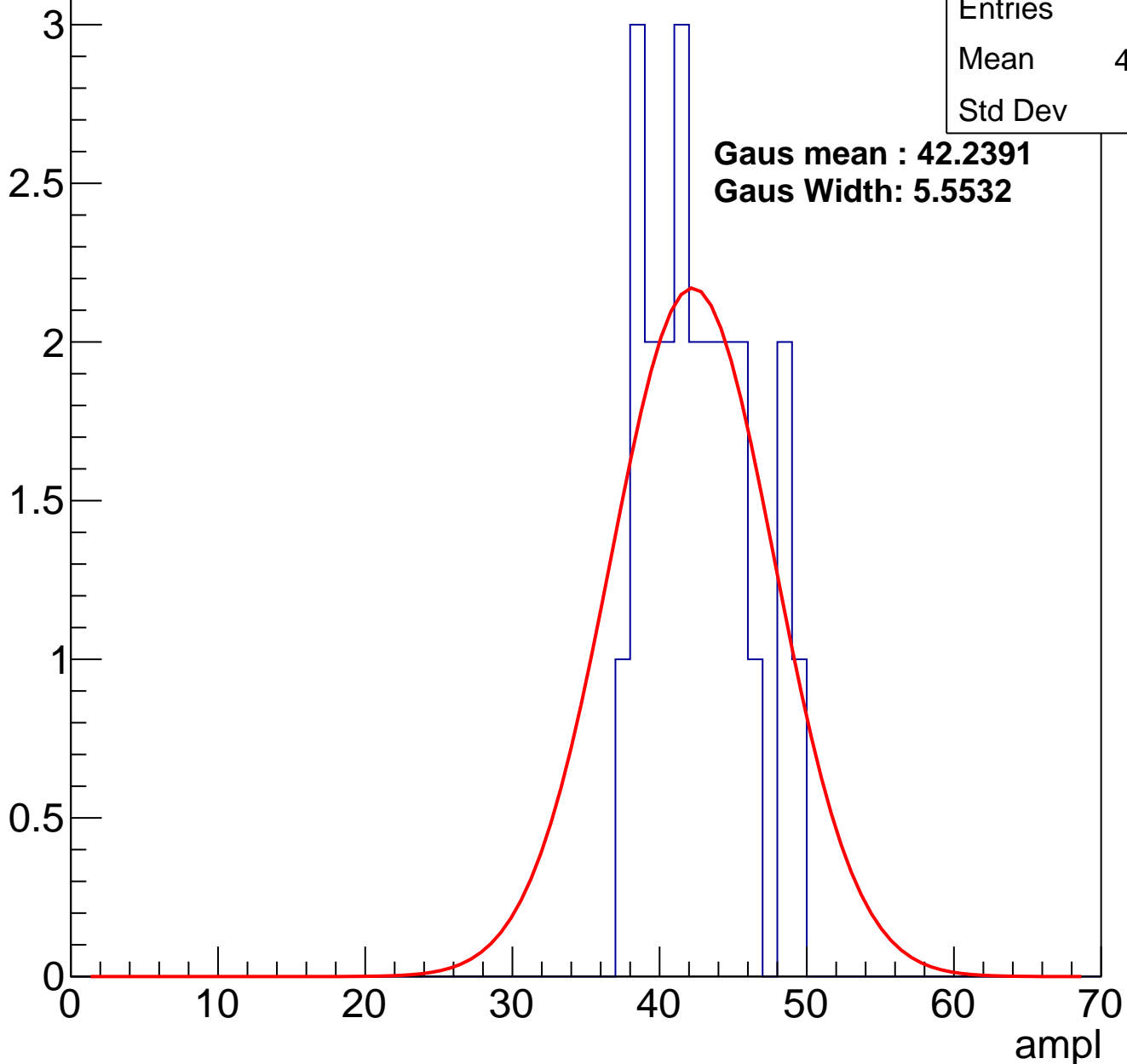
Entry



B1L103S, U15-ch104, adc2

calib_packv5_042523_0143.root, FC#7, port C2

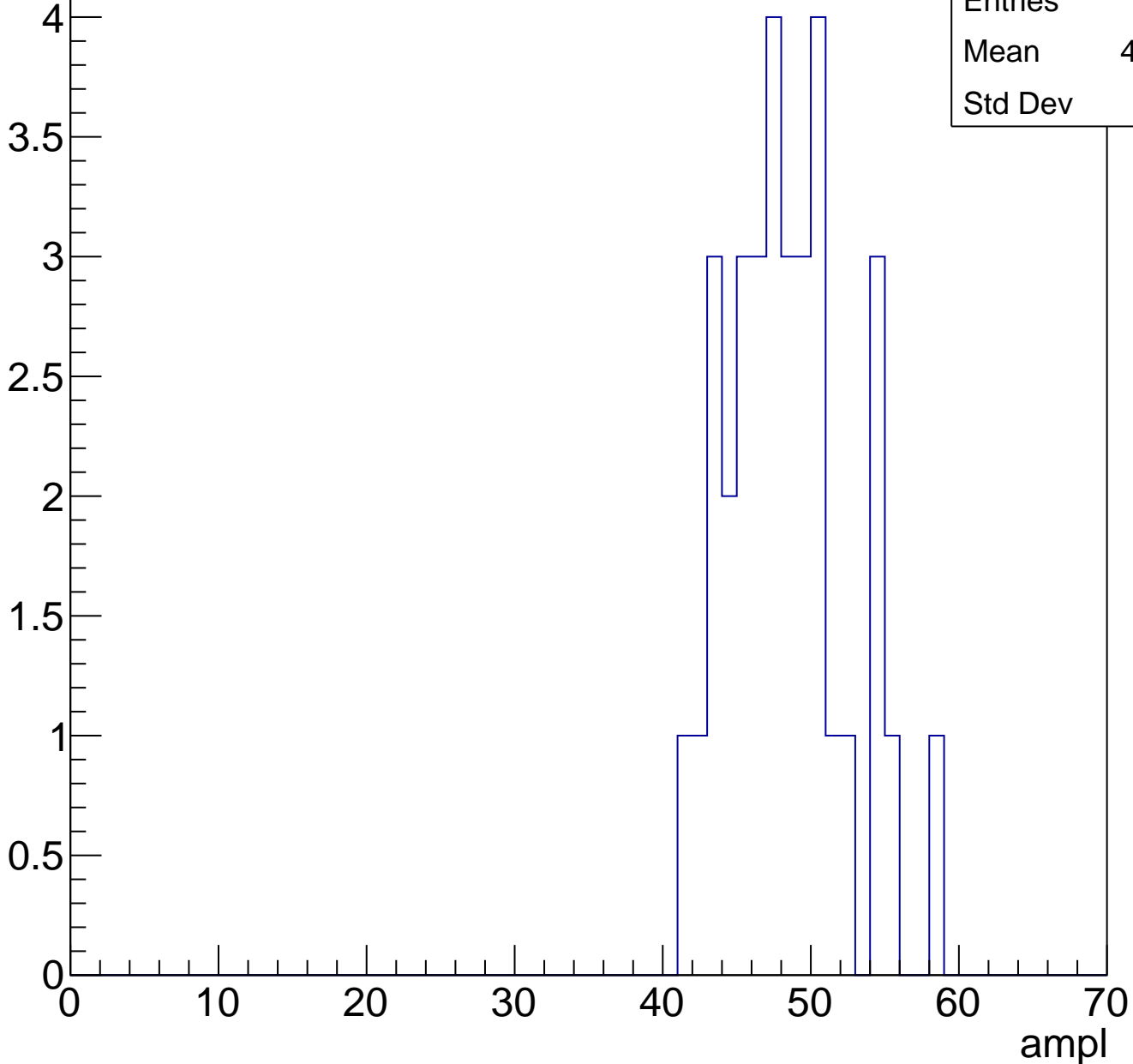
Entry



B1L103S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



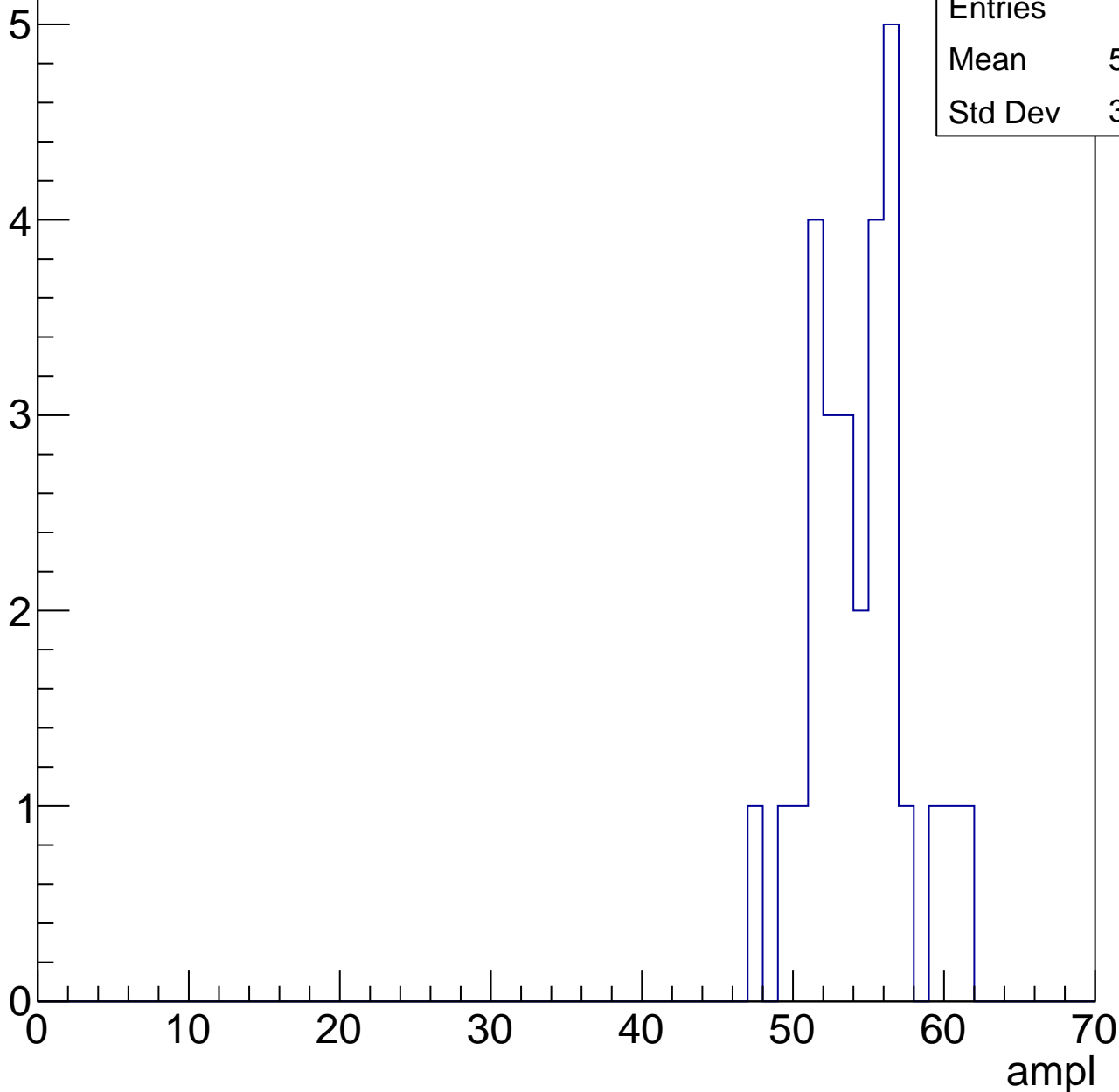
Entries	34
Mean	47.94
Std Dev	3.97

B1L103S, U15-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

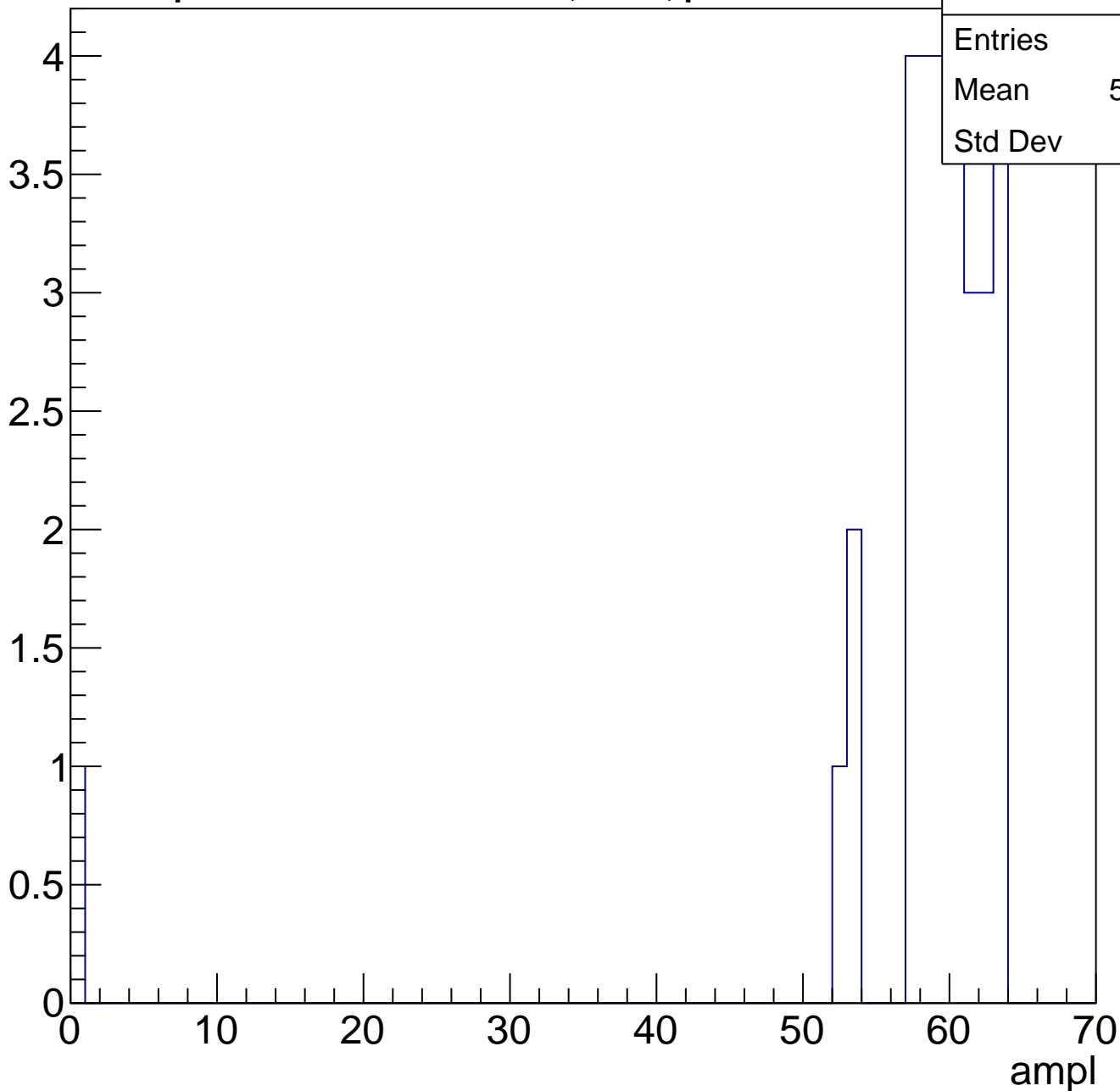
Entries	28
Mean	53.93
Std Dev	3.184



B1L103S, U15-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch104, adc6

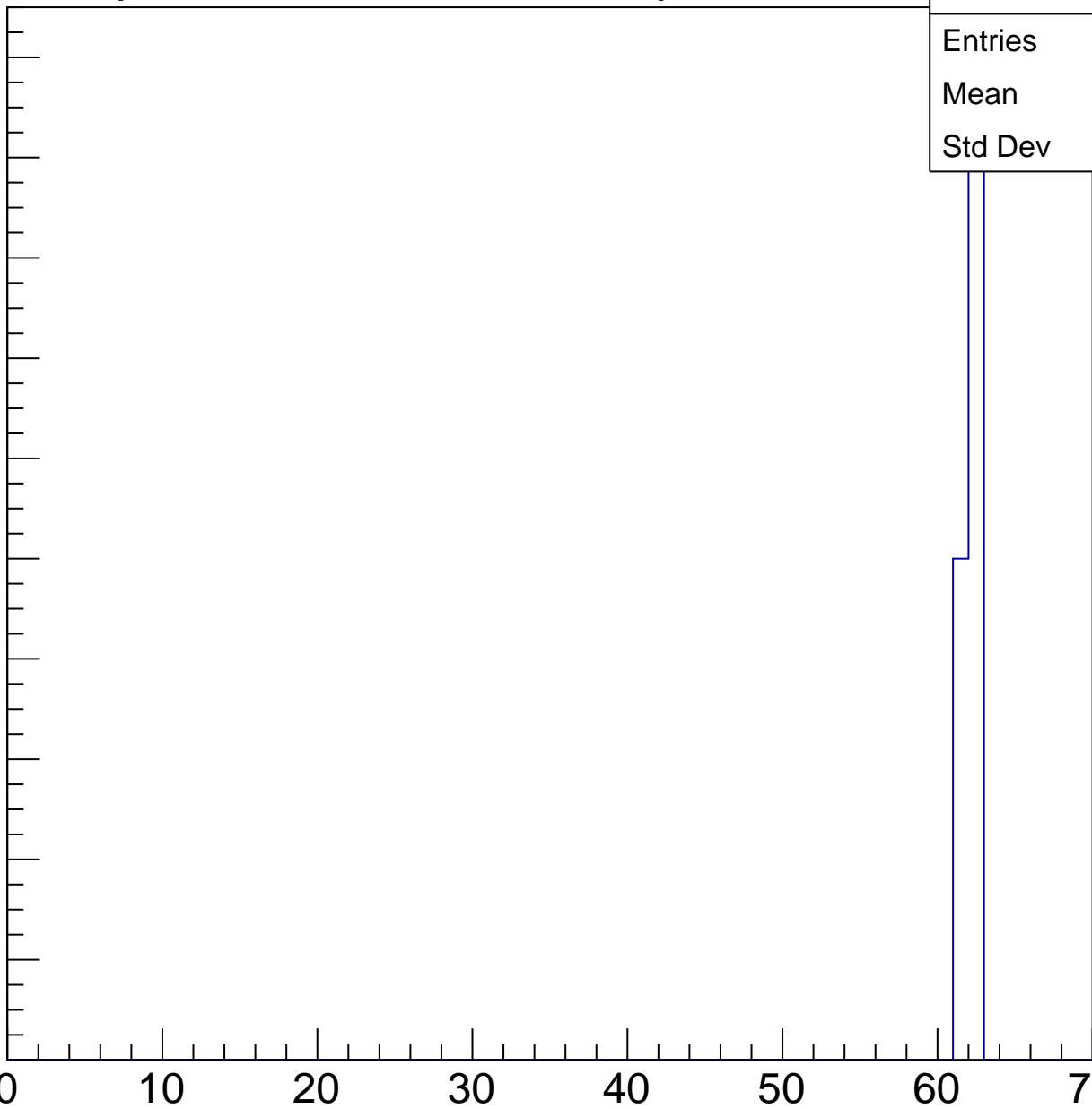
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl



B1L103S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U15-ch105, adc0

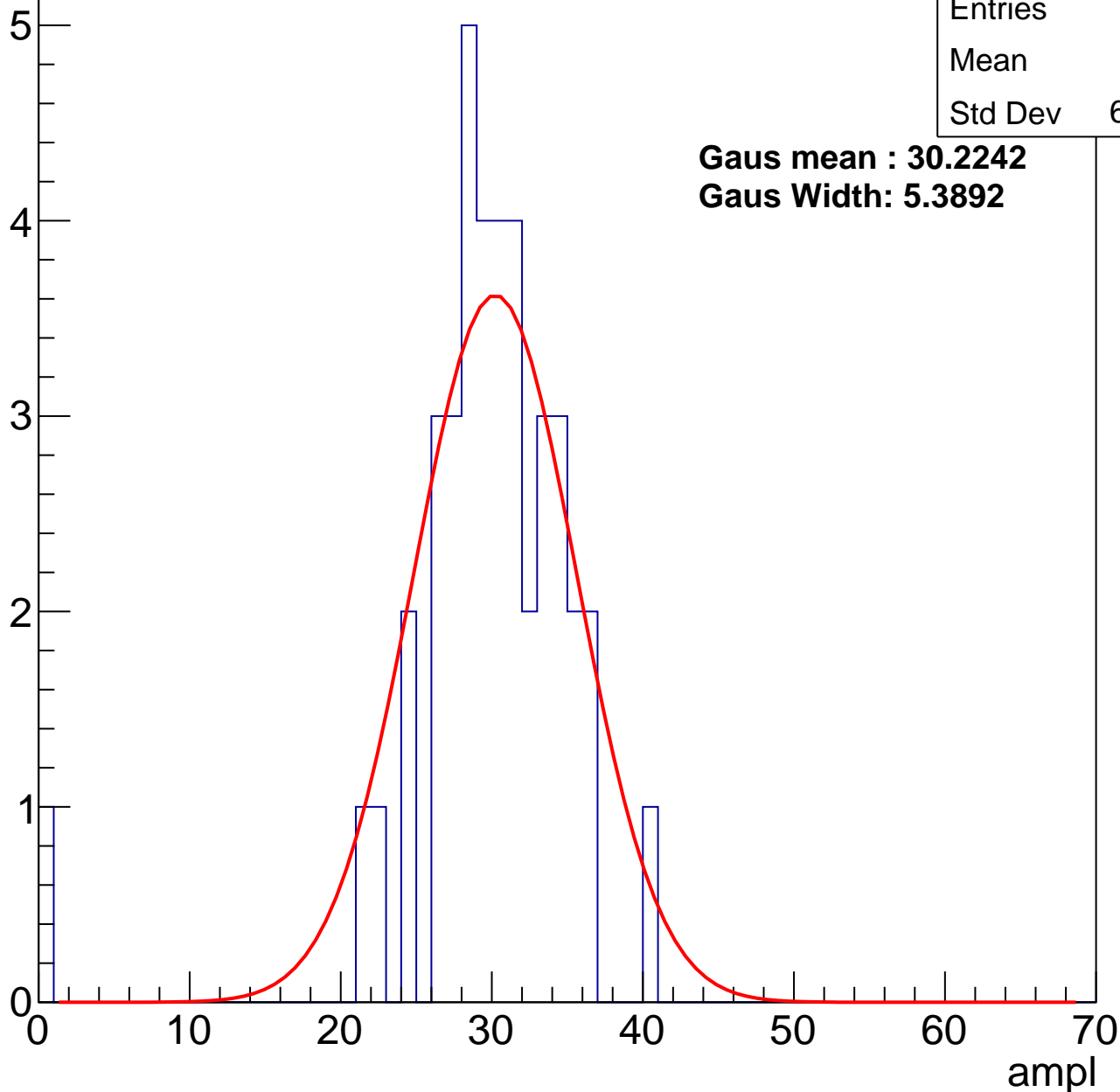
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	29.2
Std Dev	6.045

Gaus mean : 30.2242

Gaus Width: 5.3892



B1L103S, U15-ch105, adc1

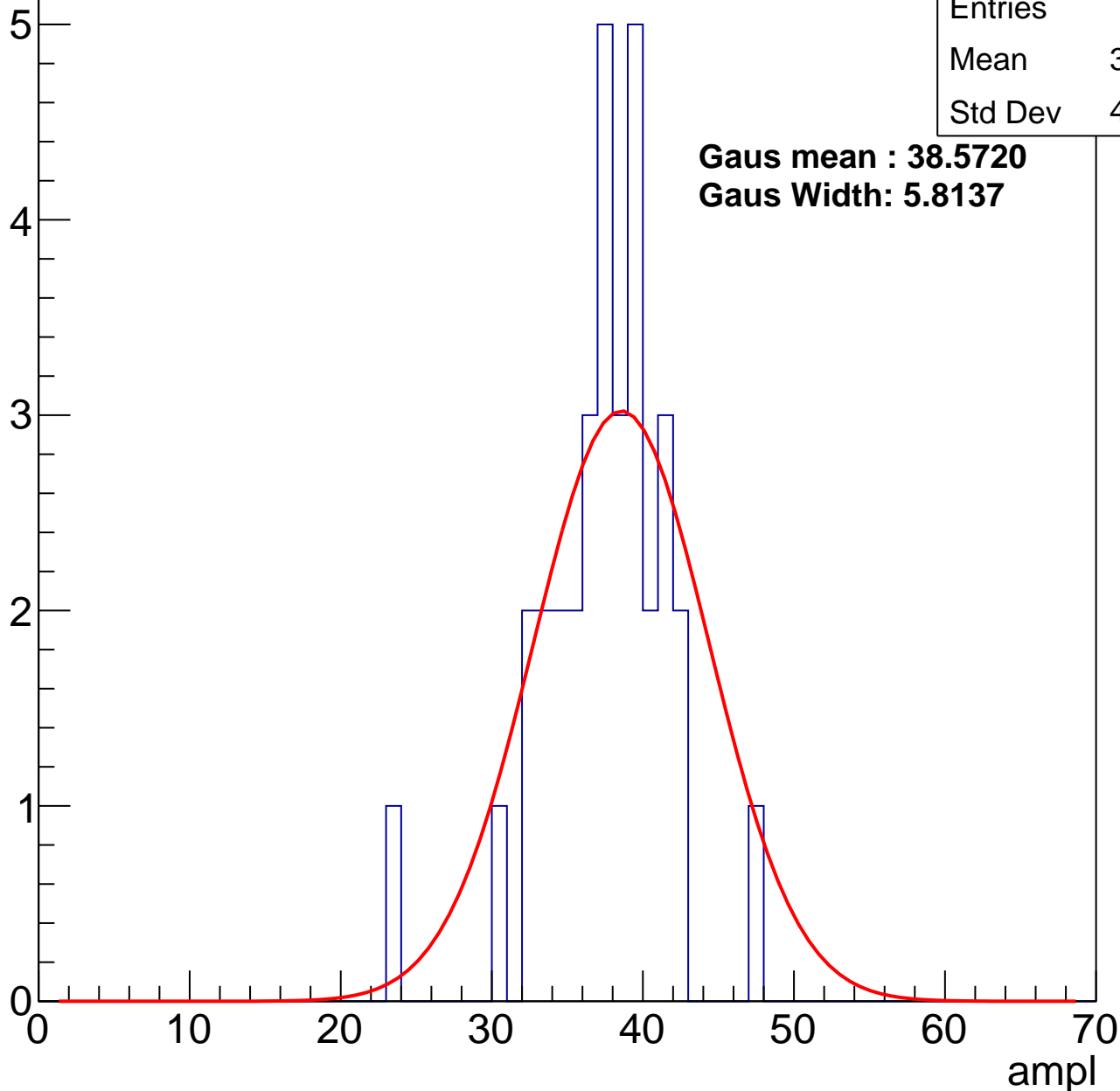
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	36.97
Std Dev	4.183

Gaus mean : 38.5720

Gaus Width: 5.8137



B1L103S, U15-ch105, adc2

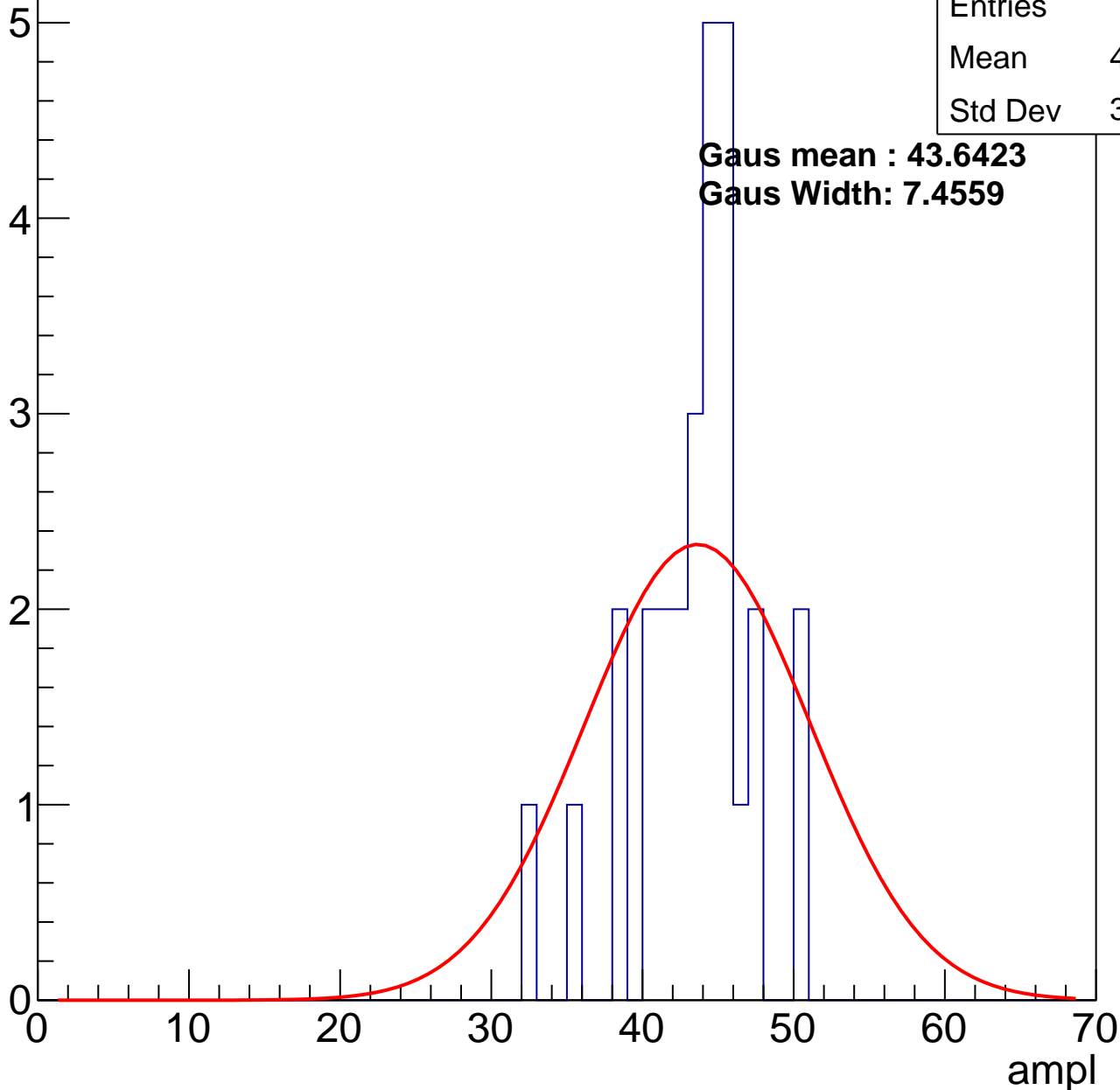
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	42.96
Std Dev	3.905

Gaus mean : 43.6423

Gaus Width: 7.4559

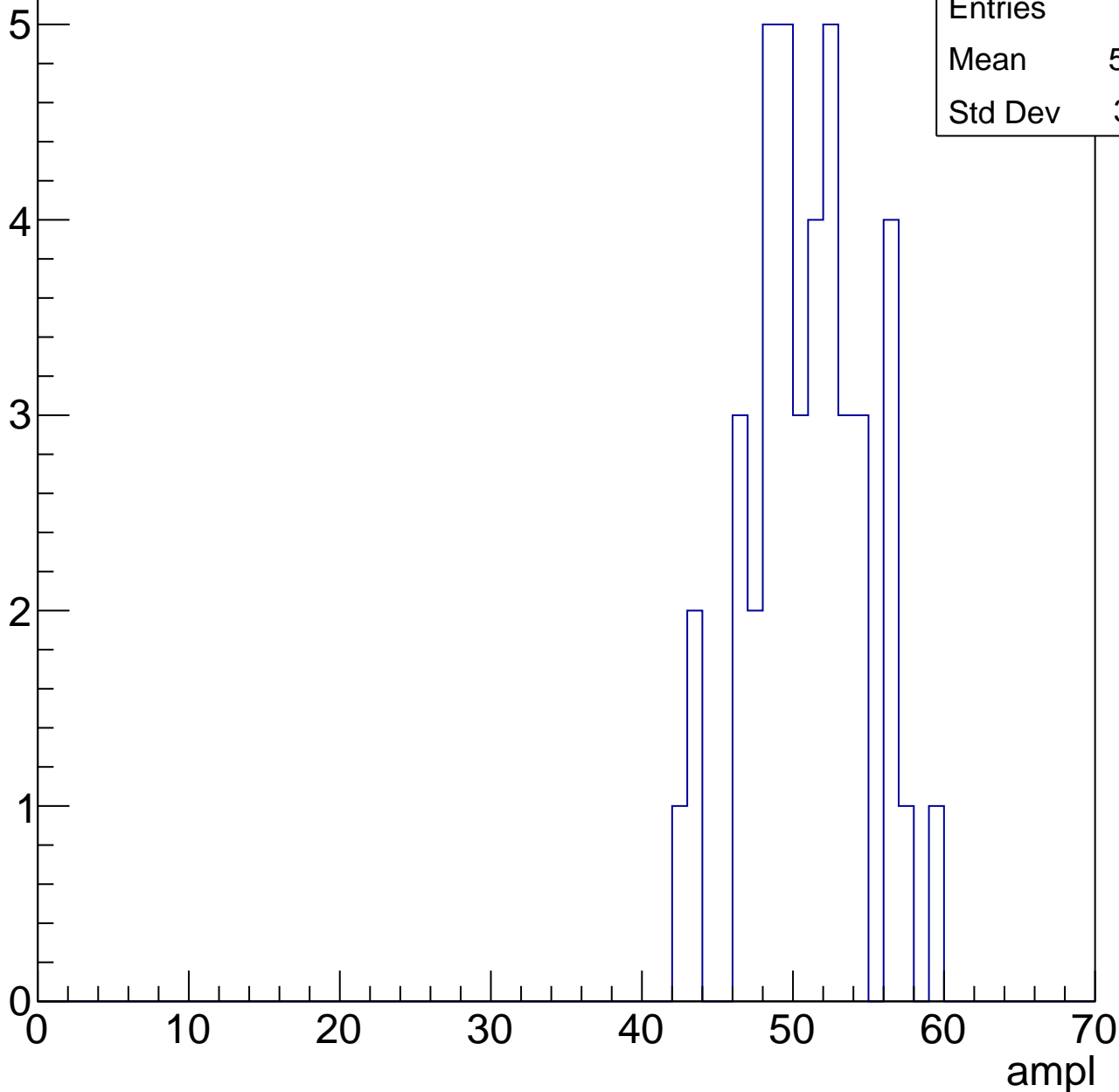


B1L103S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

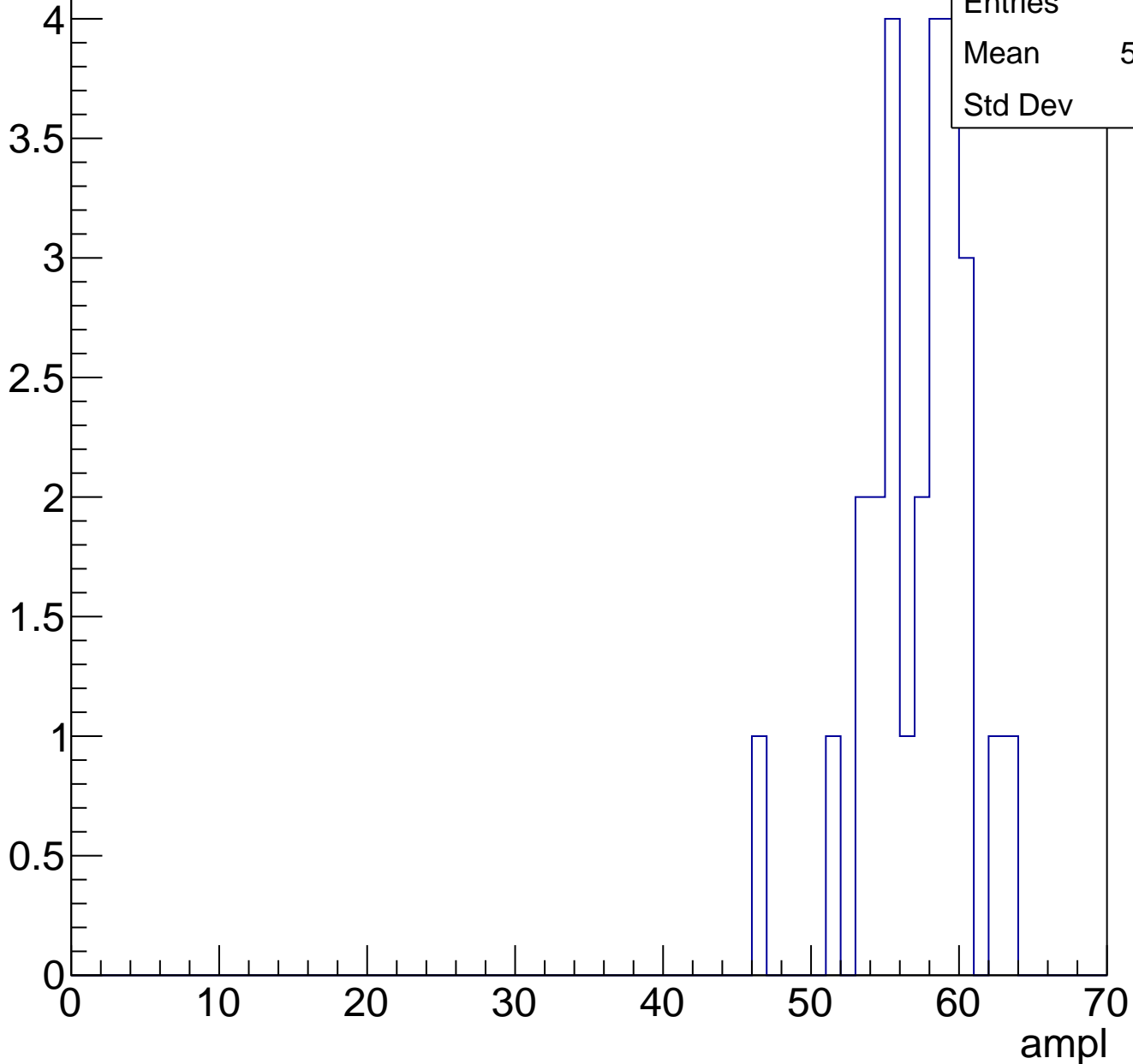
Entries	42
Mean	50.48
Std Dev	3.831



B1L103S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

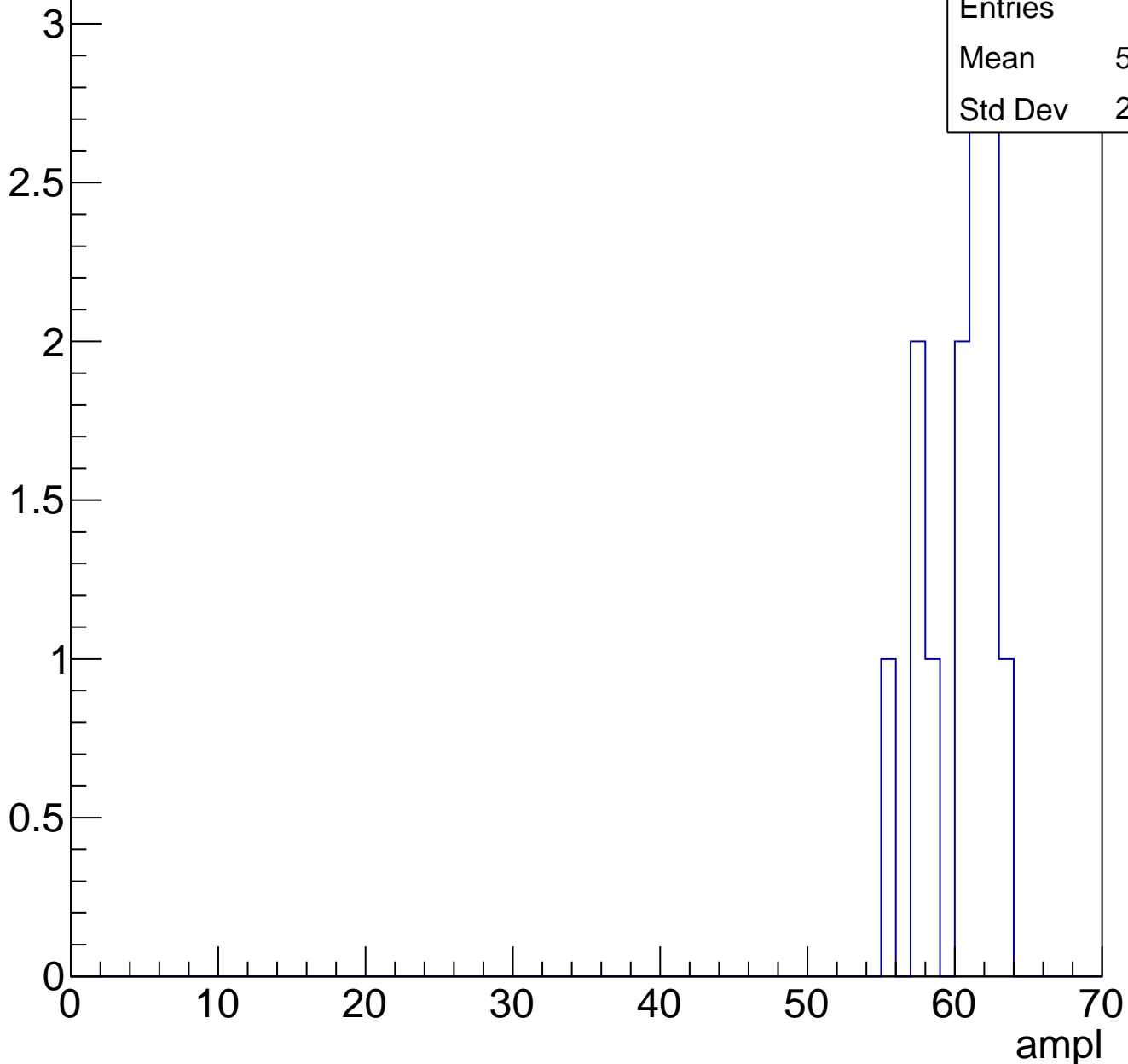
Entry



B1L103S, U15-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

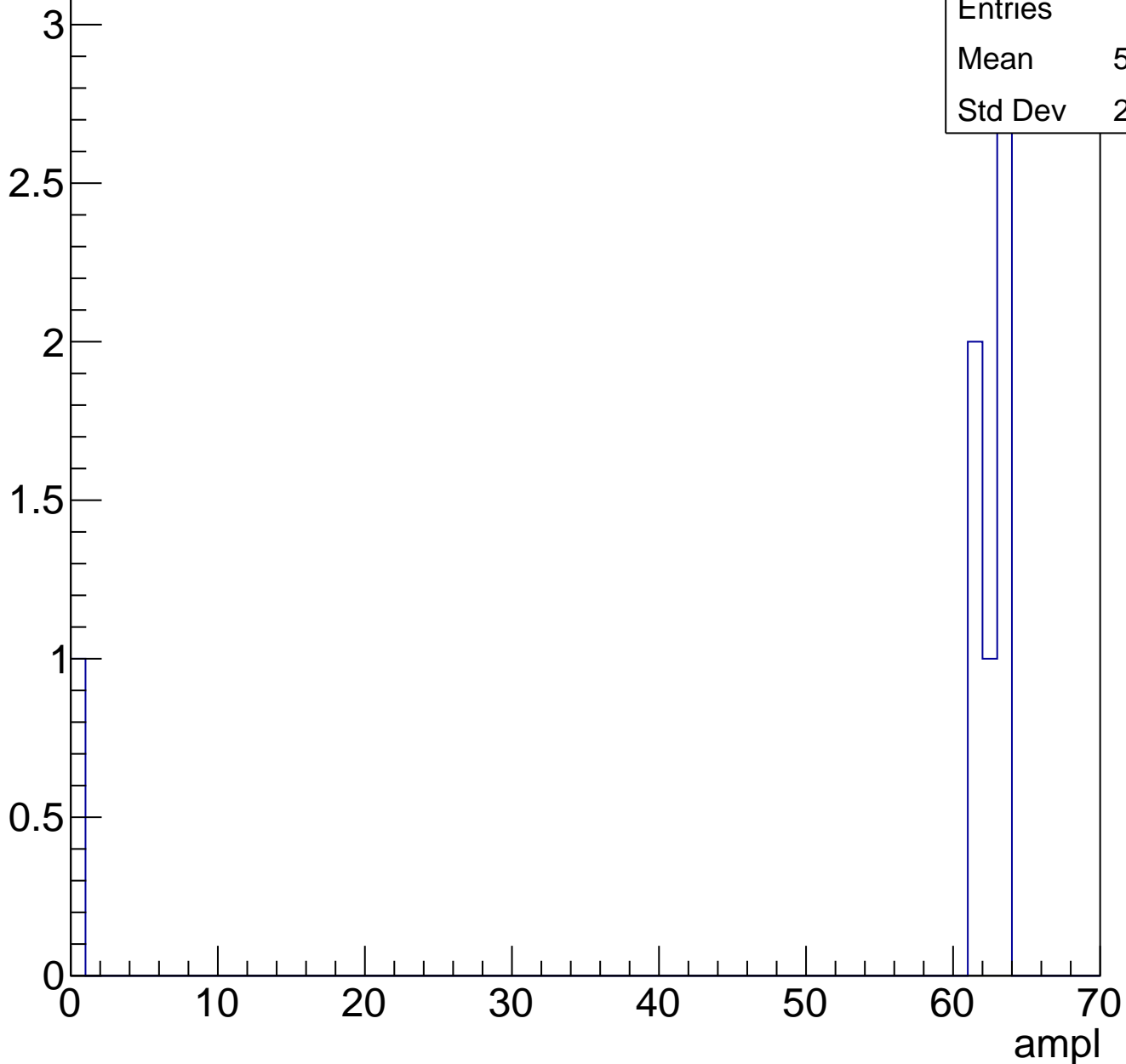


Entries	13
Mean	59.92
Std Dev	2.336

B1L103S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch106, adc0

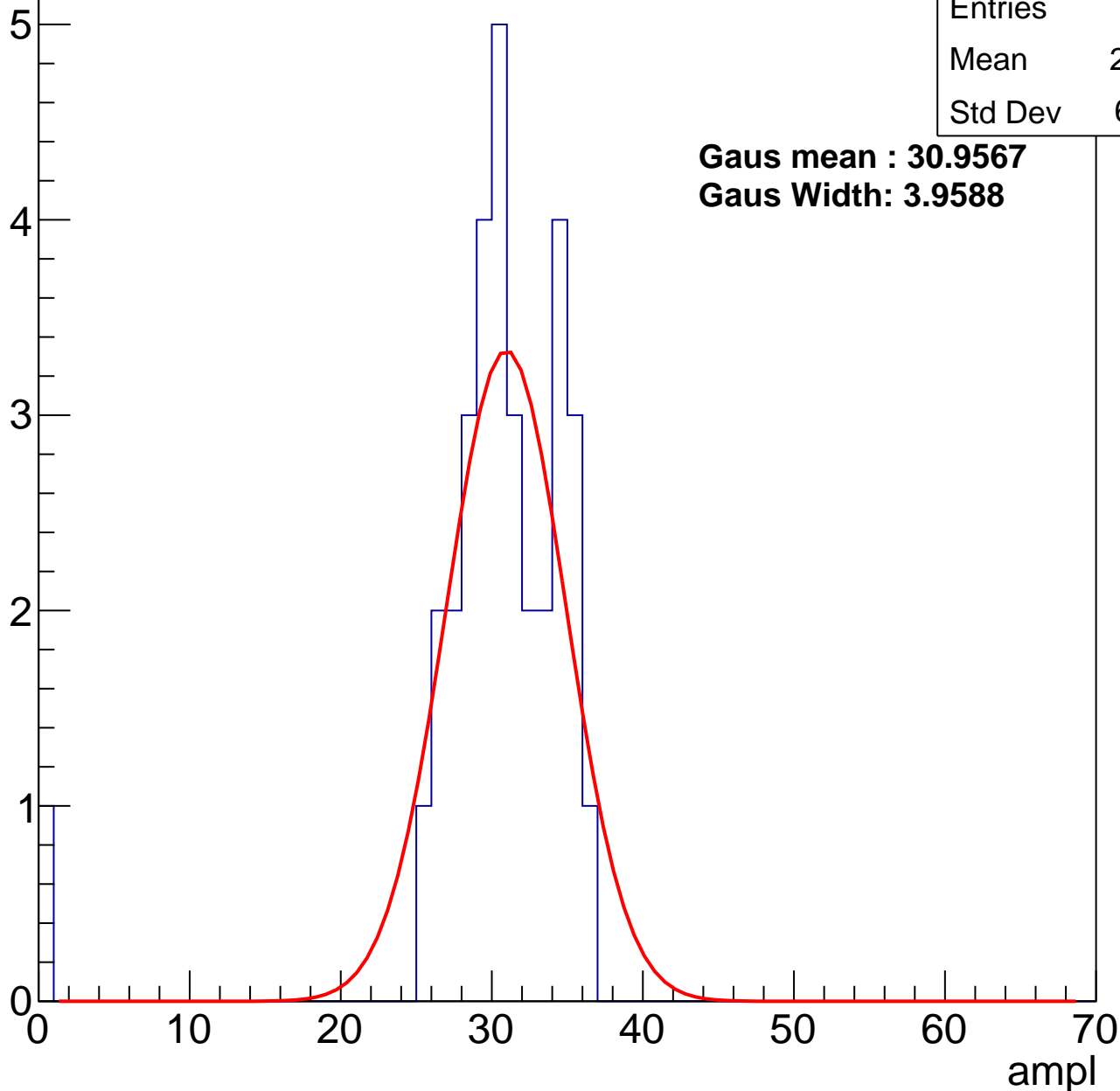
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	29.73
Std Dev	6.011

Gaus mean : 30.9567

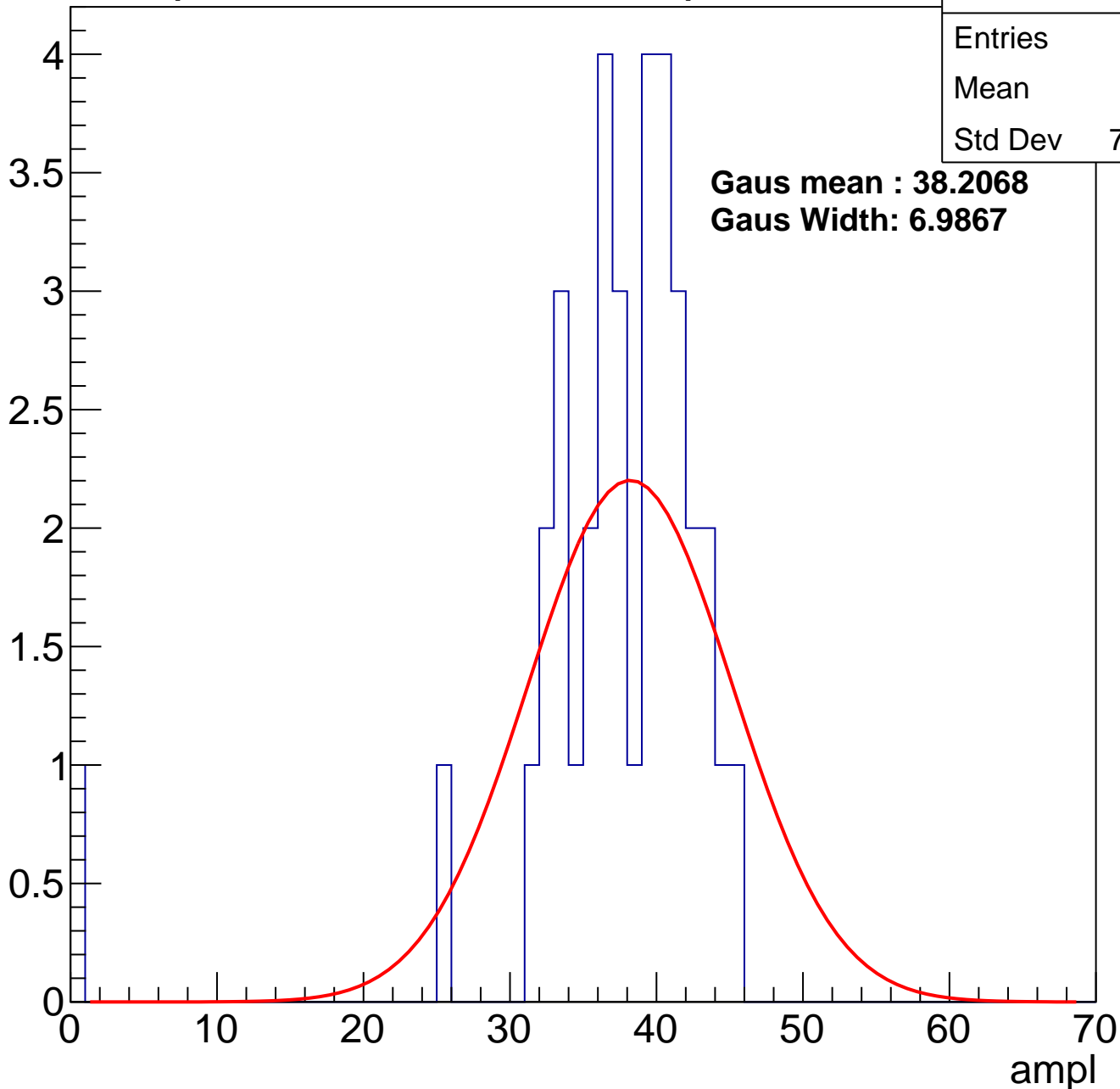
Gaus Width: 3.9588



B1L103S, U15-ch106, adc1

calib_packv5_042523_0143.root, FC#7, port C2

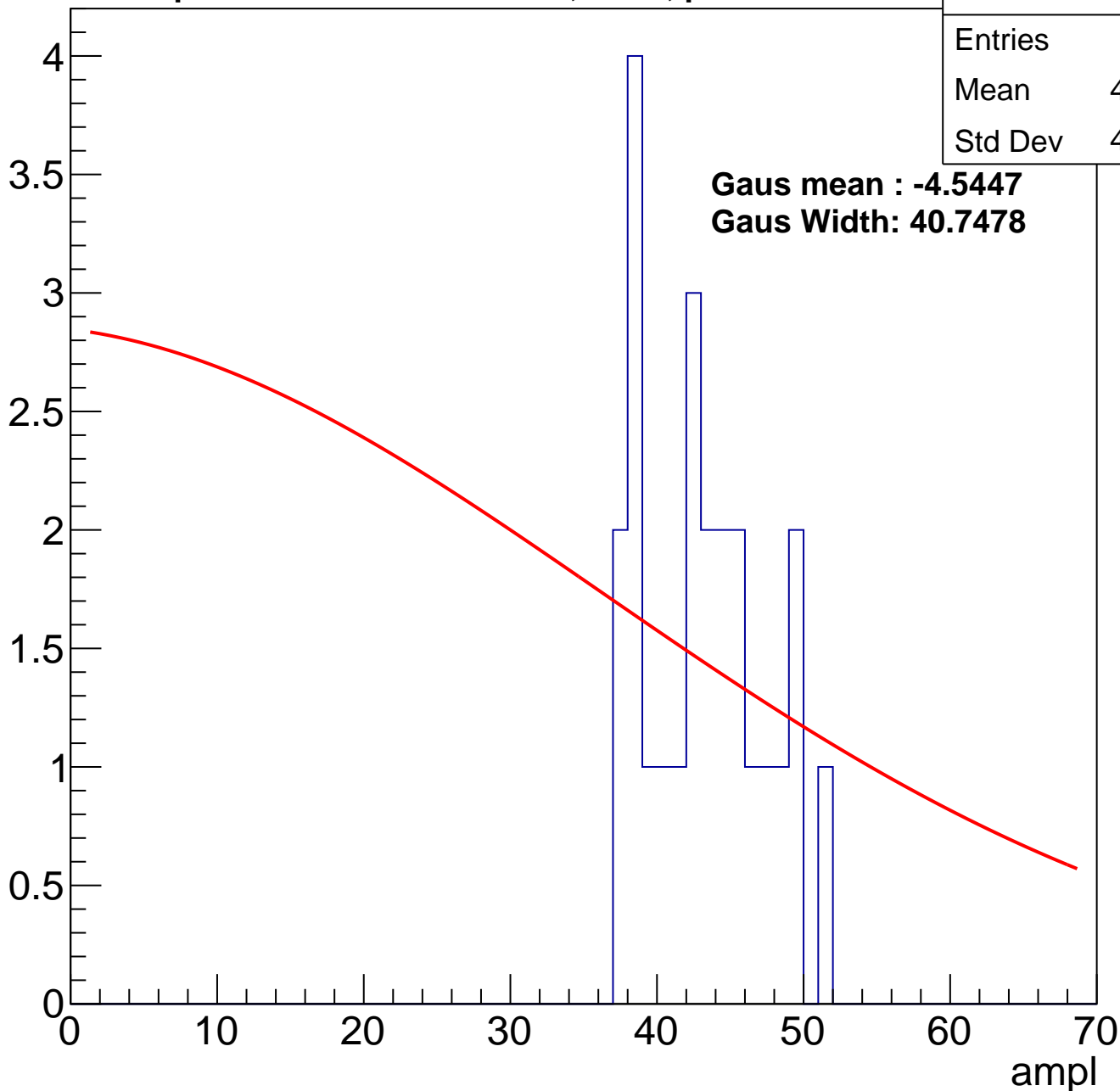
Entry



B1L103S, U15-ch106, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	37
Mean	49.57
Std Dev	3.983

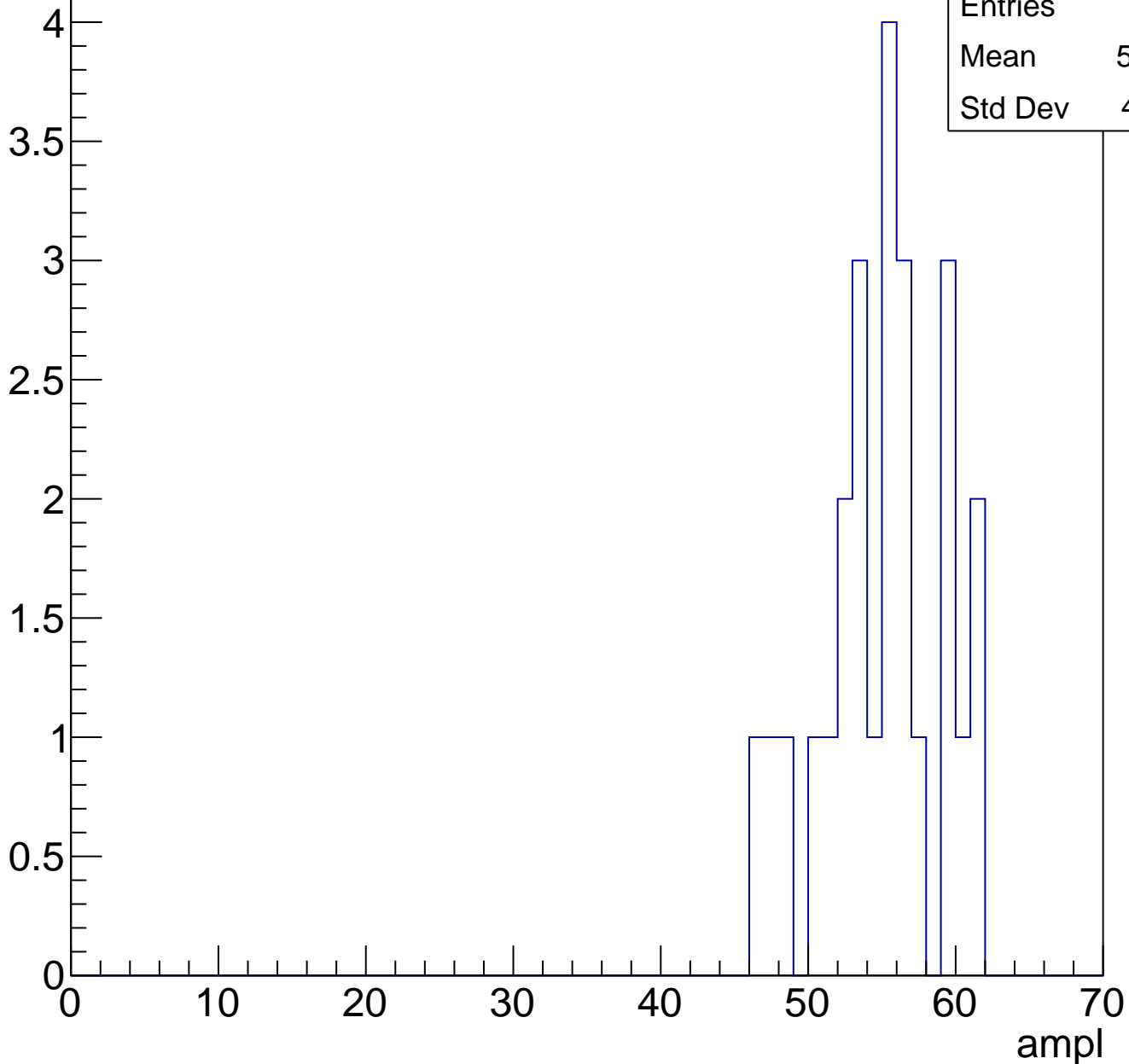
ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	24
Mean	59.83
Std Dev	2.321

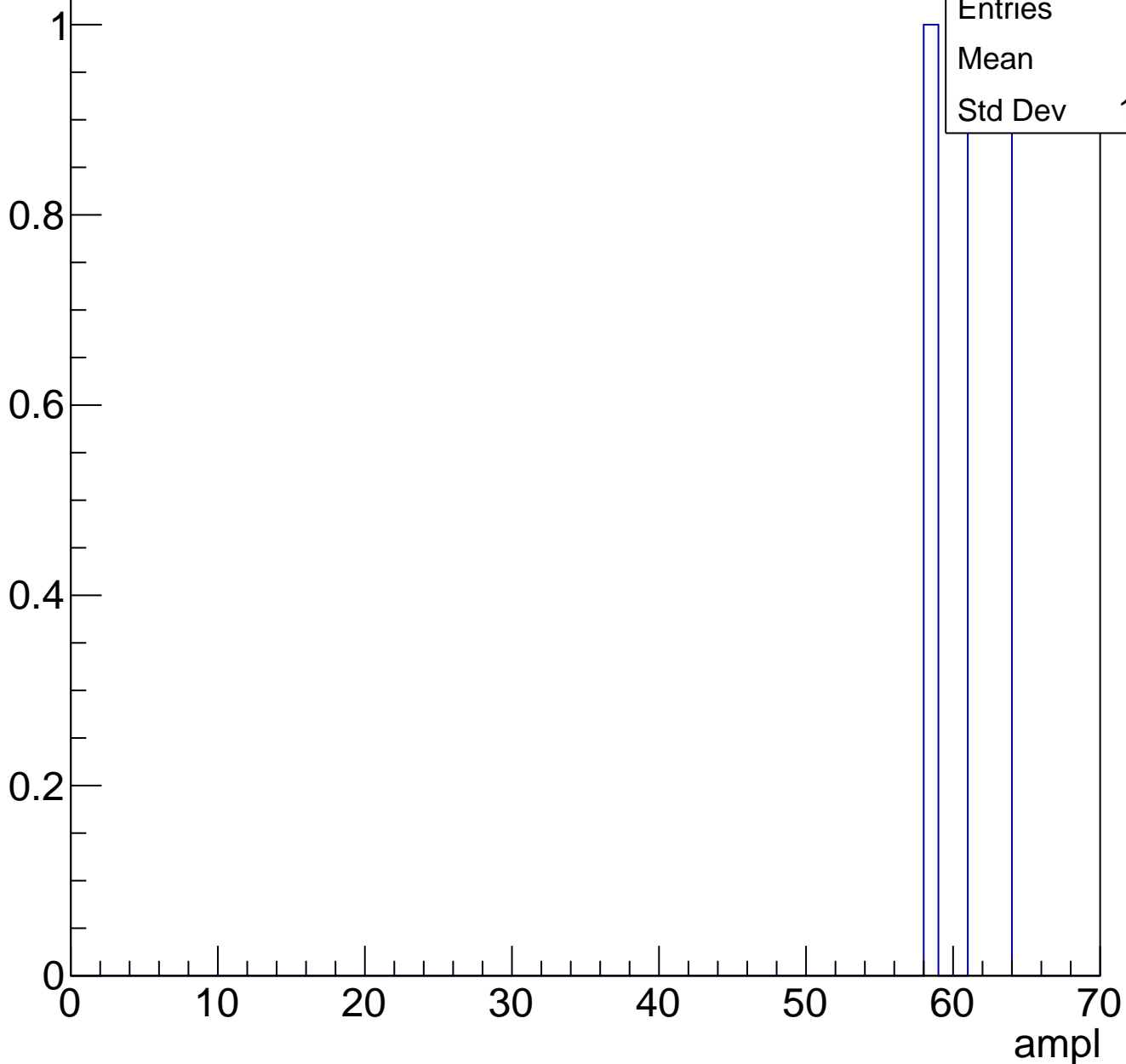
ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch107, adc0

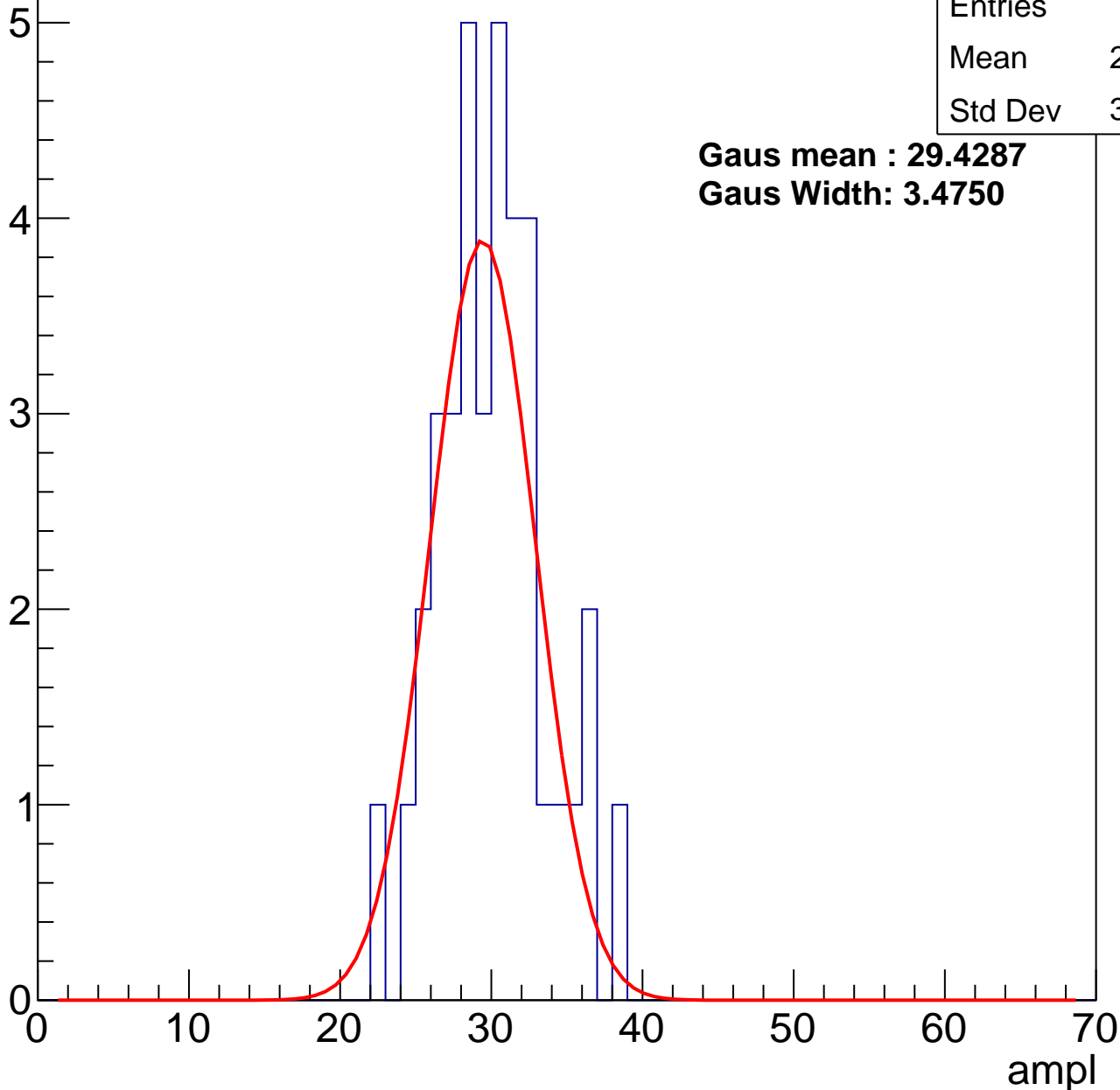
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	29.62
Std Dev	3.482

Gaus mean : 29.4287

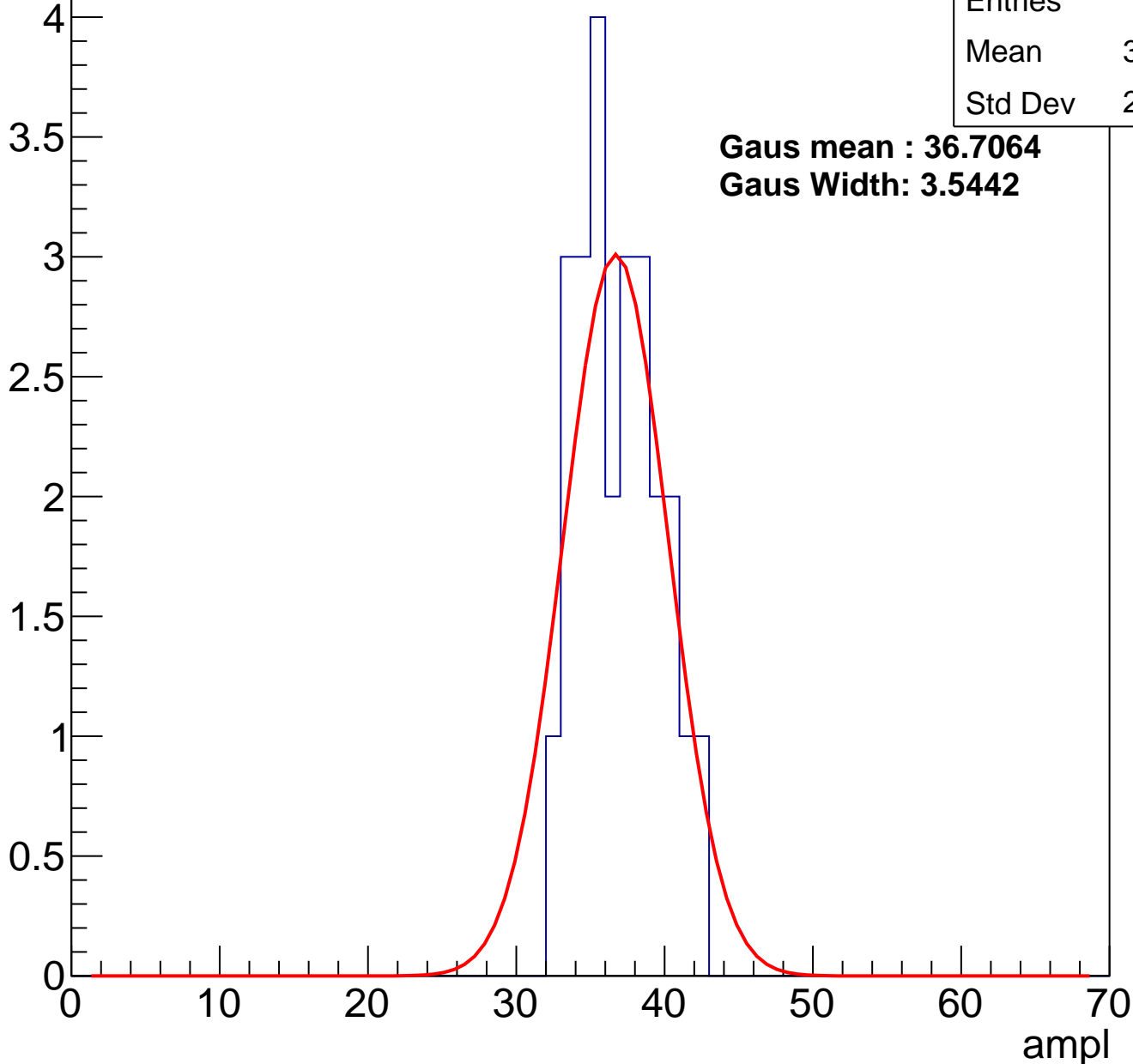
Gaus Width: 3.4750



B1L103S, U15-ch107, adc1

calib_packv5_042523_0143.root, FC#7, port C2

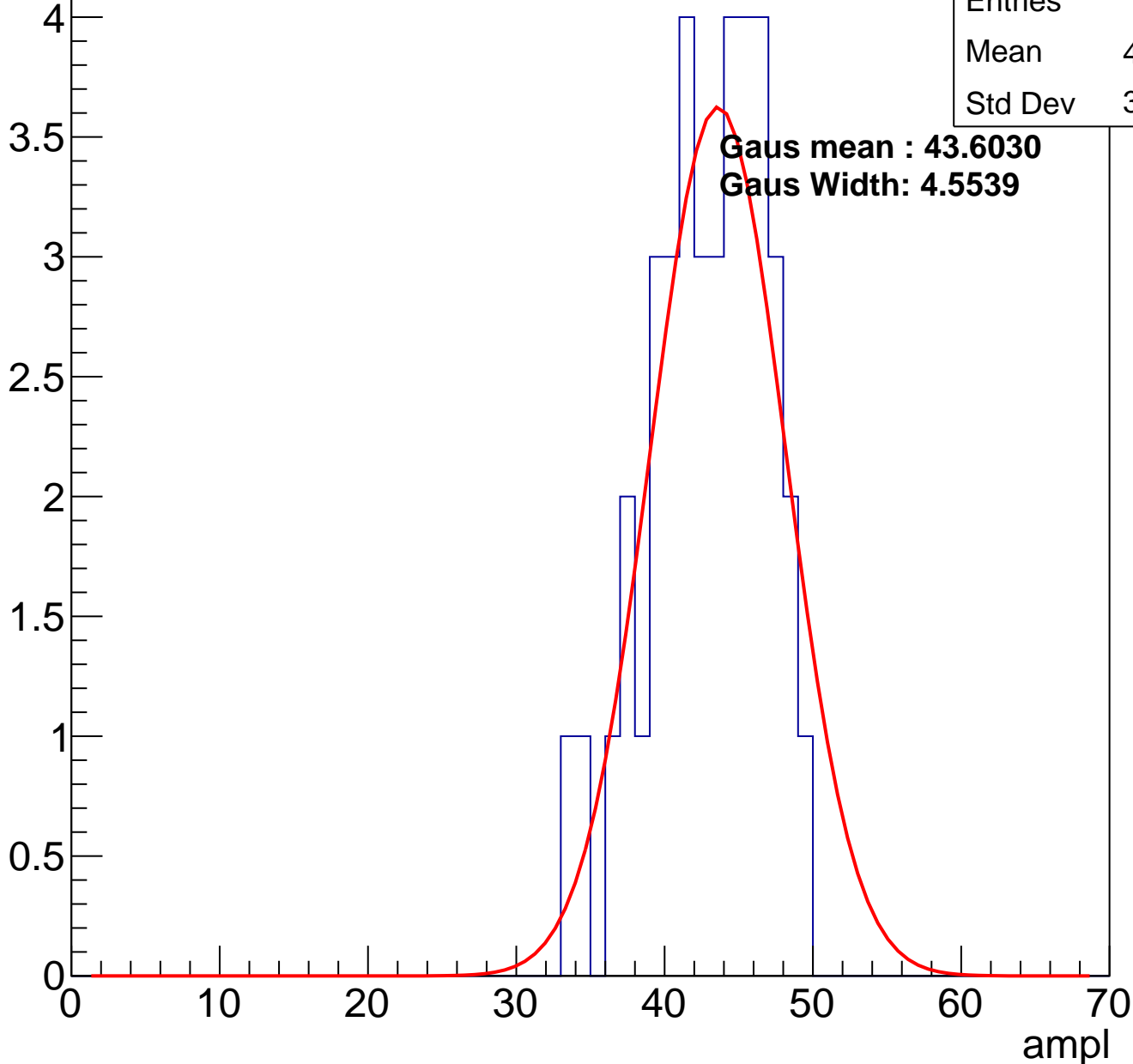
Entry



B1L103S, U15-ch107, adc2

calib_packv5_042523_0143.root, FC#7, port C2

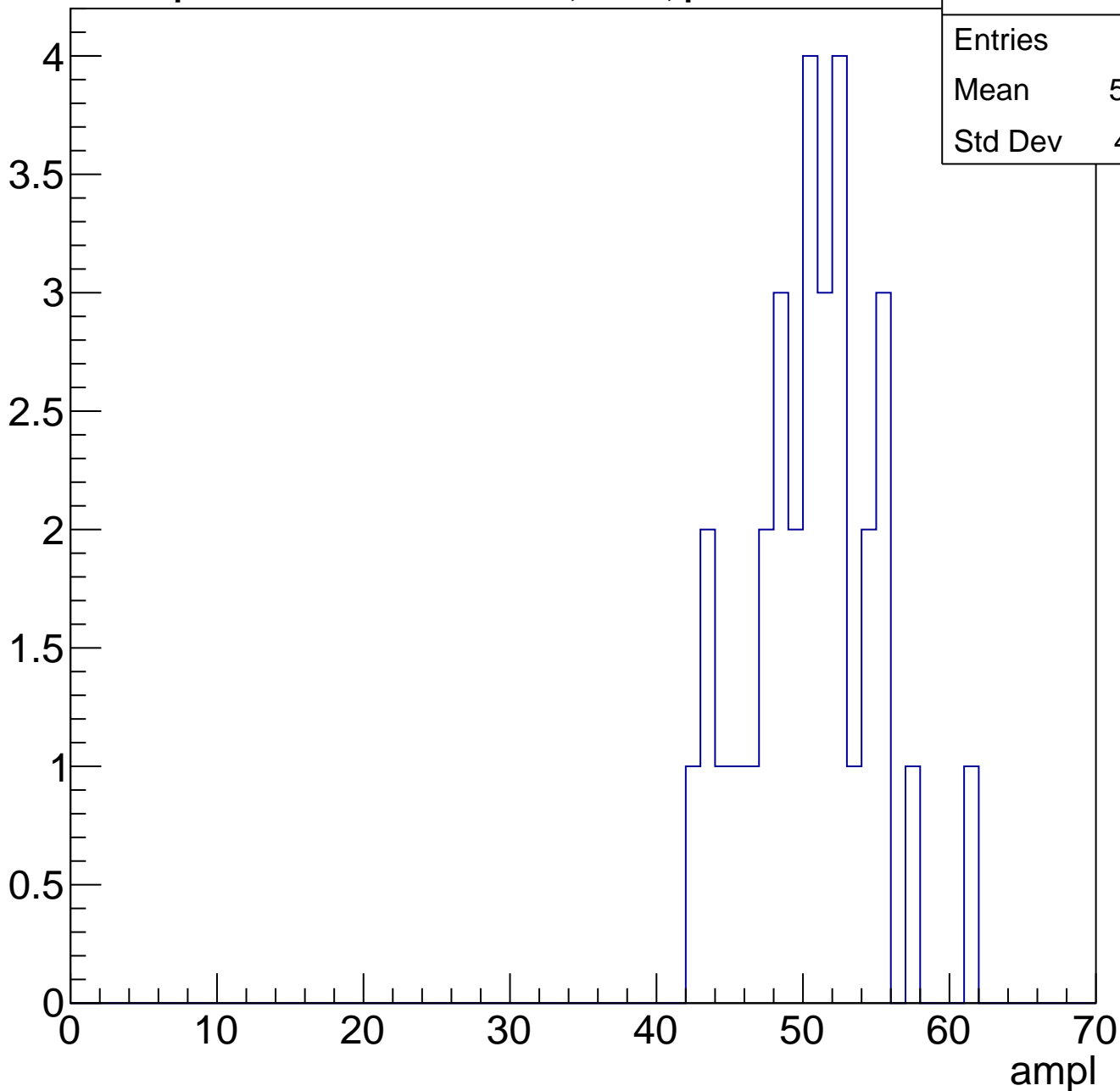
Entry



B1L103S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

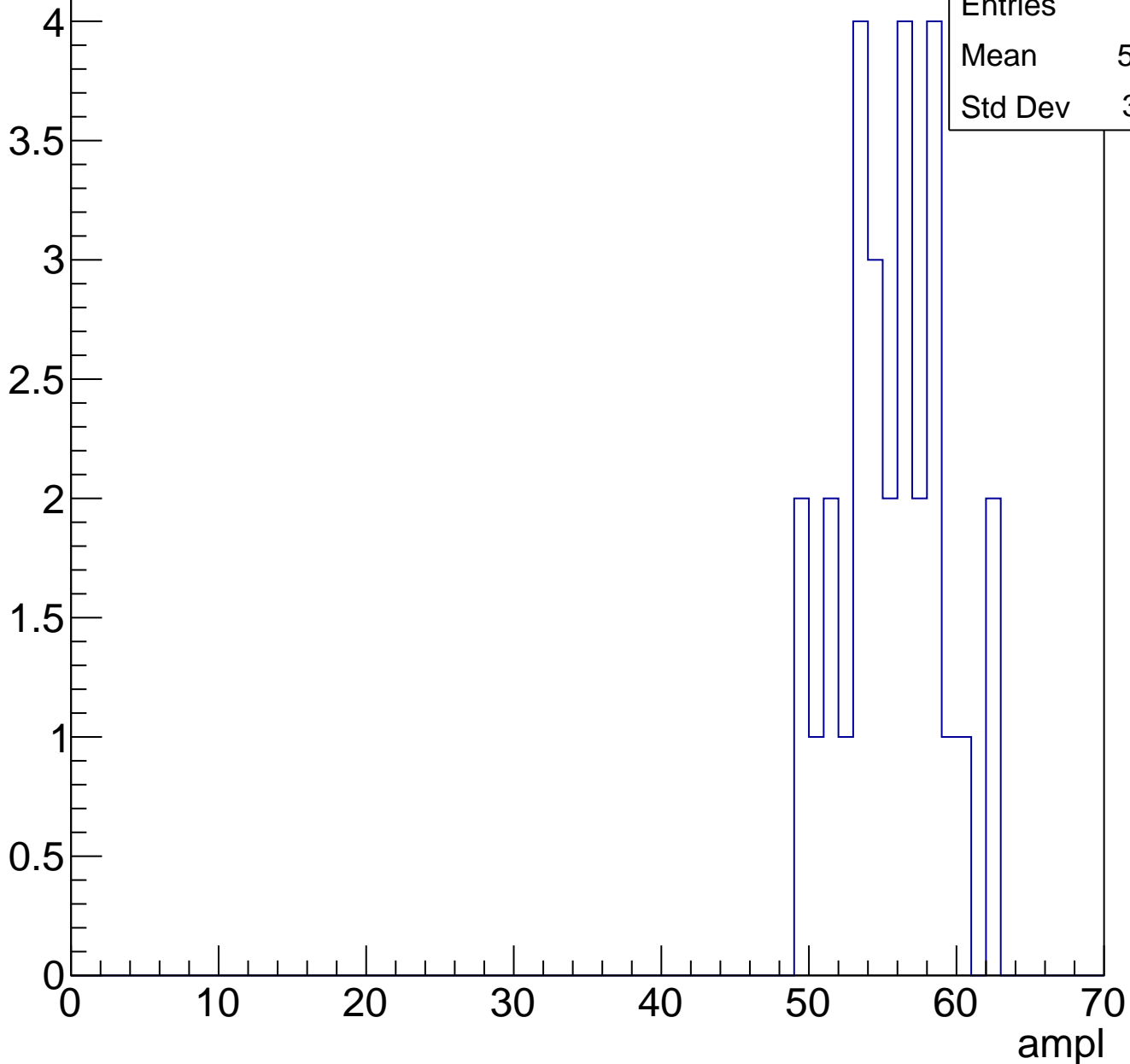
Entry



B1L103S, U15-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

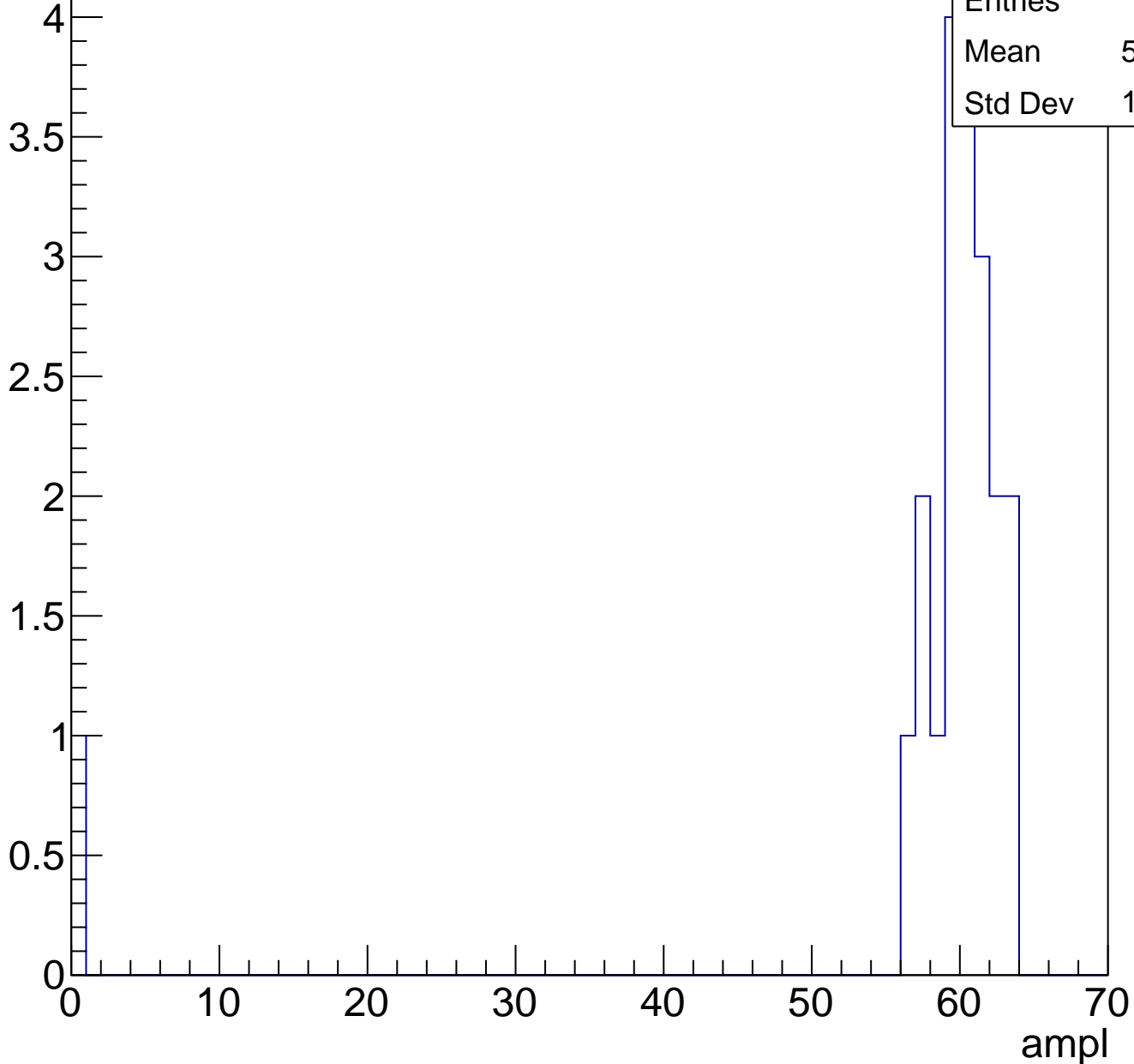
Entry



B1L103S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	62.4
Std Dev	0.8

B1L103S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

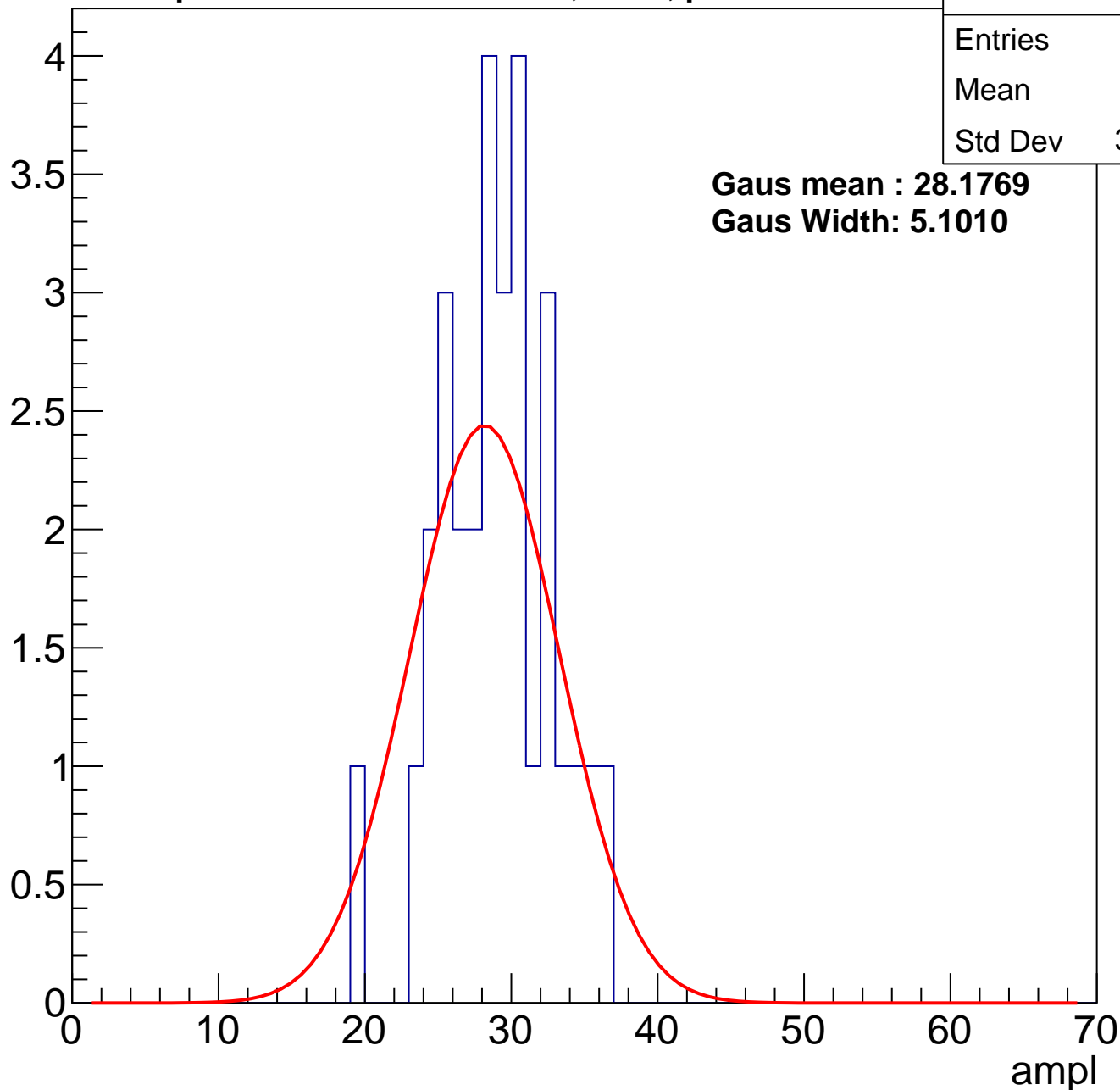


Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch108, adc0

calib_packv5_042523_0143.root, FC#7, port C2

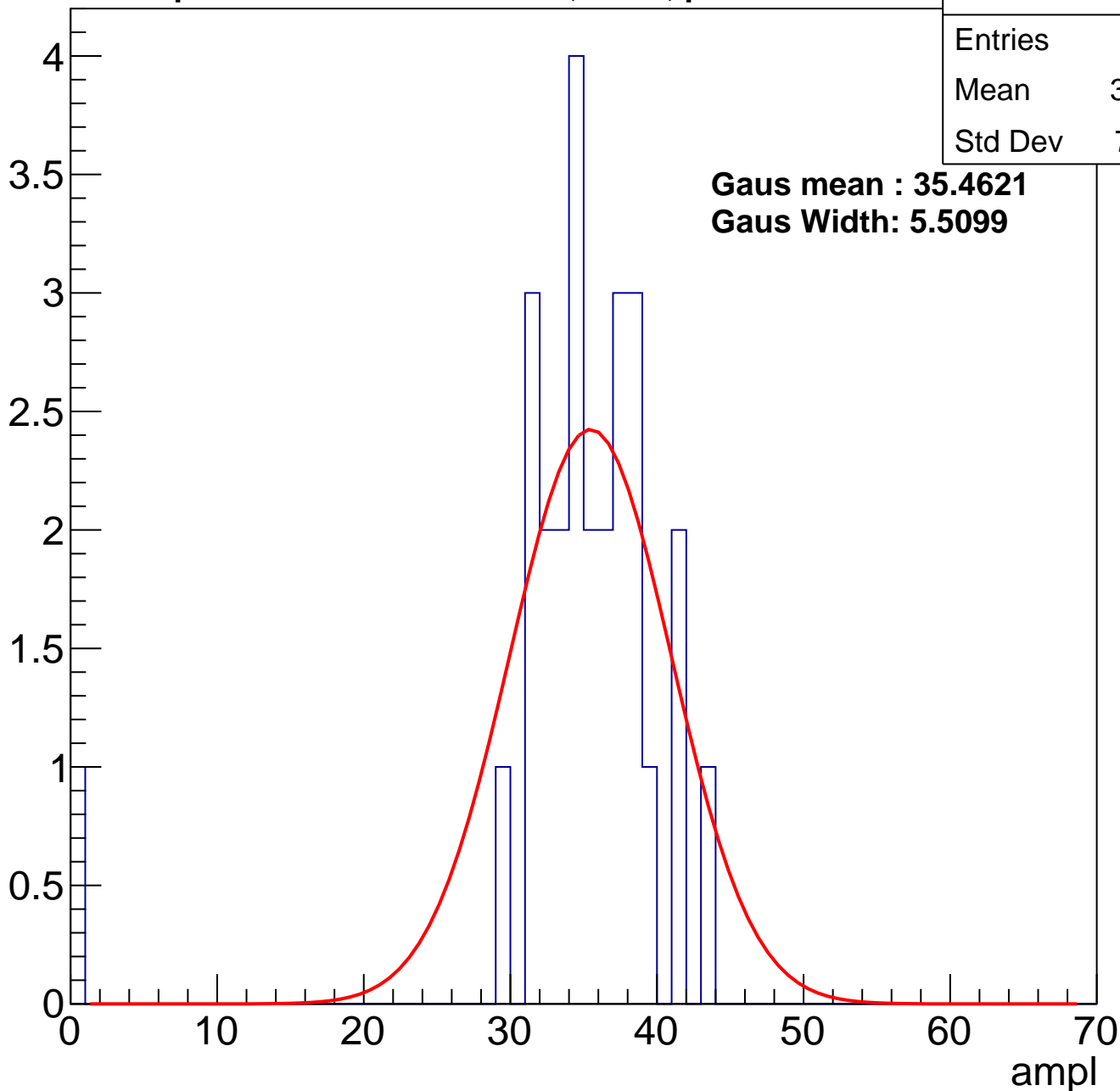
Entry



B1L103S, U15-ch108, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch108, adc2

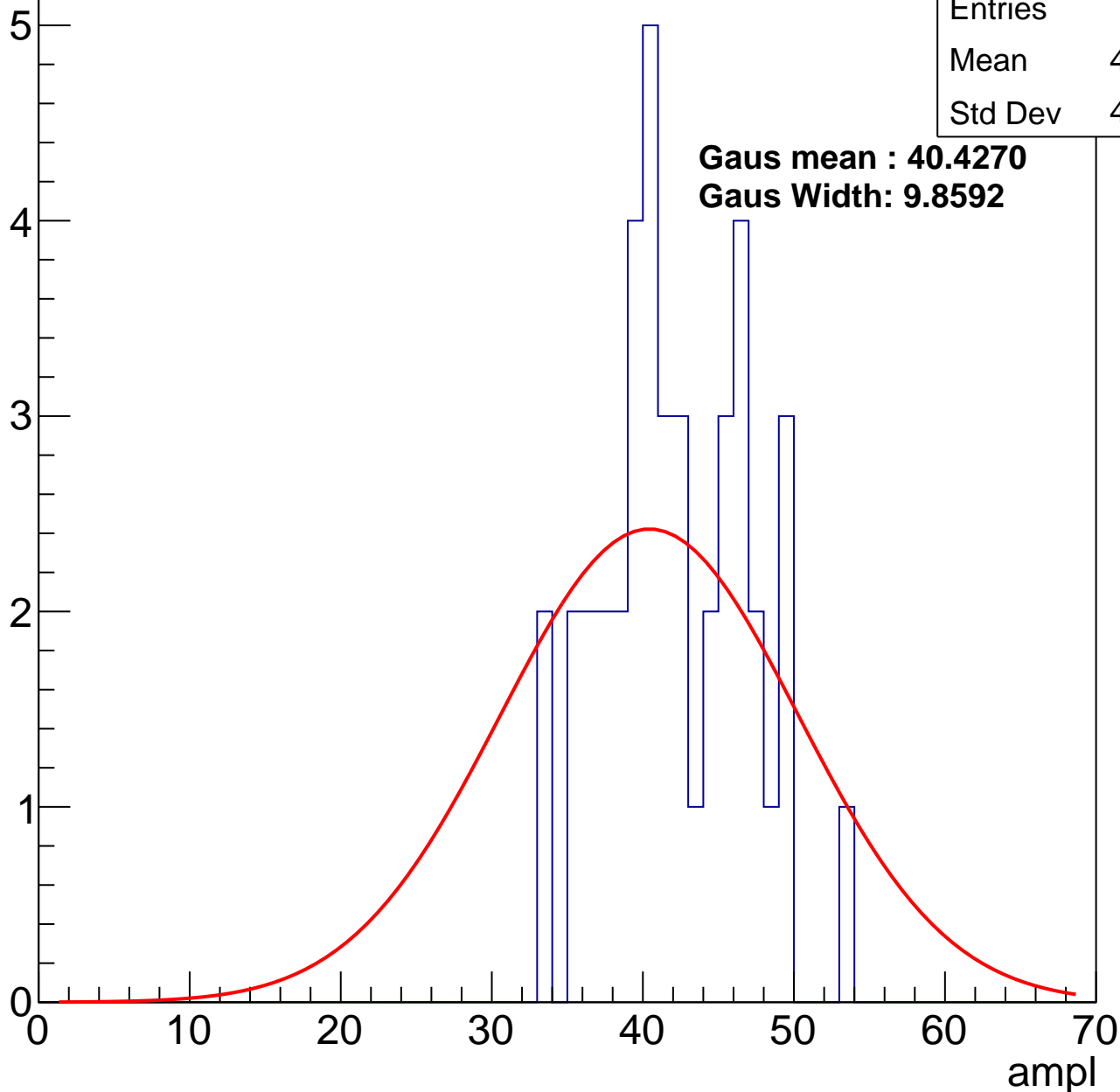
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	41.79
Std Dev	4.693

Gaus mean : 40.4270

Gaus Width: 9.8592



B1L103S, U15-ch108, adc3

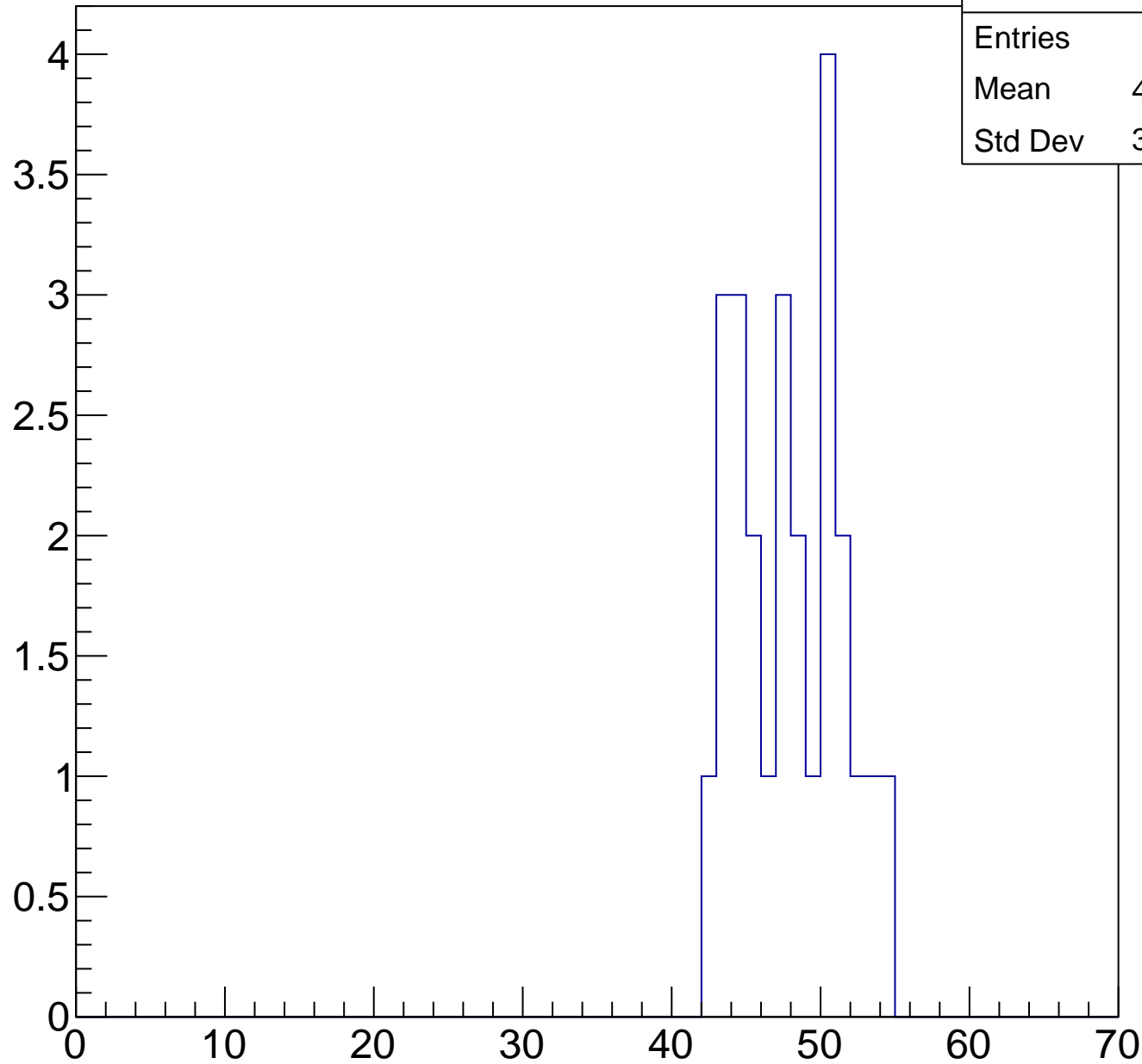
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	25
Mean	47.44
Std Dev	3.395

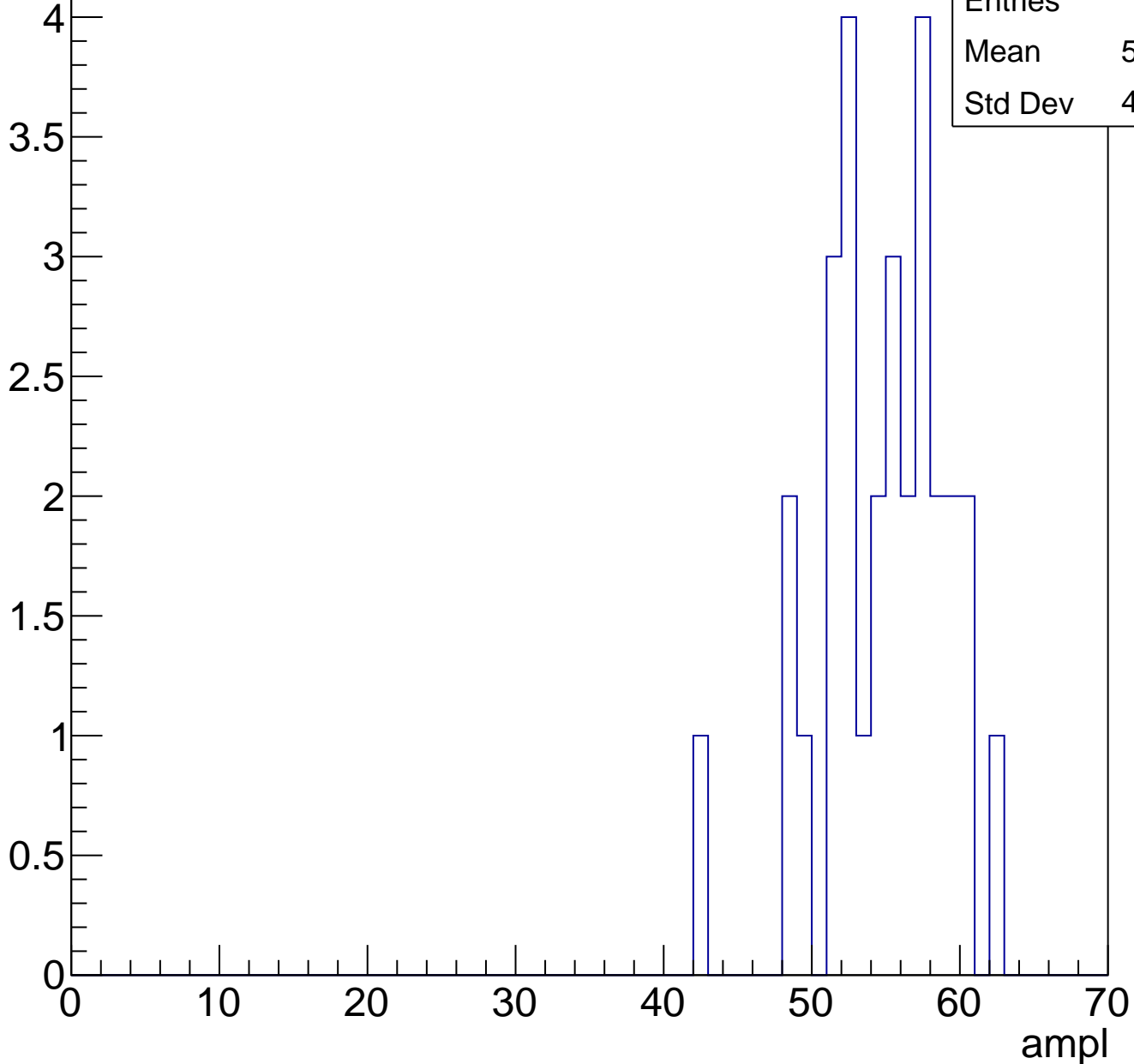
ampl



B1L103S, U15-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

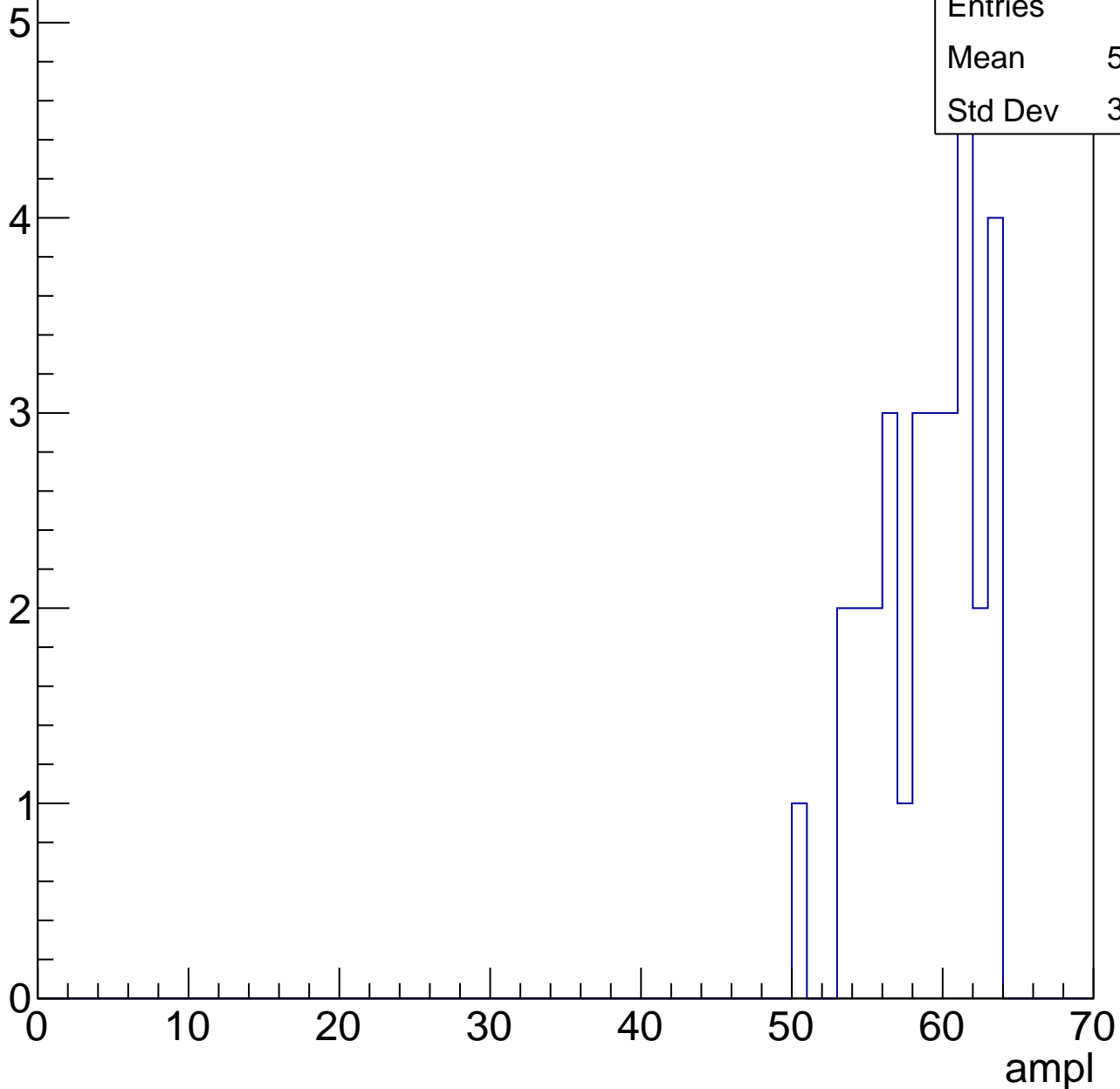


B1L103S, U15-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	58.42
Std Dev	3.425



B1L103S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L103S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

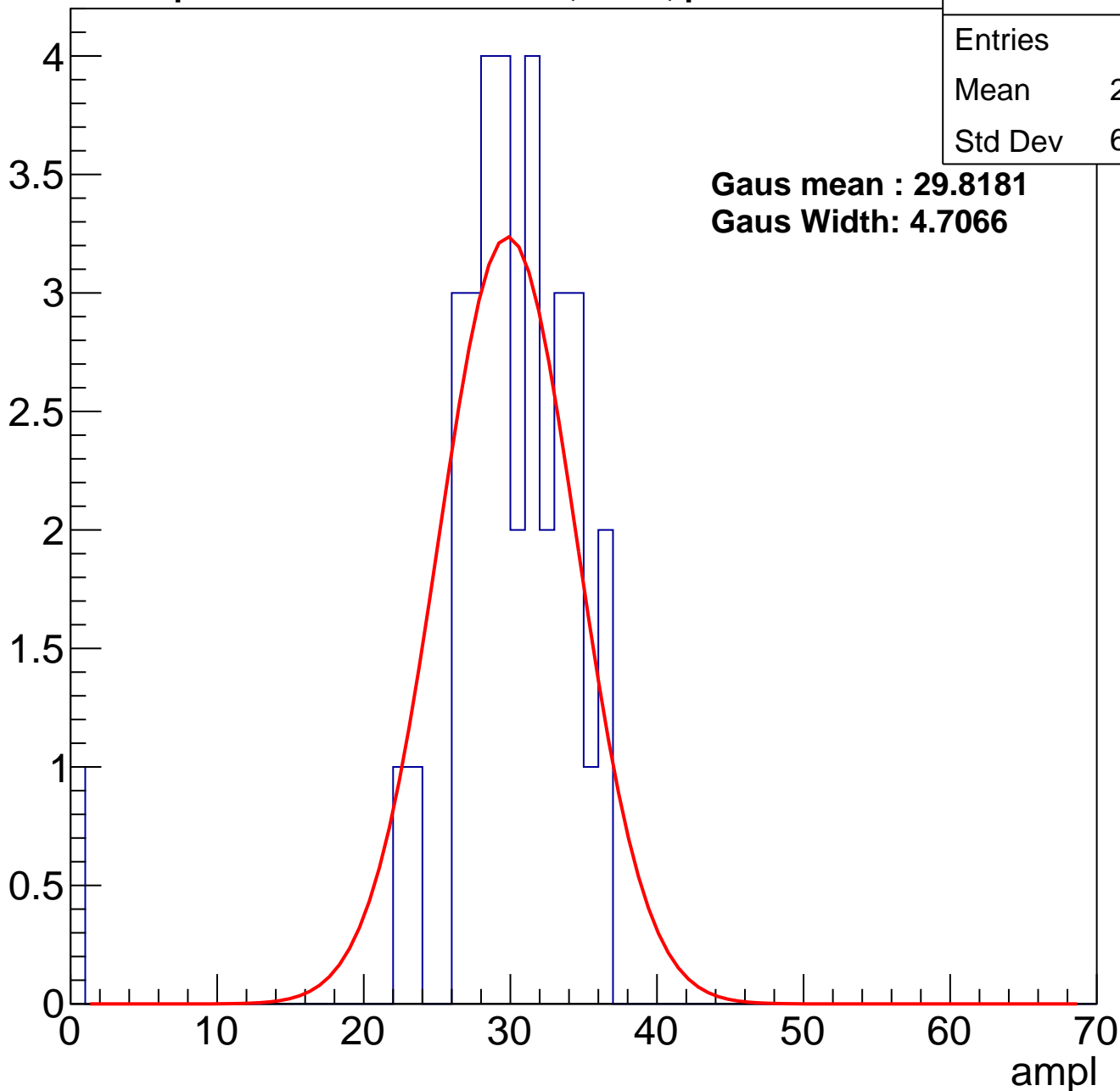


Entries	1
Mean	0
Std Dev	0

B1L103S, U15-ch109, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	34
Mean	29.06
Std Dev	6.092

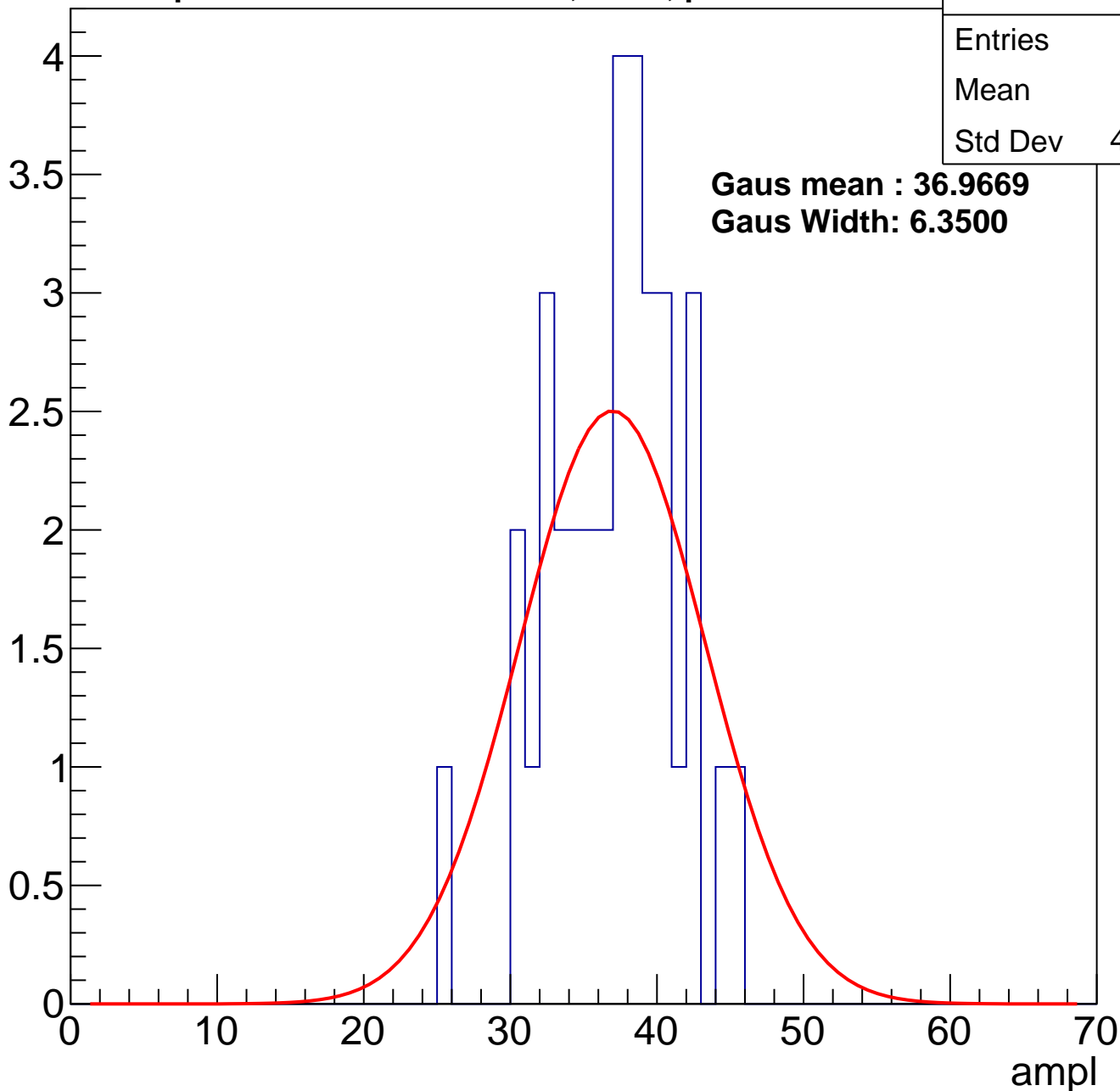
Gaus mean : 29.8181

Gaus Width: 4.7066

B1L103S, U15-ch109, adc1

calib_packv5_042523_0143.root, FC#7, port C2

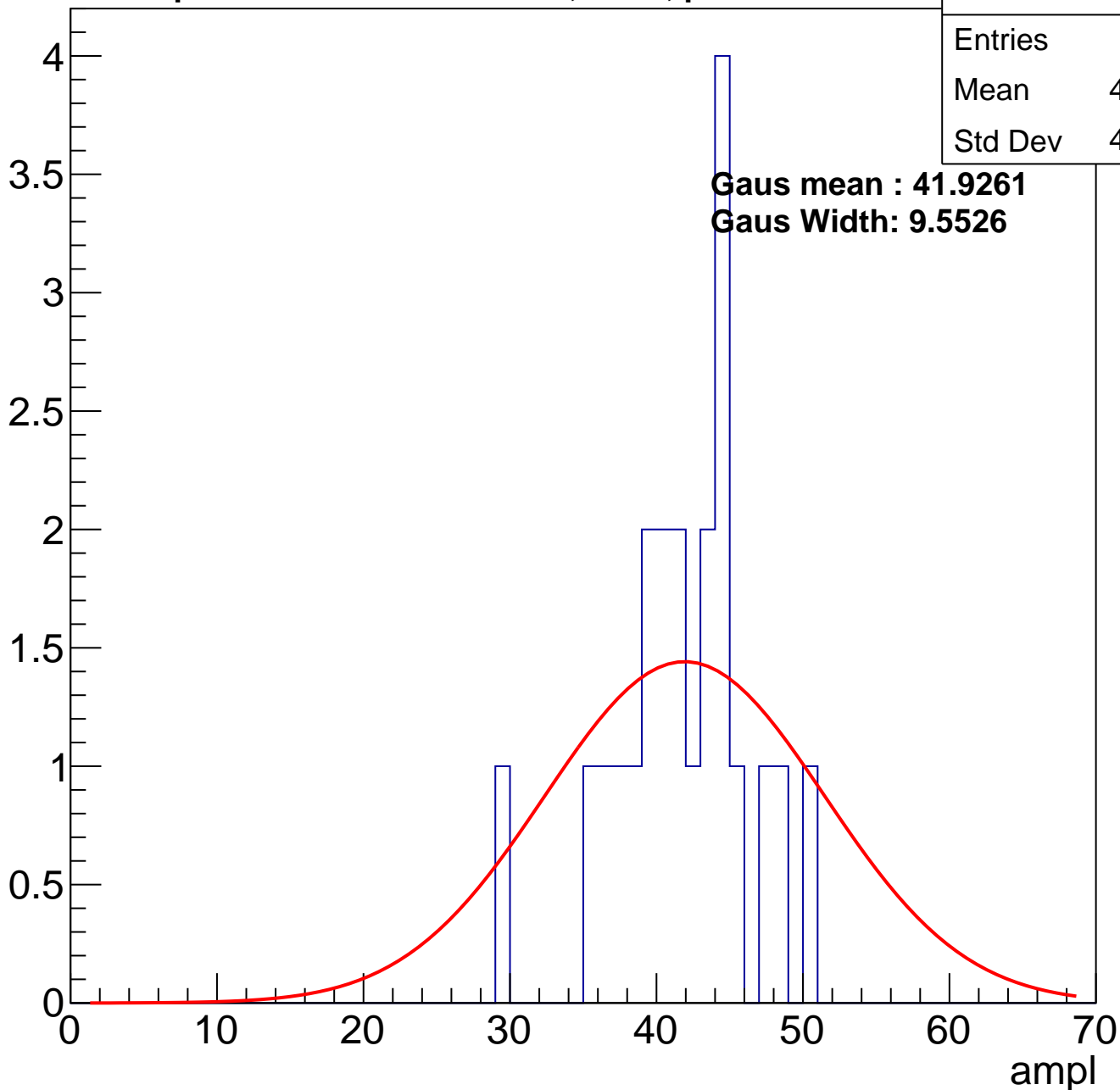
Entry



B1L103S, U15-ch109, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

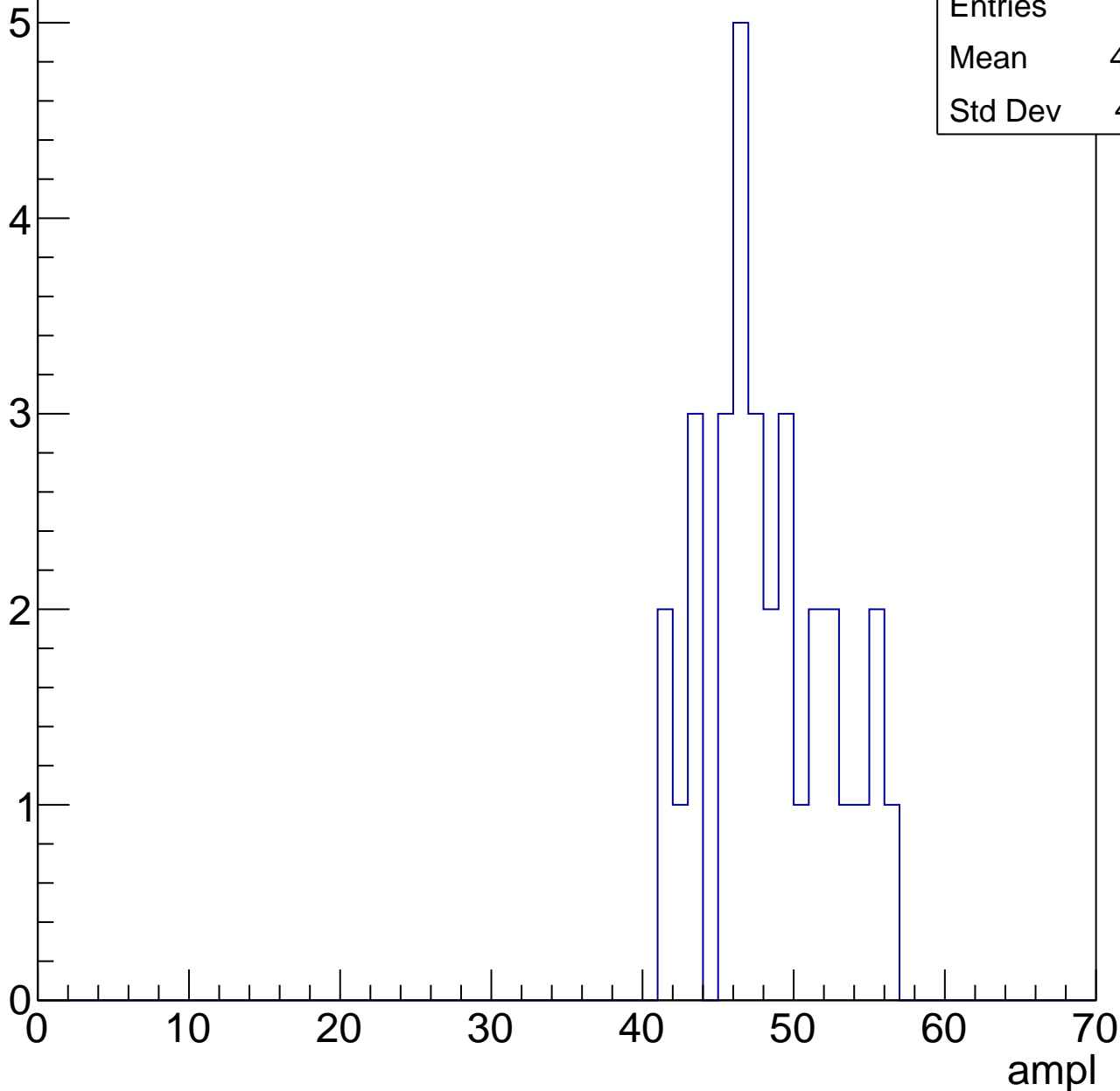


B1L103S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	47.84
Std Dev	4.101



B1L103S, U15-ch109, adc4

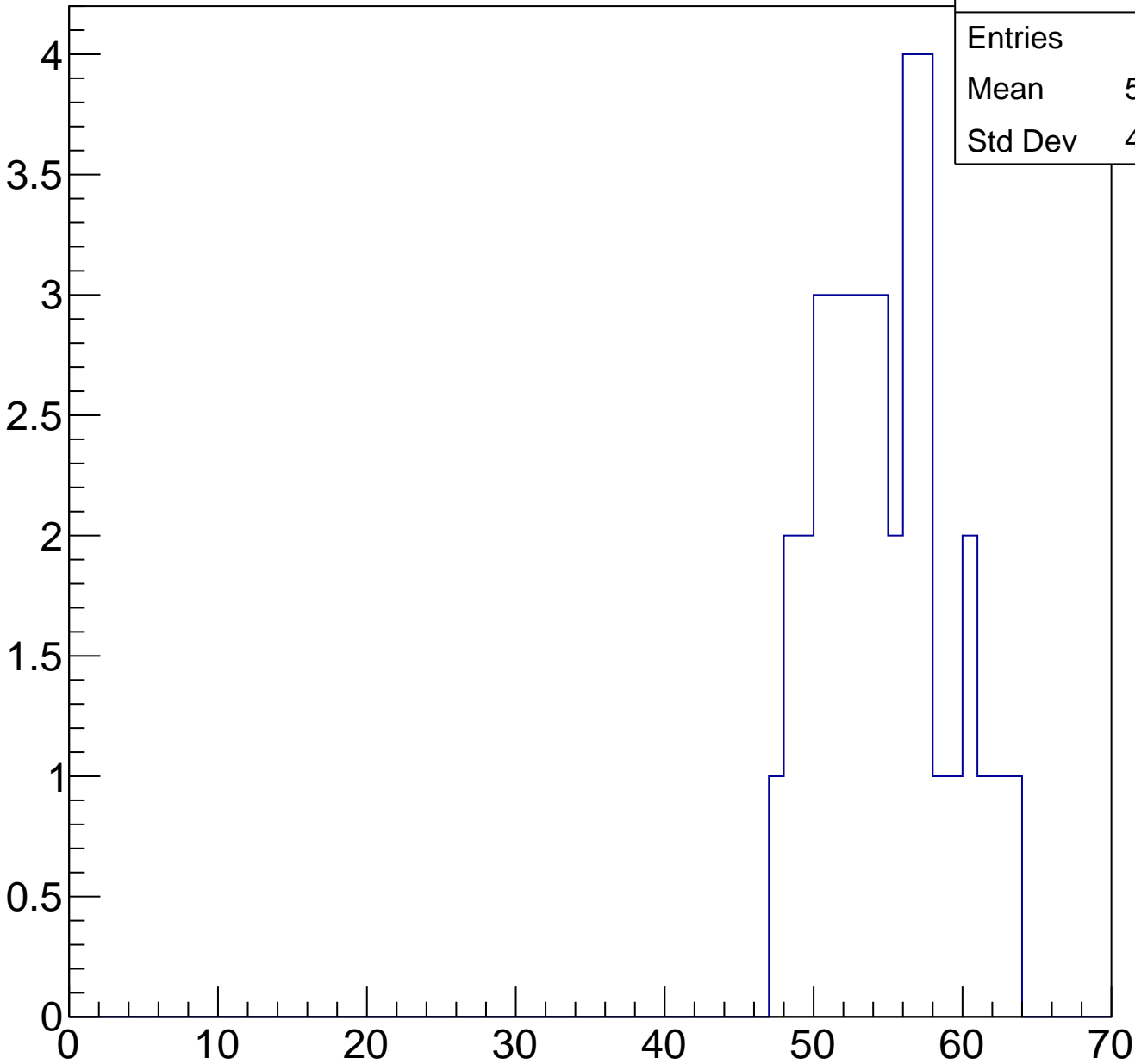
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	37
Mean	54.22
Std Dev	4.094

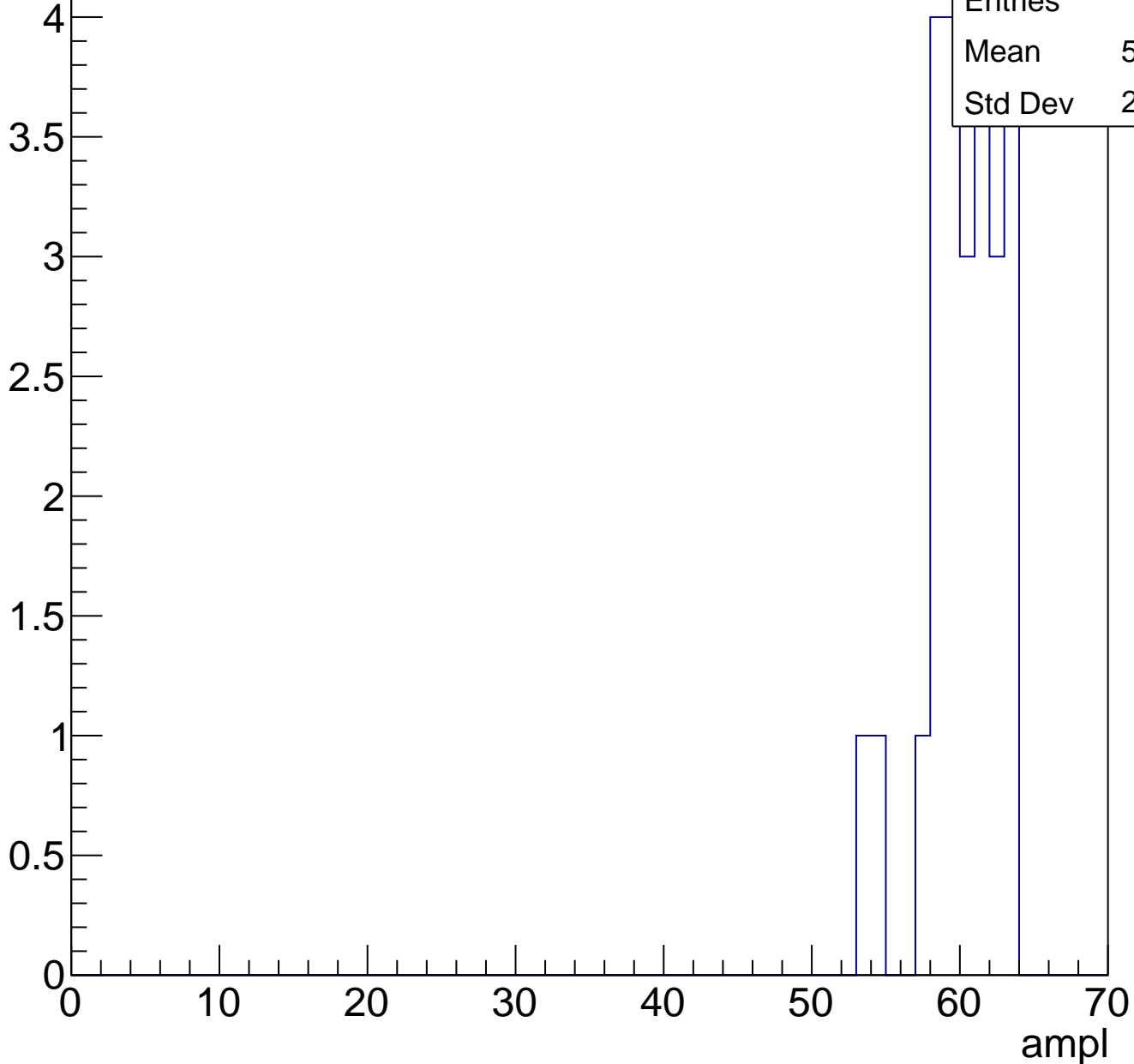
ampl



B1L103S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

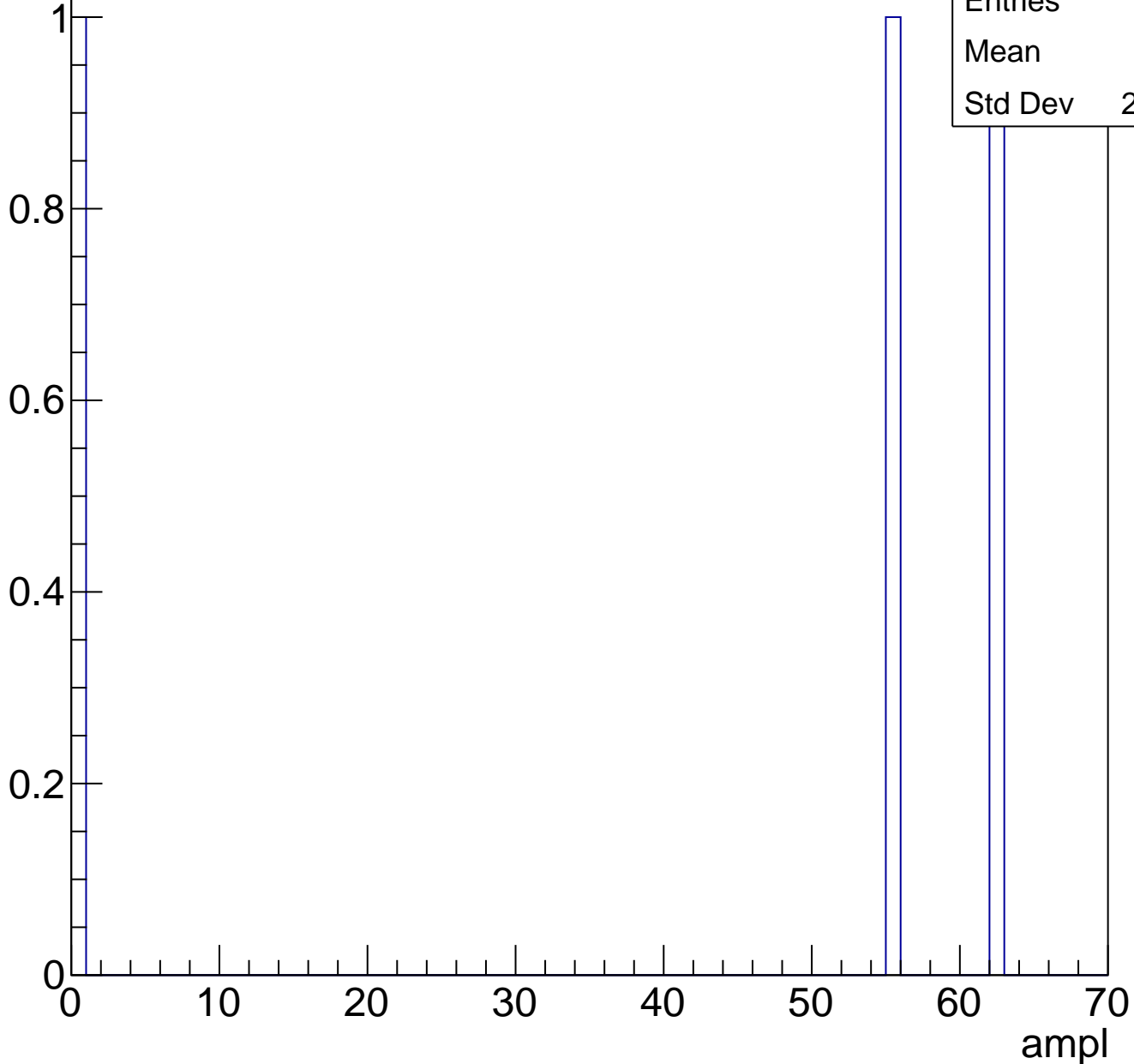
Entry



B1L103S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

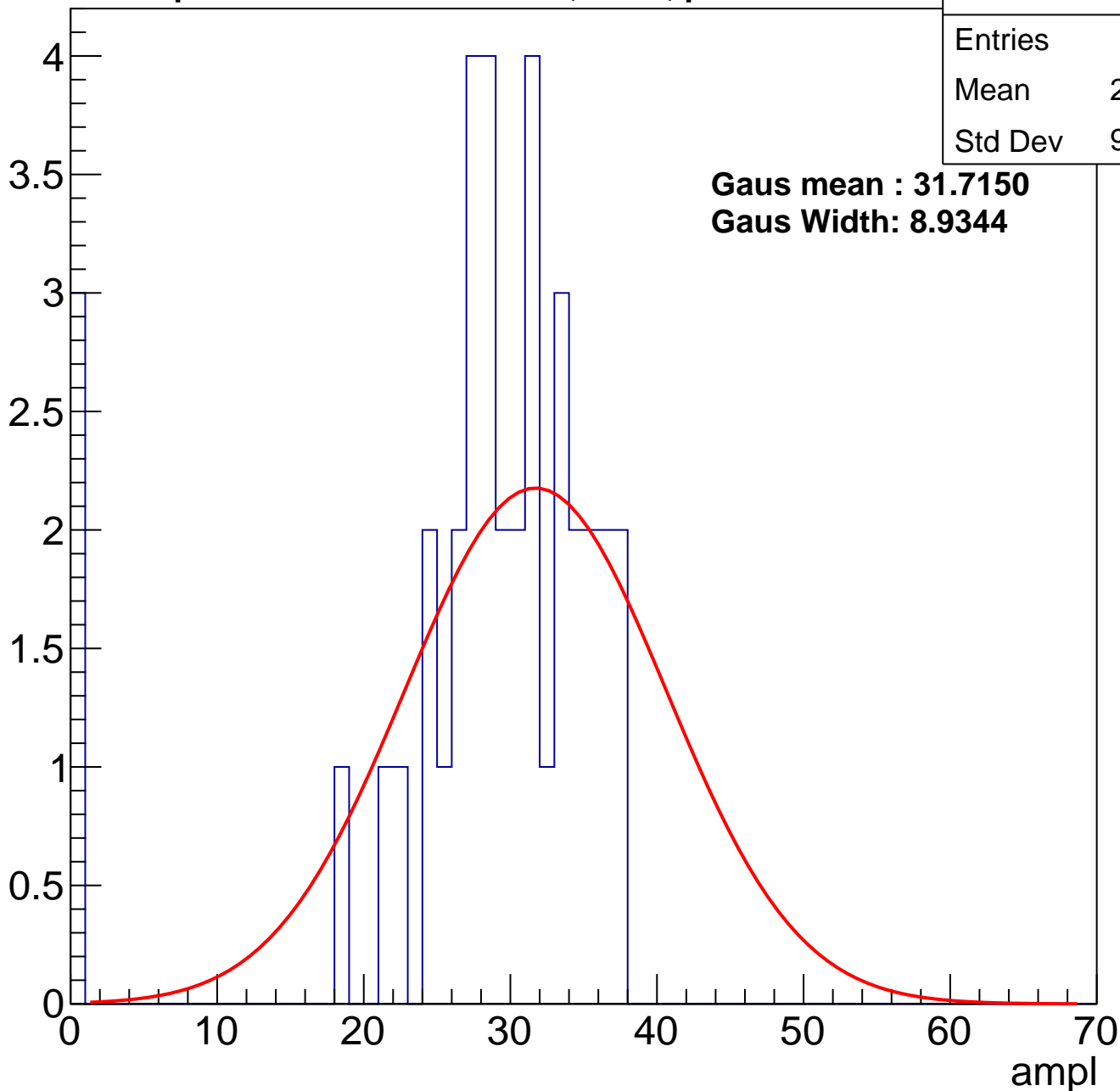
Entry



B1L103S, U15-ch110, adc0

calib_packv5_042523_0143.root, FC#7, port C2

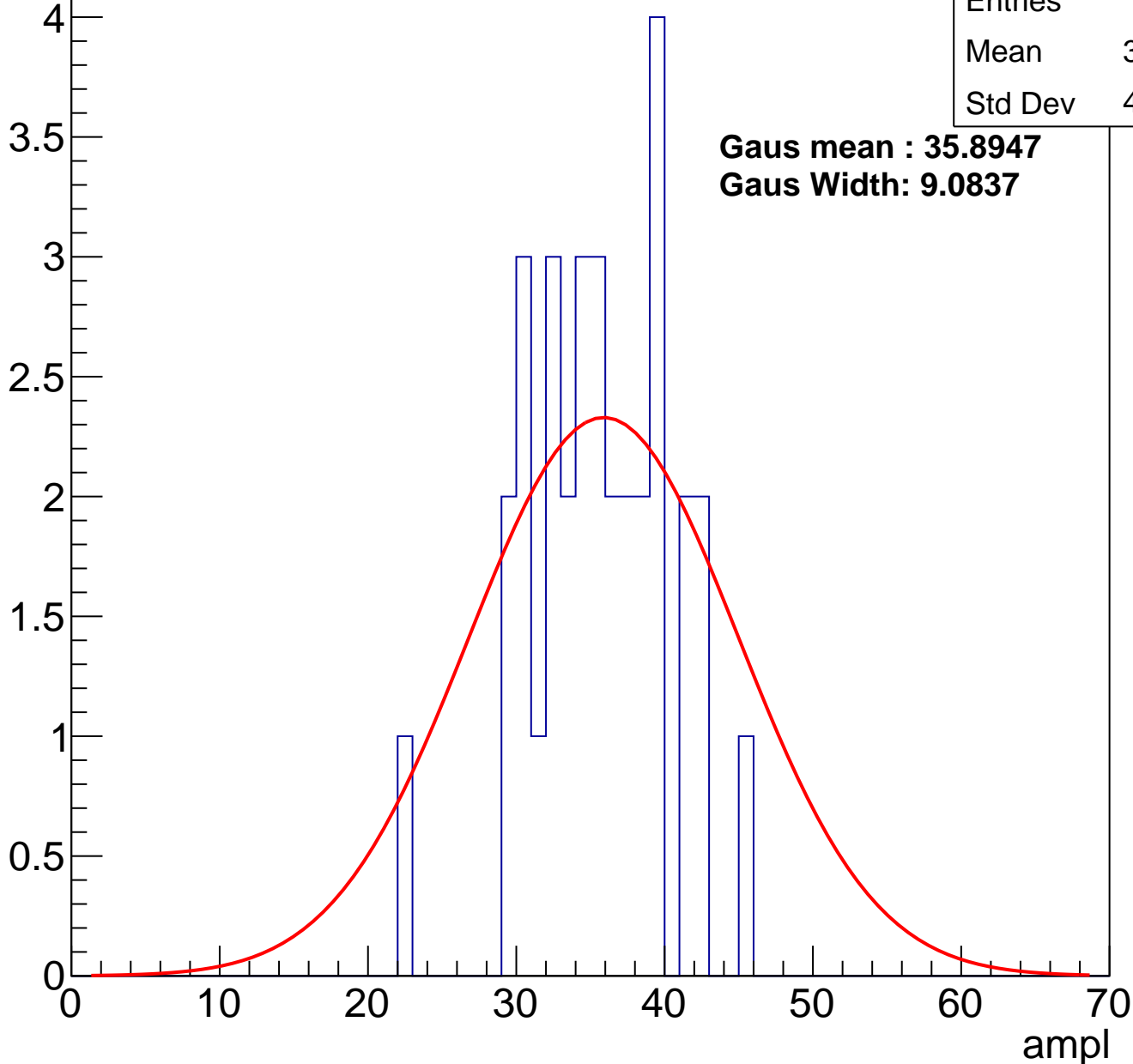
Entry



B1L103S, U15-ch110, adc1

calib_packv5_042523_0143.root, FC#7, port C2

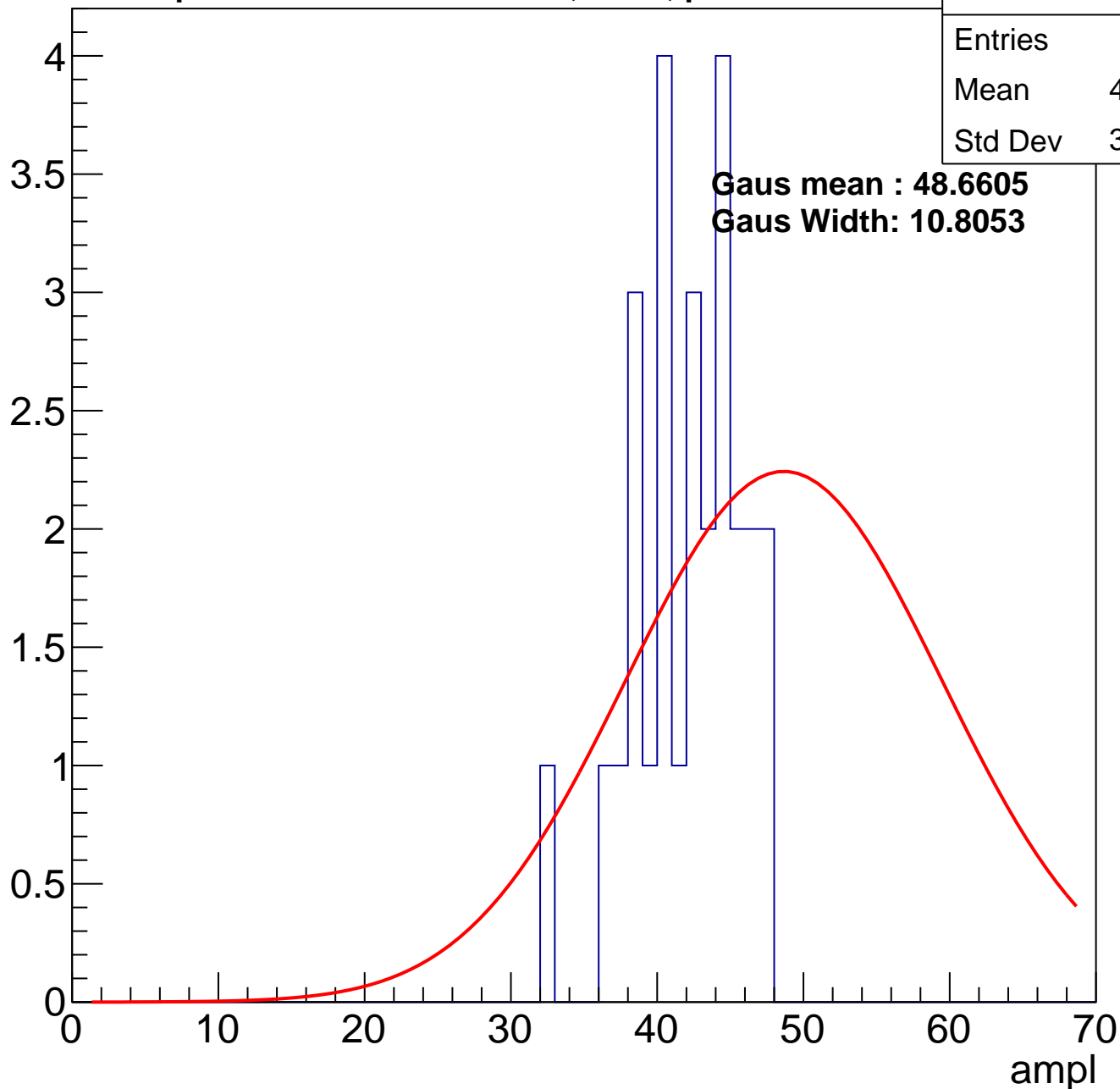
Entry



B1L103S, U15-ch110, adc2

calib_packv5_042523_0143.root, FC#7, port C2

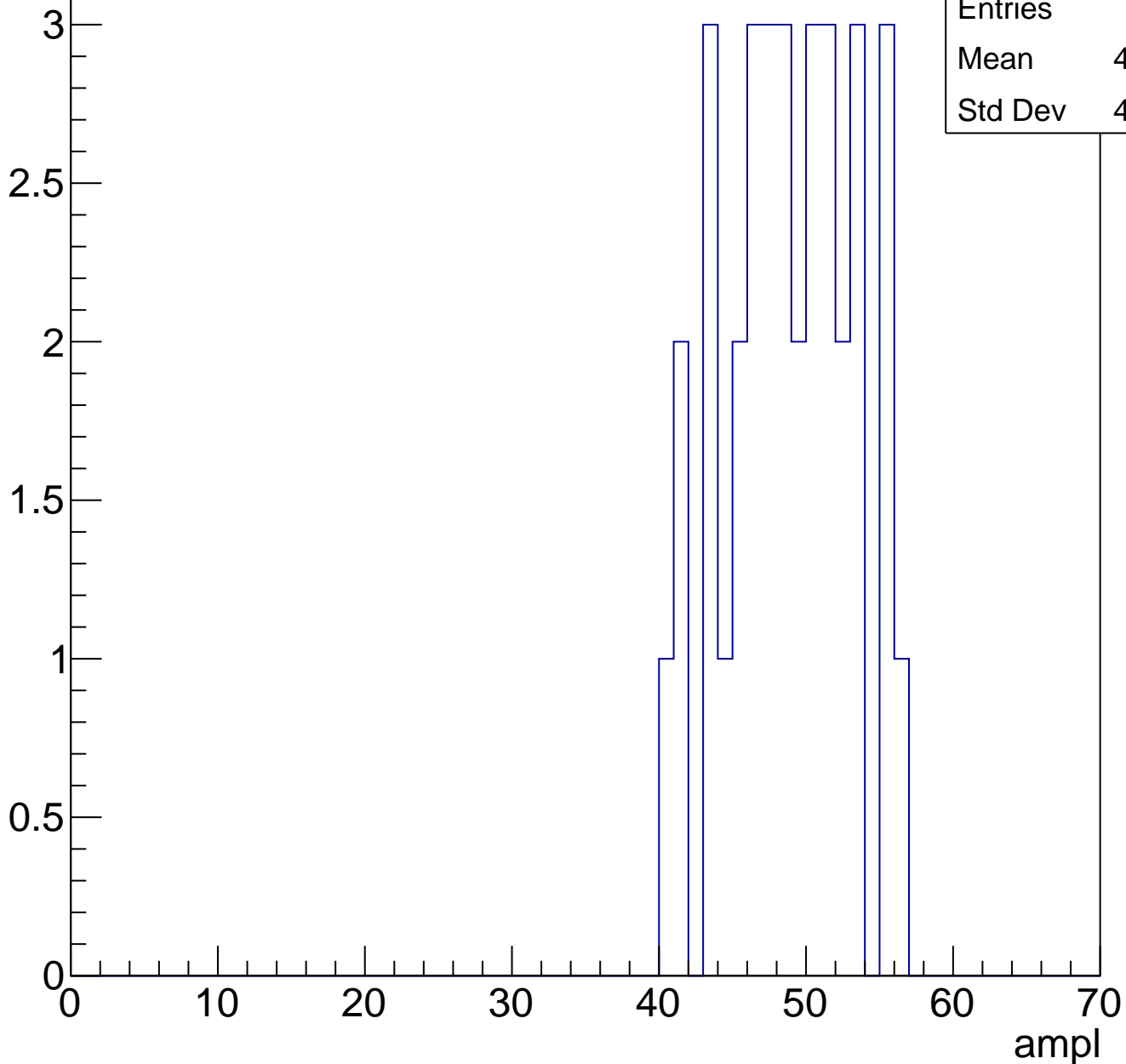
Entry



B1L103S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



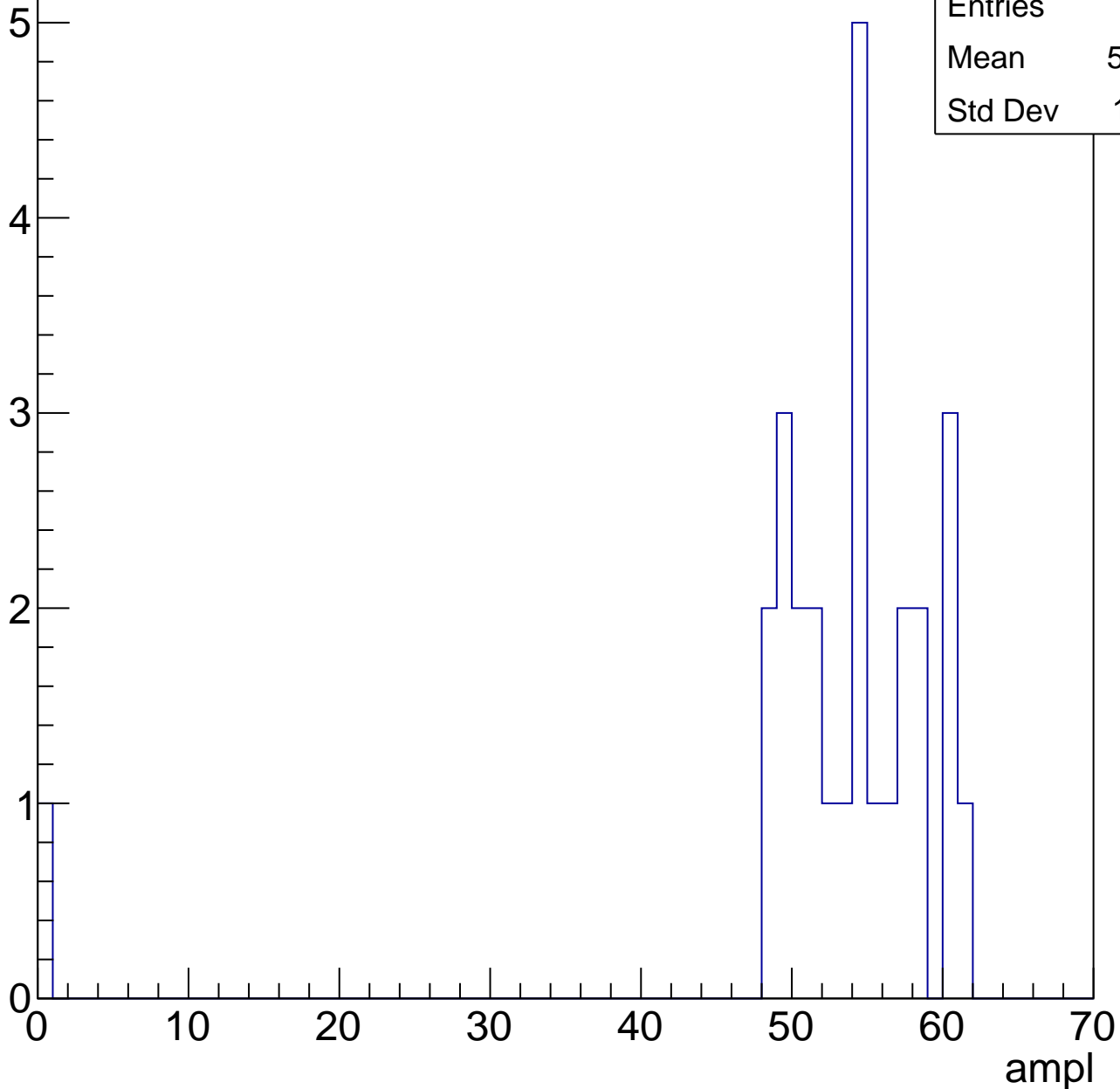
Entries	35
Mean	48.37
Std Dev	4.277

B1L103S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

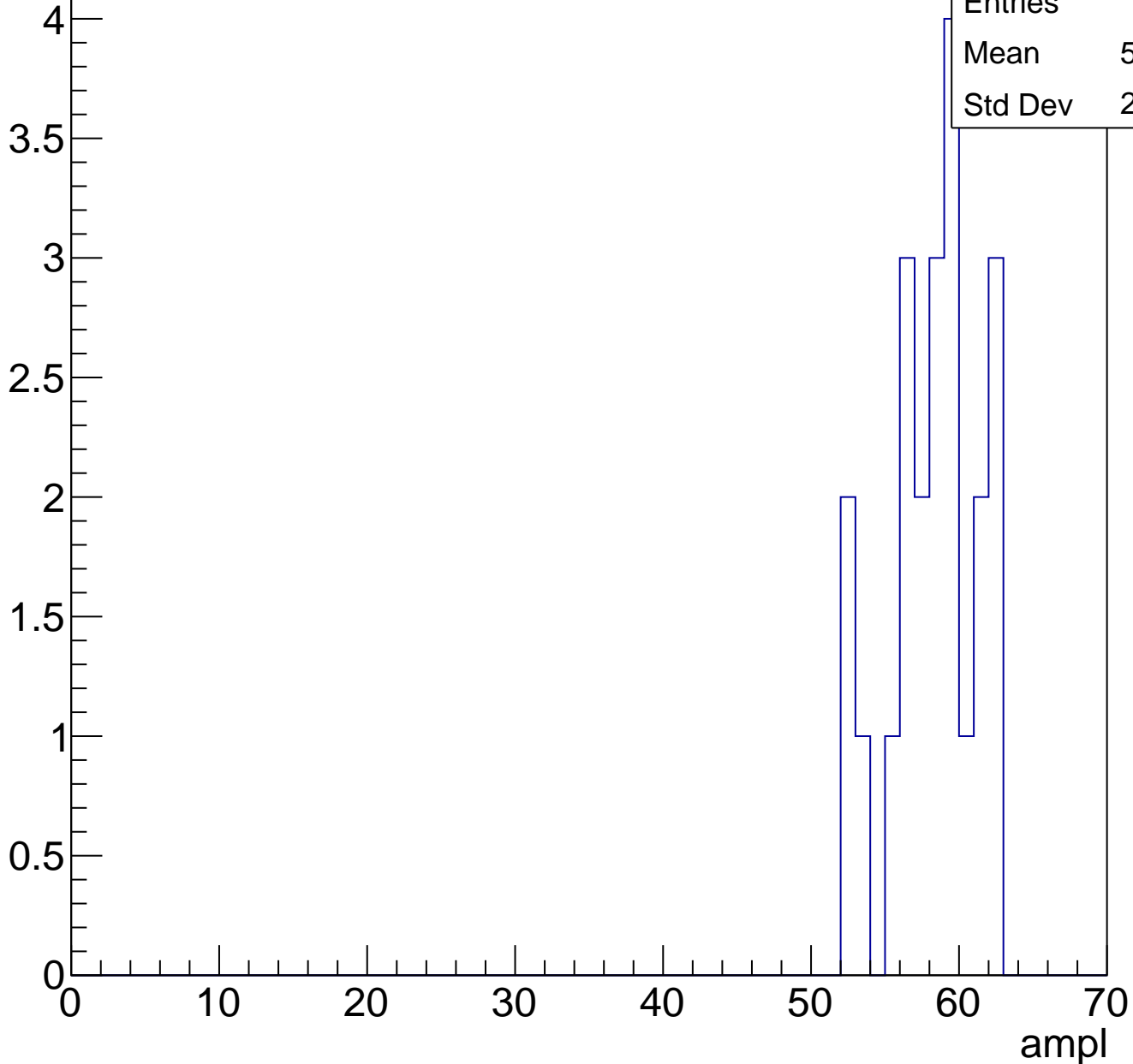
Entries	27
Mean	51.93
Std Dev	10.91



B1L103S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

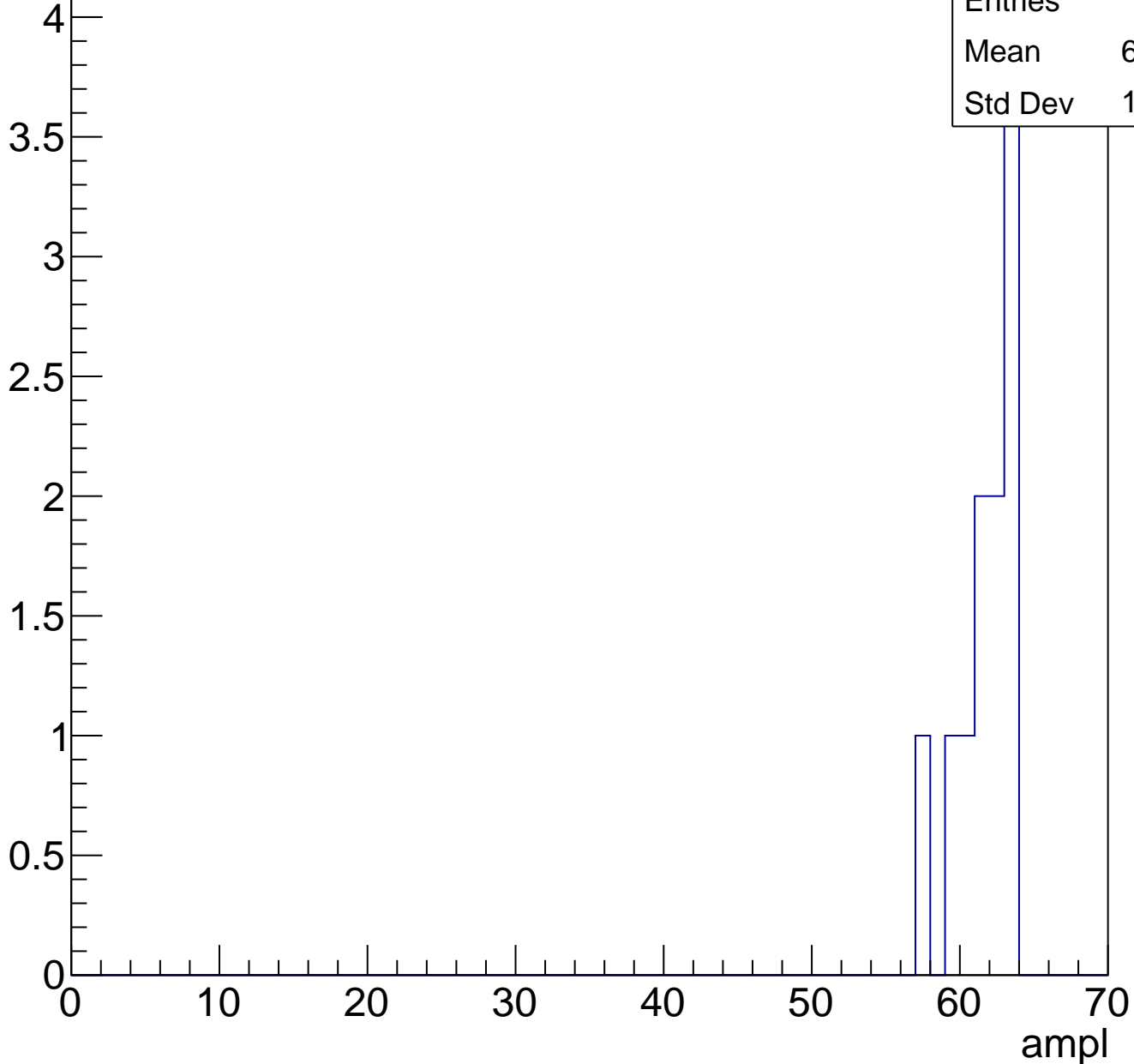
Entry



B1L103S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U15-ch111, adc0

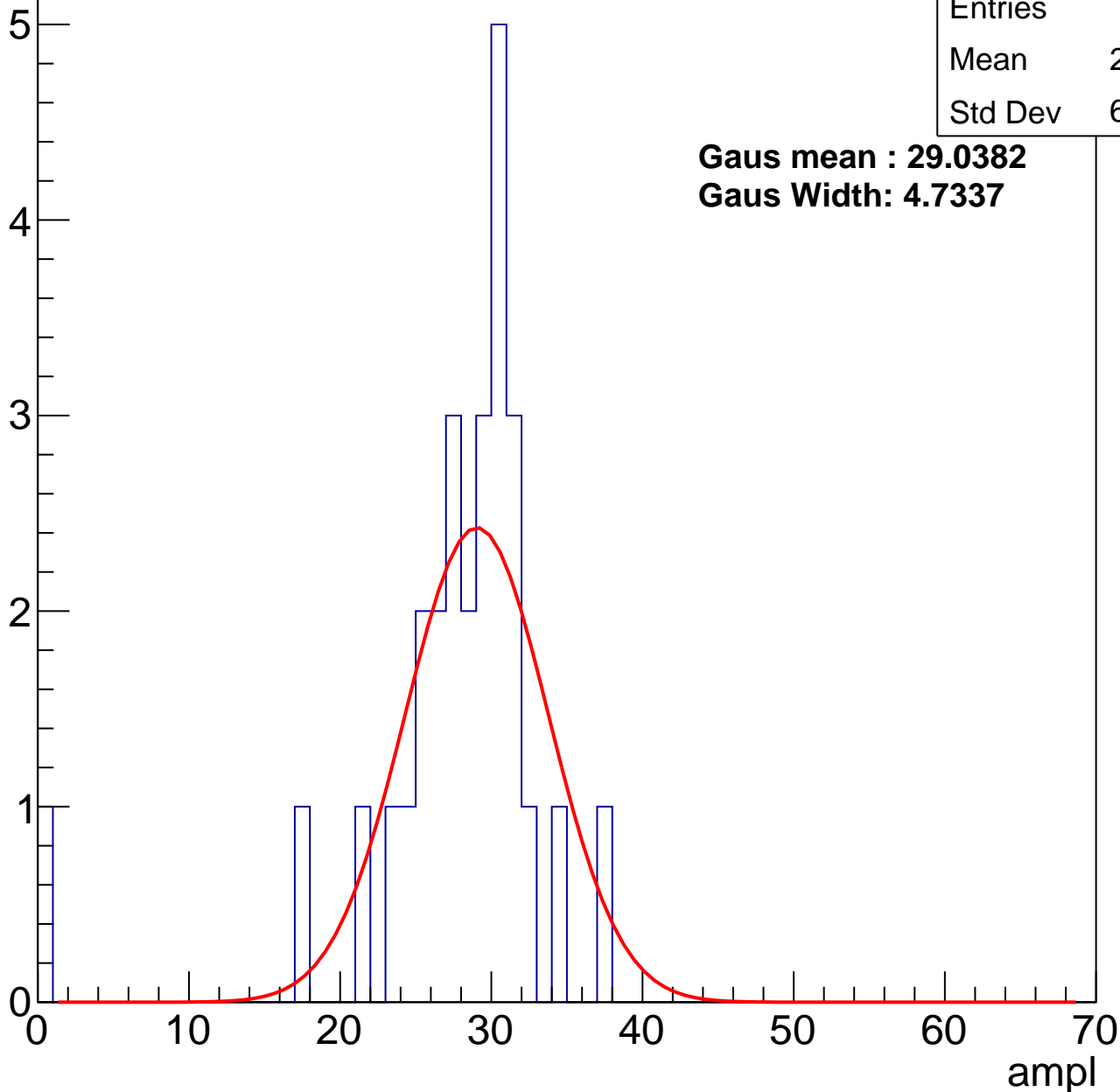
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	27.04
Std Dev	6.494

Gaus mean : 29.0382

Gaus Width: 4.7337



B1L103S, U15-ch111, adc1

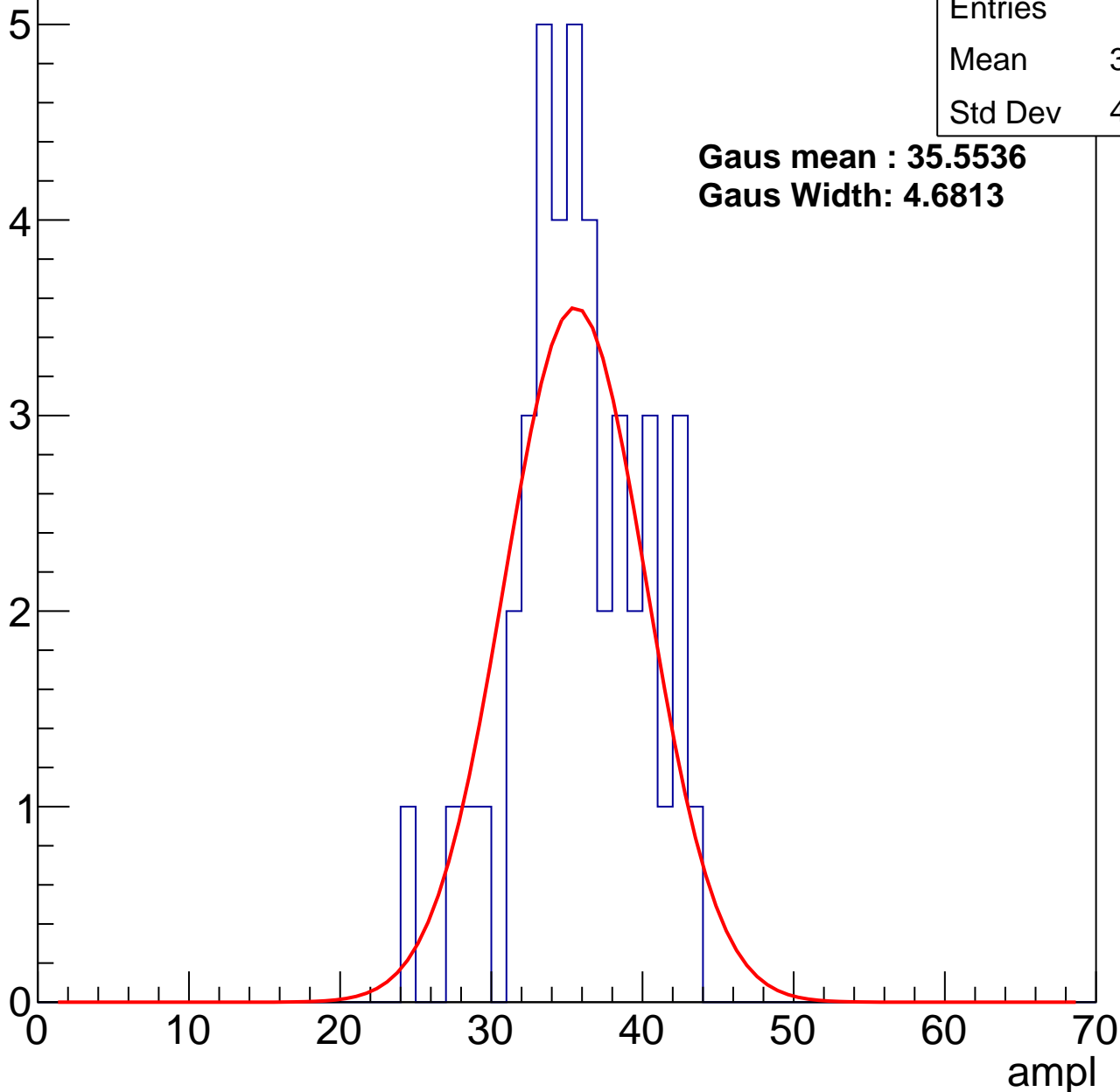
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	35.29
Std Dev	4.205

Gaus mean : 35.5536

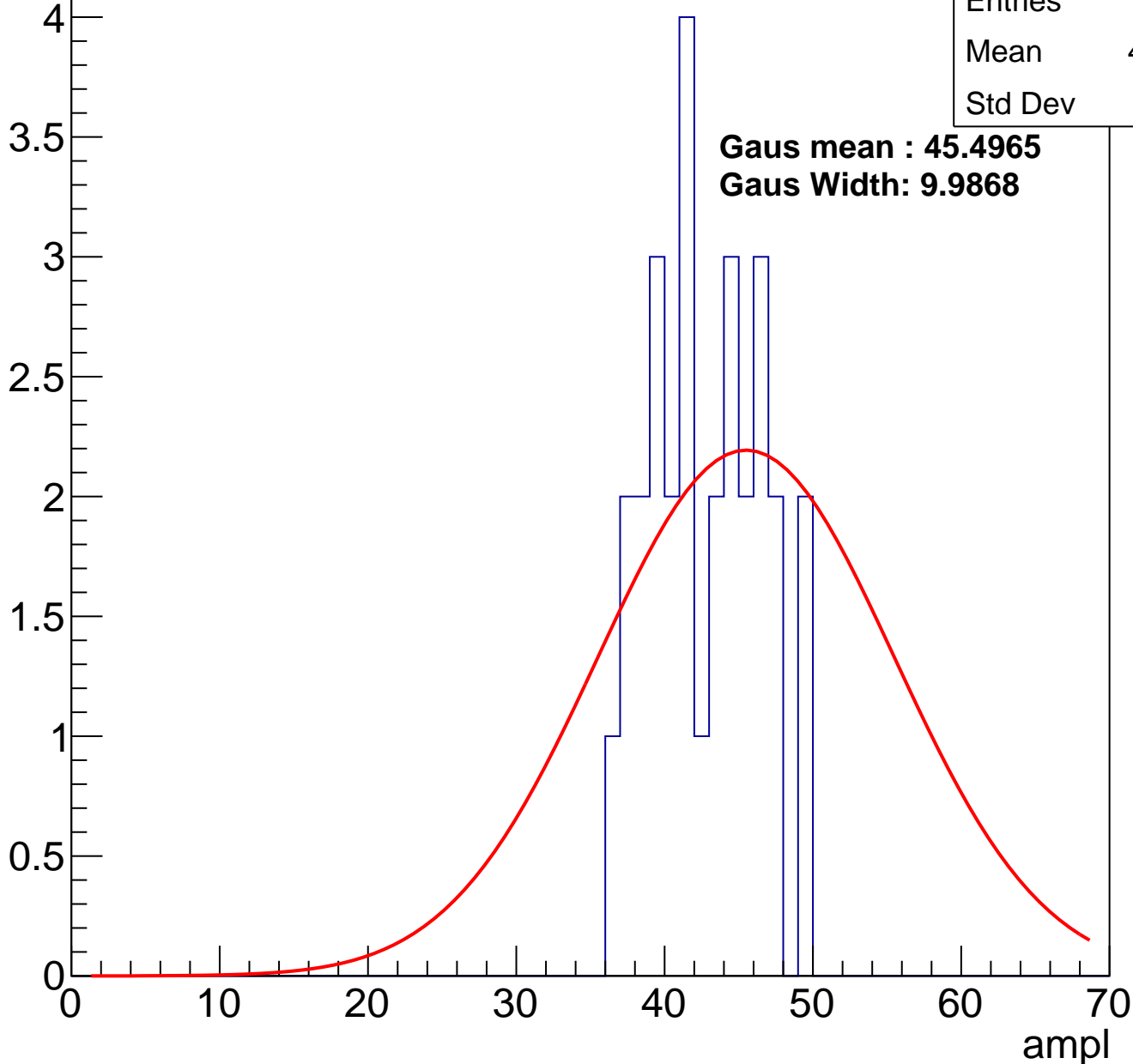
Gaus Width: 4.6813



B1L103S, U15-ch111, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

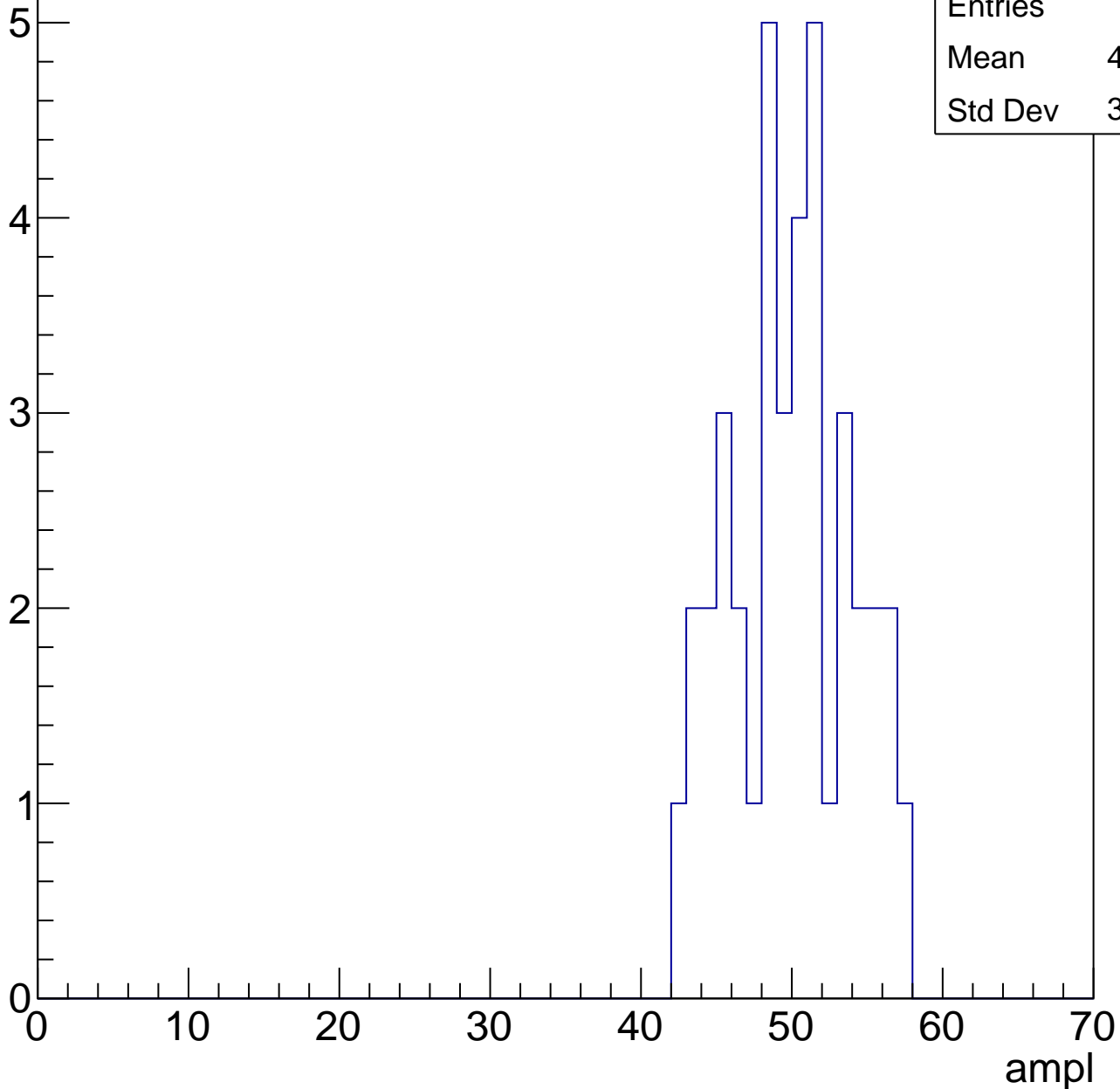


B1L103S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

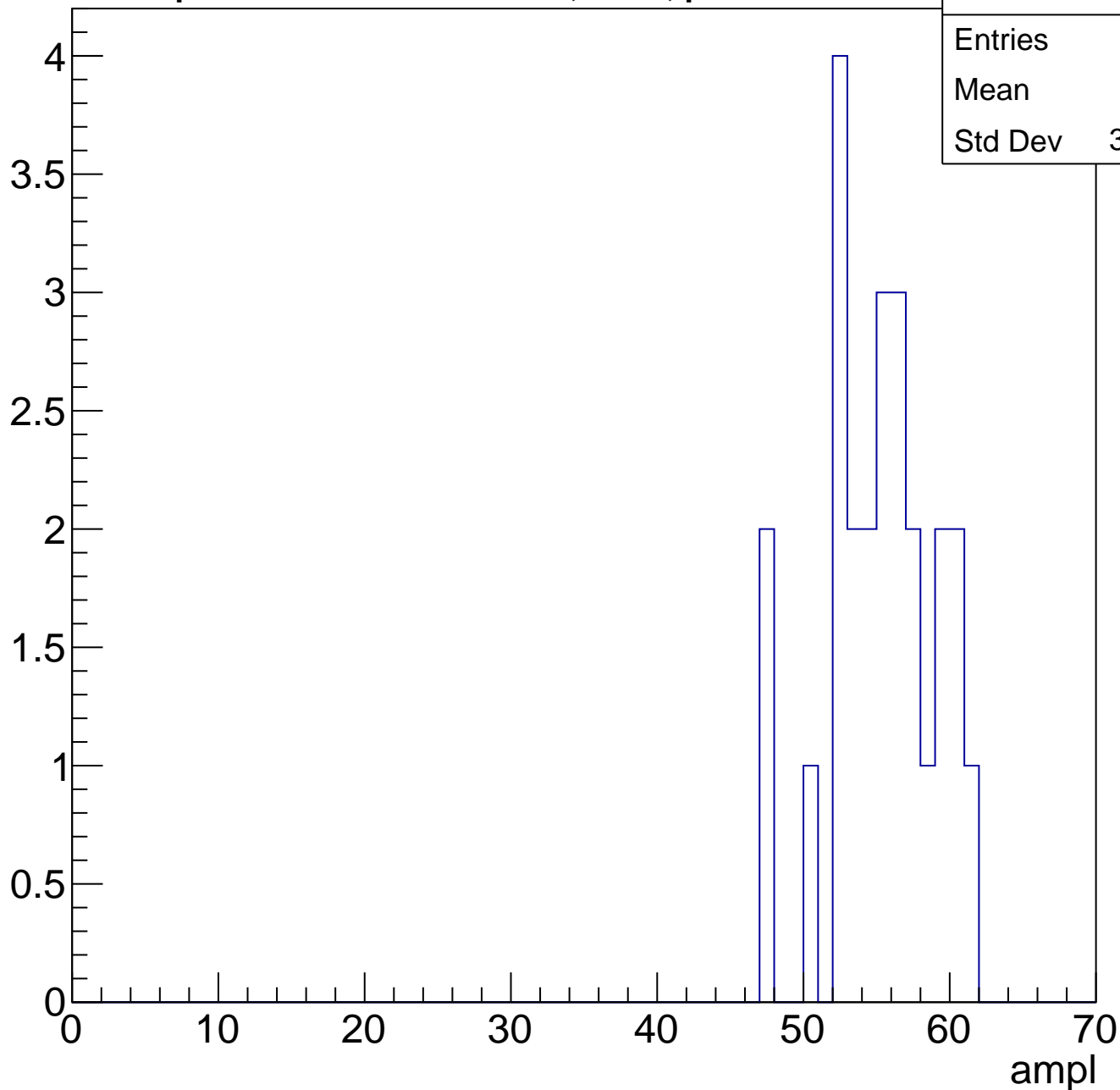
Entries	39
Mean	49.49
Std Dev	3.928



B1L103S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

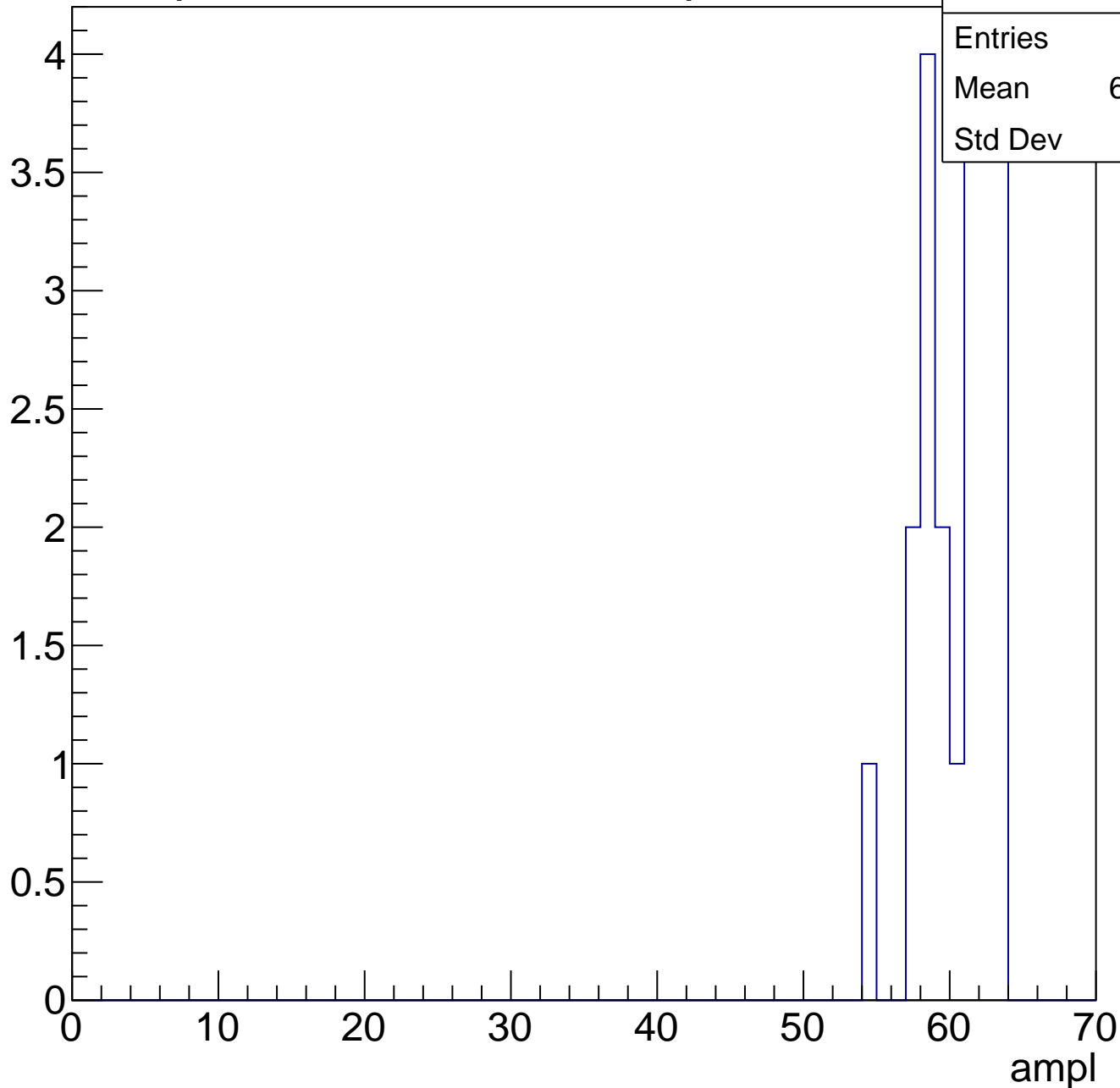
Entry



B1L103S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.8
Std Dev	1.47

ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

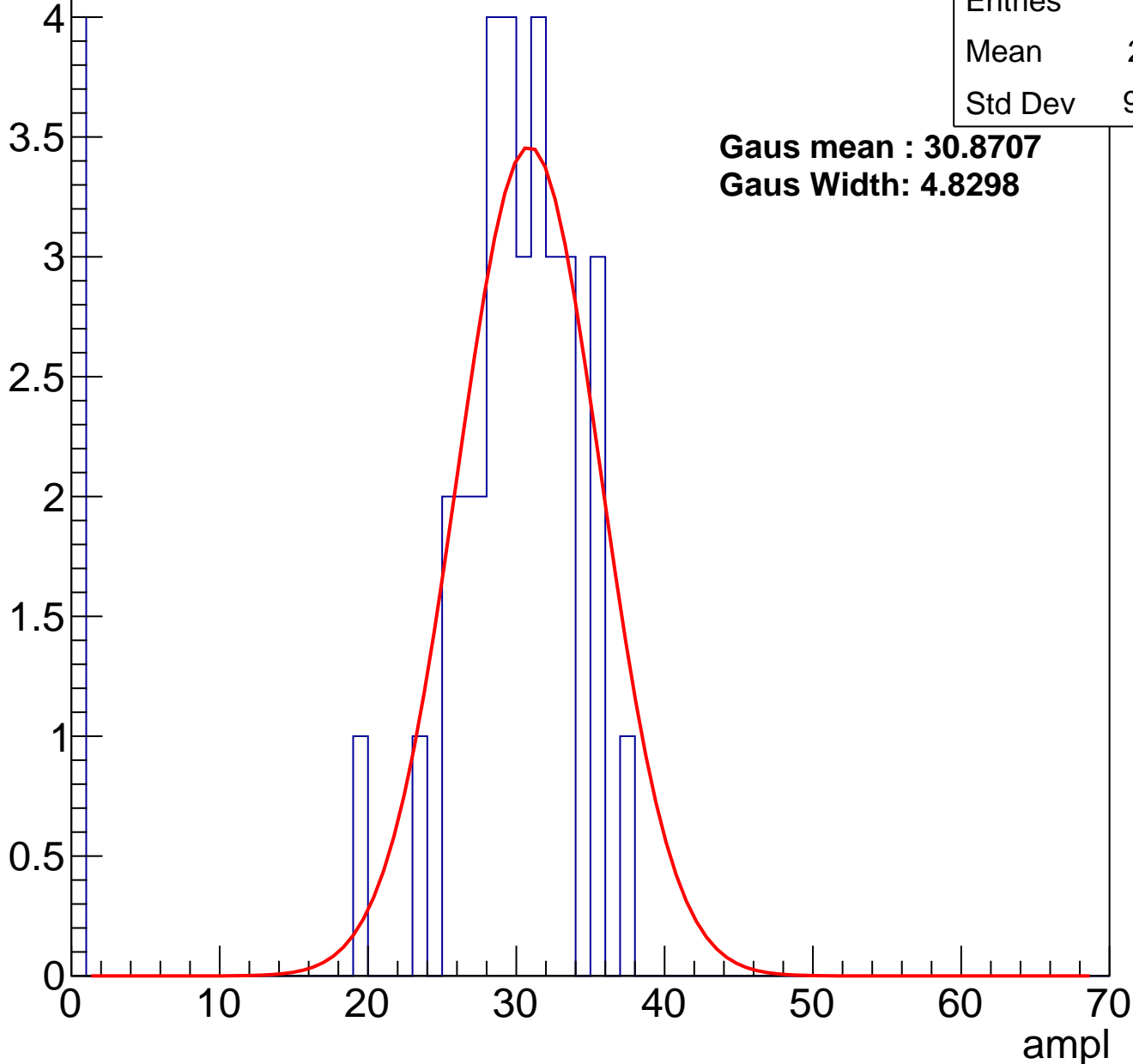
Entry



B1L103S, U15-ch112, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch112, adc1

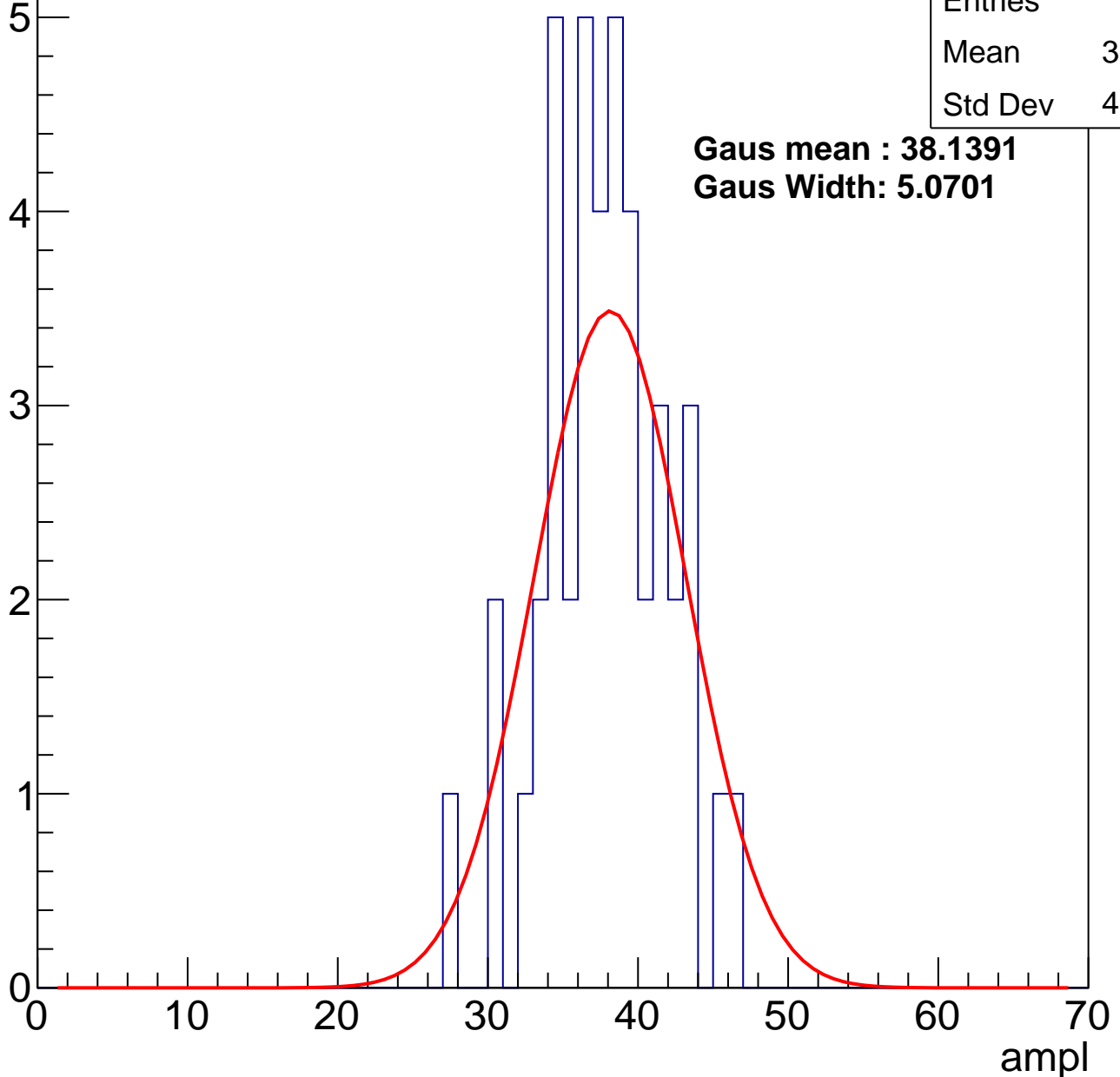
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	37.35
Std Dev	4.028

Gaus mean : 38.1391

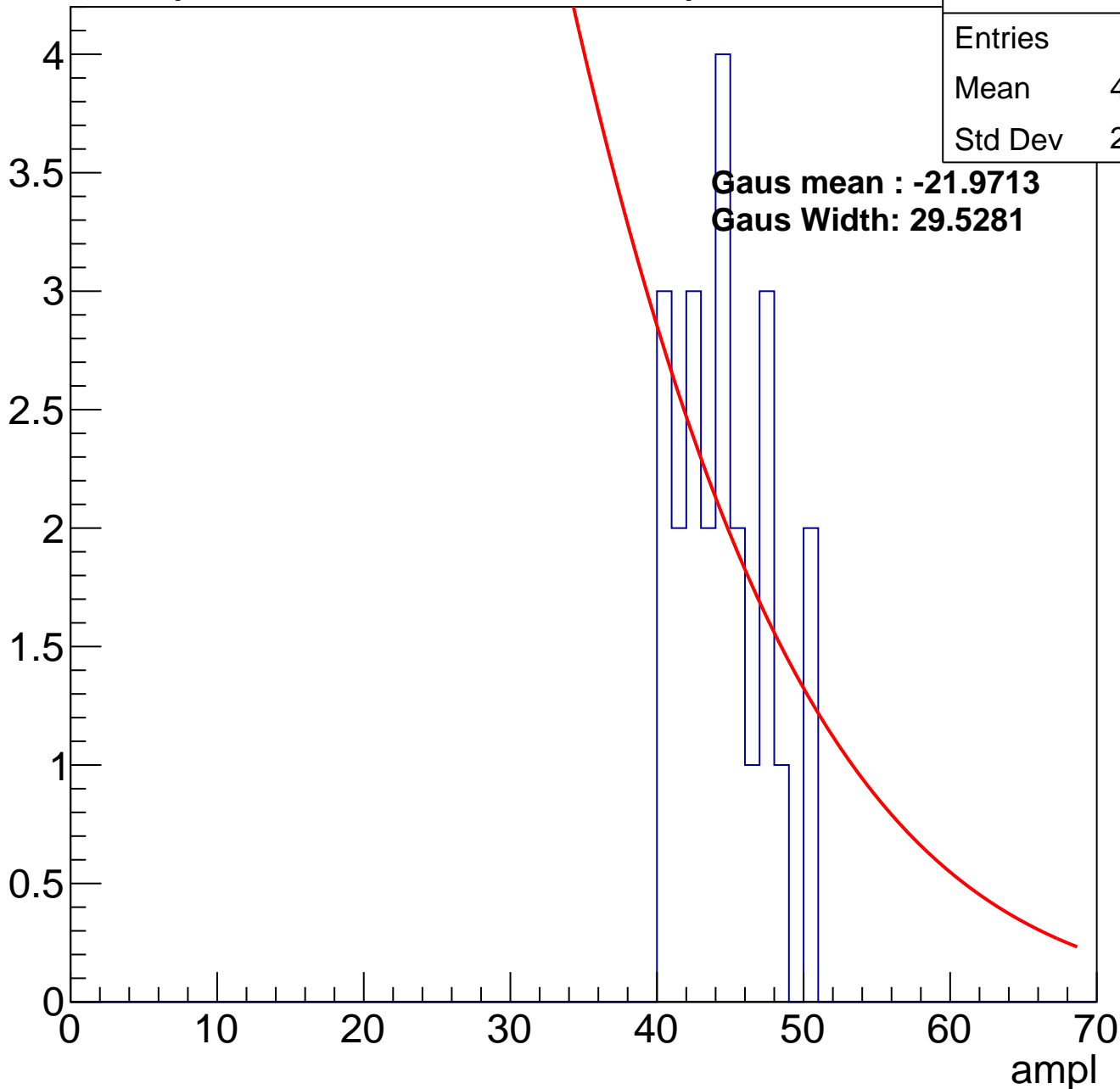
Gaus Width: 5.0701



B1L103S, U15-ch112, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

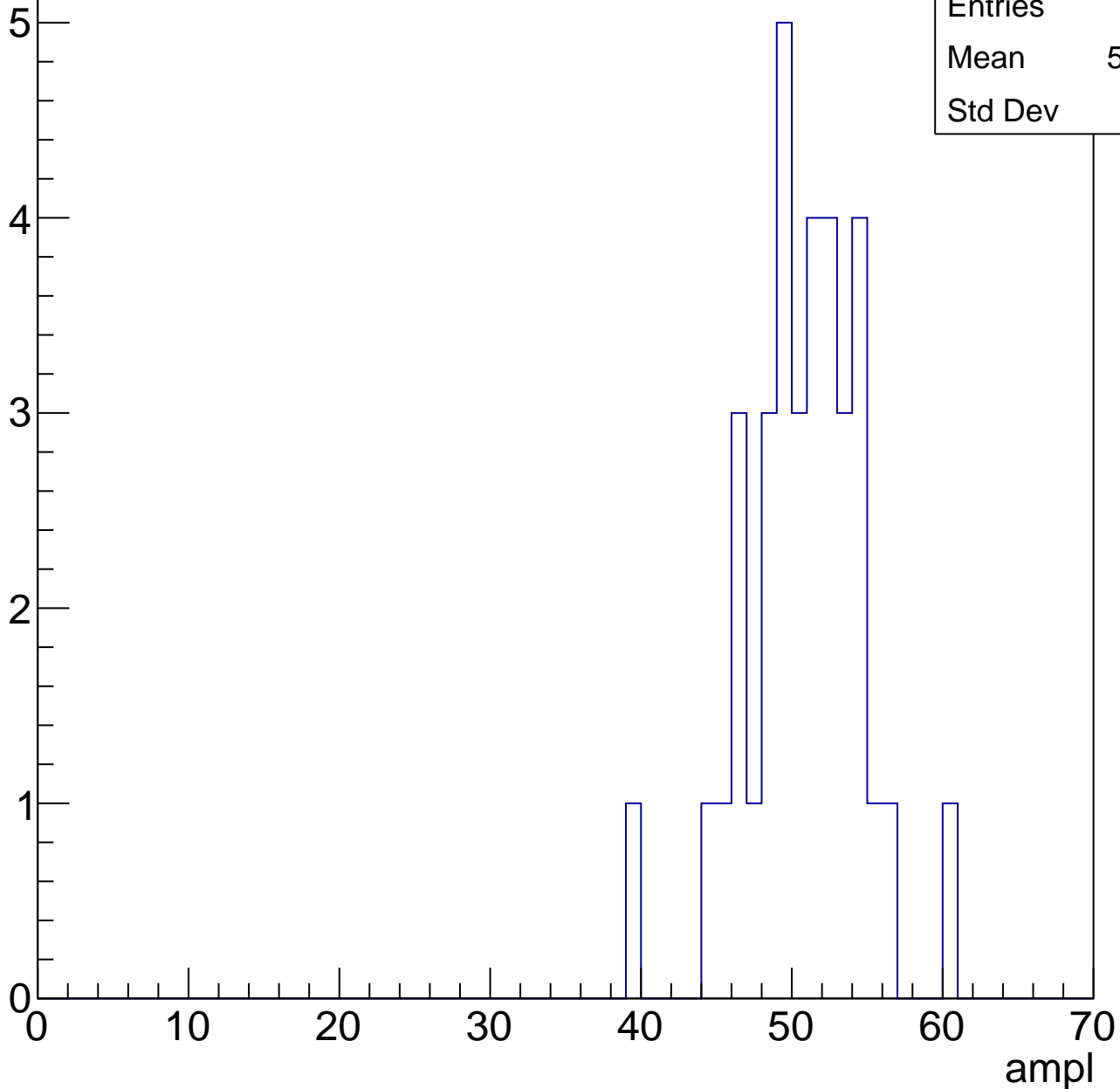


B1L103S, U15-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

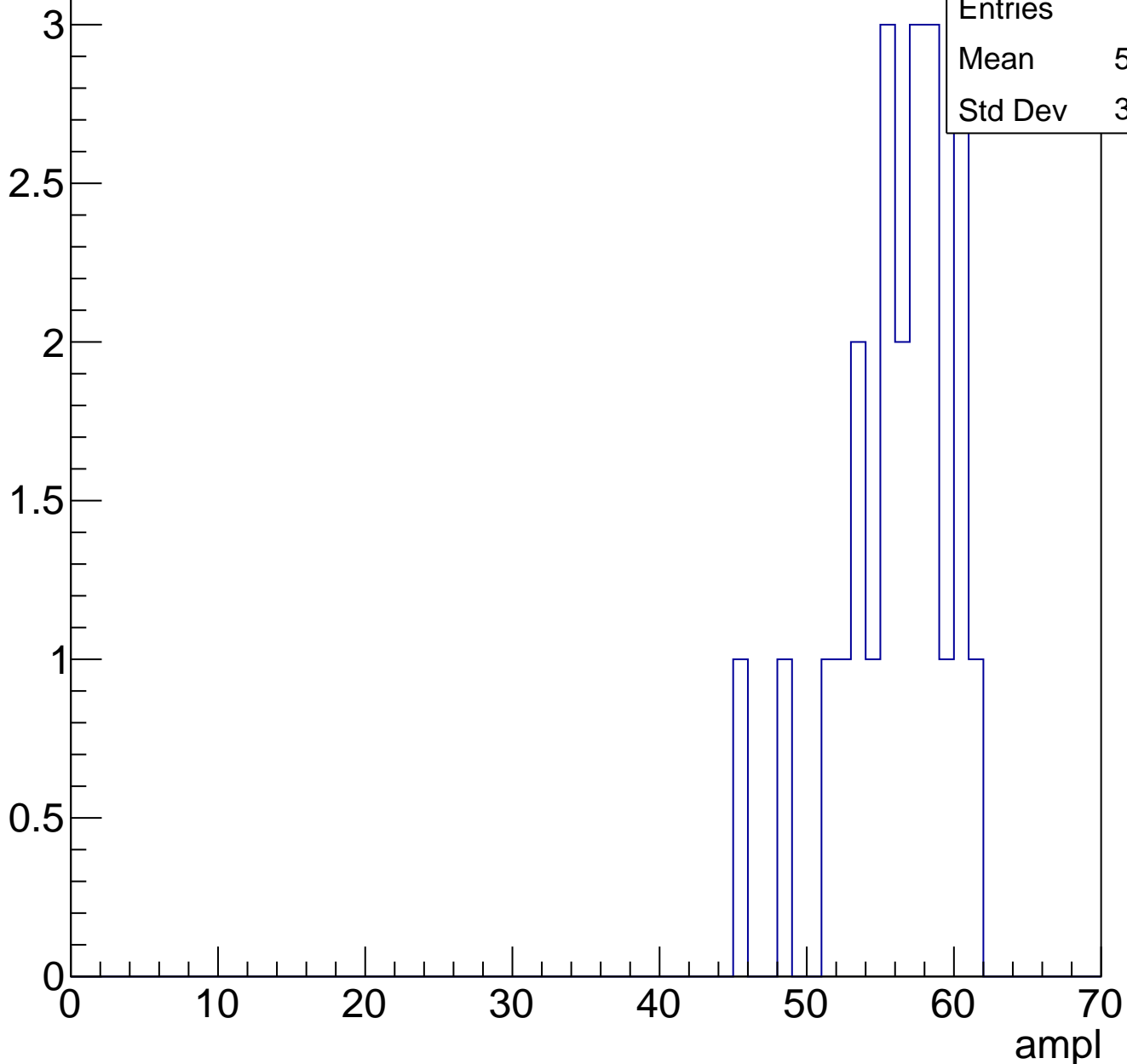
Entries	36
Mean	50.28
Std Dev	3.82



B1L103S, U15-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

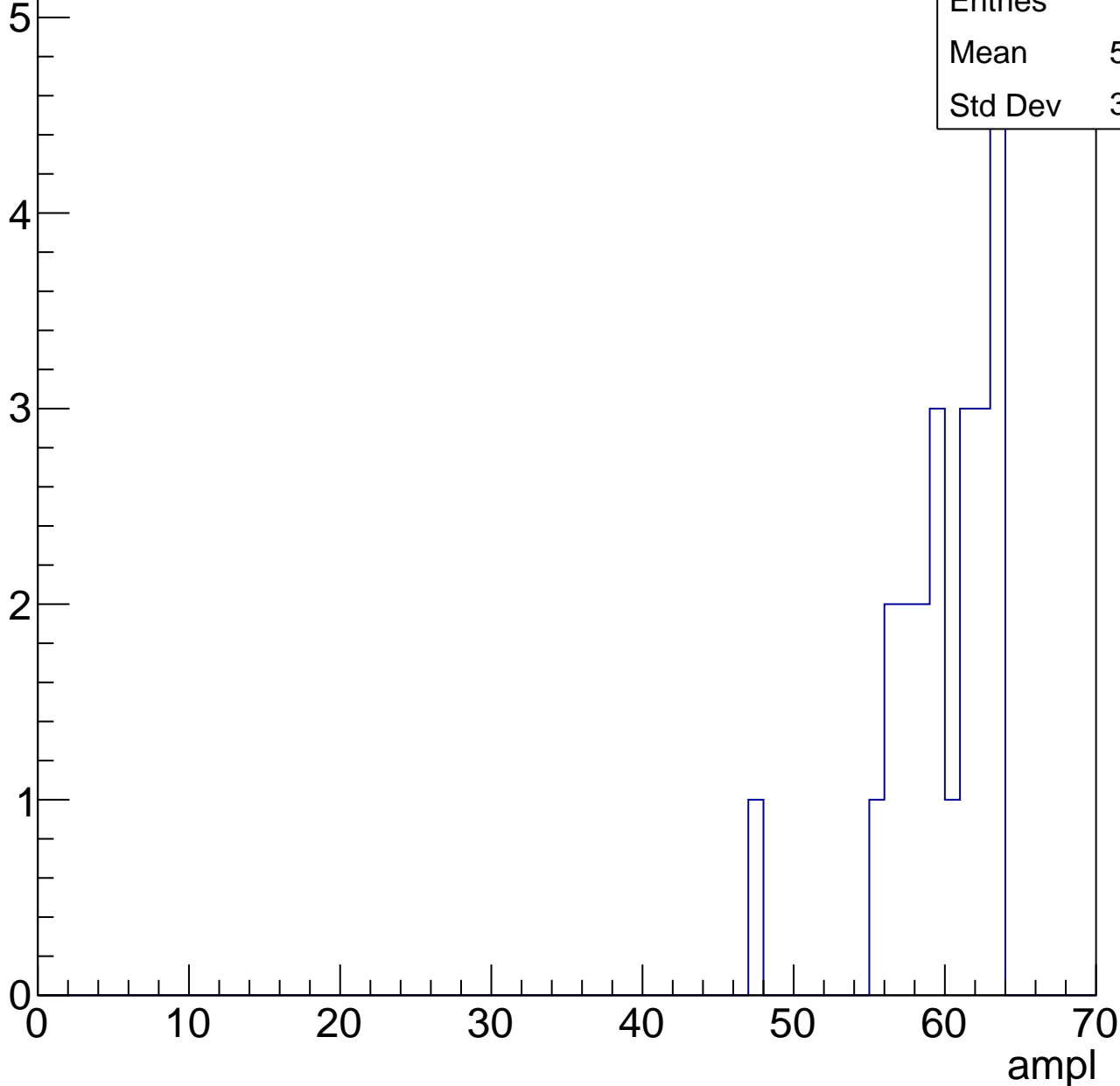


B1L103S, U15-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

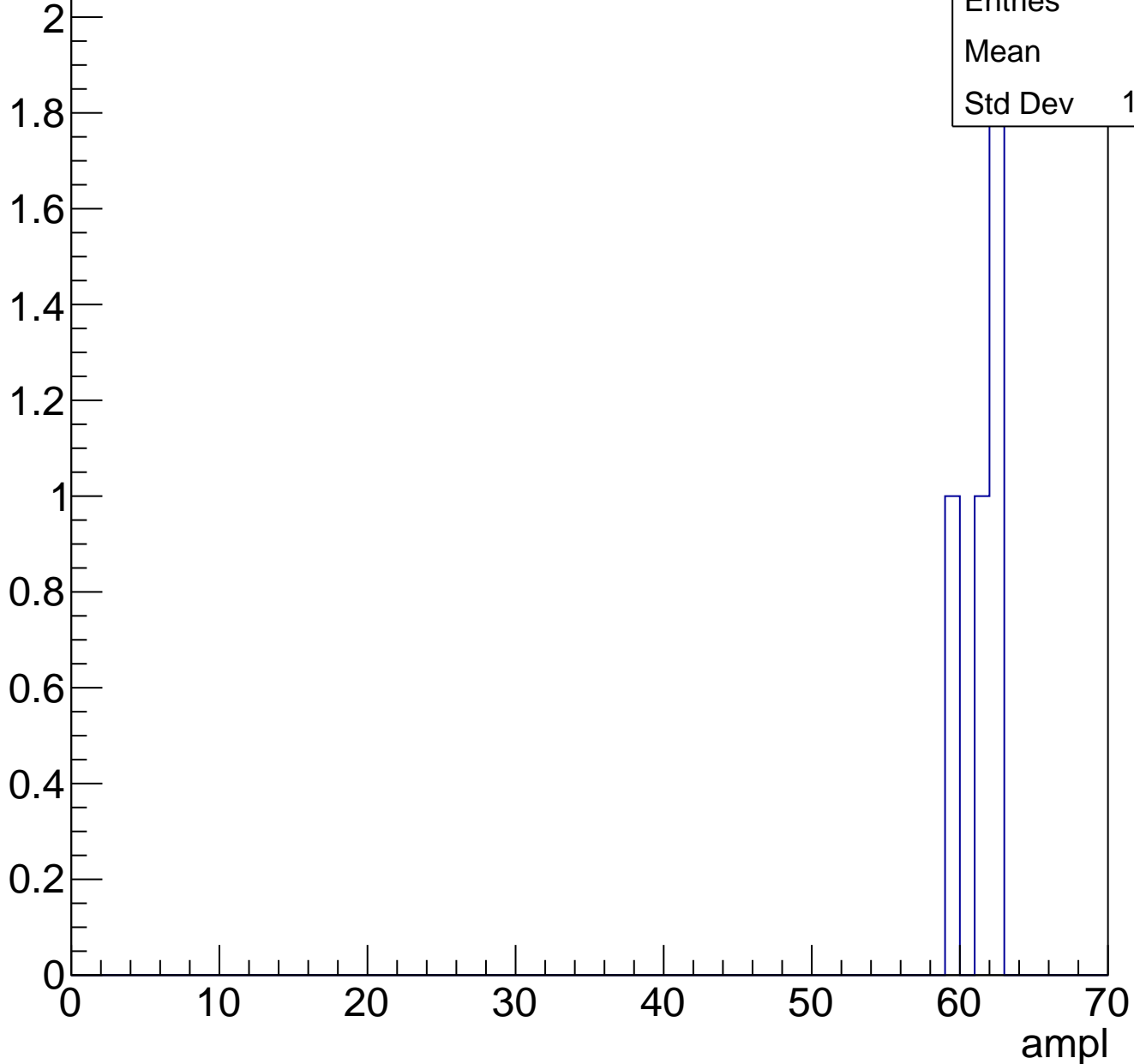
Entries	23
Mean	59.35
Std Dev	3.643



B1L103S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	61
Std Dev	1.225

B1L103S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

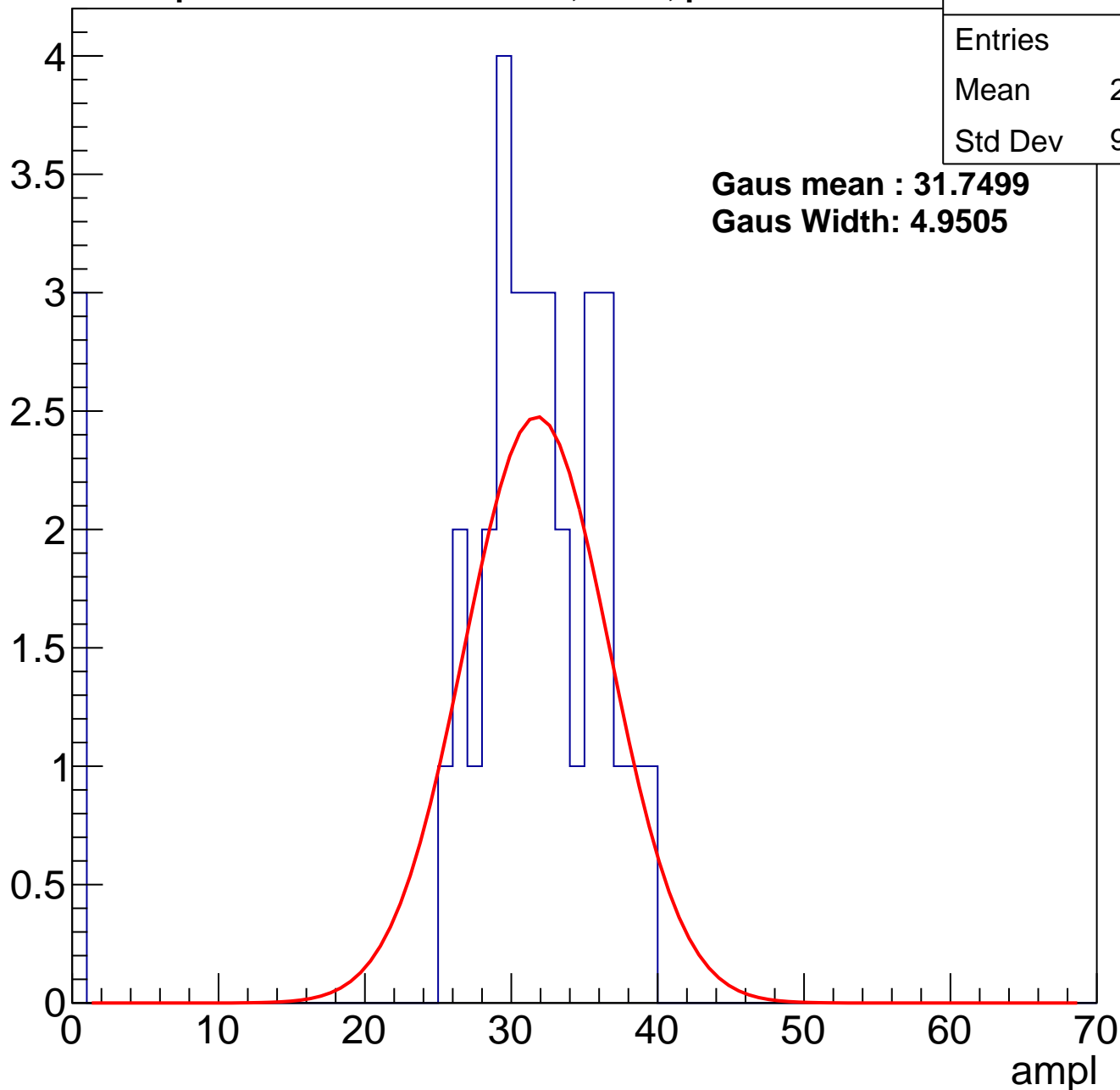
Entry



B1L103S, U15-ch113, adc0

calib_packv5_042523_0143.root, FC#7, port C2

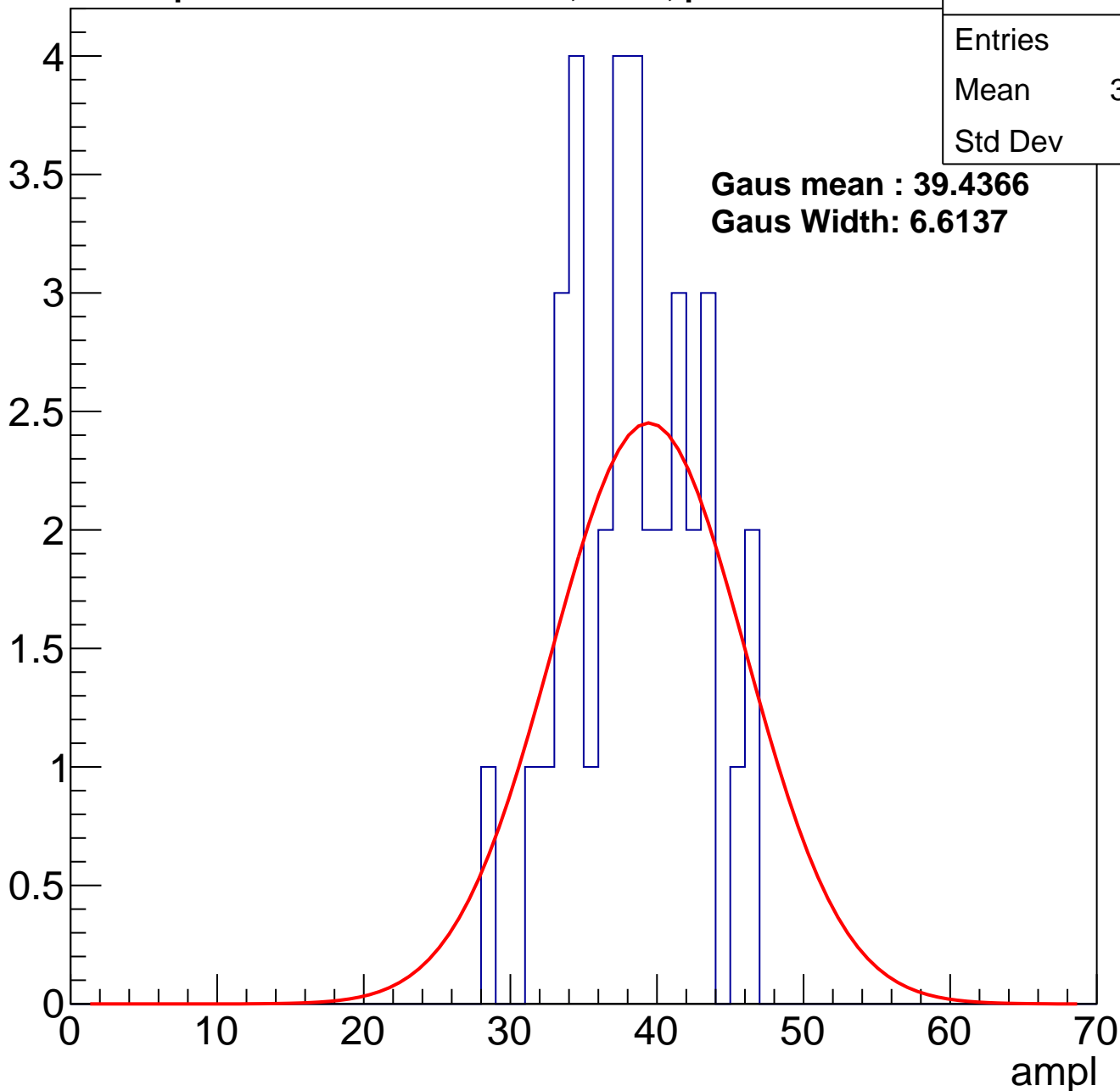
Entry



B1L103S, U15-ch113, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch113, adc2

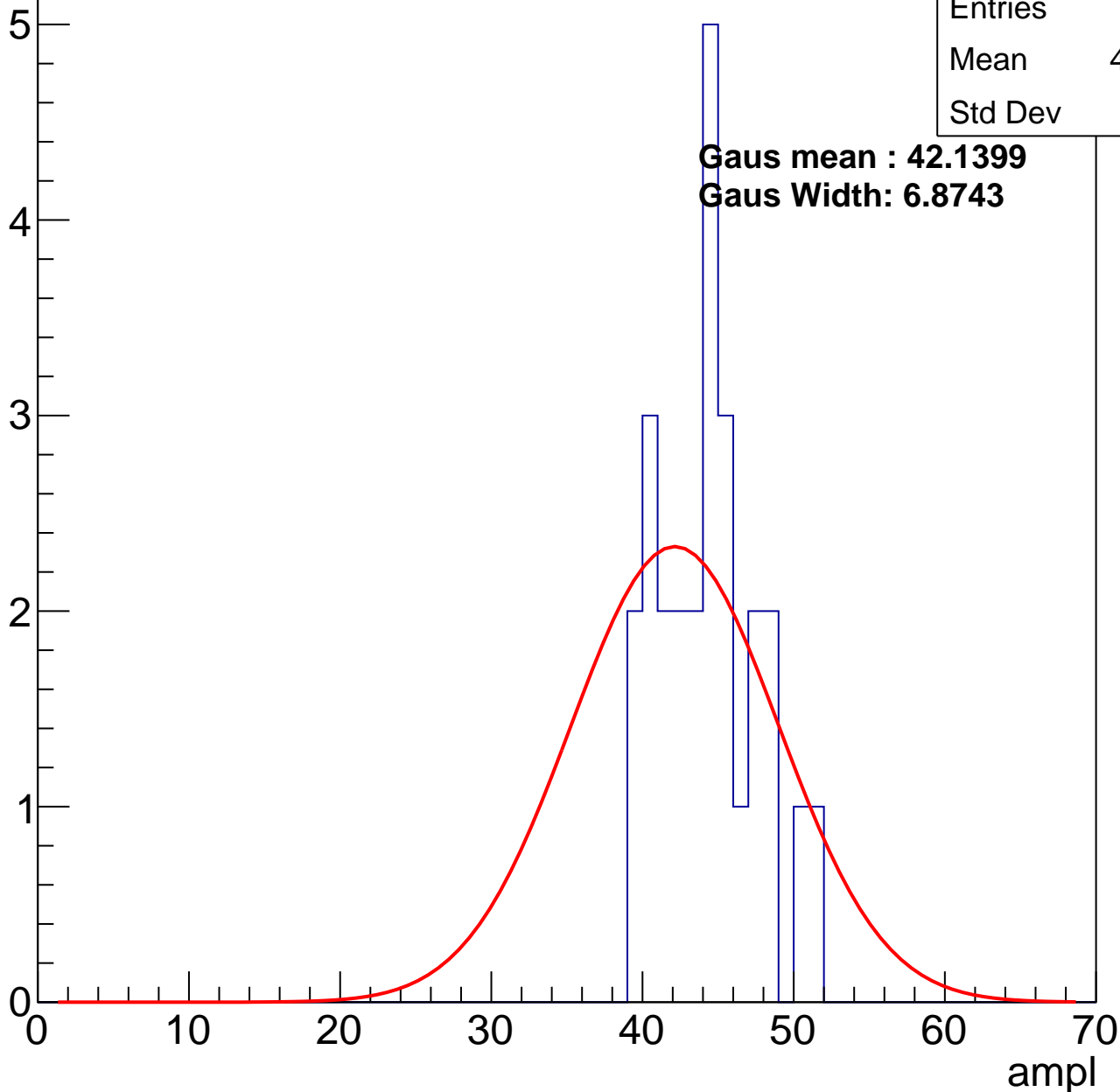
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	26
Mean	43.92
Std Dev	3.21

Gaus mean : 42.1399

Gaus Width: 6.8743

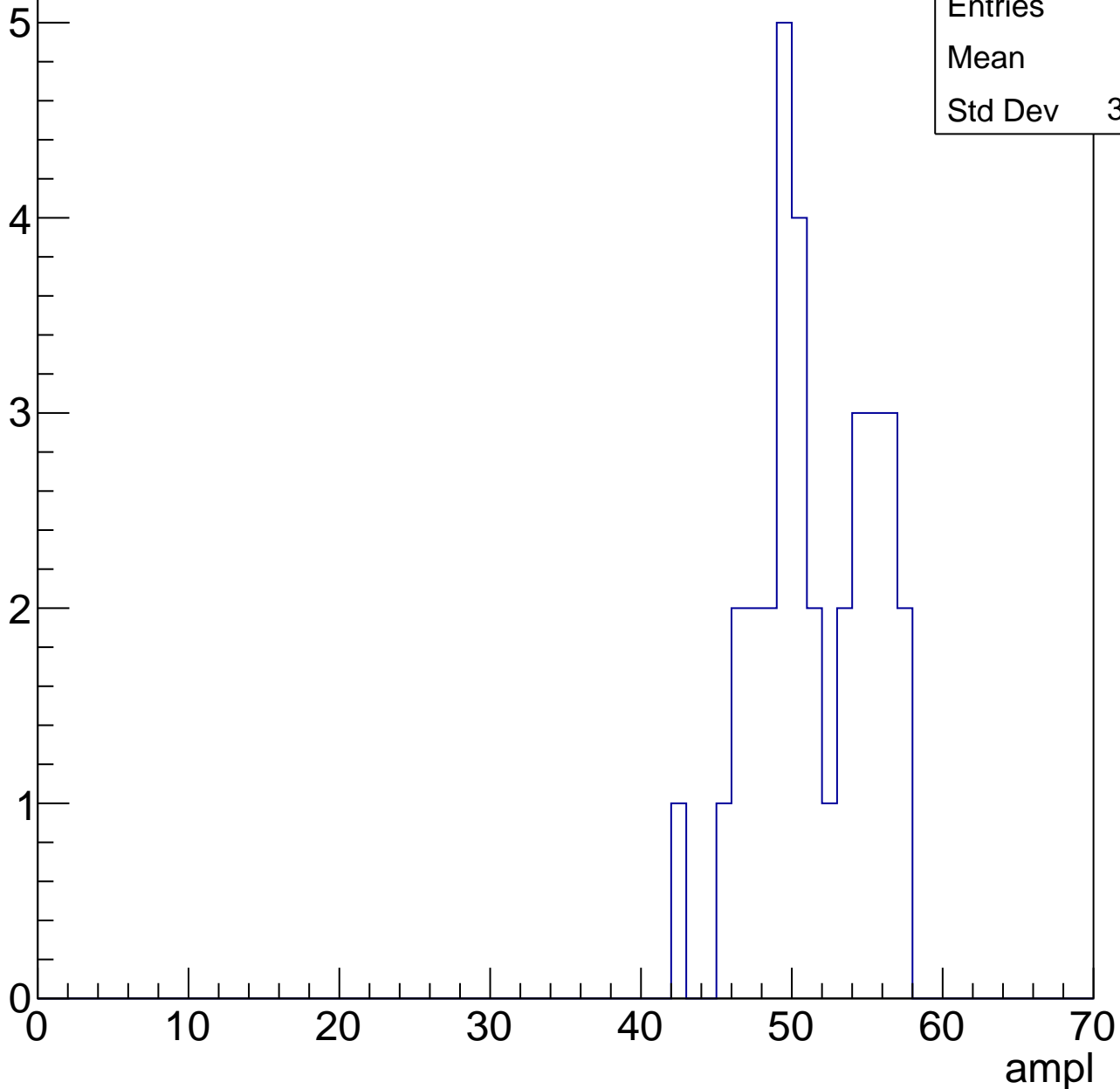


B1L103S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	51
Std Dev	3.782

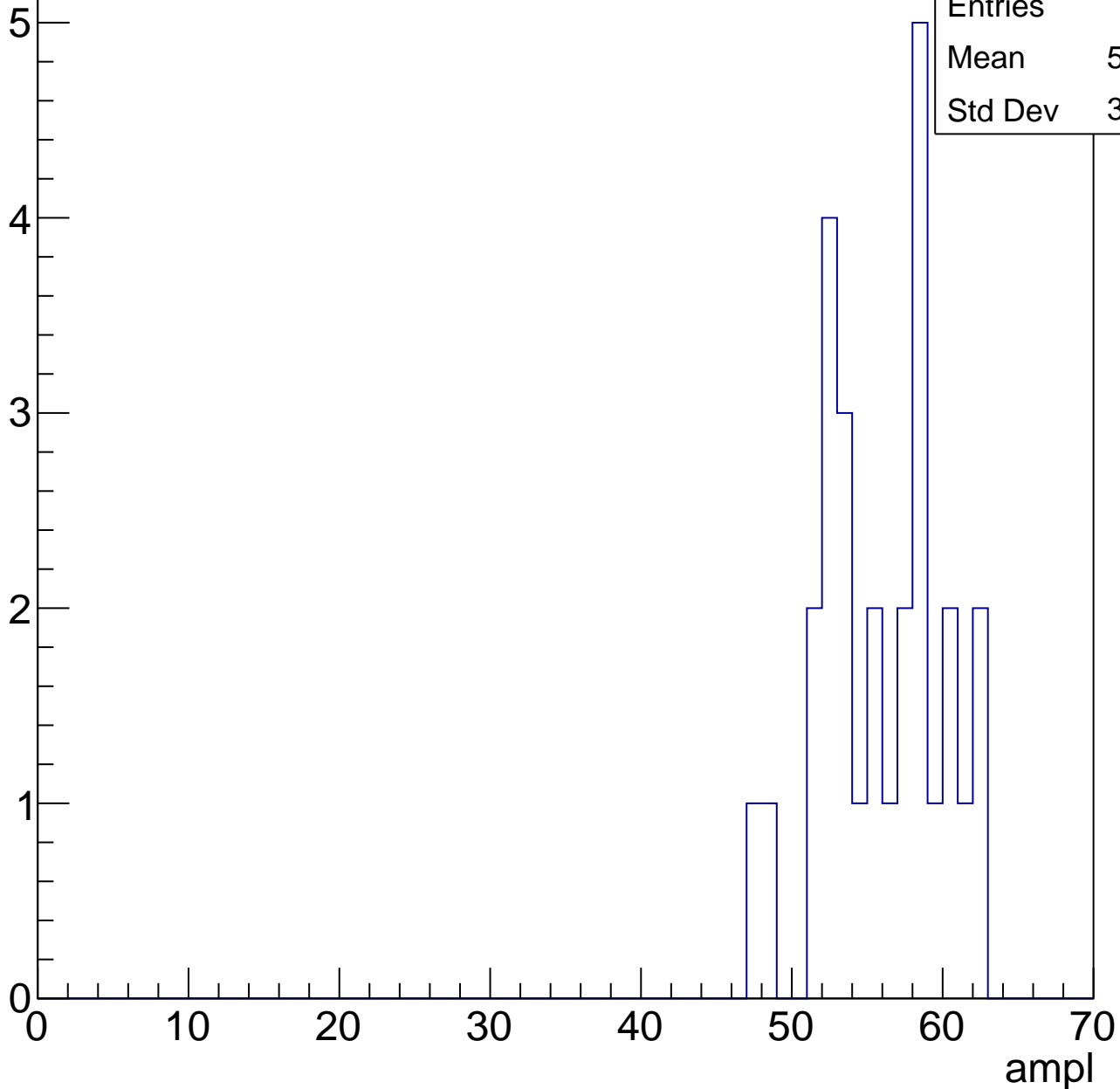


B1L103S, U15-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

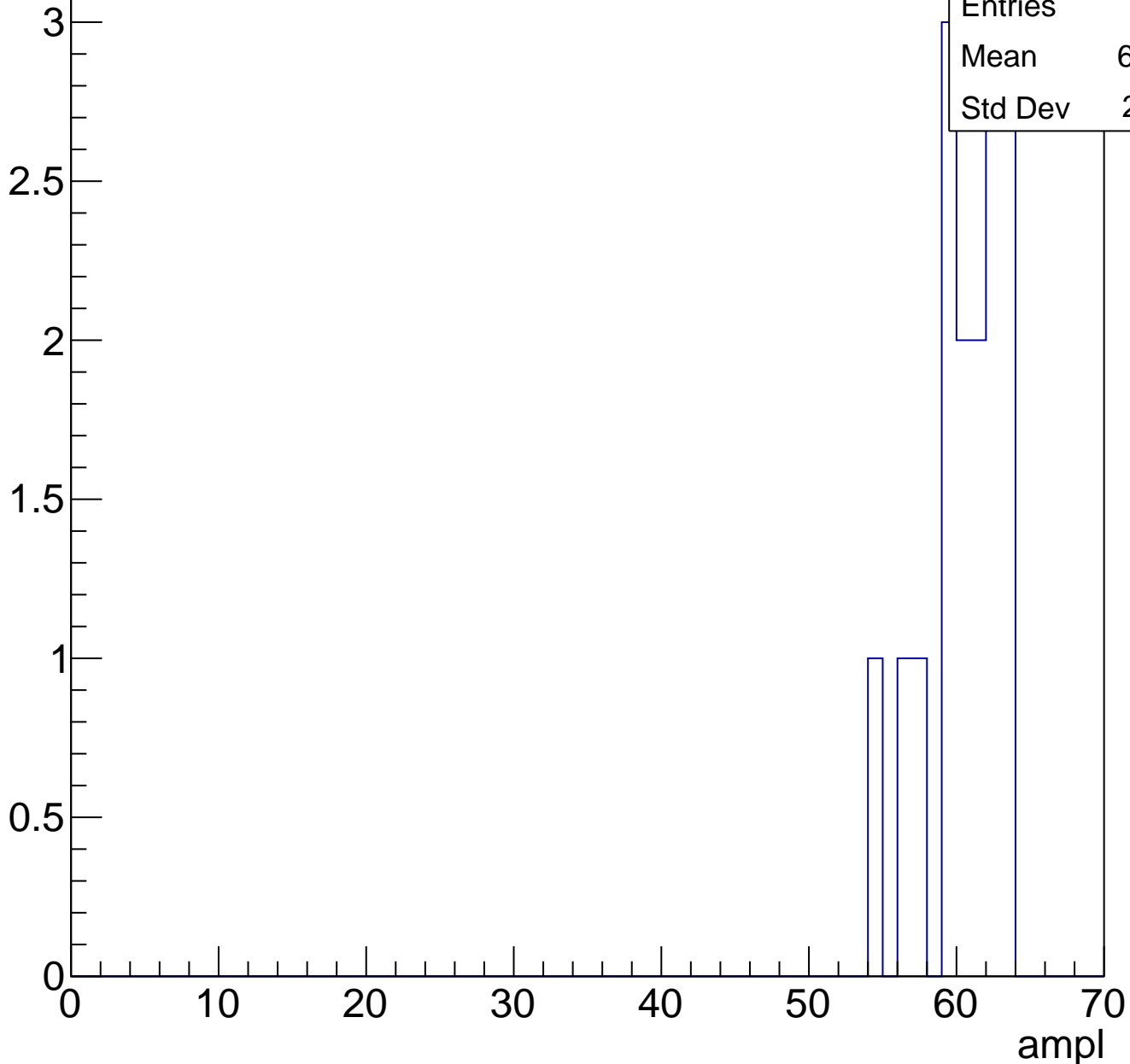
Entries	28
Mean	55.43
Std Dev	3.977



B1L103S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	16
Mean	60.06
Std Dev	2.561

B1L103S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

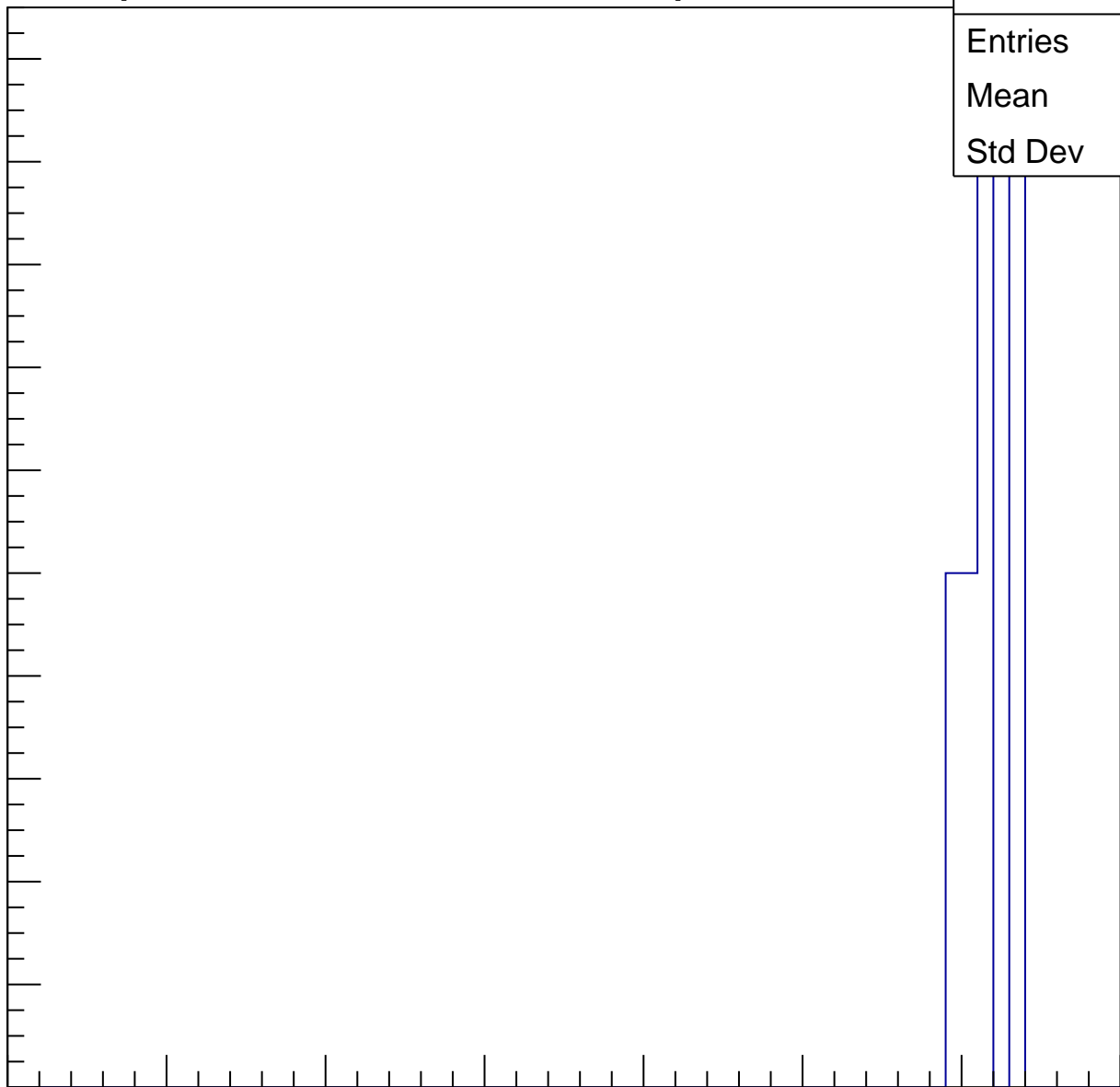
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.462

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

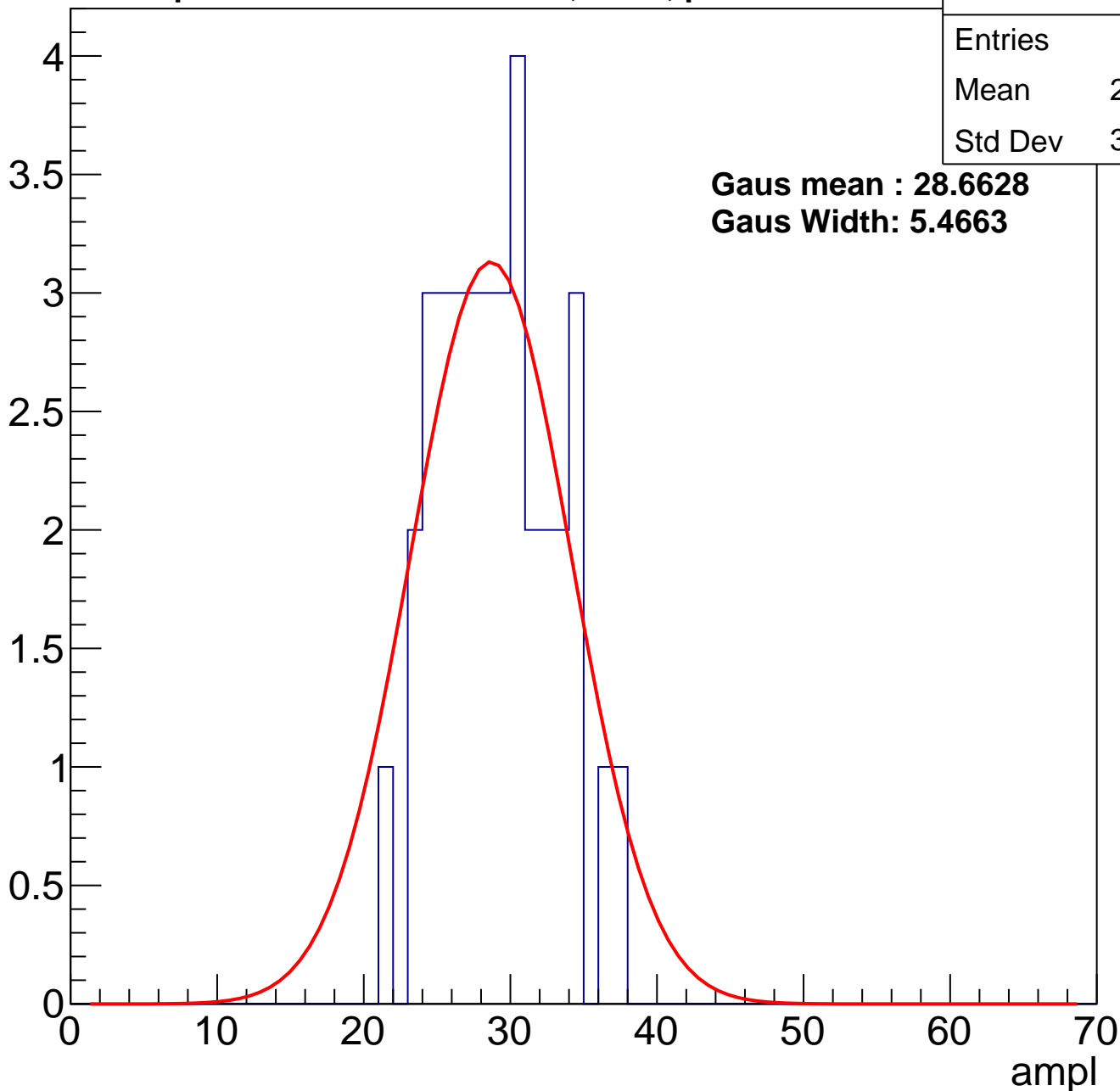
Entry



B1L103S, U15-ch114, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	36
Mean	28.64
Std Dev	3.896

B1L103S, U15-ch114, adc1

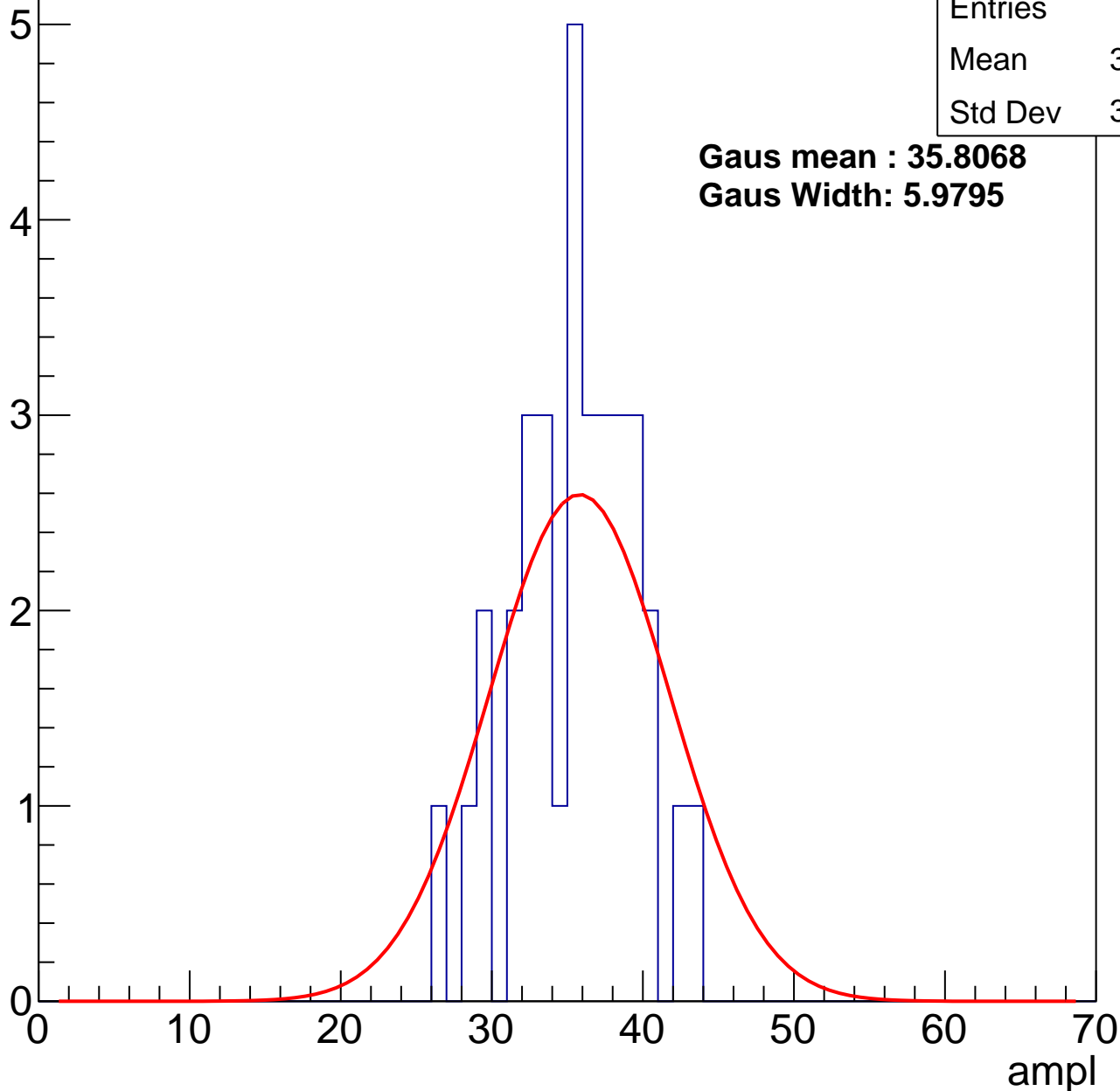
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	35.09
Std Dev	3.936

Gaus mean : 35.8068

Gaus Width: 5.9795



B1L103S, U15-ch114, adc2

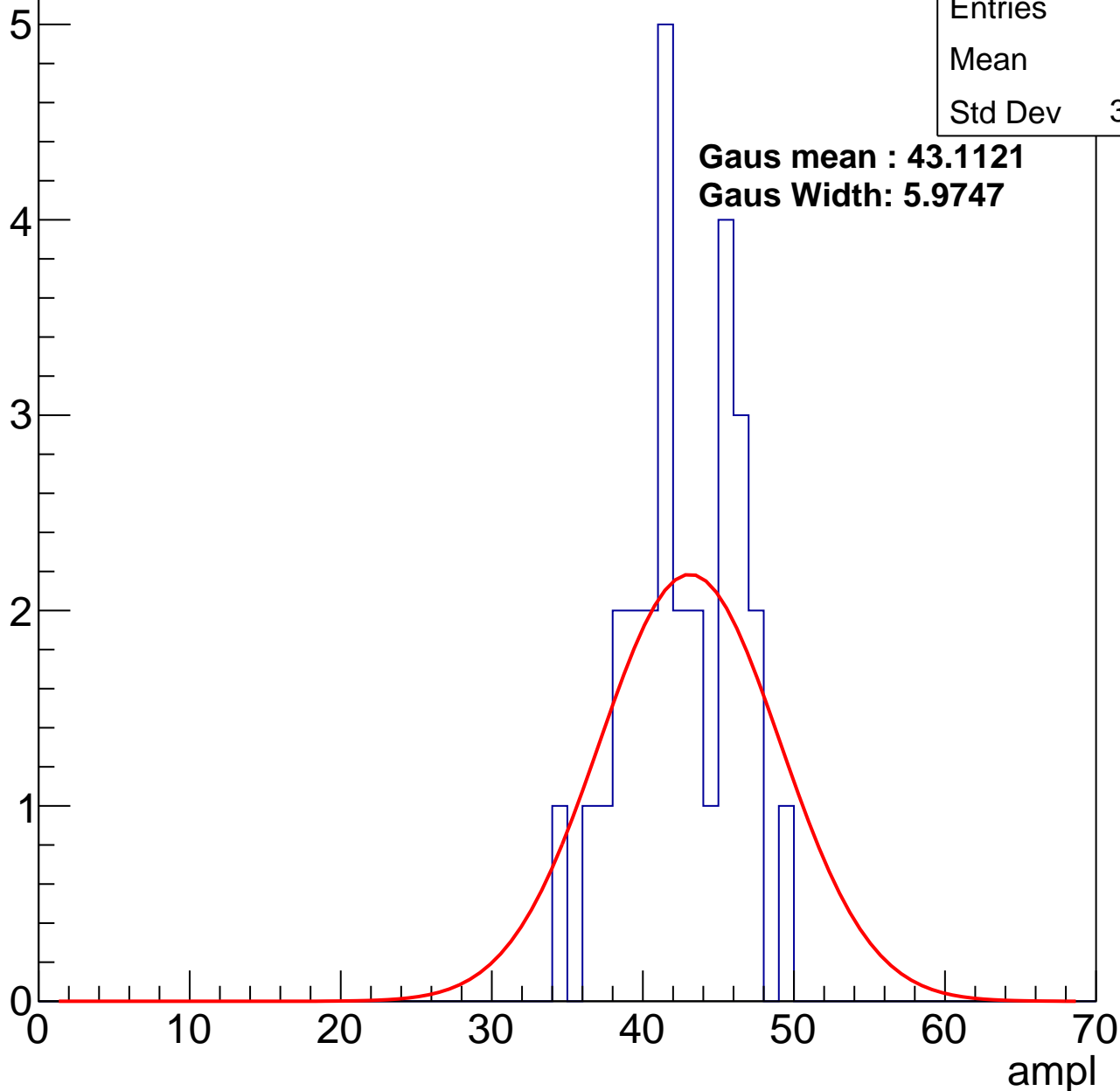
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	42.1
Std Dev	3.604

Gaus mean : 43.1121

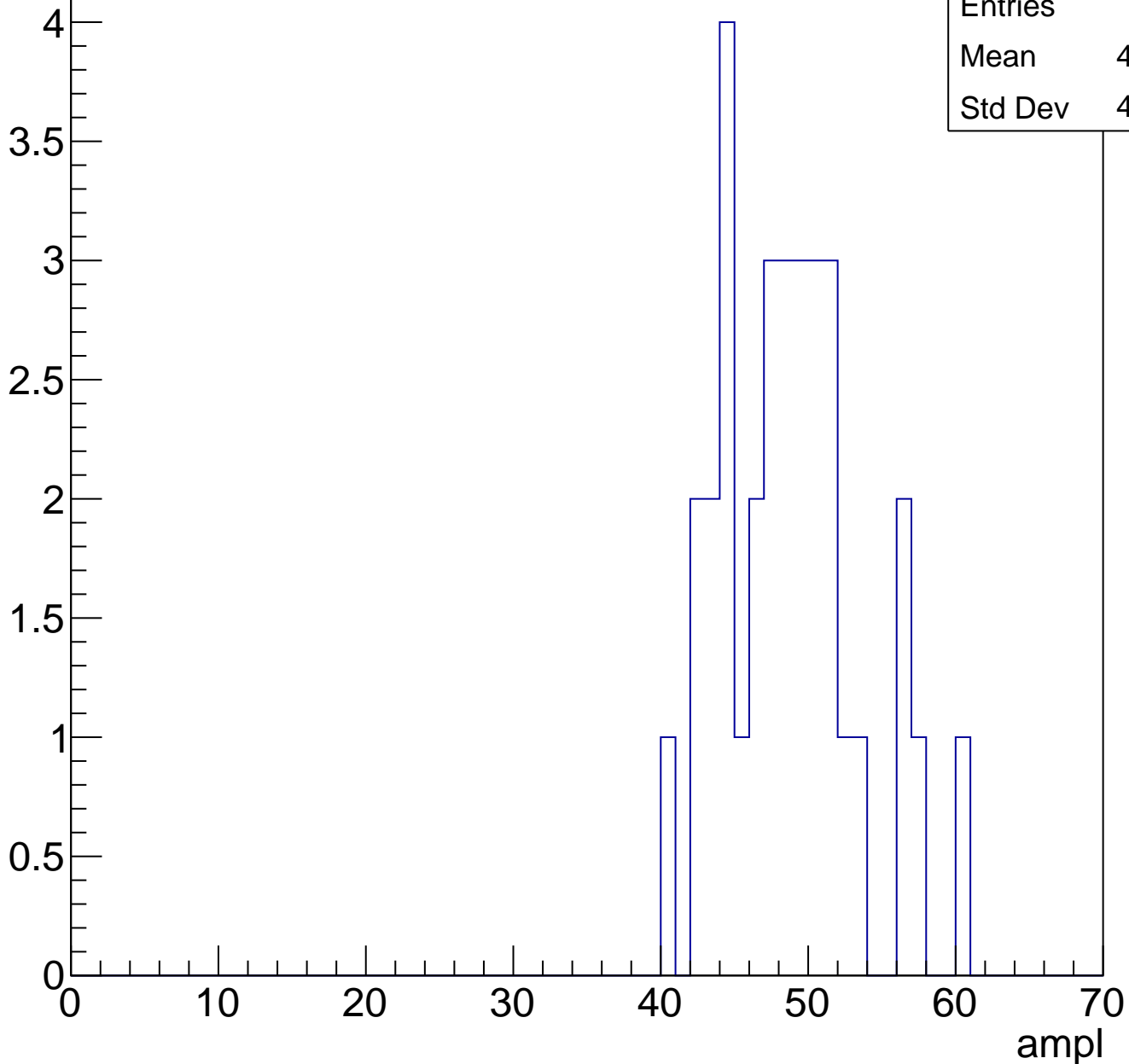
Gaus Width: 5.9747



B1L103S, U15-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

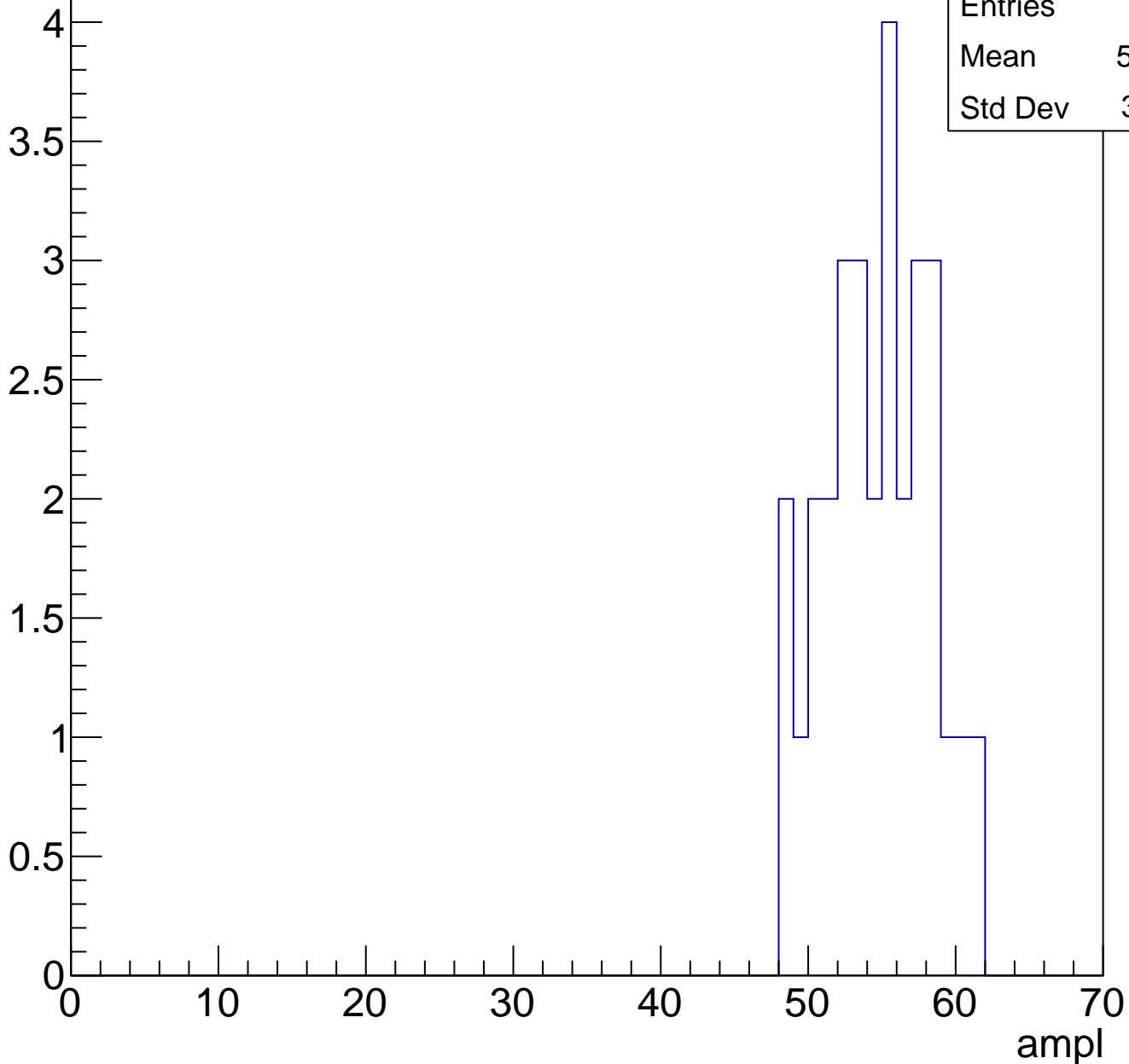
Entry



B1L103S, U15-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

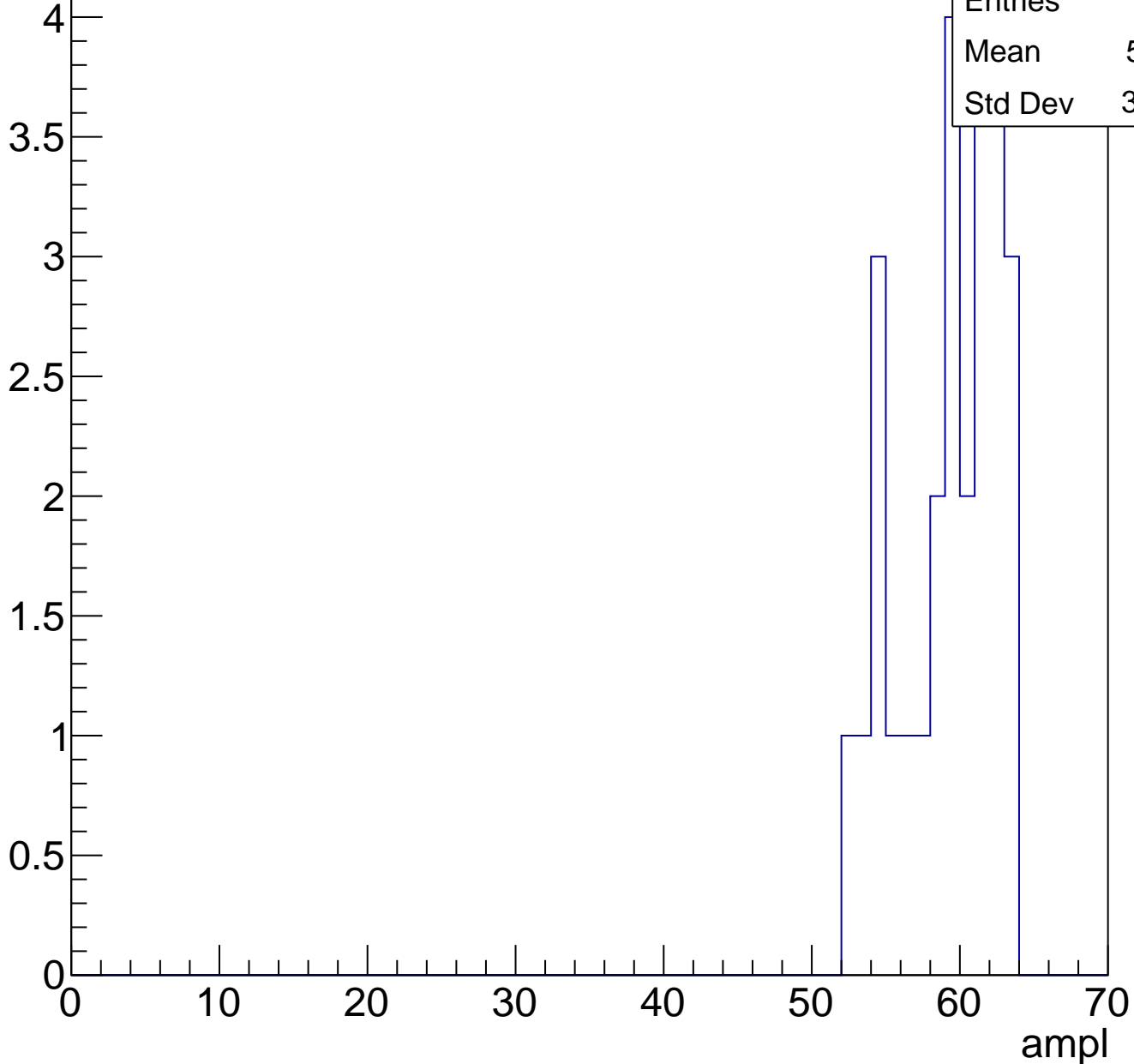
Entry



B1L103S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

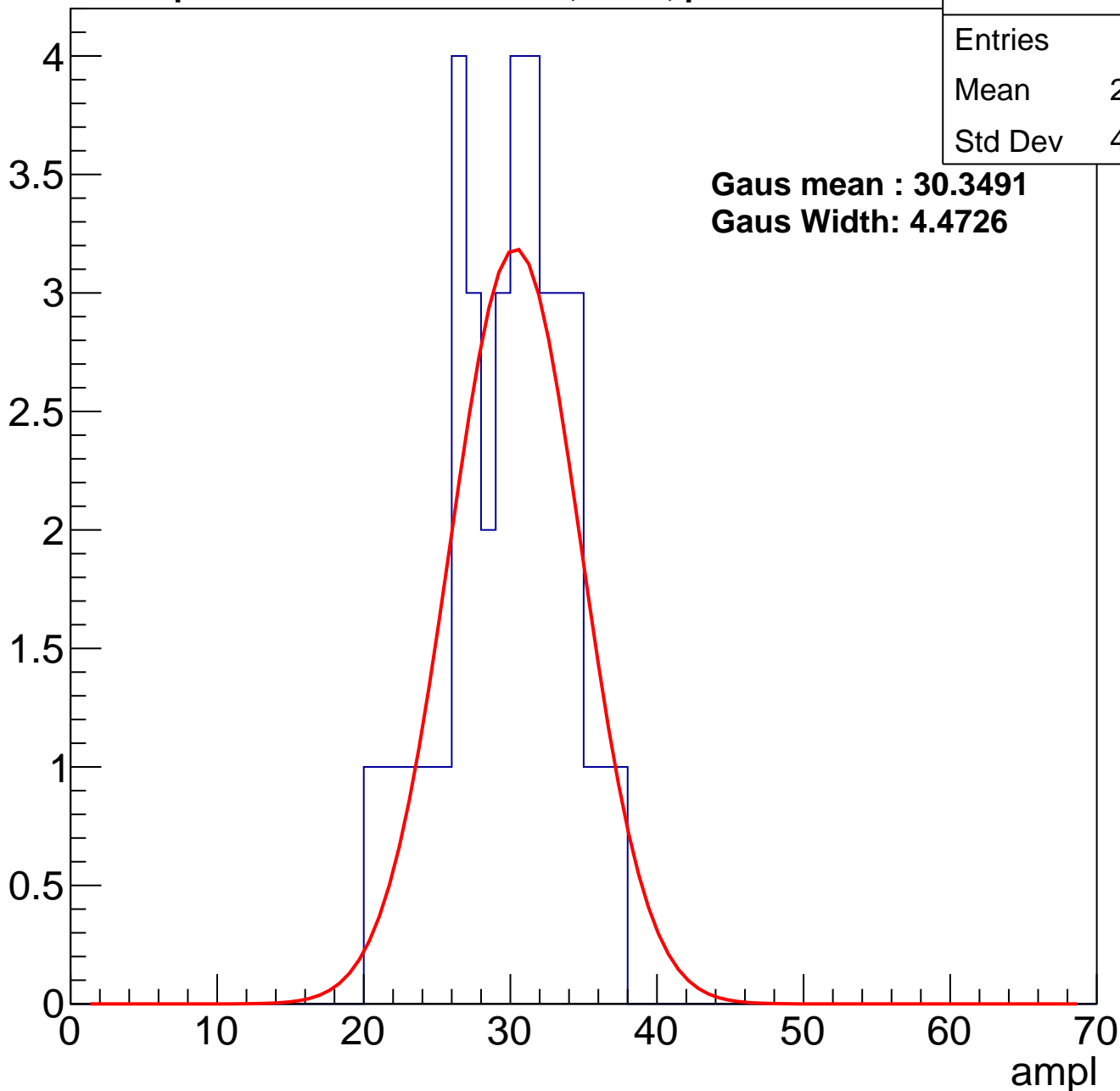
Entry



B1L103S, U15-ch115, adc0

calib_packv5_042523_0143.root, FC#7, port C2

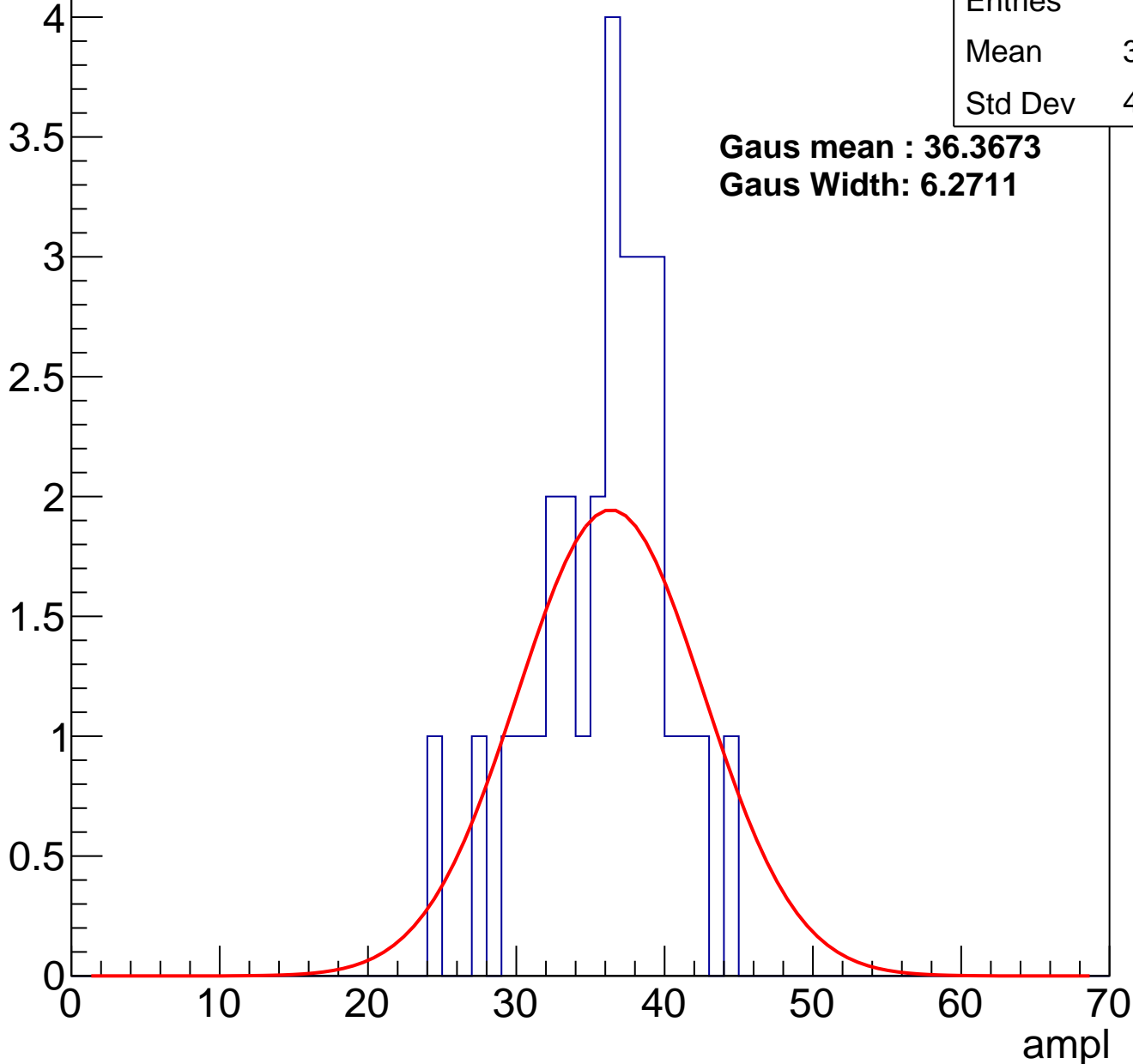
Entry



B1L103S, U15-ch115, adc1

calib_packv5_042523_0143.root, FC#7, port C2

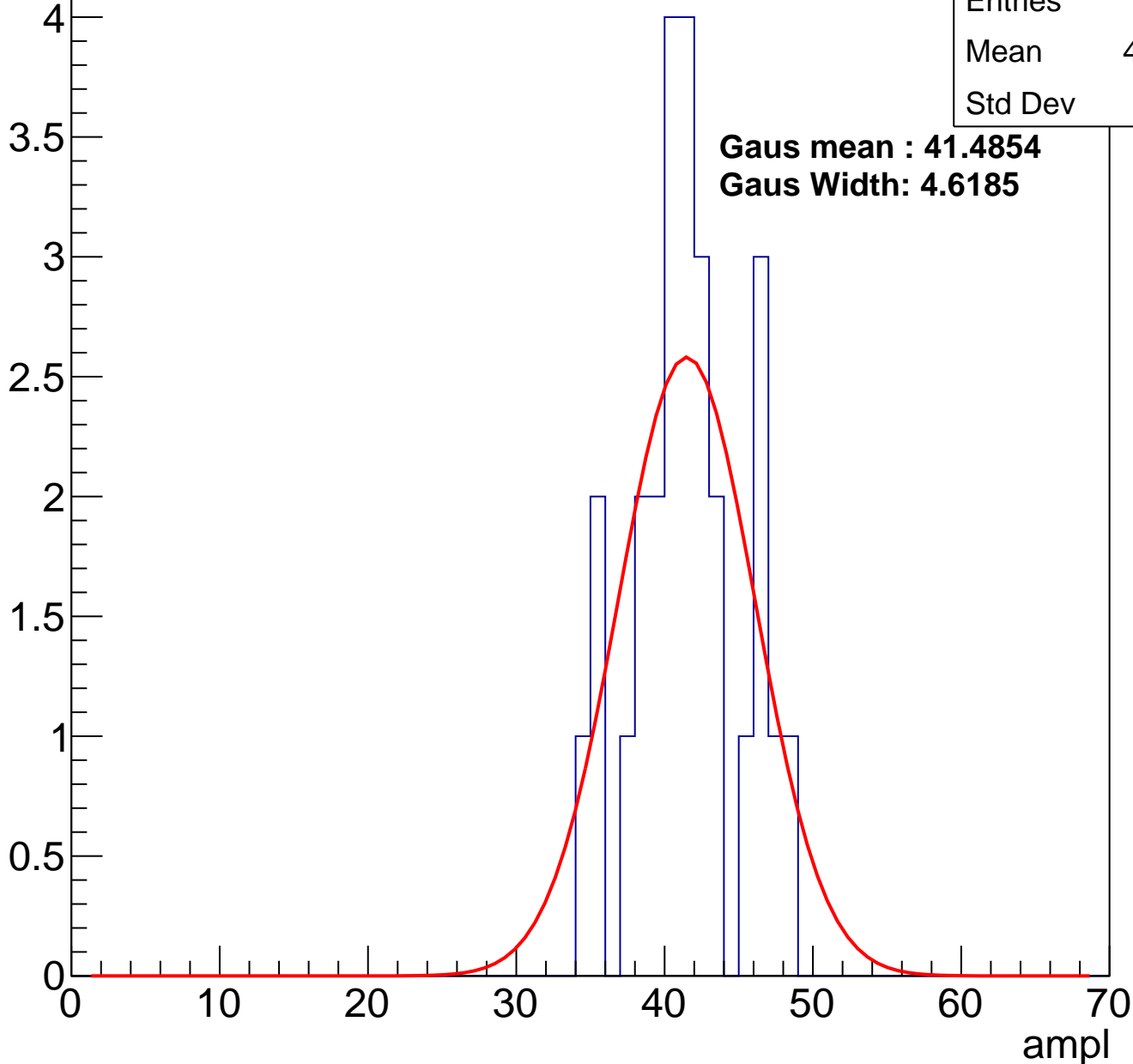
Entry



B1L103S, U15-ch115, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

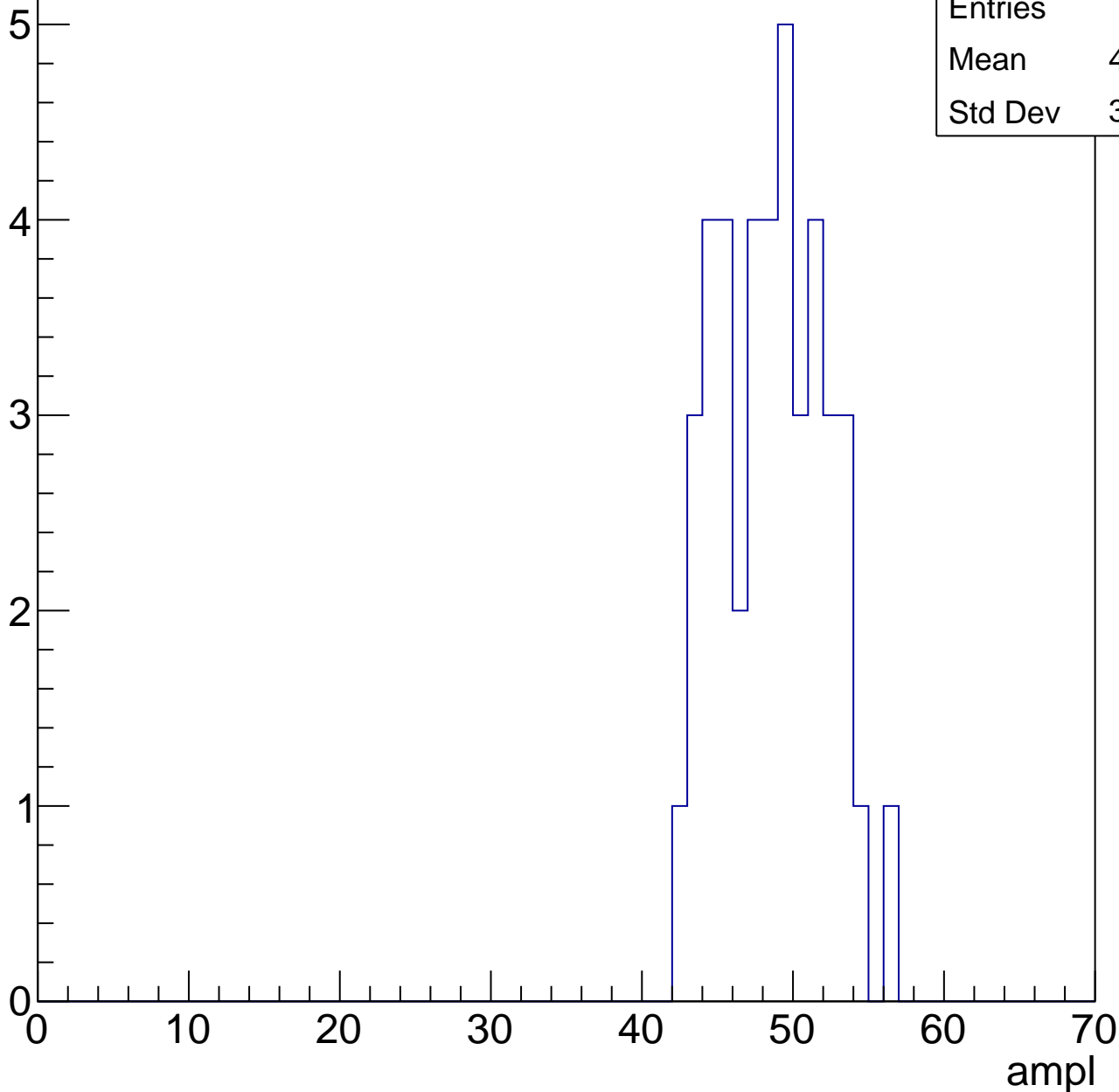


B1L103S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

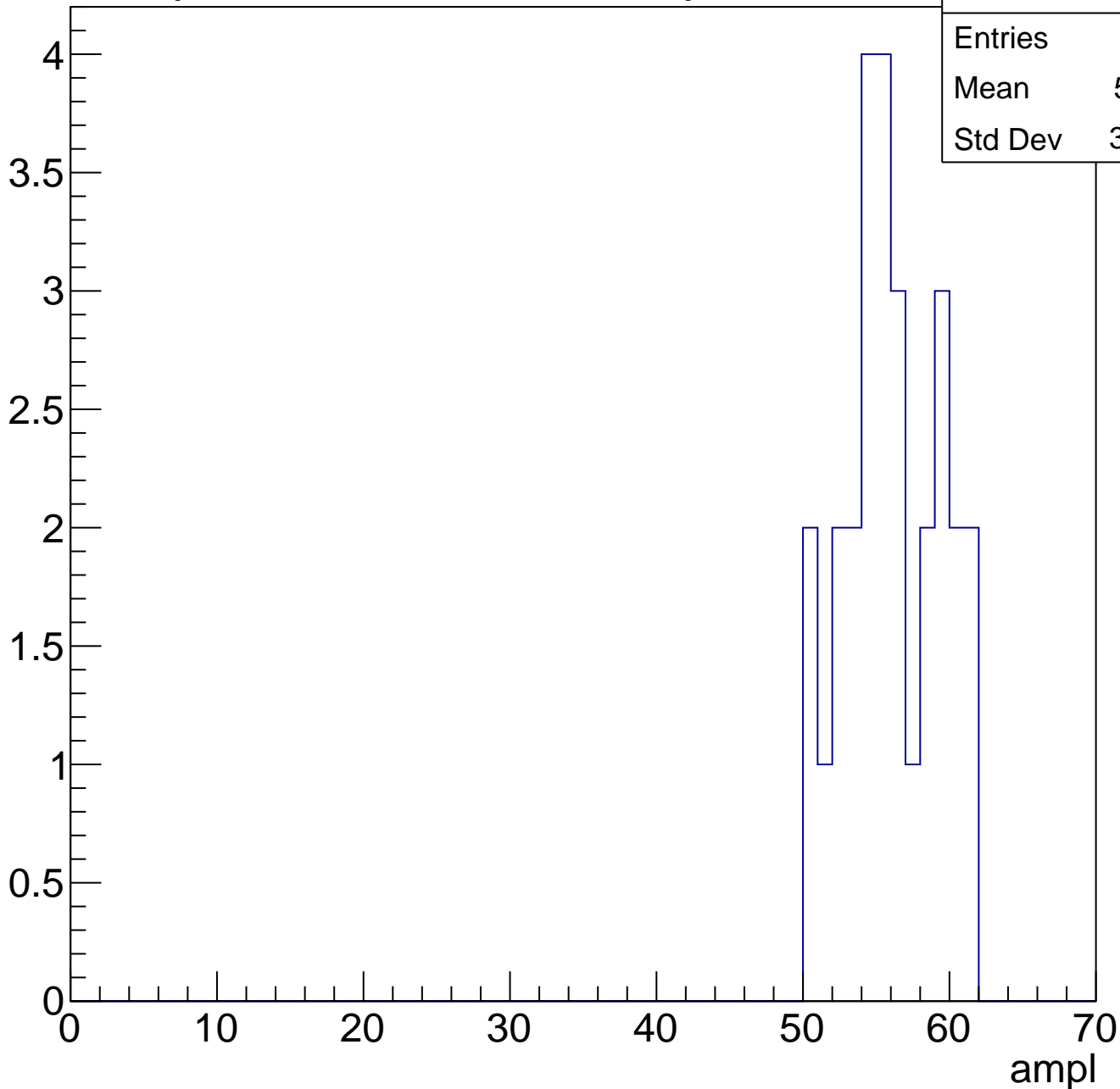
Entries	42
Mean	48.17
Std Dev	3.443



B1L103S, U15-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

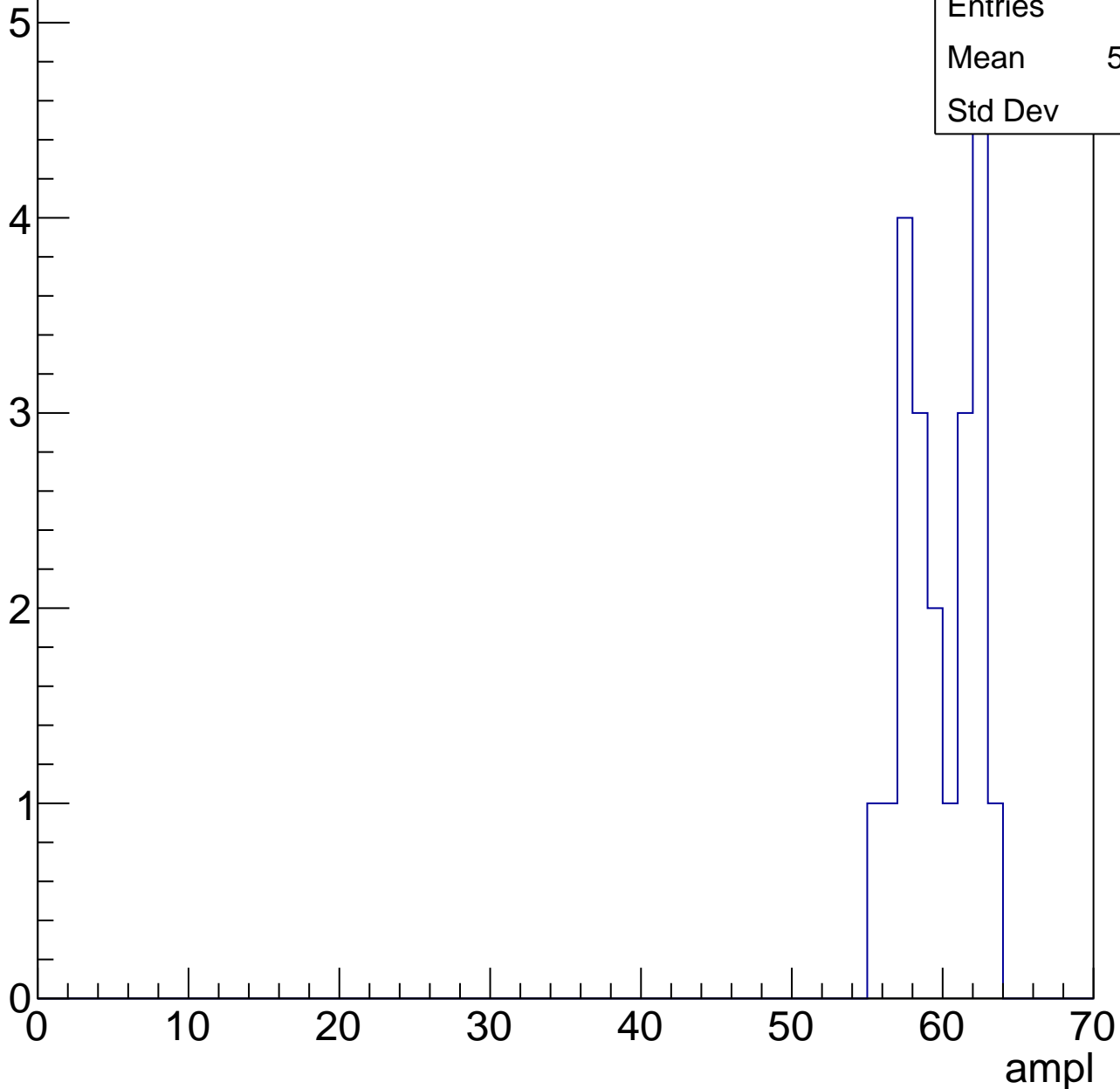


B1L103S, U15-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

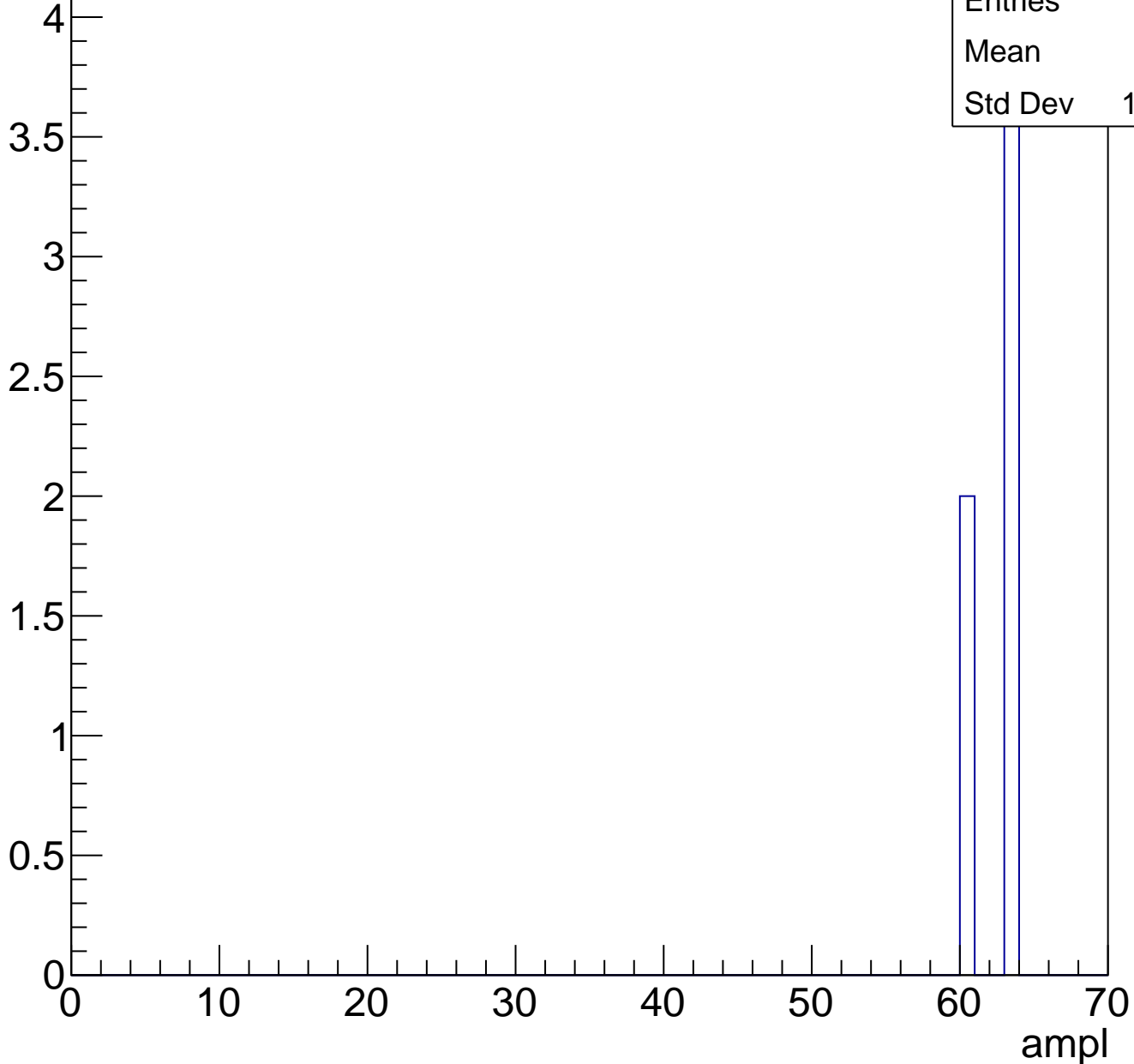
Entries	21
Mean	59.38
Std Dev	2.34



B1L103S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

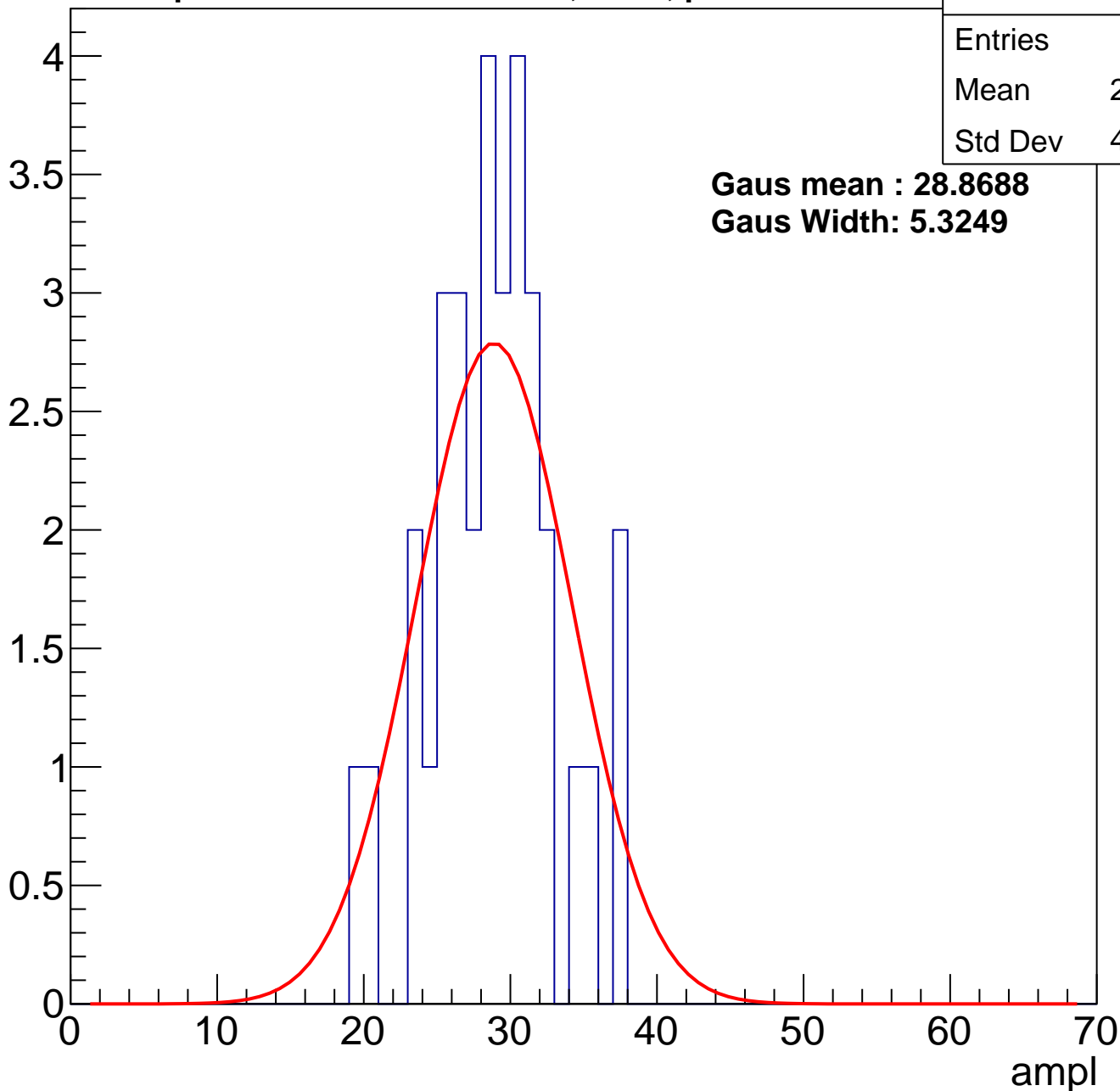
Entry



B1L103S, U15-ch116, adc0

calib_packv5_042523_0143.root, FC#7, port C2

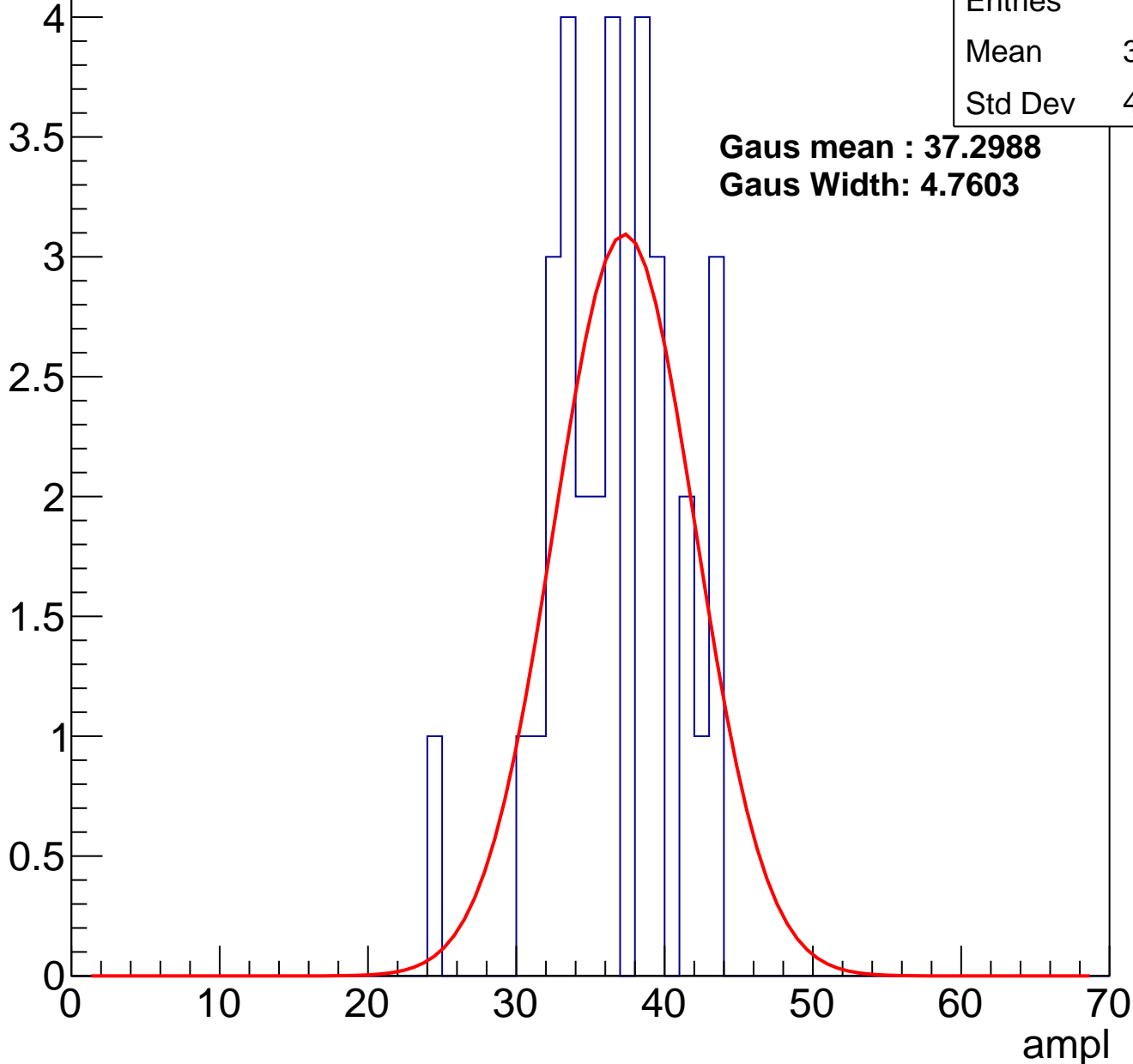
Entry



B1L103S, U15-ch116, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch116, adc2

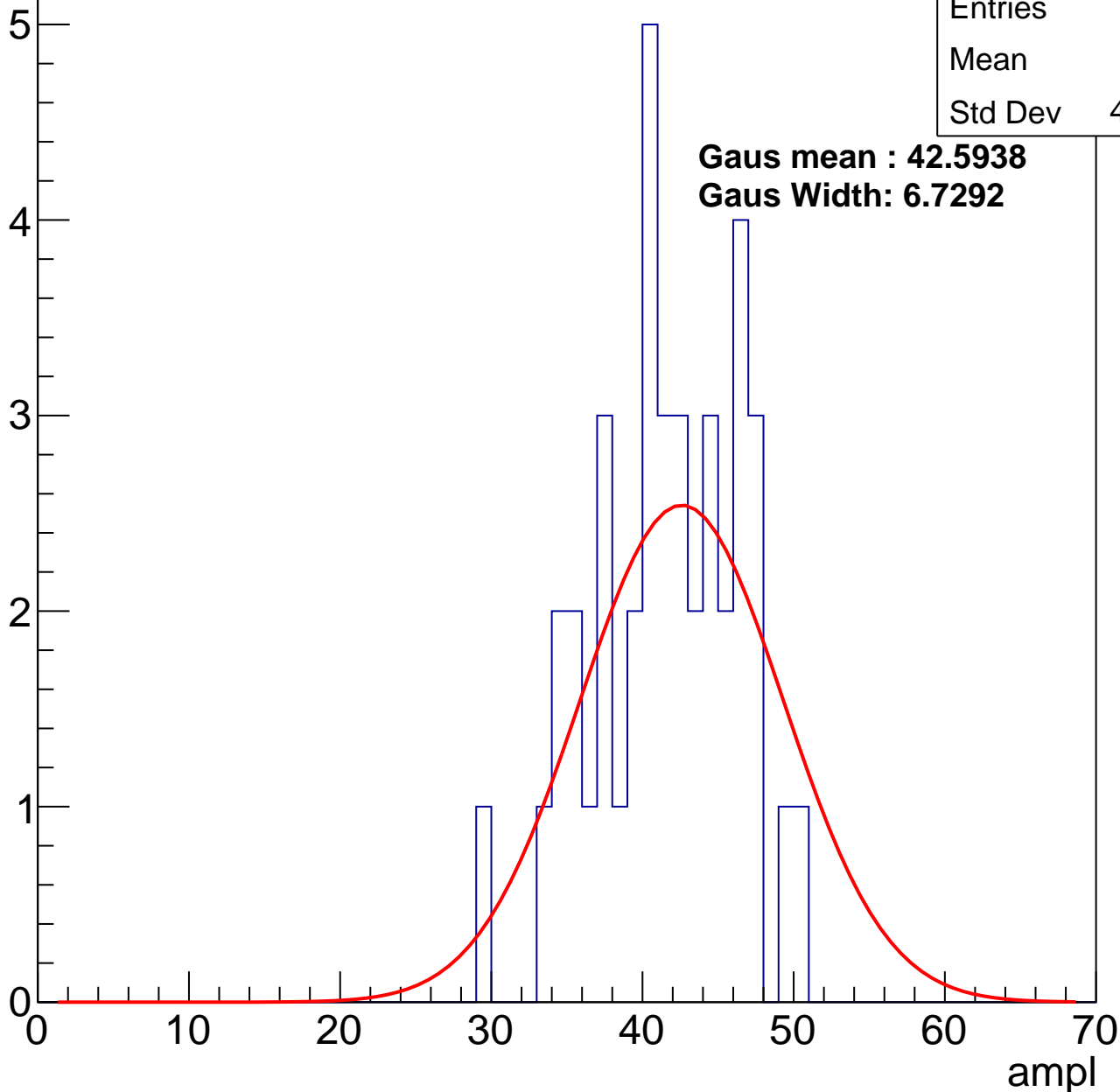
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	41.1
Std Dev	4.732

Gaus mean : 42.5938

Gaus Width: 6.7292



B1L103S, U15-ch116, adc3

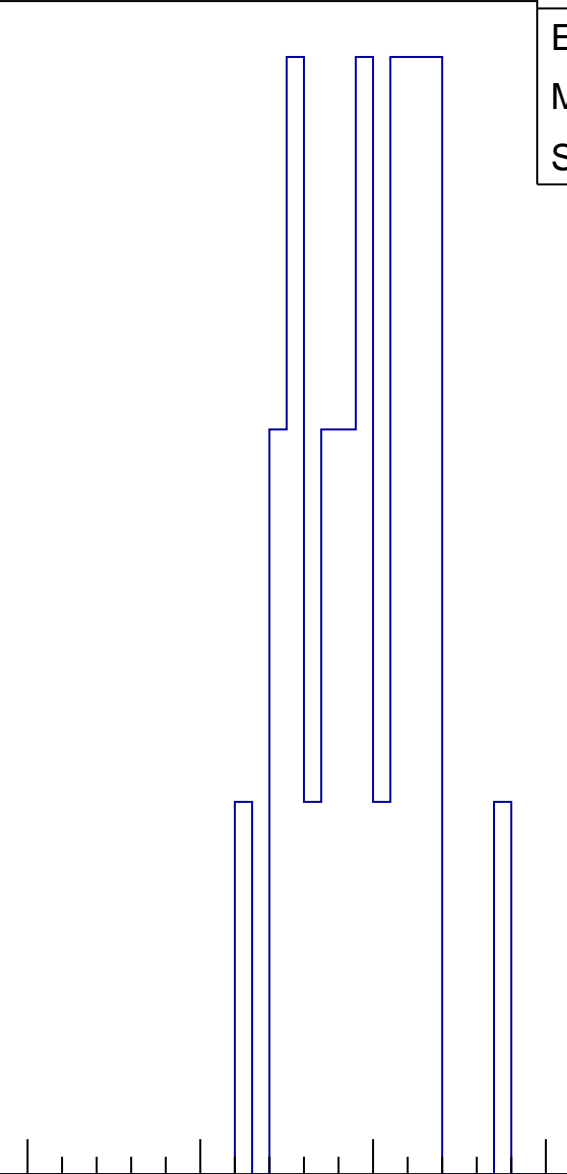
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	25
Mean	48.92
Std Dev	3.566

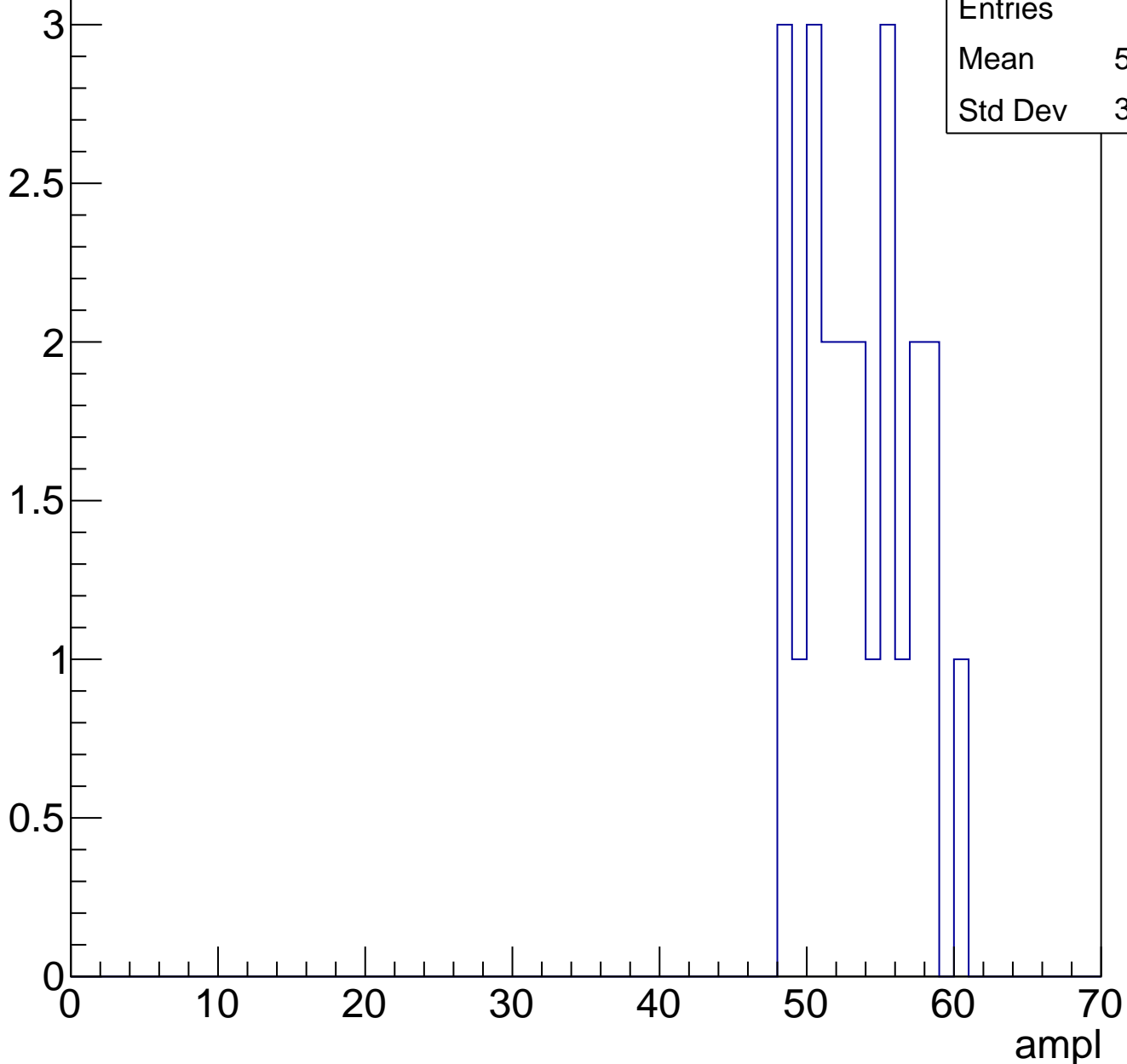
ampl



B1L103S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch116, adc5

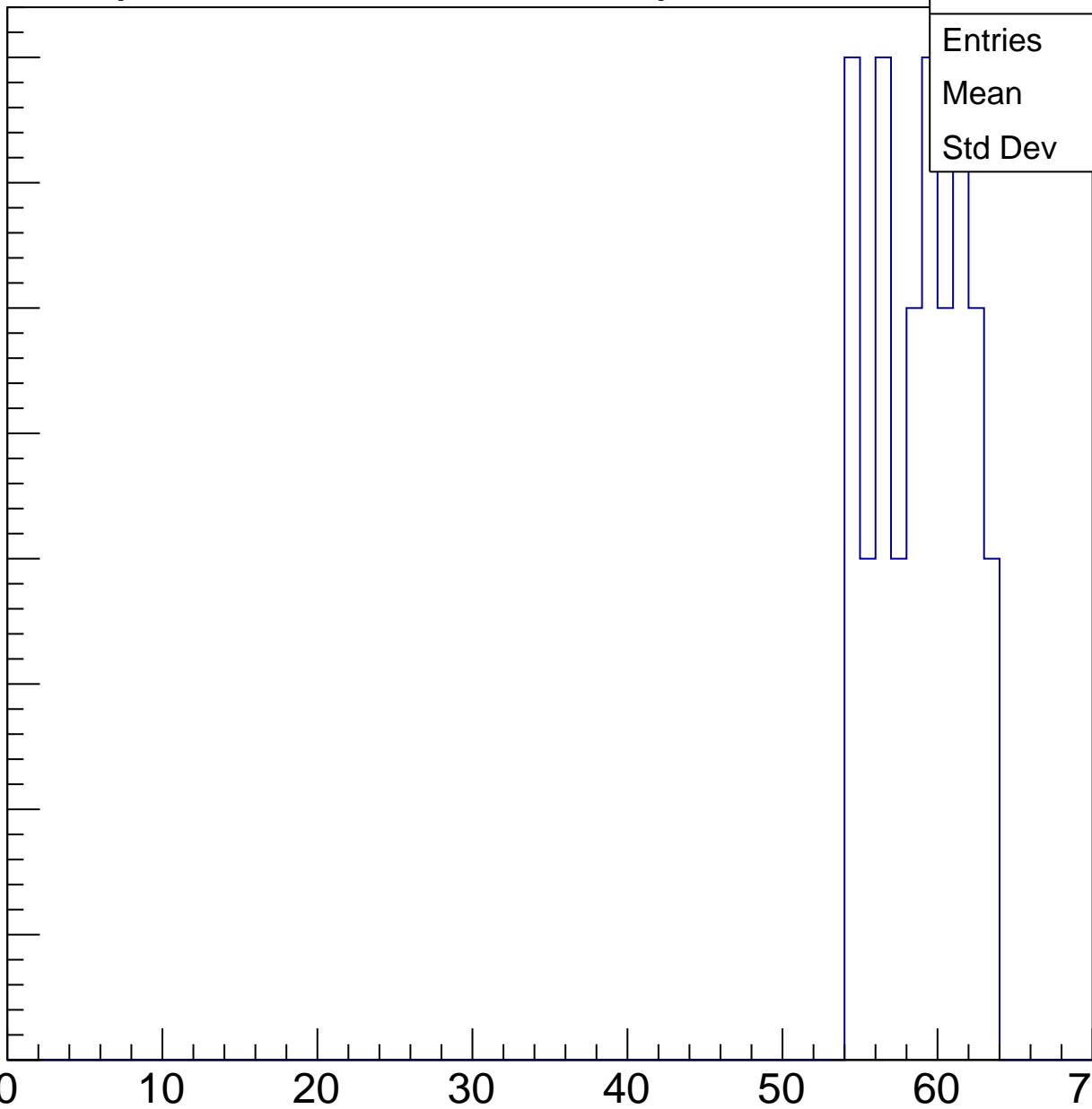
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	31
Mean	58.39
Std Dev	2.813

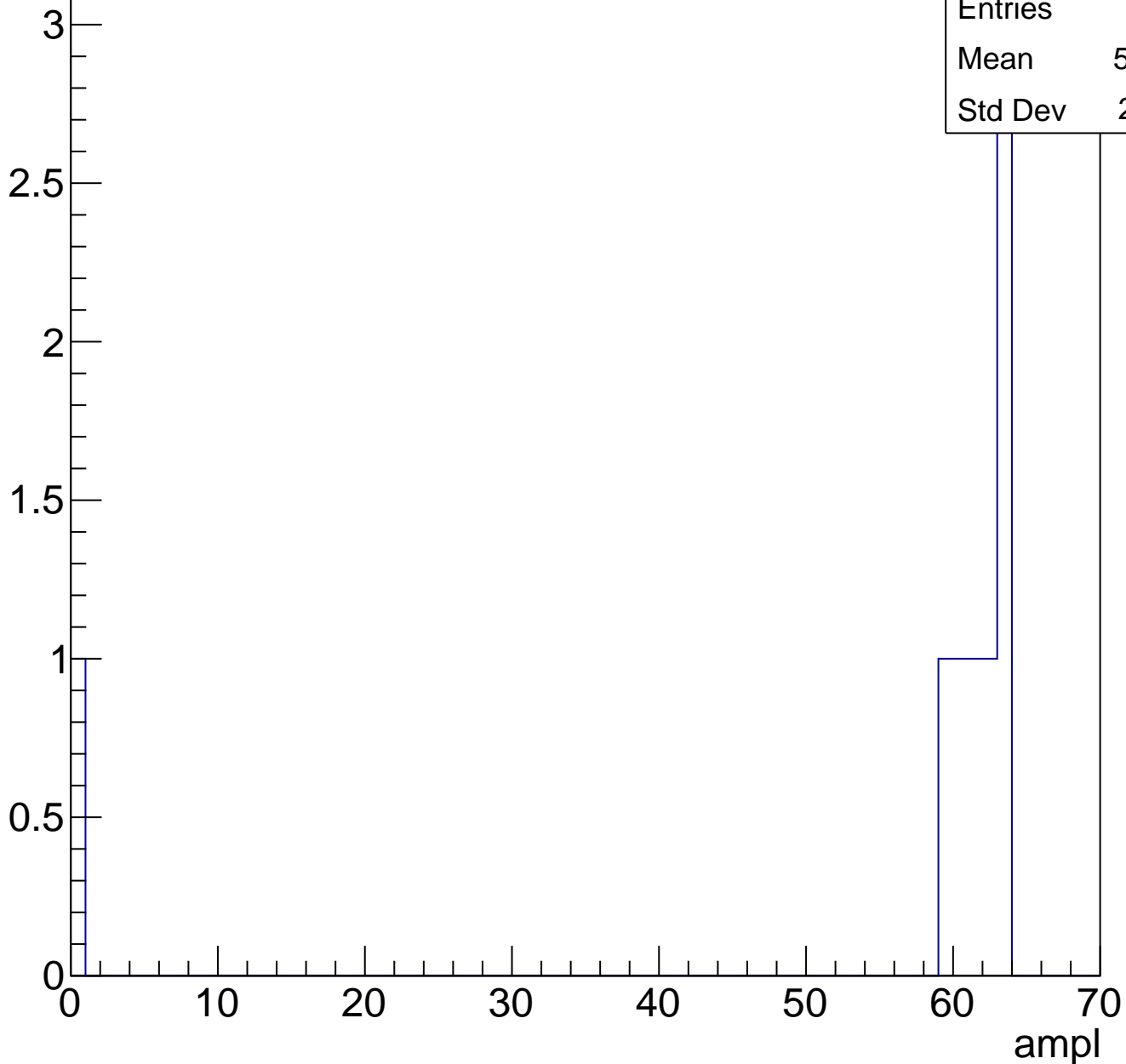
ampl



B1L103S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

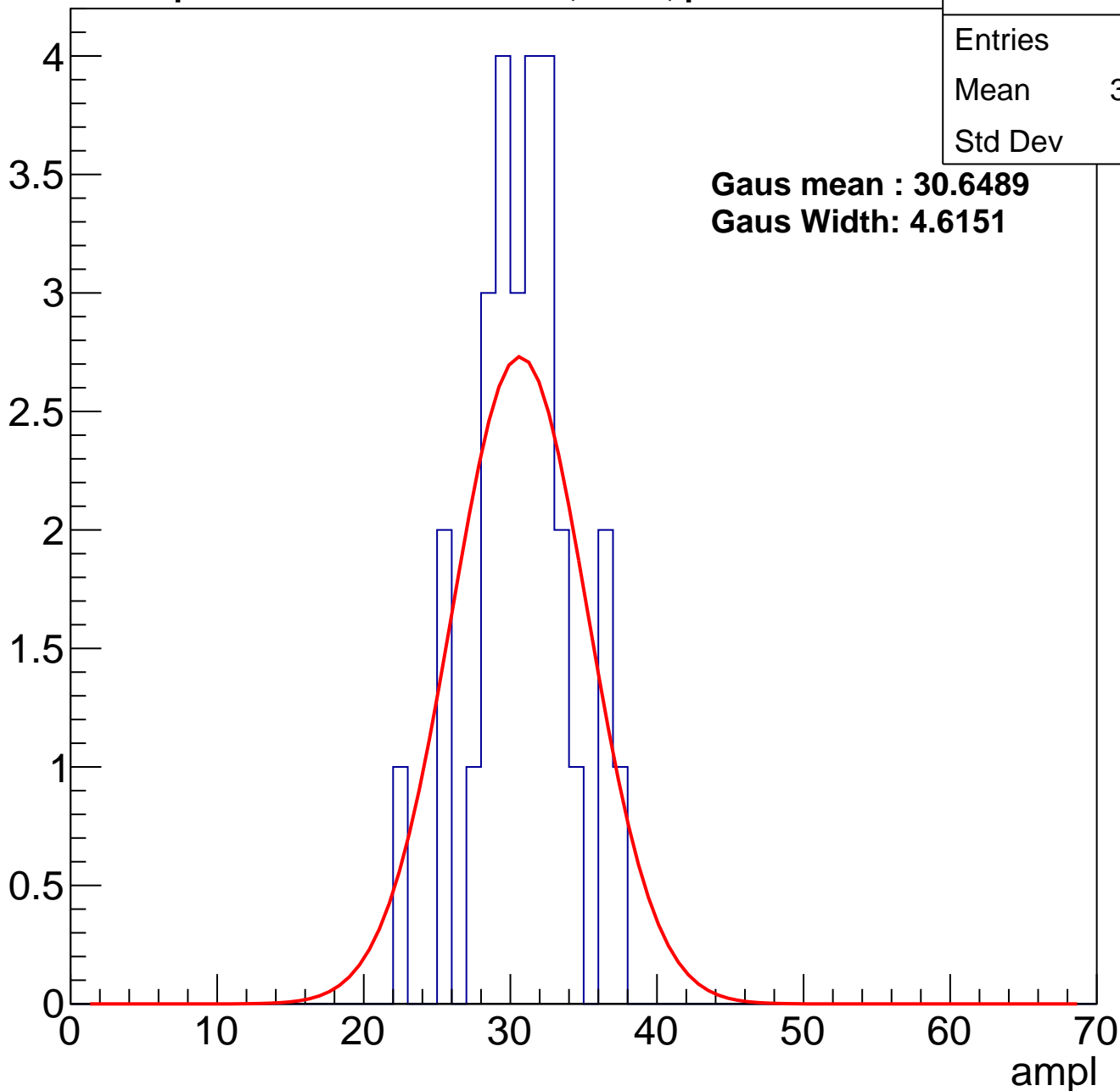


Entries	1
Mean	62
Std Dev	0

B1L103S, U15-ch117, adc0

calib_packv5_042523_0143.root, FC#7, port C2

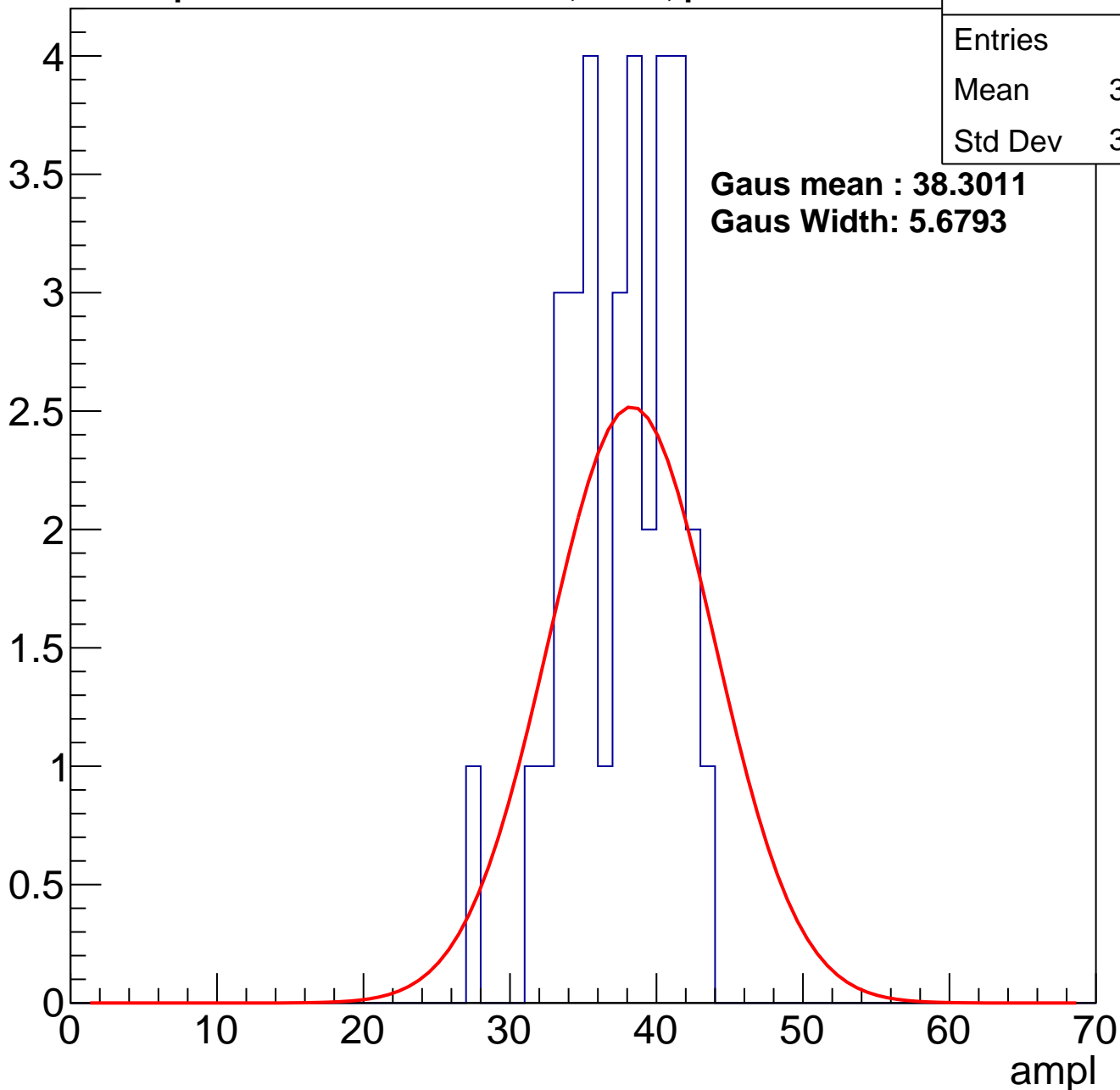
Entry



B1L103S, U15-ch117, adc1

calib_packv5_042523_0143.root, FC#7, port C2

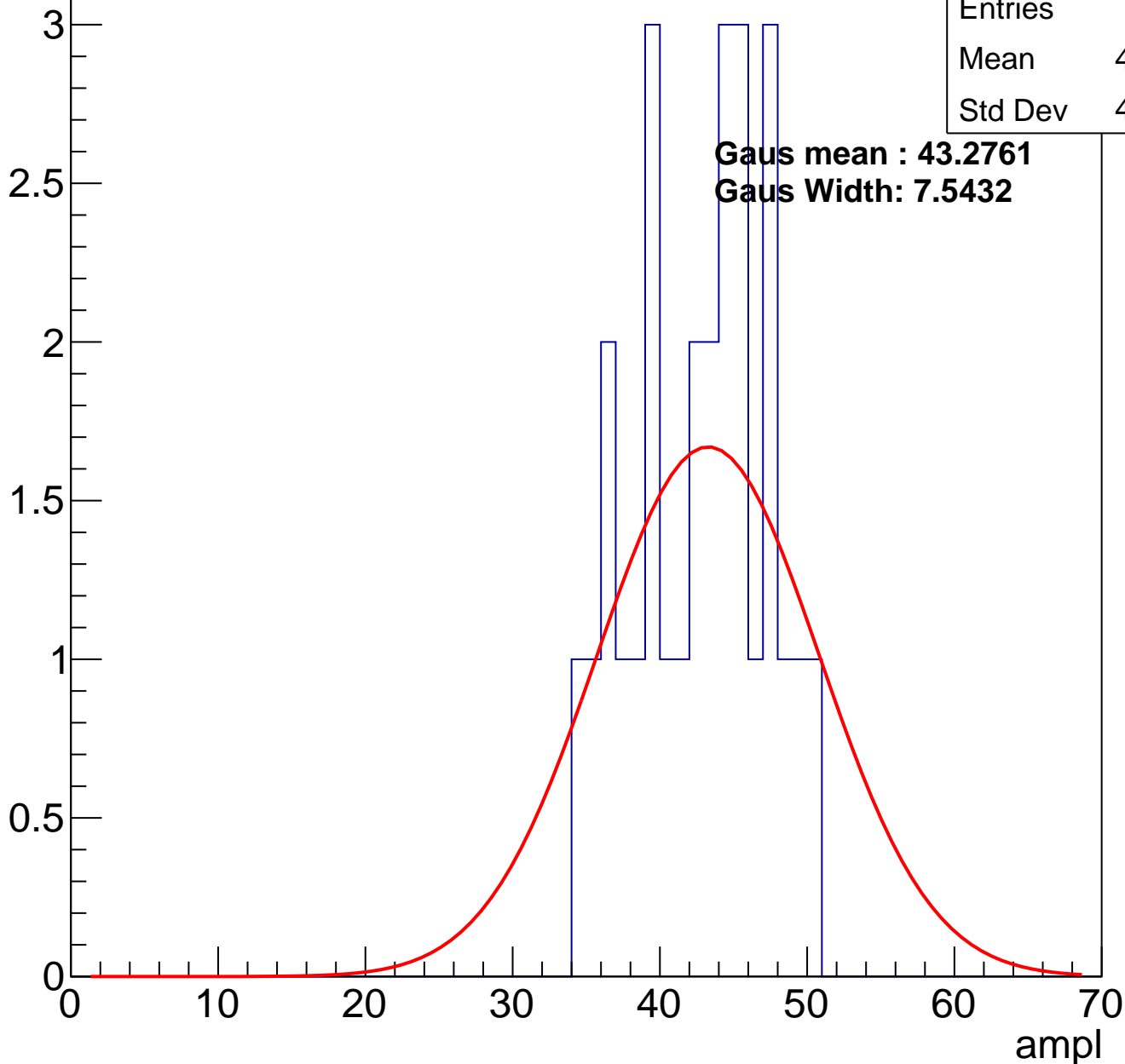
Entry



B1L103S, U15-ch117, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

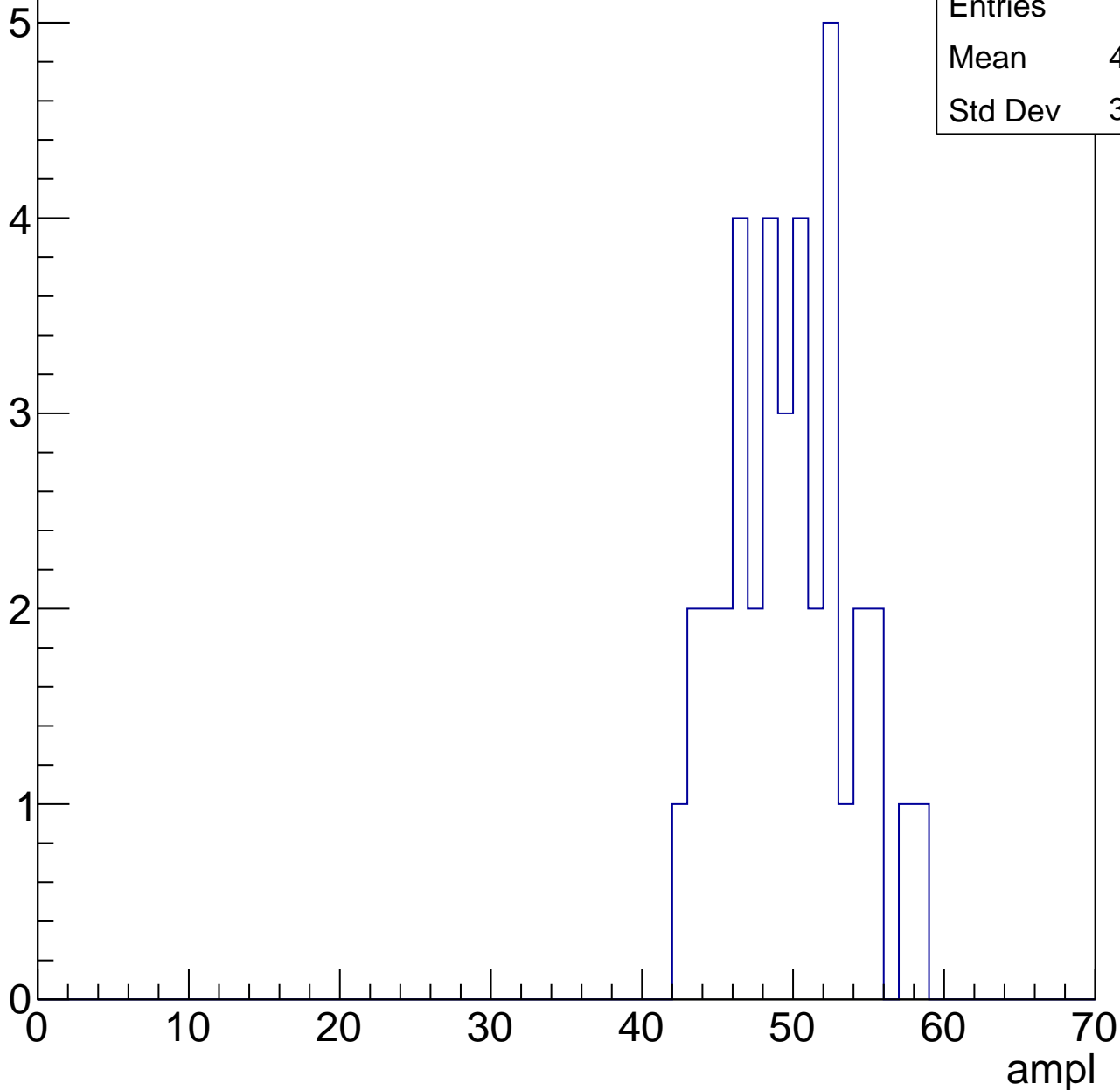


B1L103S, U15-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

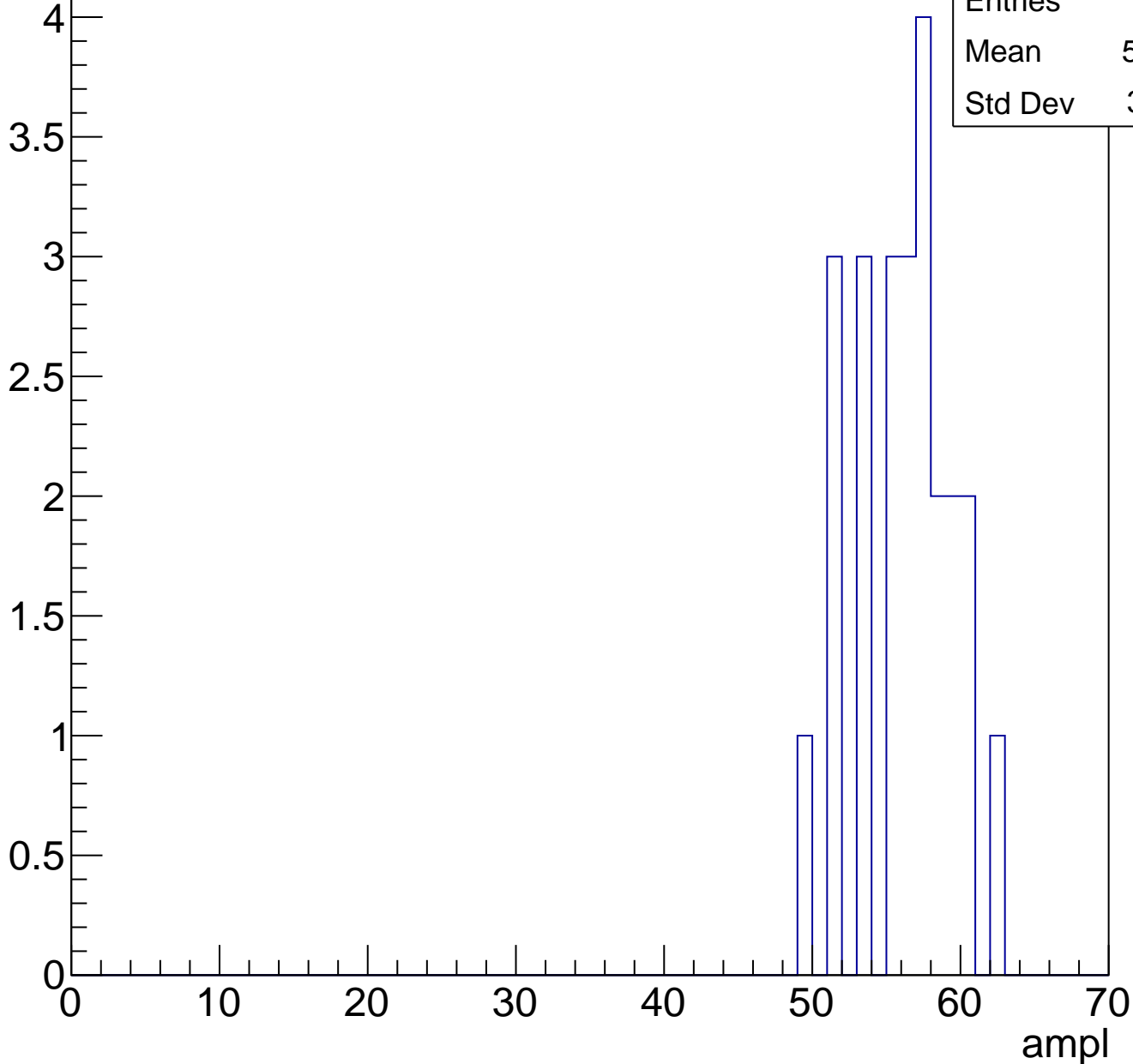
Entries	38
Mean	49.24
Std Dev	3.937



B1L103S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

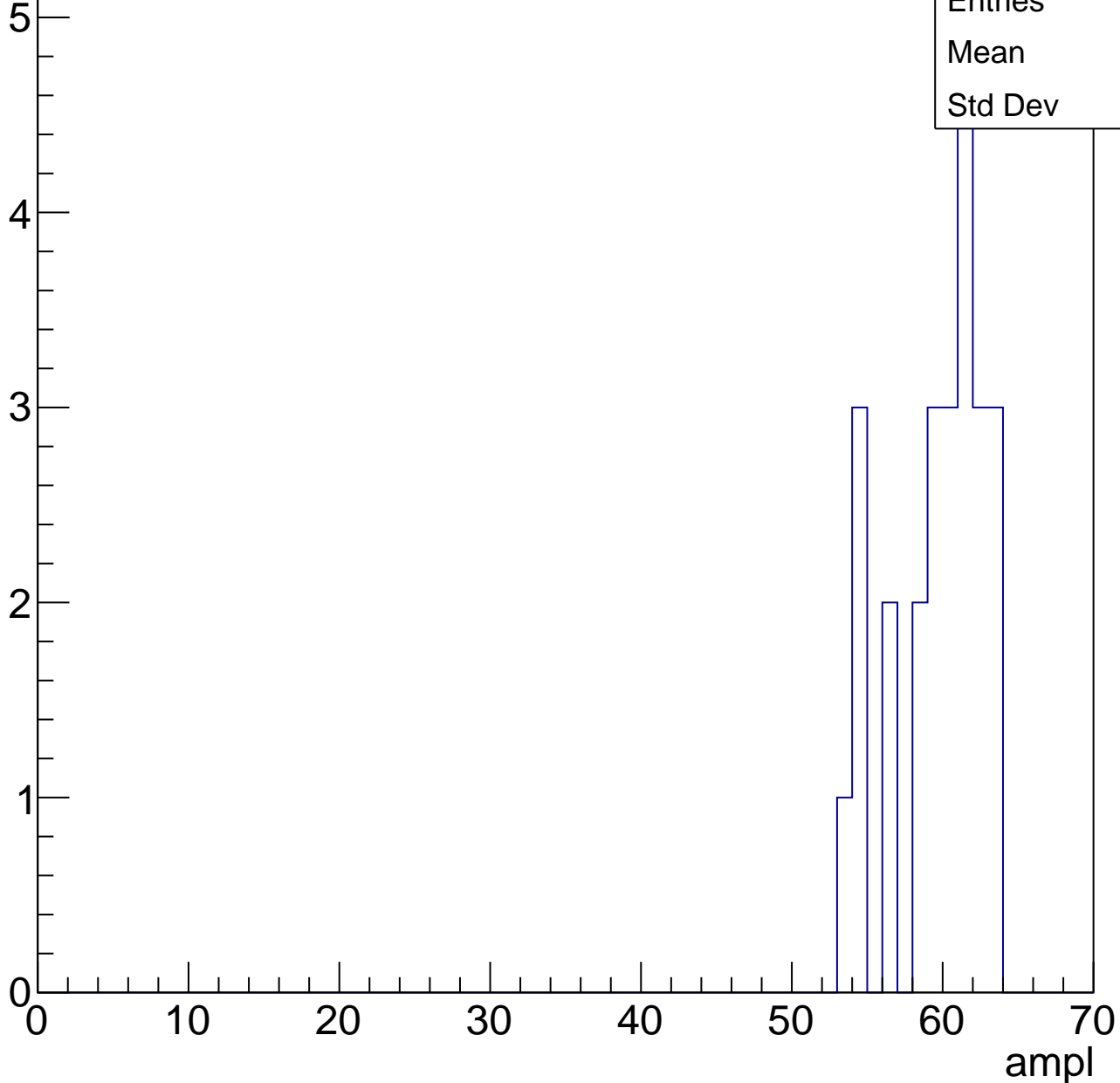


B1L103S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	59.2
Std Dev	3.02



B1L103S, U15-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch118, adc0

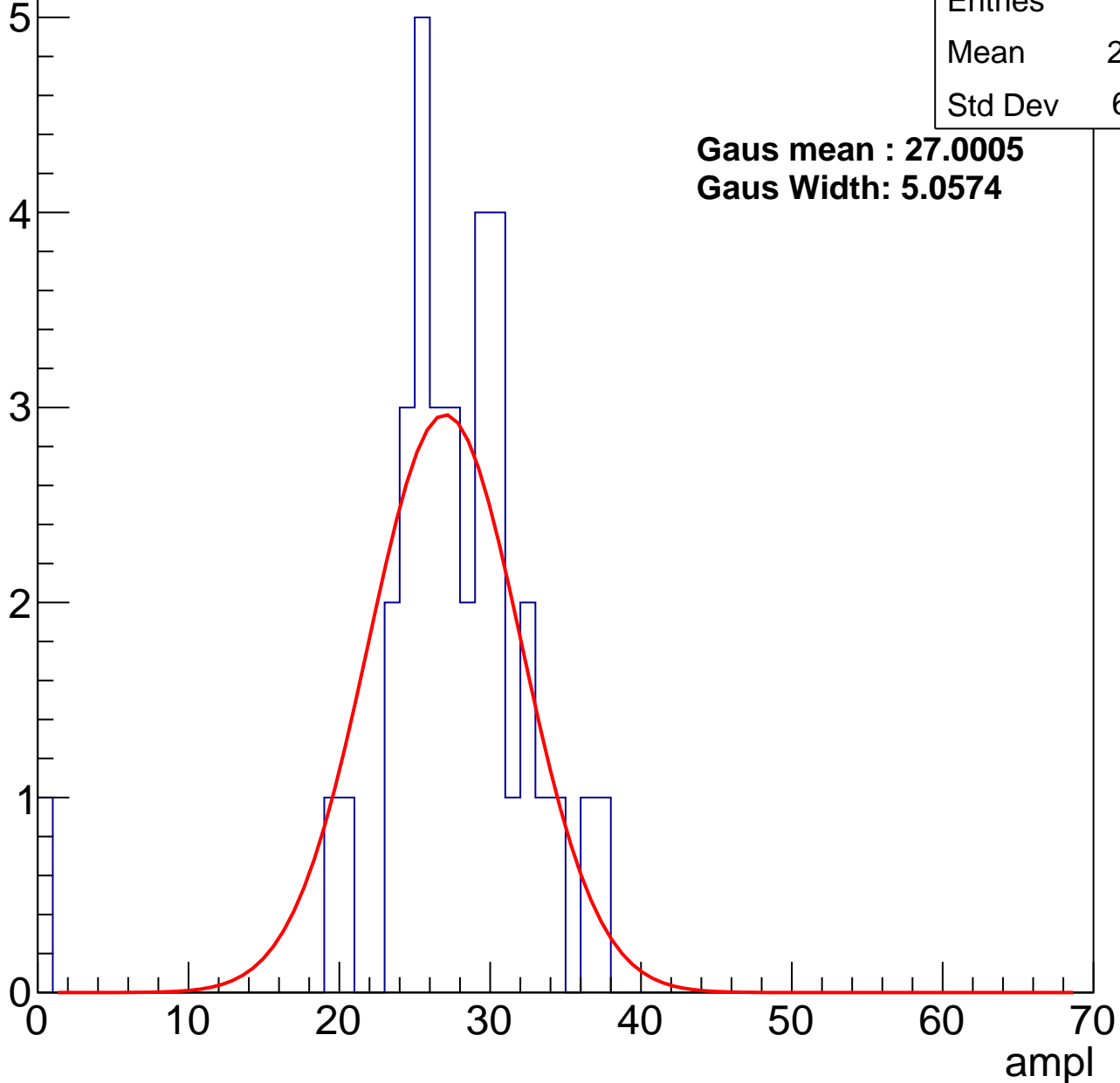
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	26.89
Std Dev	6.031

Gaus mean : 27.0005

Gaus Width: 5.0574



B1L103S, U15-ch118, adc1

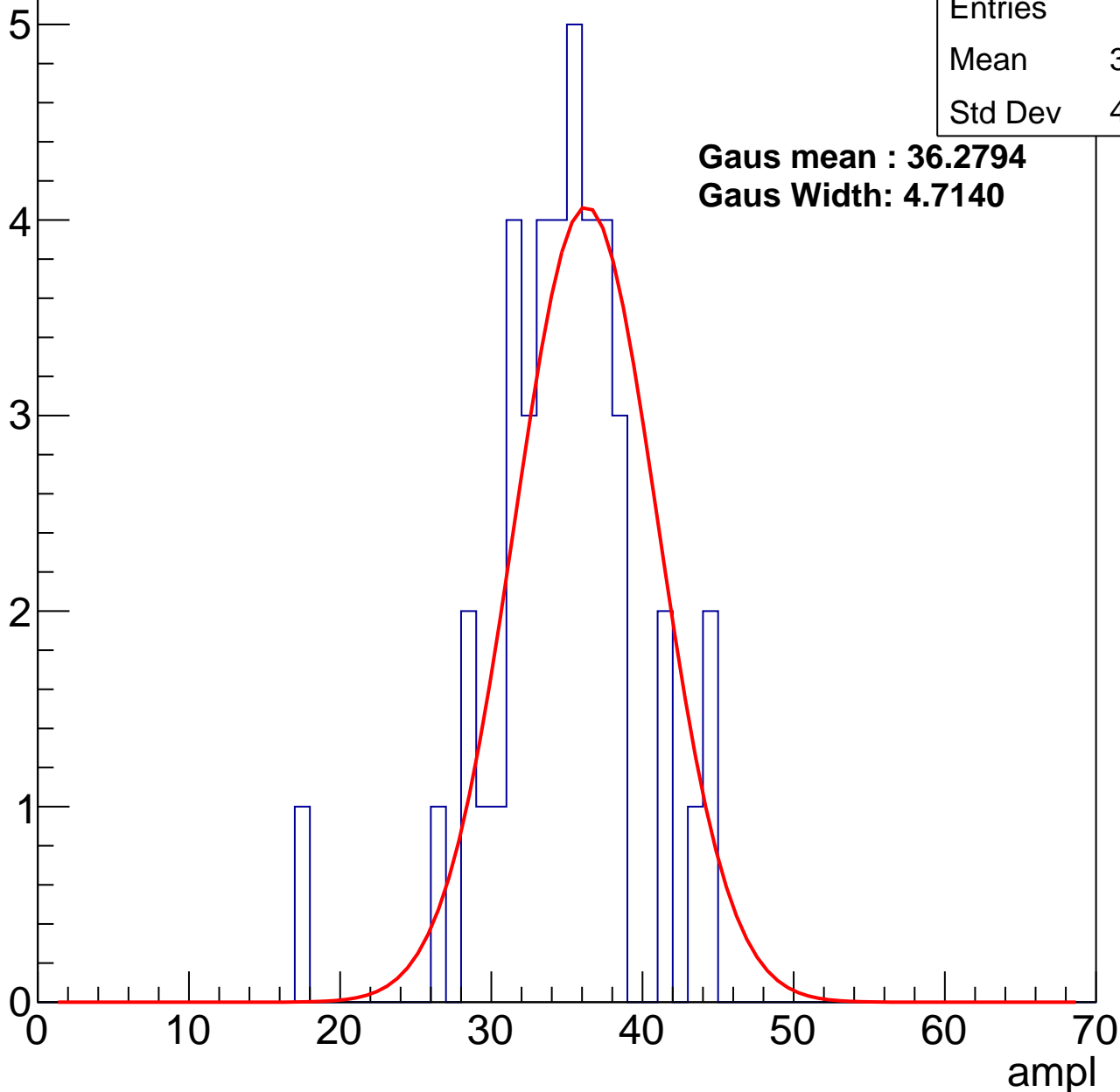
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	34.29
Std Dev	4.876

Gaus mean : 36.2794

Gaus Width: 4.7140



B1L103S, U15-ch118, adc2

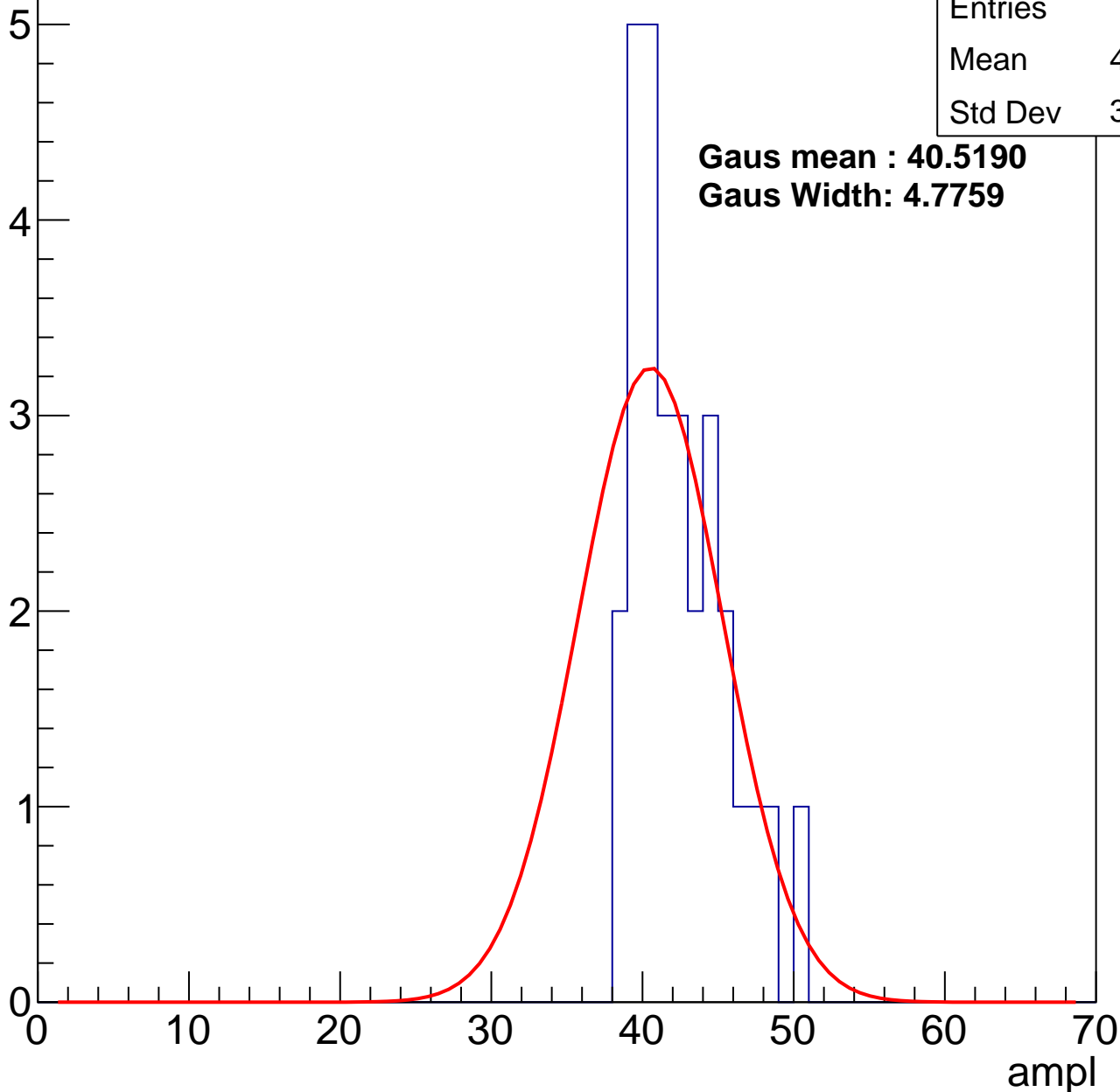
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	42.03
Std Dev	3.068

Gaus mean : 40.5190

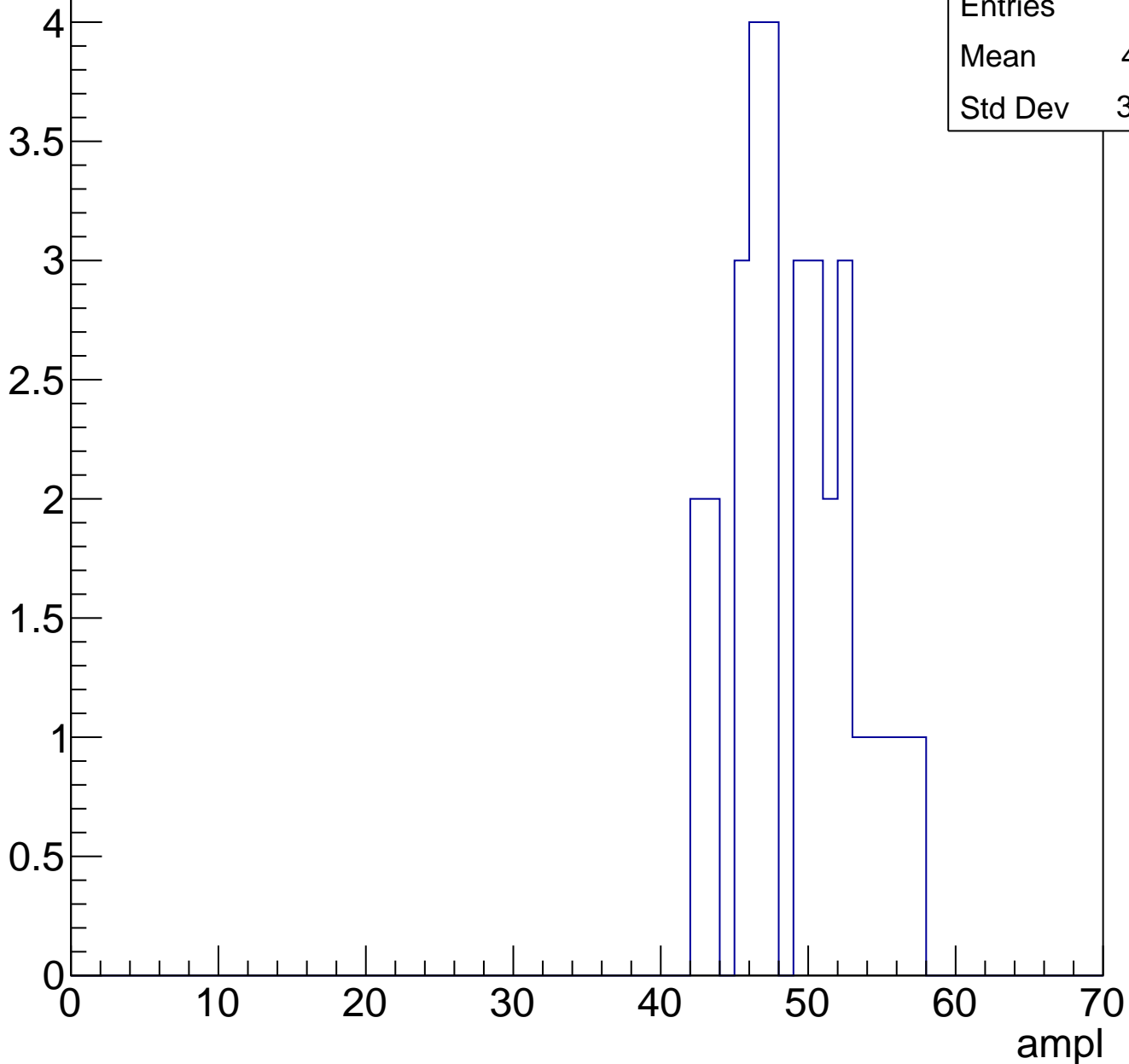
Gaus Width: 4.7759



B1L103S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

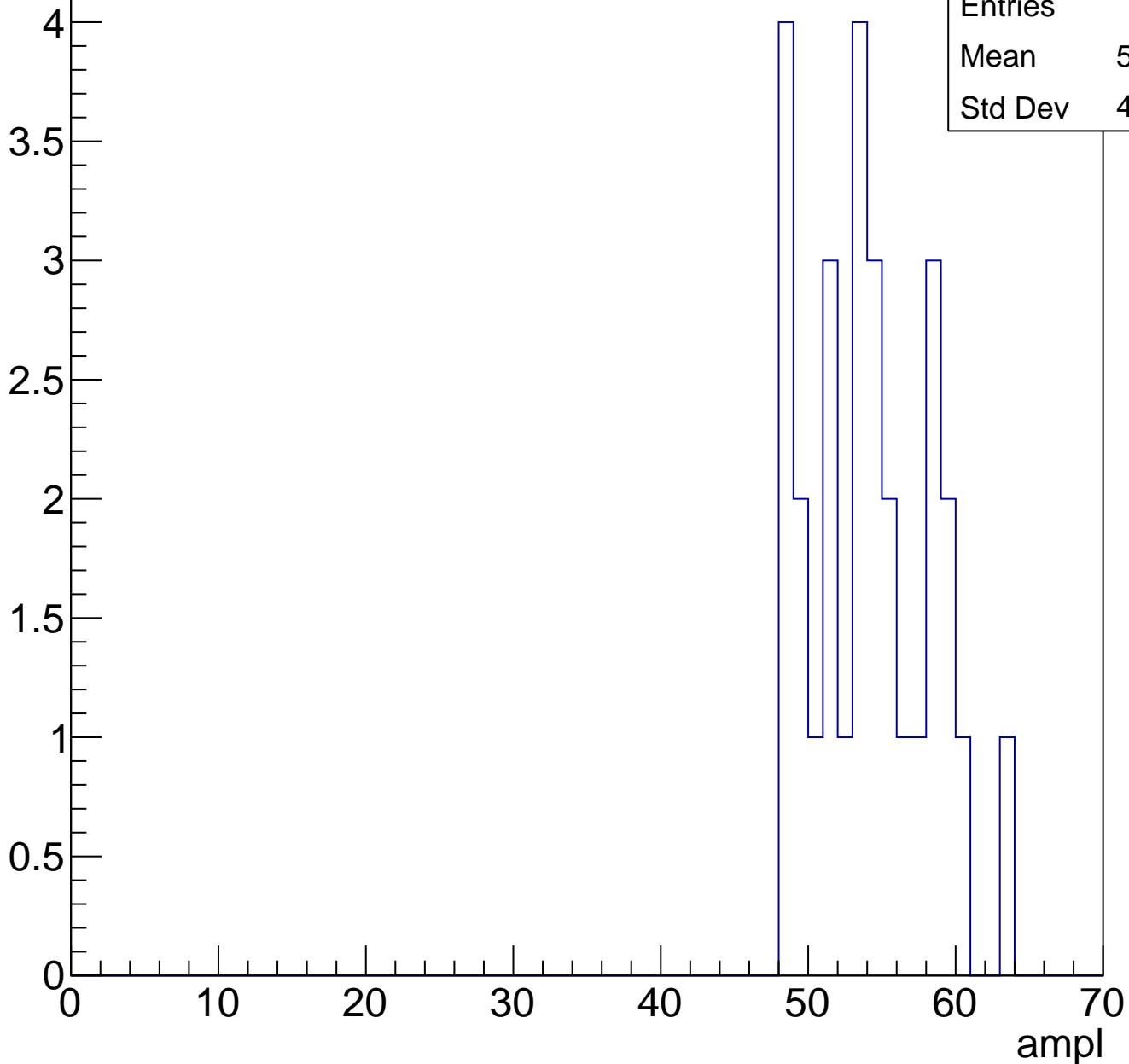
Entry



B1L103S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

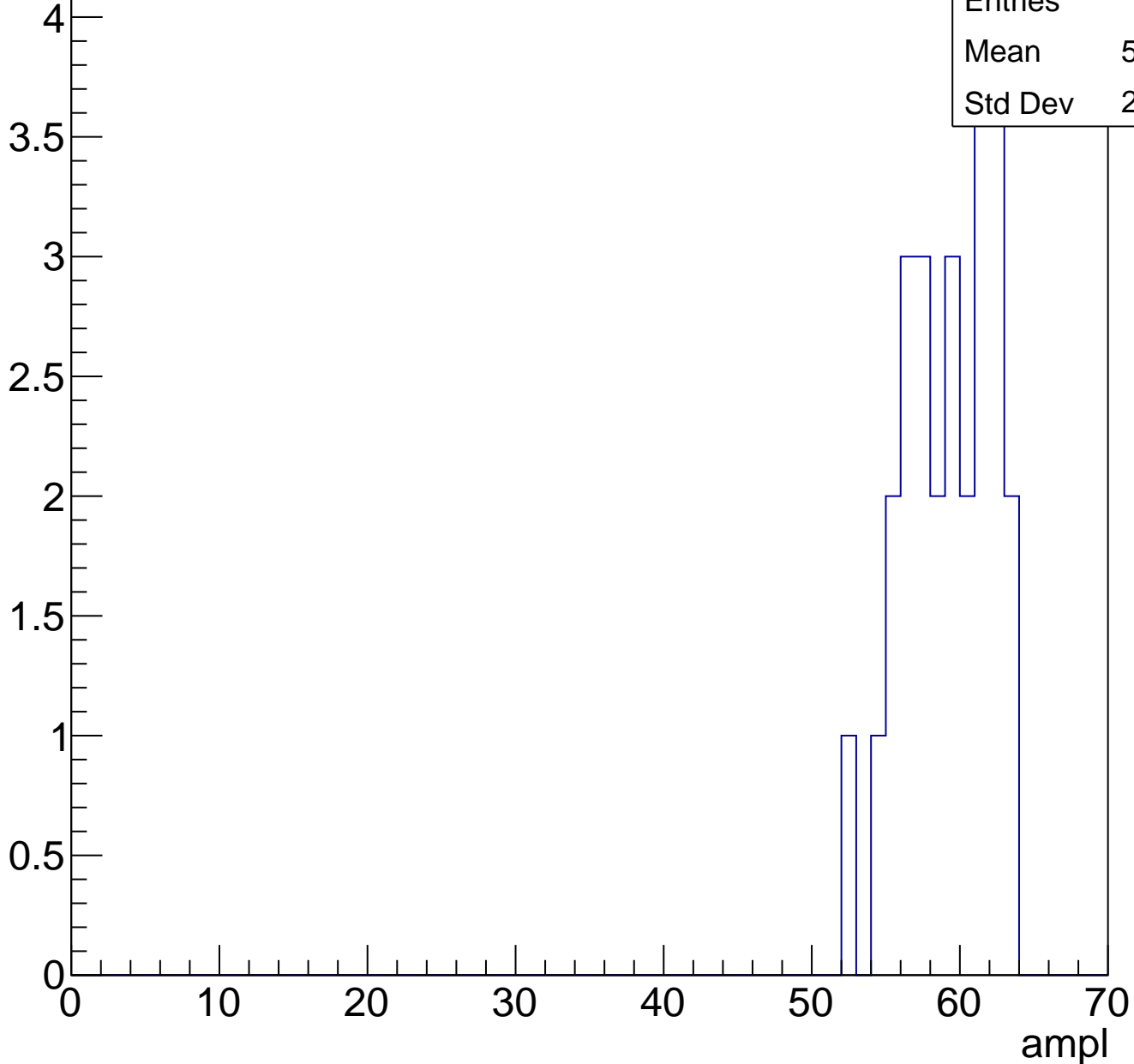


Entries	29
Mean	53.69
Std Dev	4.035

B1L103S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

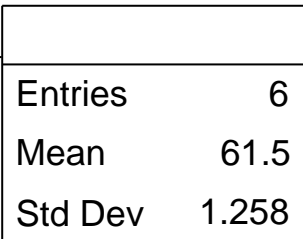
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	1.258

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch119, adc0

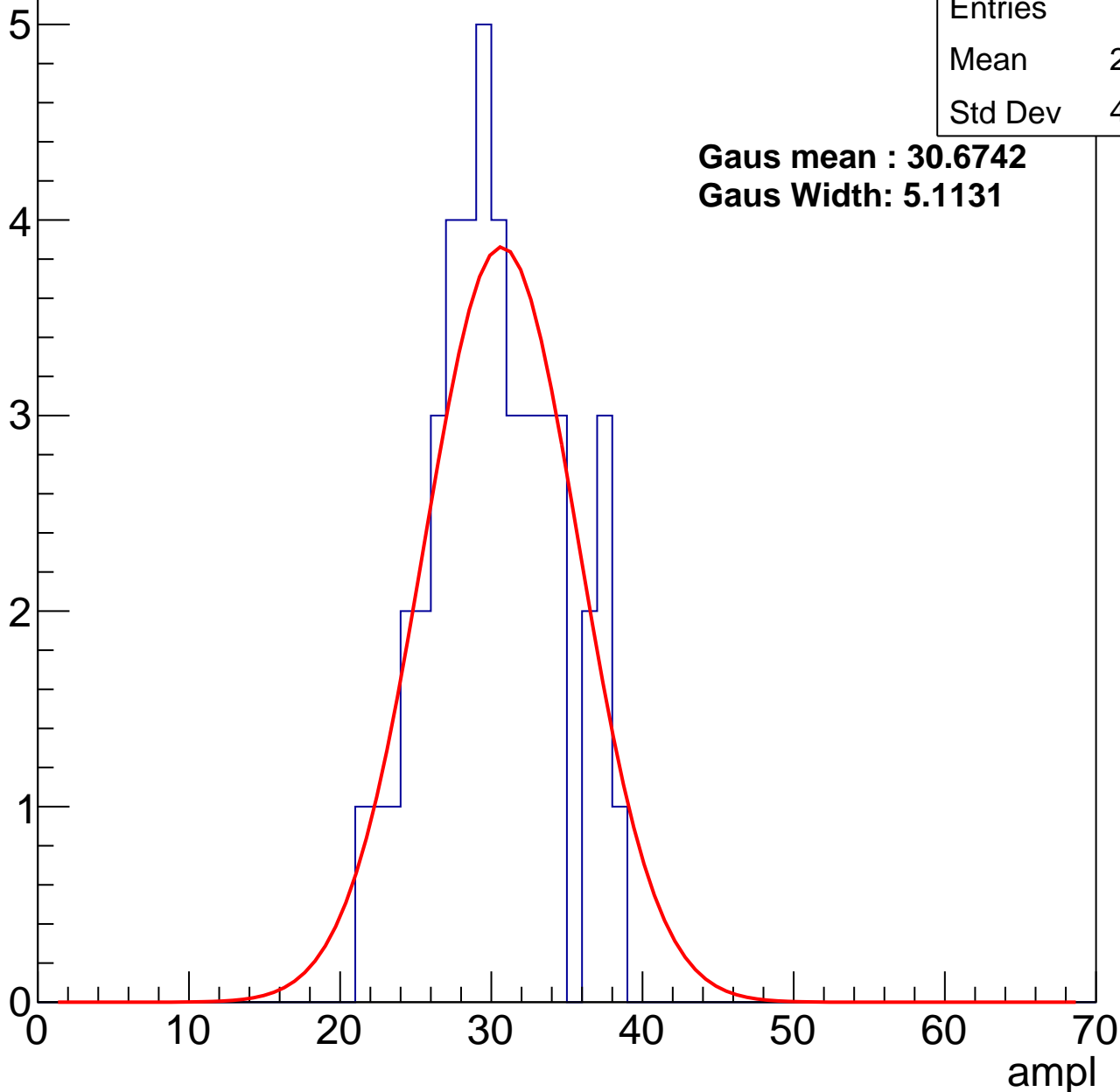
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	29.73
Std Dev	4.192

Gaus mean : 30.6742

Gaus Width: 5.1131



B1L103S, U15-ch119, adc1

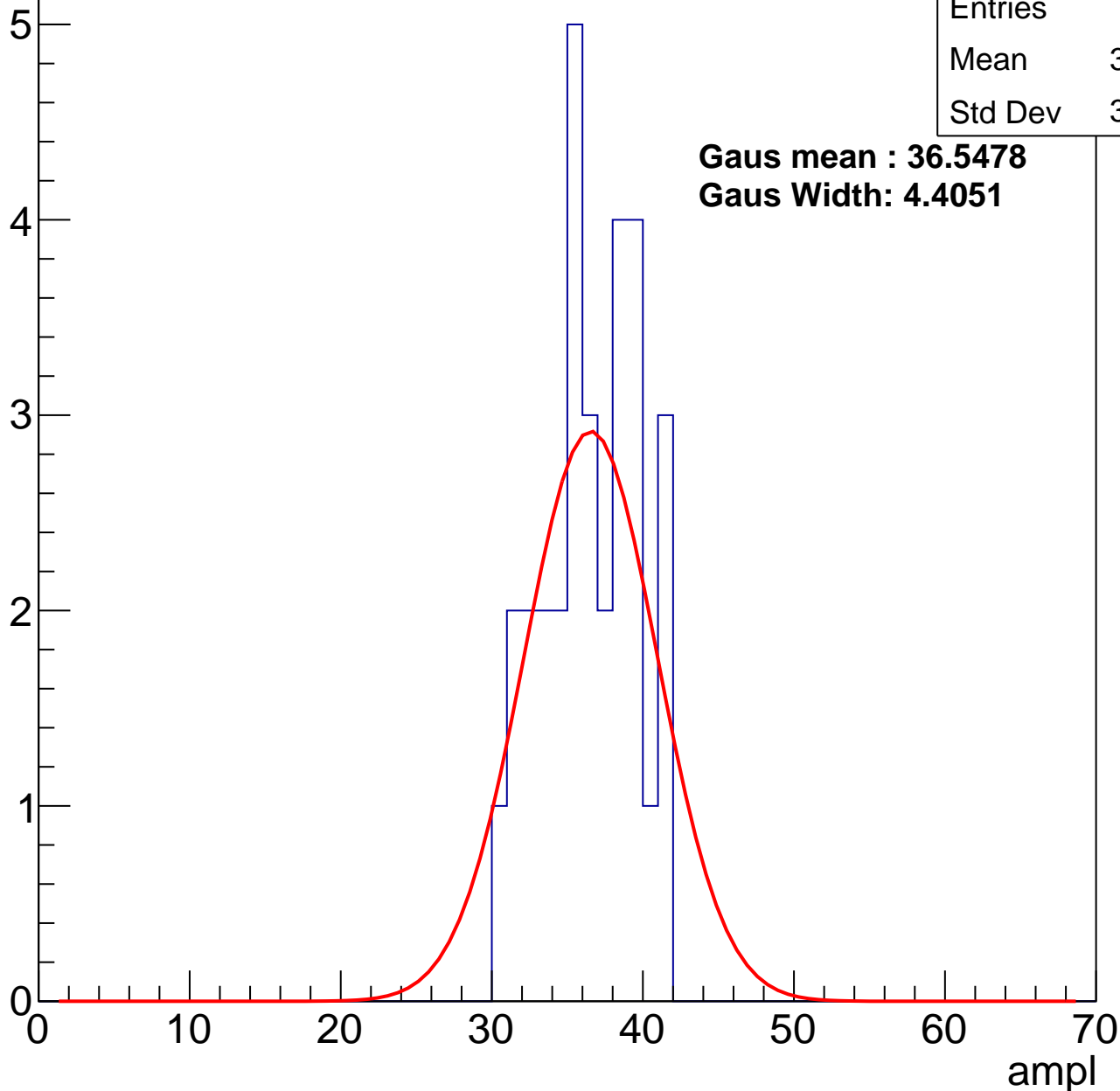
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	36.06
Std Dev	3.079

Gaus mean : 36.5478

Gaus Width: 4.4051



B1L103S, U15-ch119, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	43.85
Std Dev	2.84

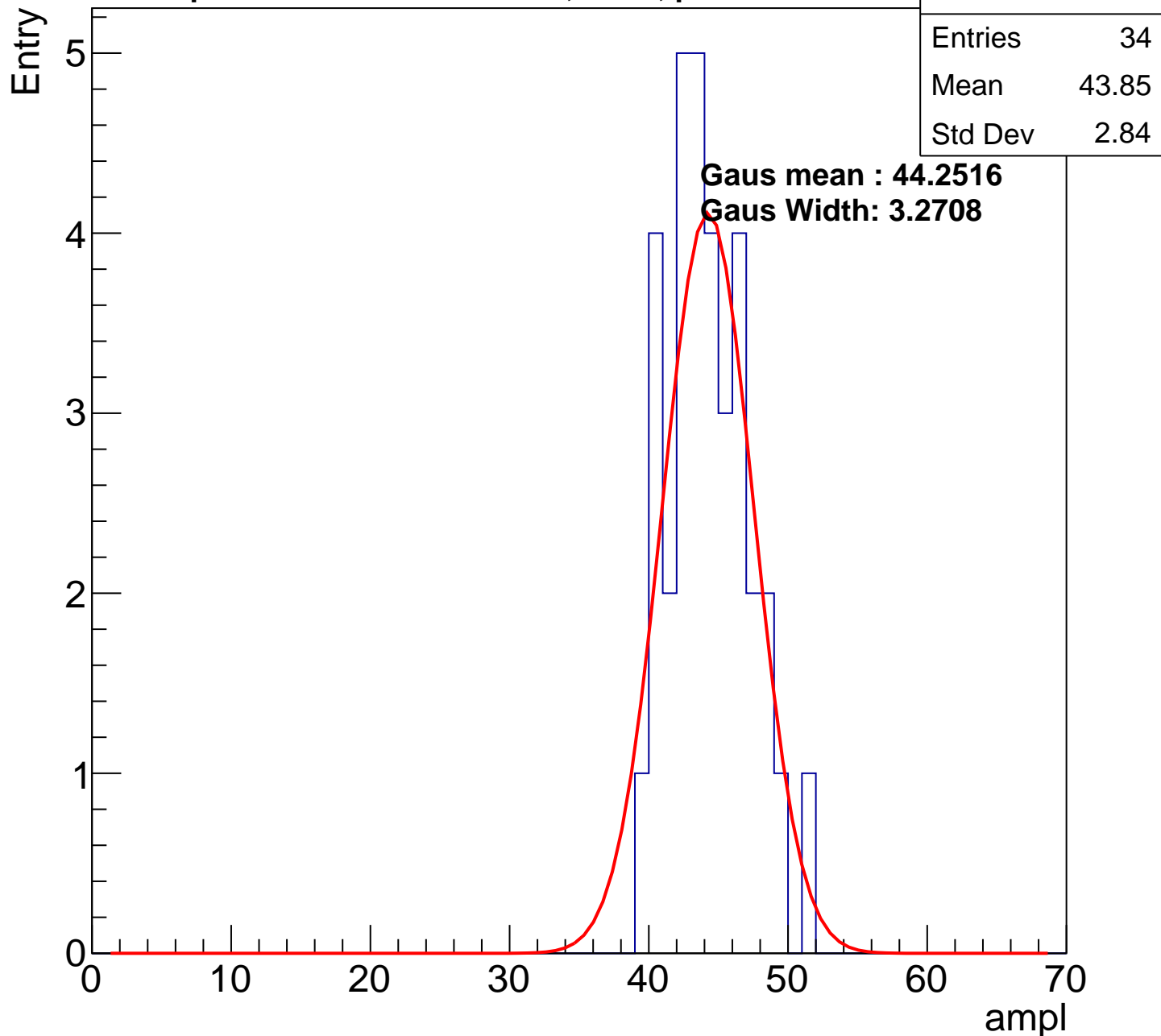
Gaus mean : 44.2516

Gaus Width: 3.2708

5
4
3
2
1
0

ampl

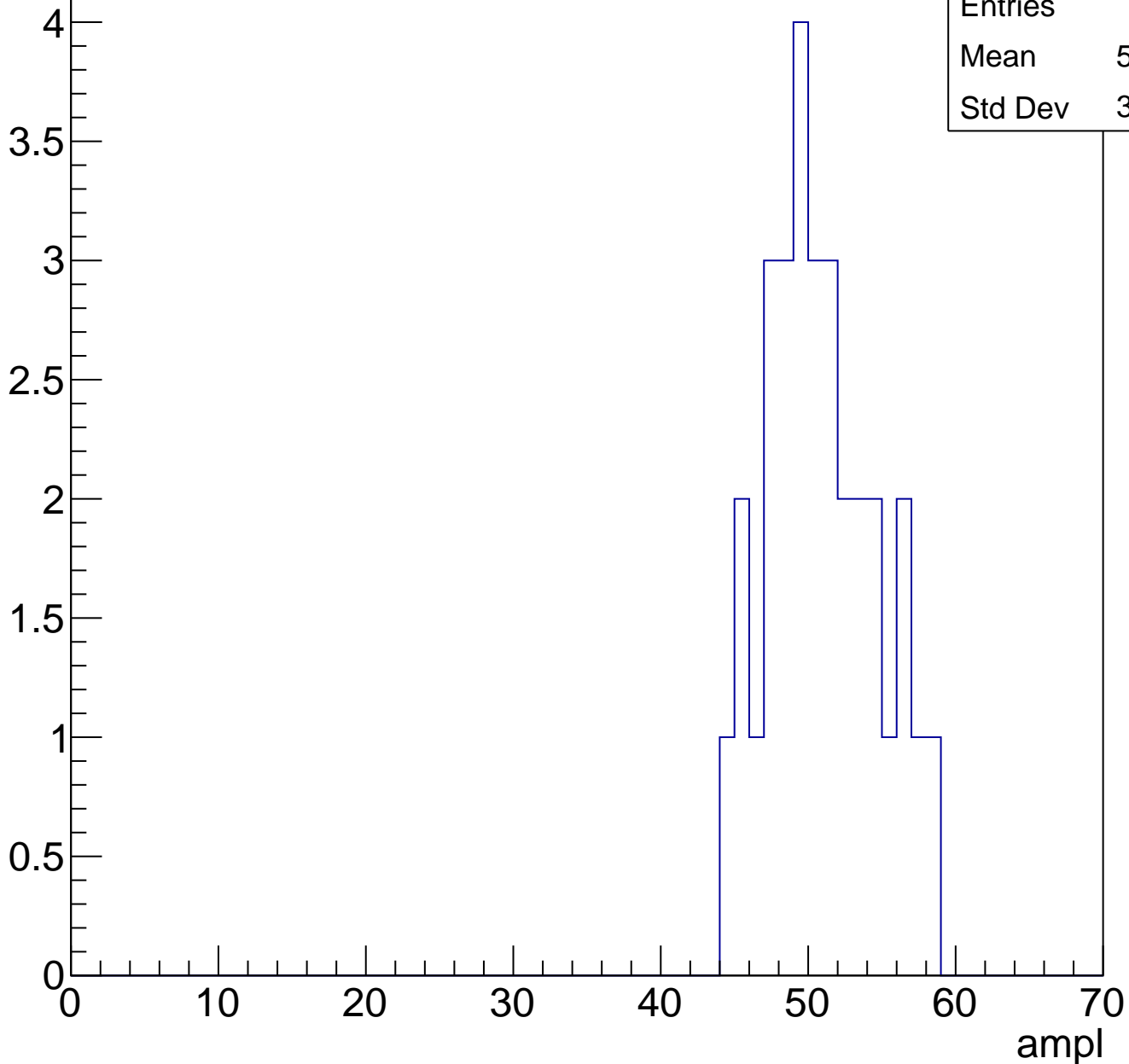
0 10 20 30 40 50 60 70



B1L103S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

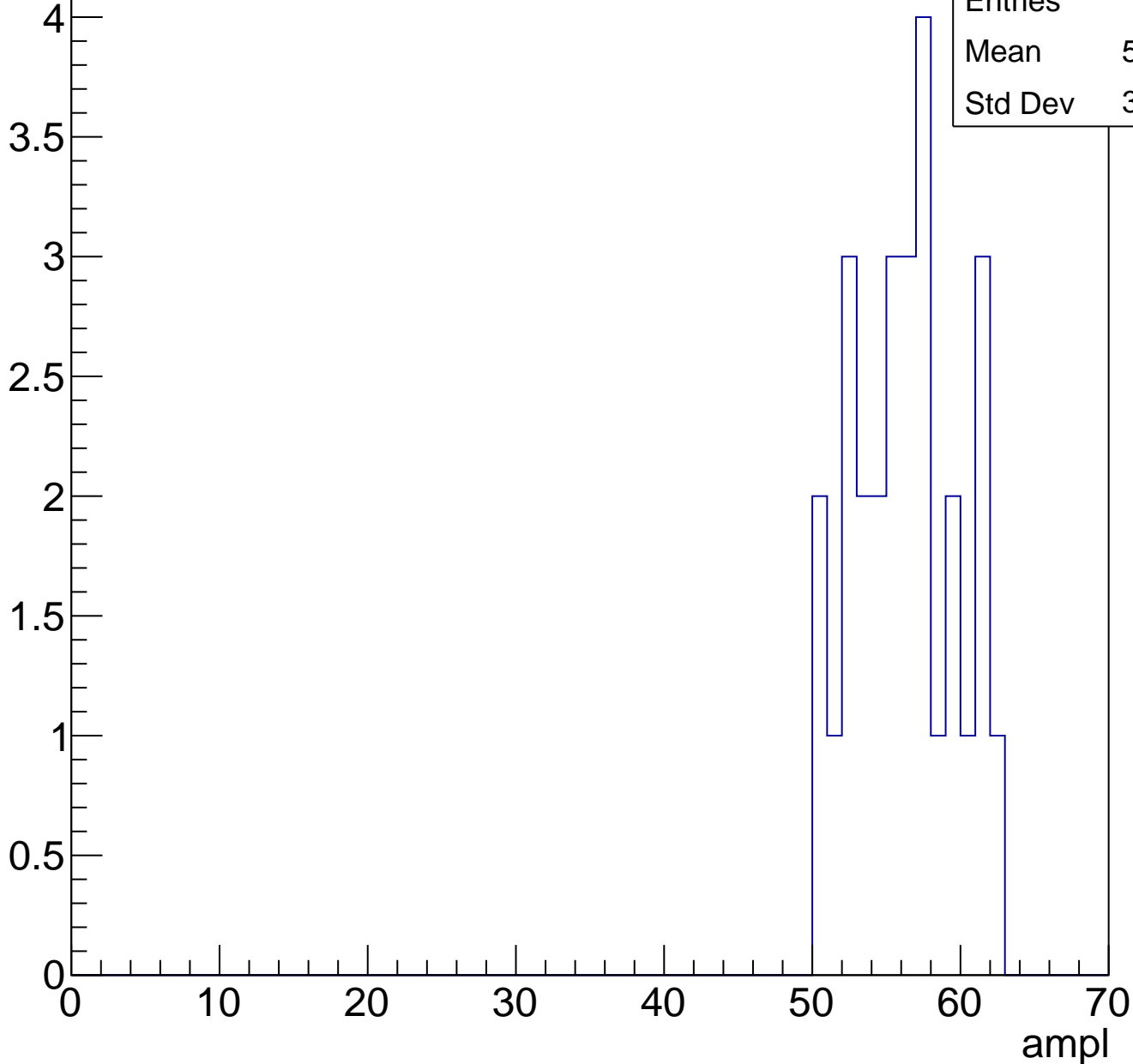
Entry



B1L103S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

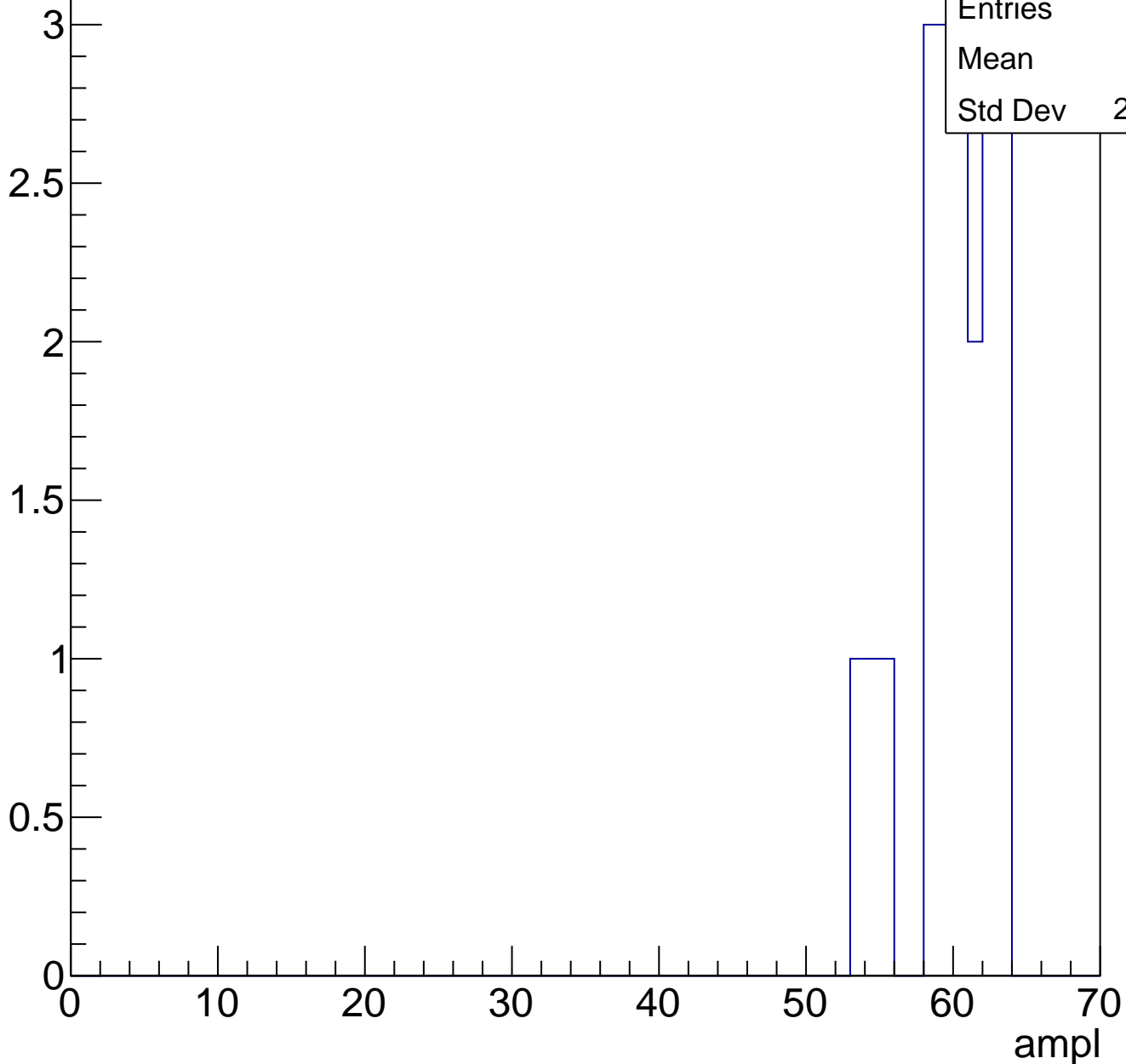
Entry



B1L103S, U15-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	1.225

ampl

60

62

64

66

68

70

B1L103S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U15-ch120, adc0

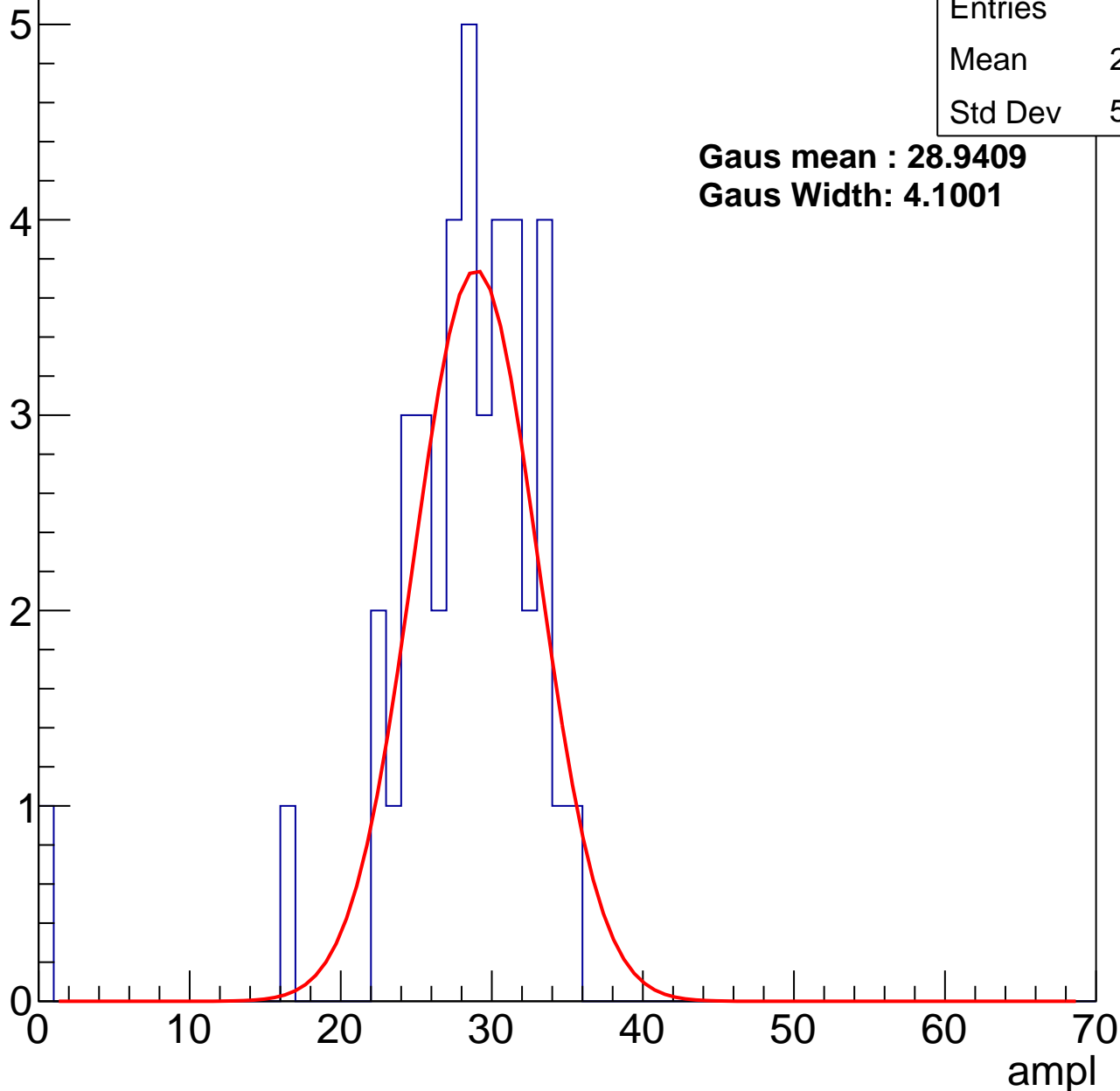
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	27.46
Std Dev	5.785

Gaus mean : 28.9409

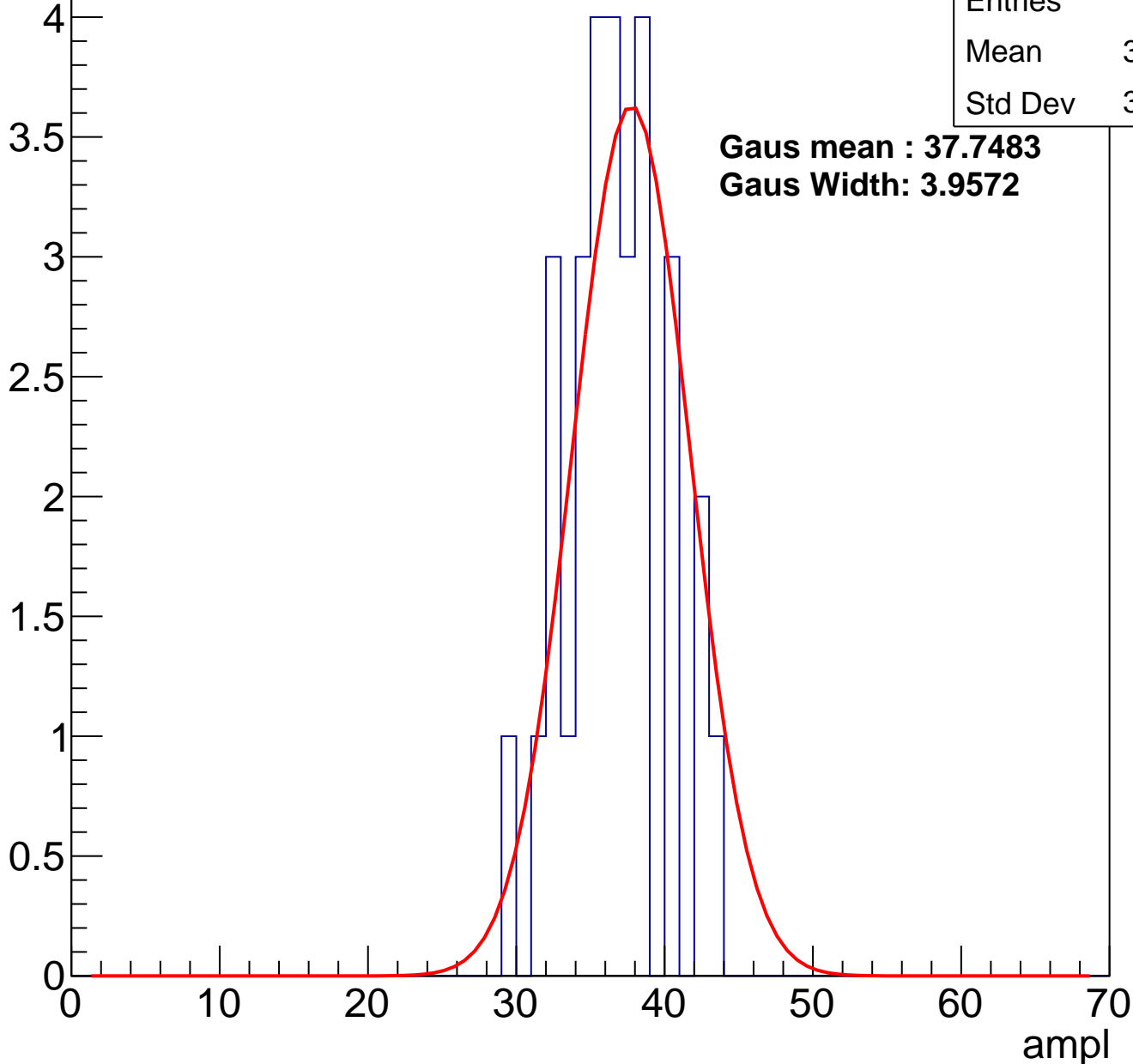
Gaus Width: 4.1001



B1L103S, U15-ch120, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch120, adc2

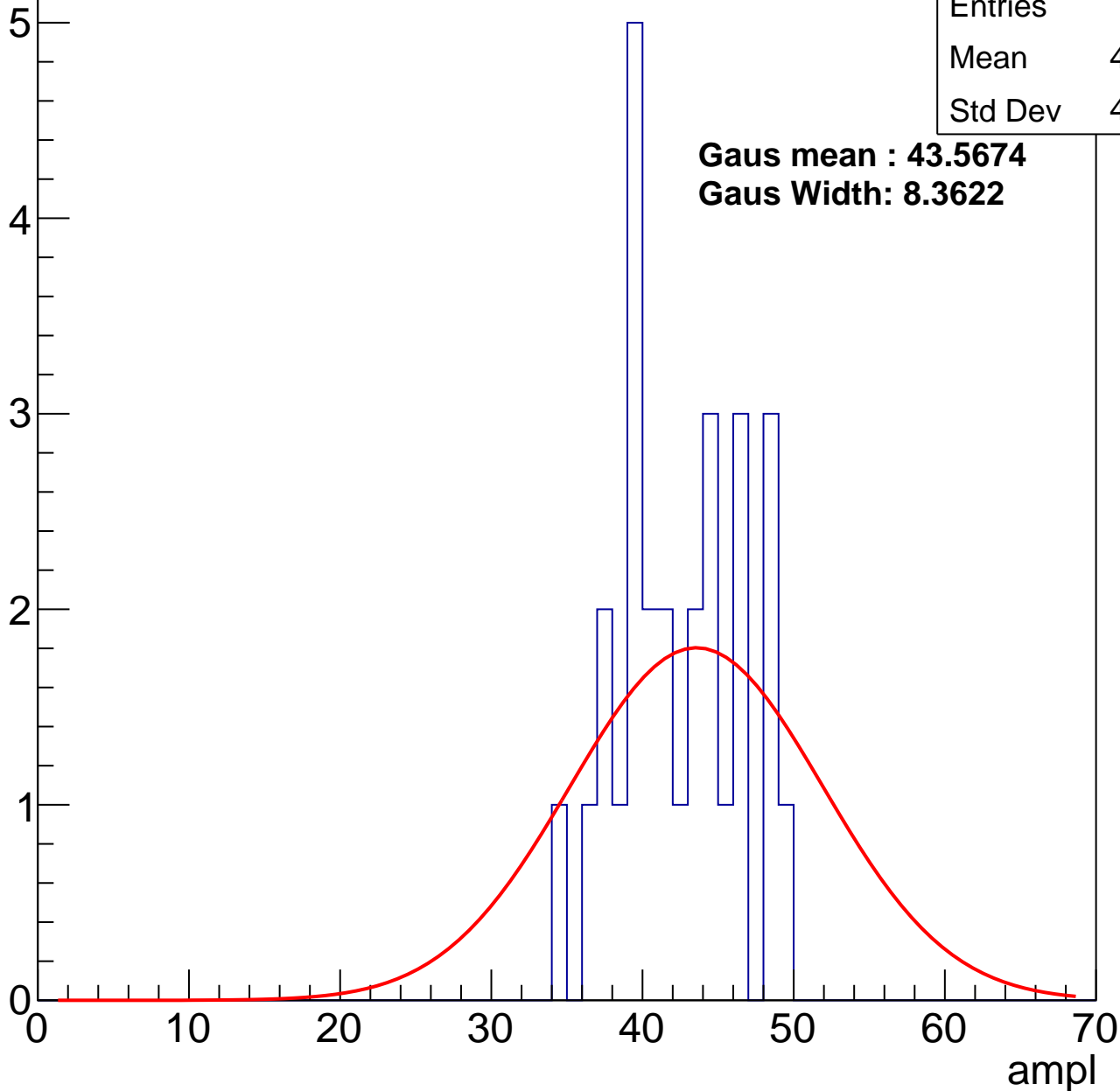
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	41.96
Std Dev	4.004

Gaus mean : 43.5674

Gaus Width: 8.3622

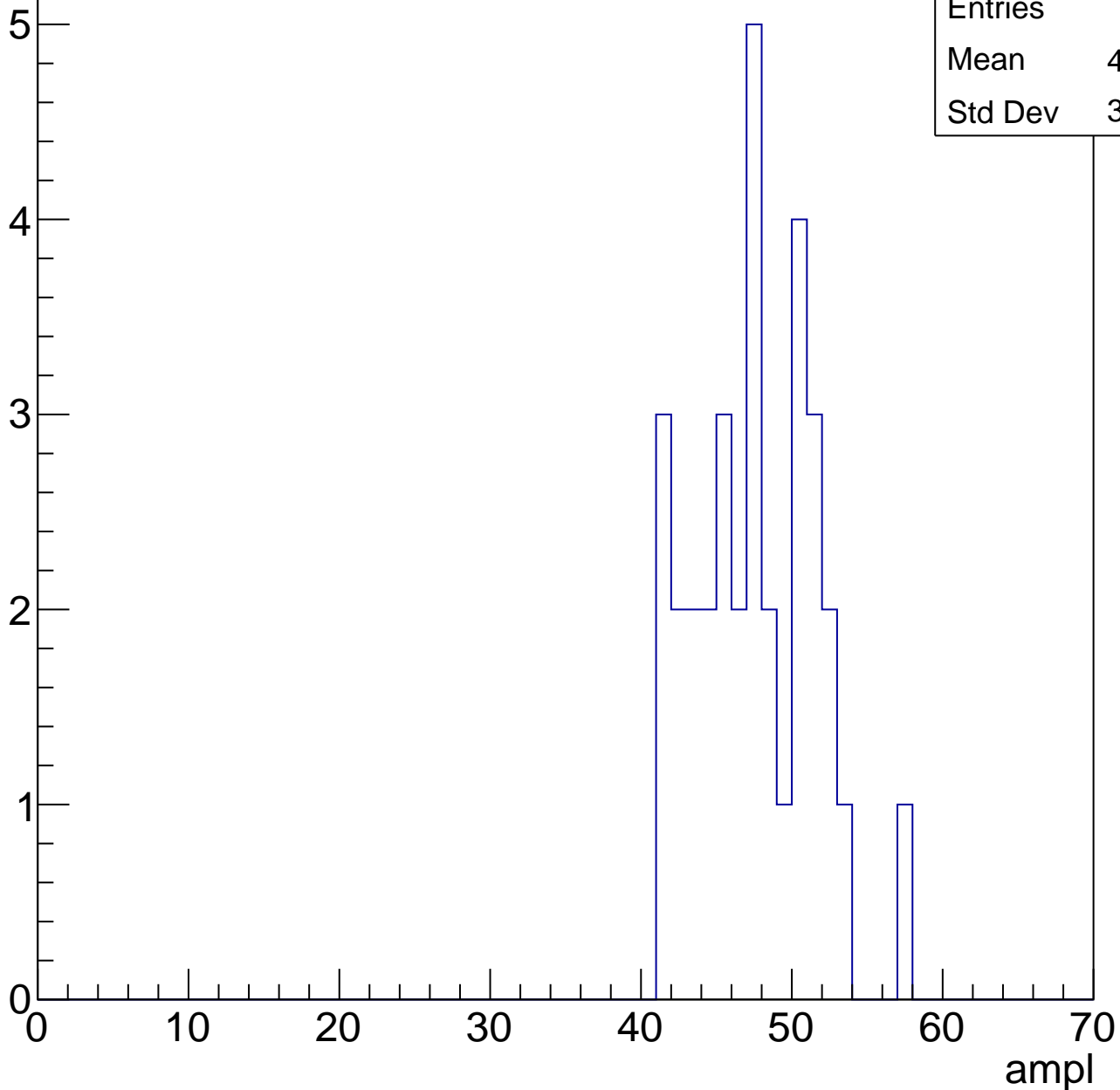


B1L103S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

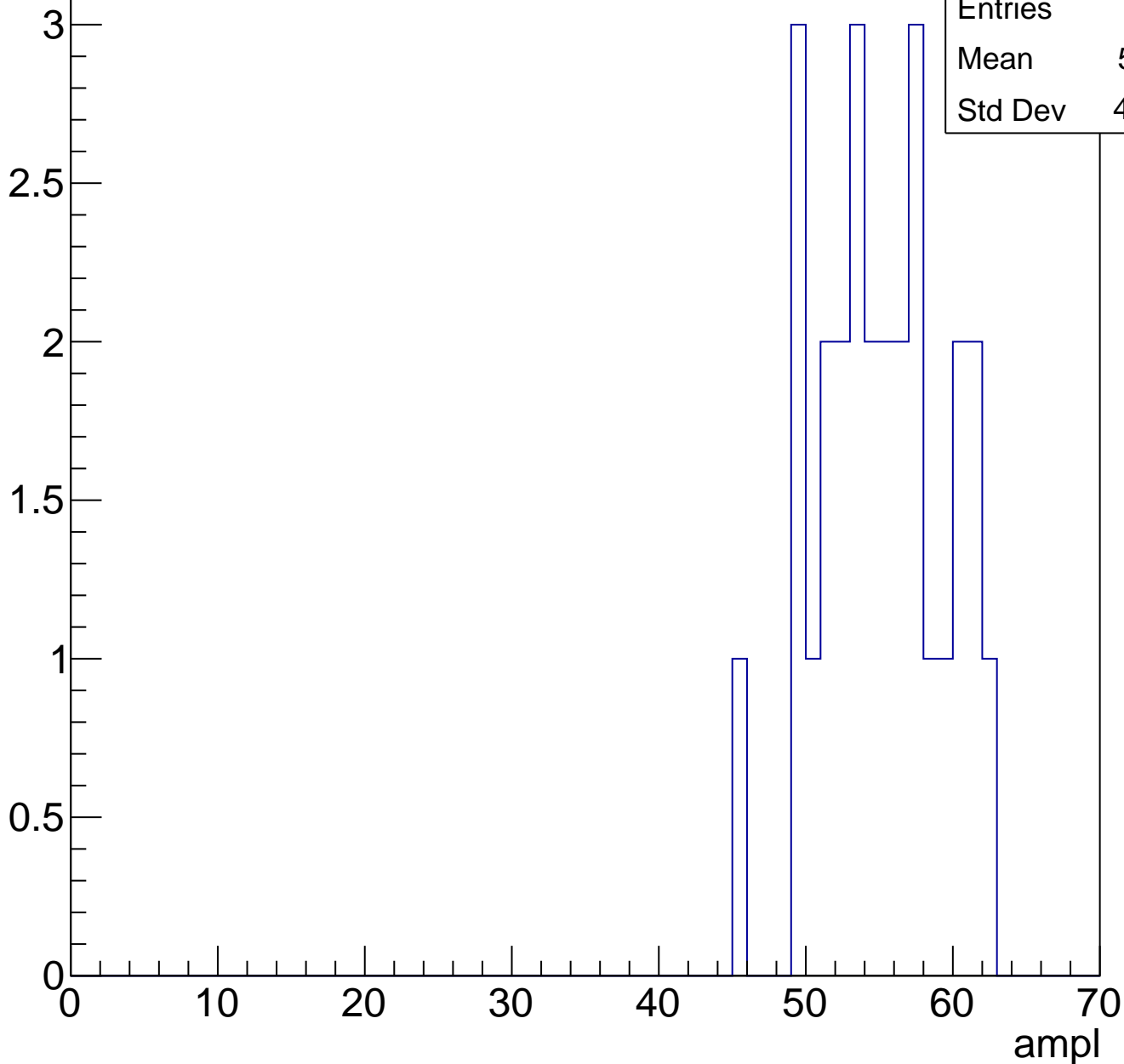
Entries	33
Mean	47.12
Std Dev	3.883



B1L103S, U15-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	28
Mean	54.61
Std Dev	4.237

B1L103S, U15-ch120, adc5

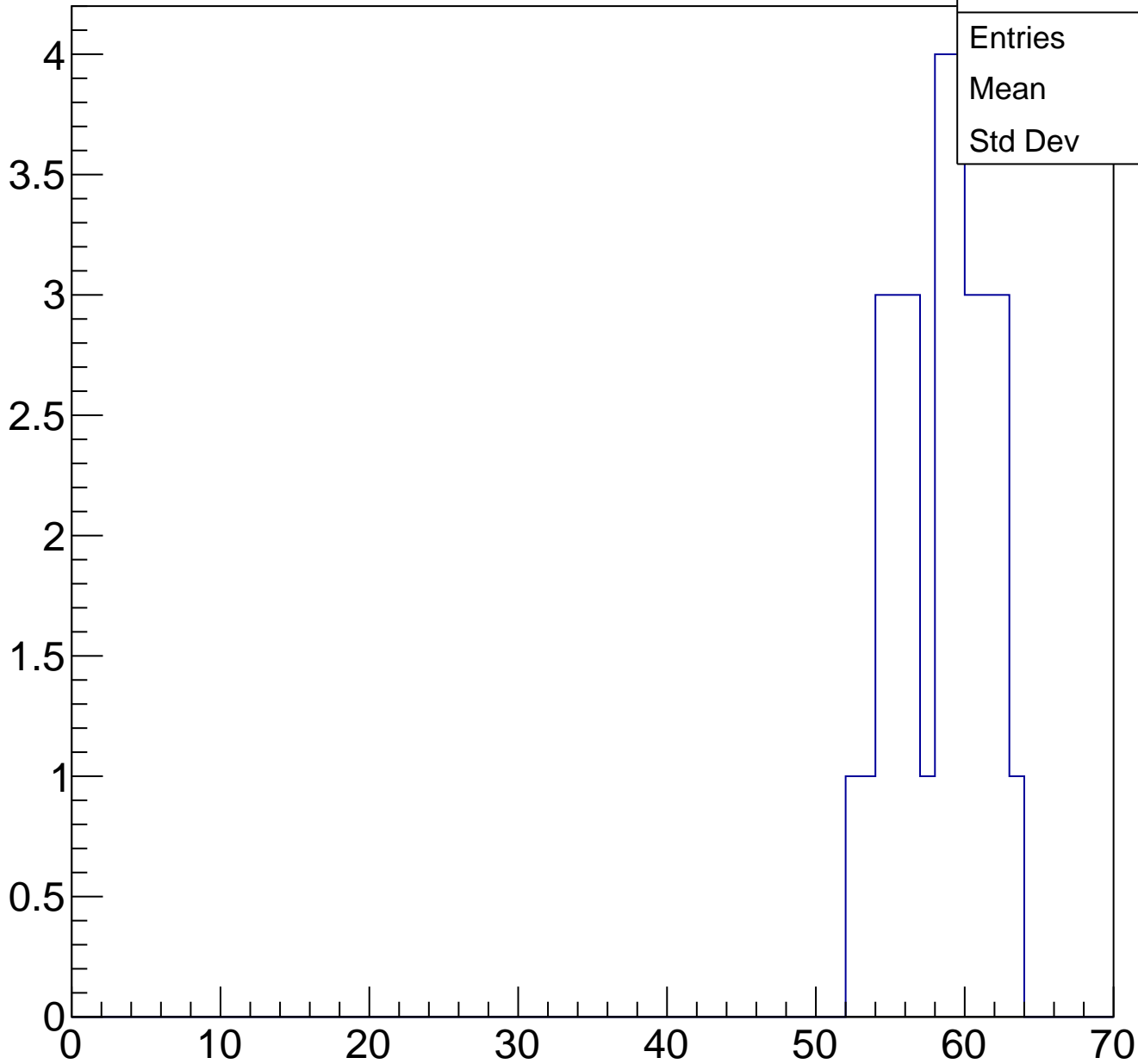
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	30
Mean	57.9
Std Dev	2.97

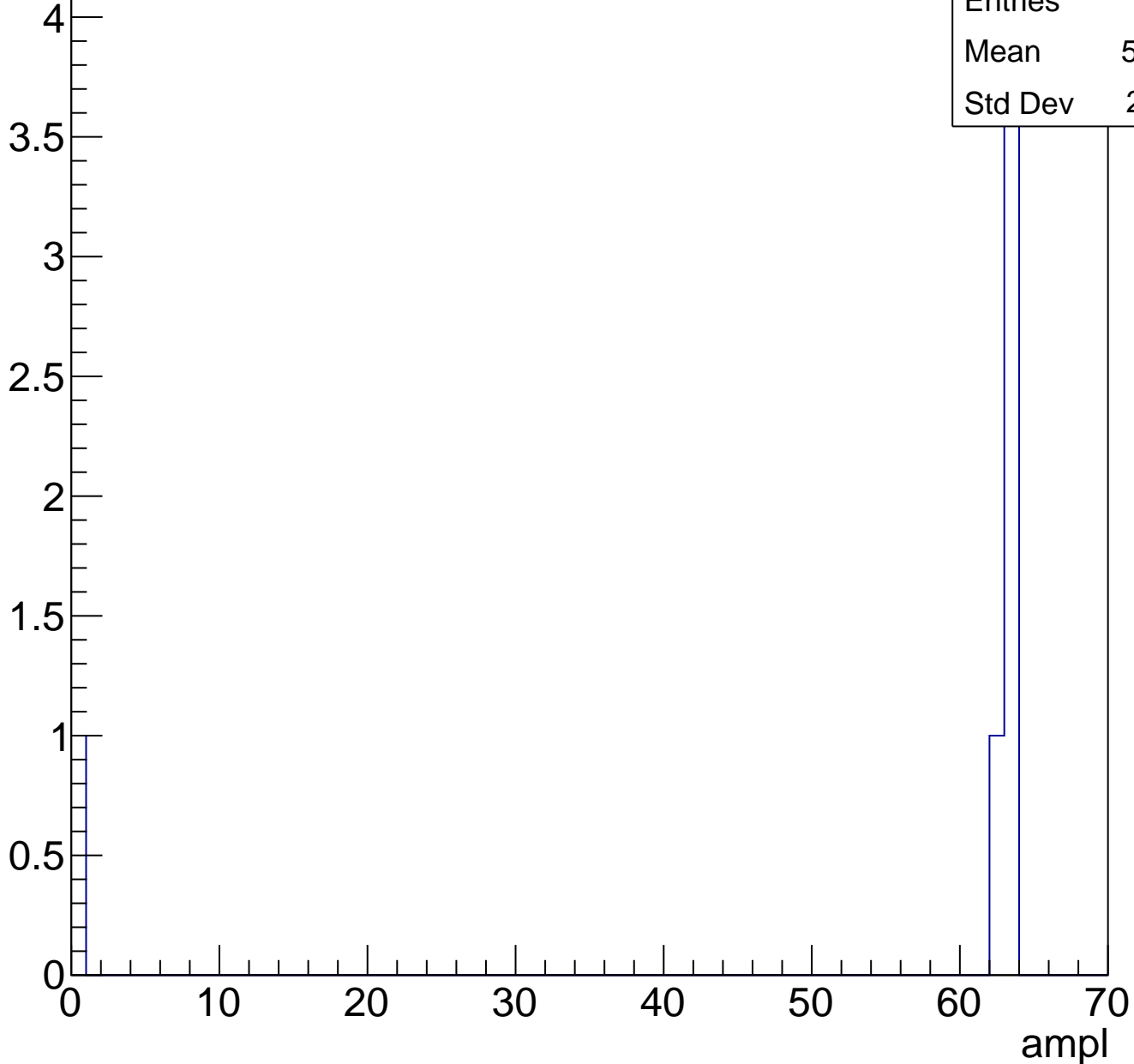
ampl



B1L103S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch121, adc0

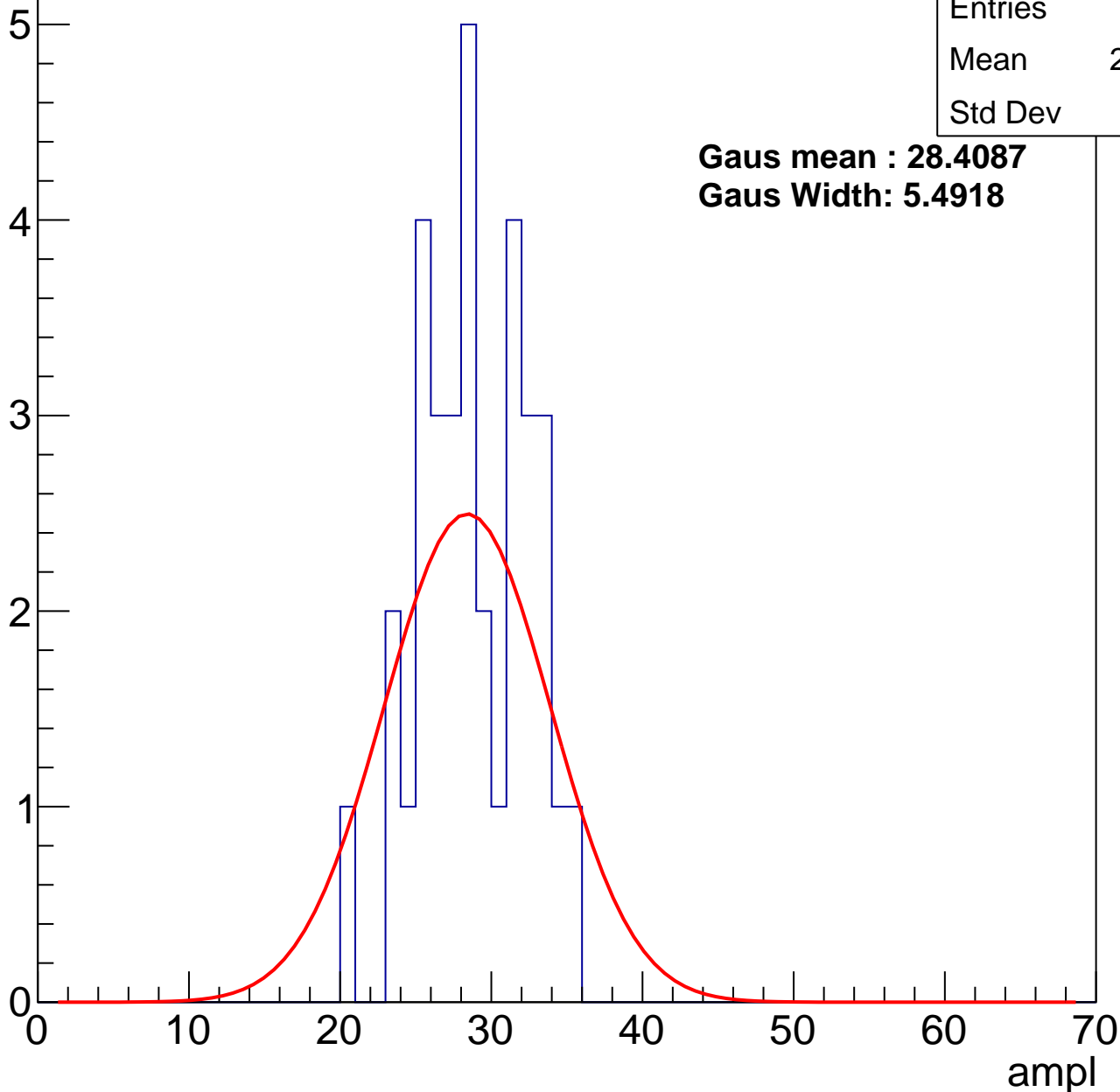
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	28.38
Std Dev	3.54

Gaus mean : 28.4087

Gaus Width: 5.4918



B1L103S, U15-ch121, adc1

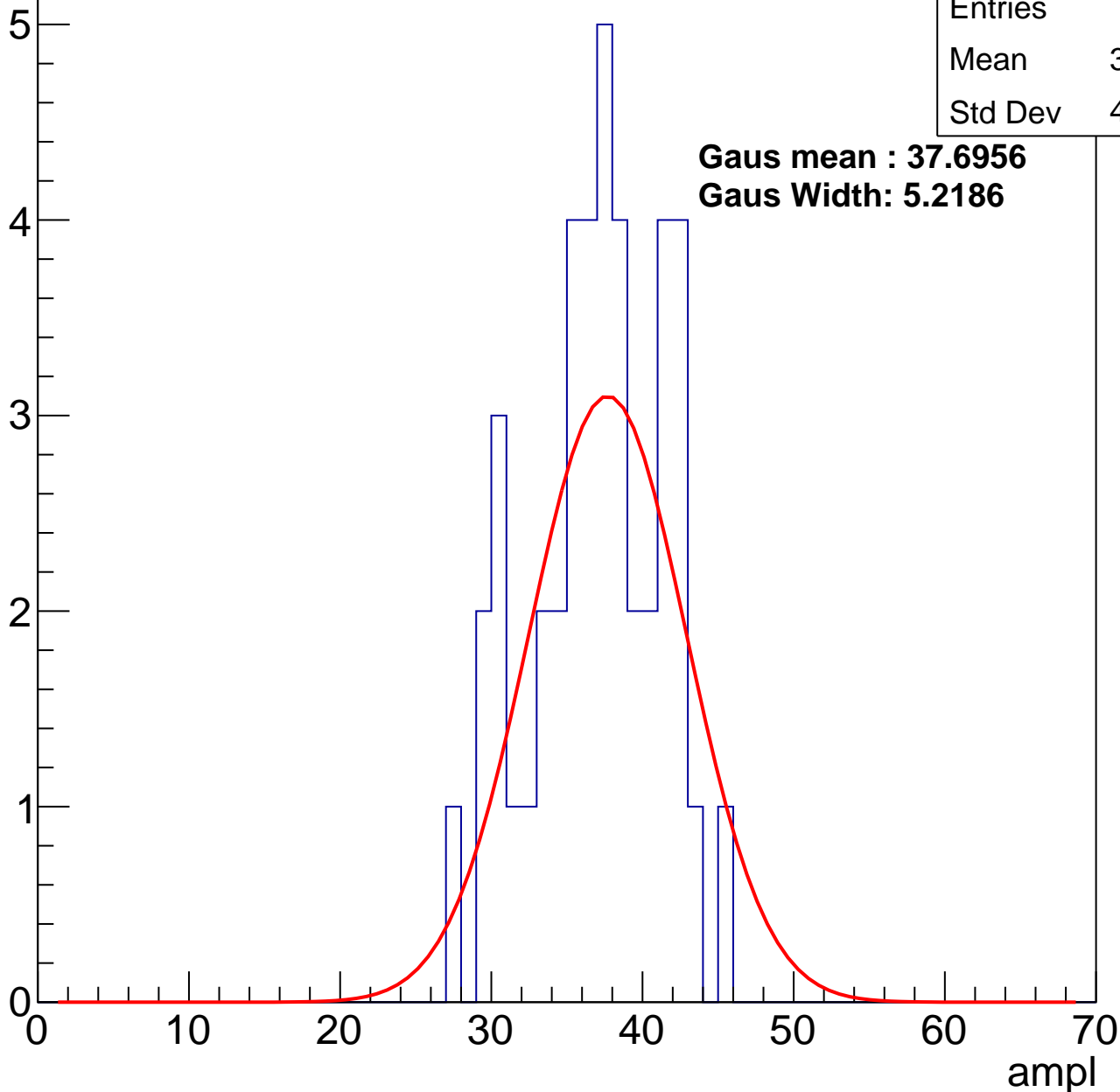
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	36.53
Std Dev	4.288

Gaus mean : 37.6956

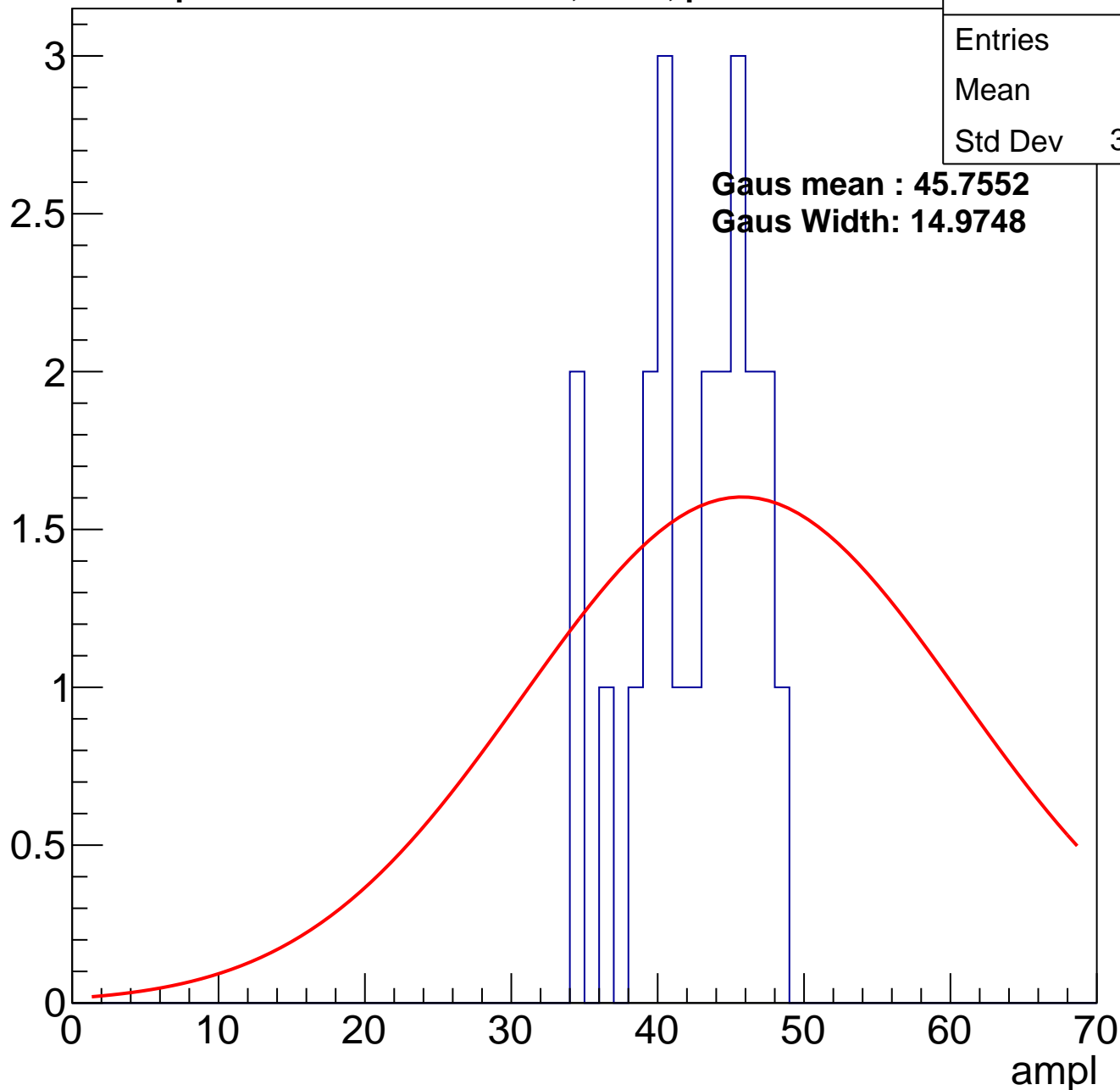
Gaus Width: 5.2186



B1L103S, U15-ch121, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

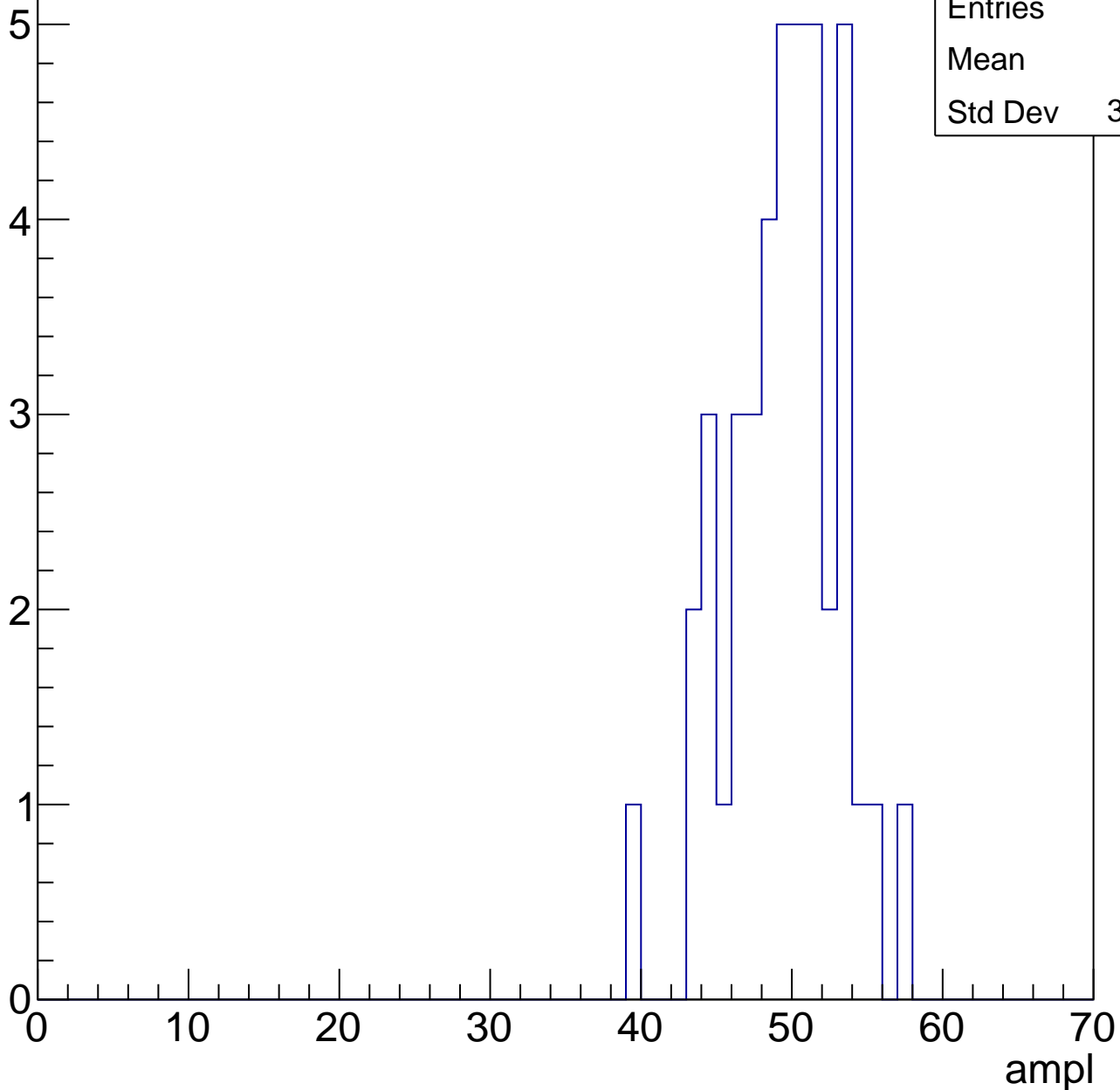


B1L103S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	49
Std Dev	3.632



B1L103S, U15-ch121, adc4

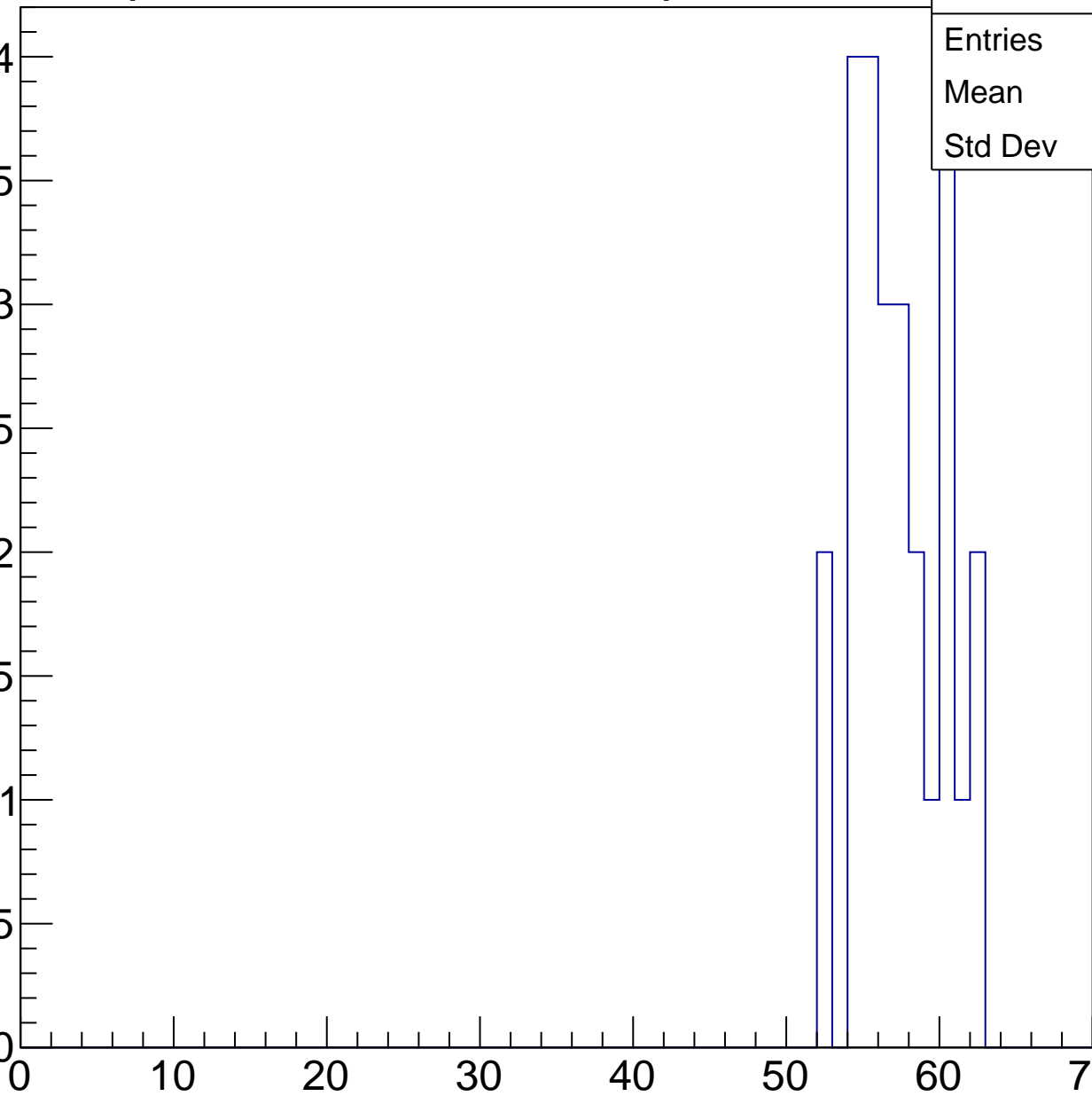
calib_packv5_042523_0143.root, FC#7, port C2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	26
Mean	56.88
Std Dev	2.86

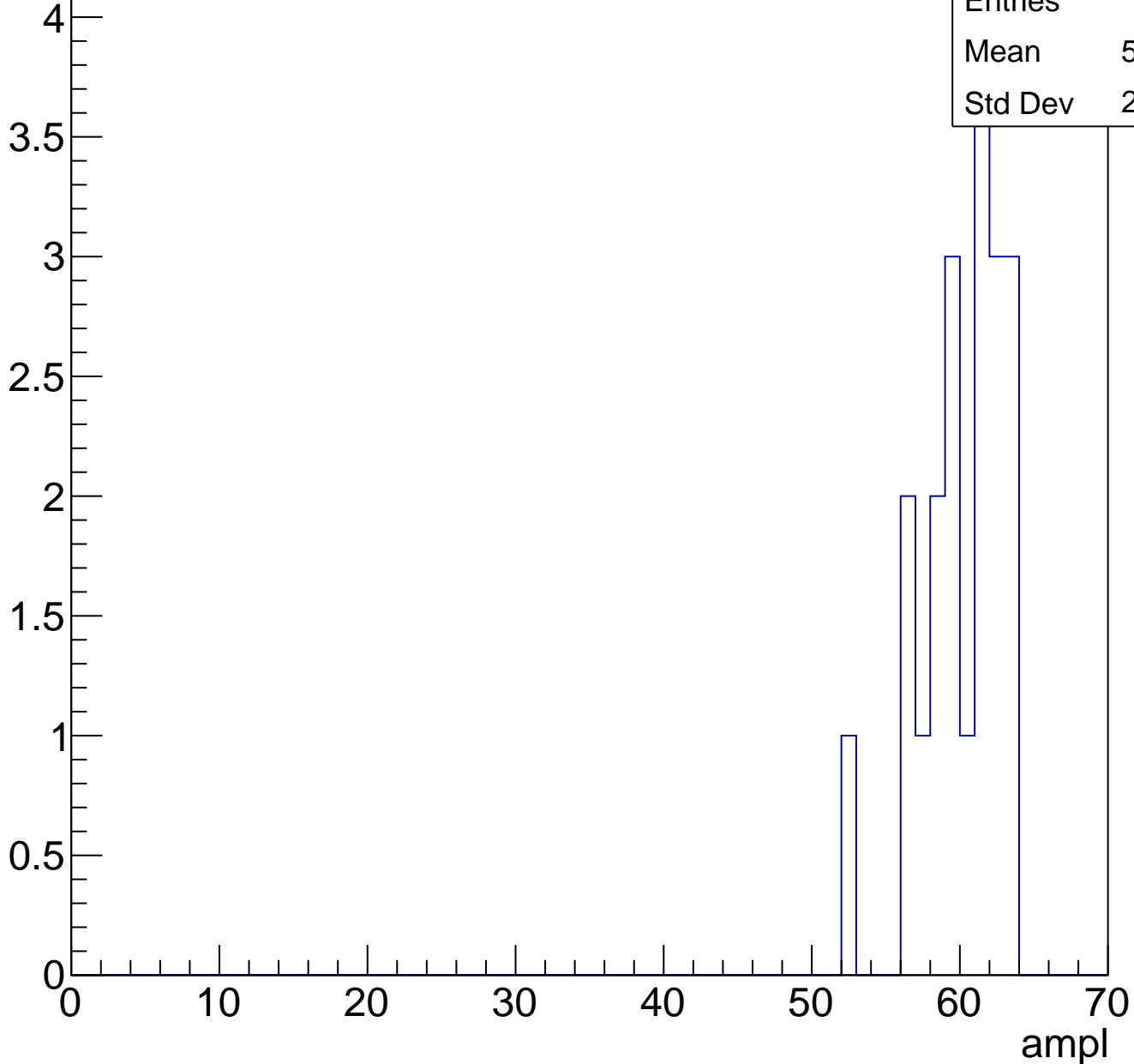
ampl



B1L103S, U15-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

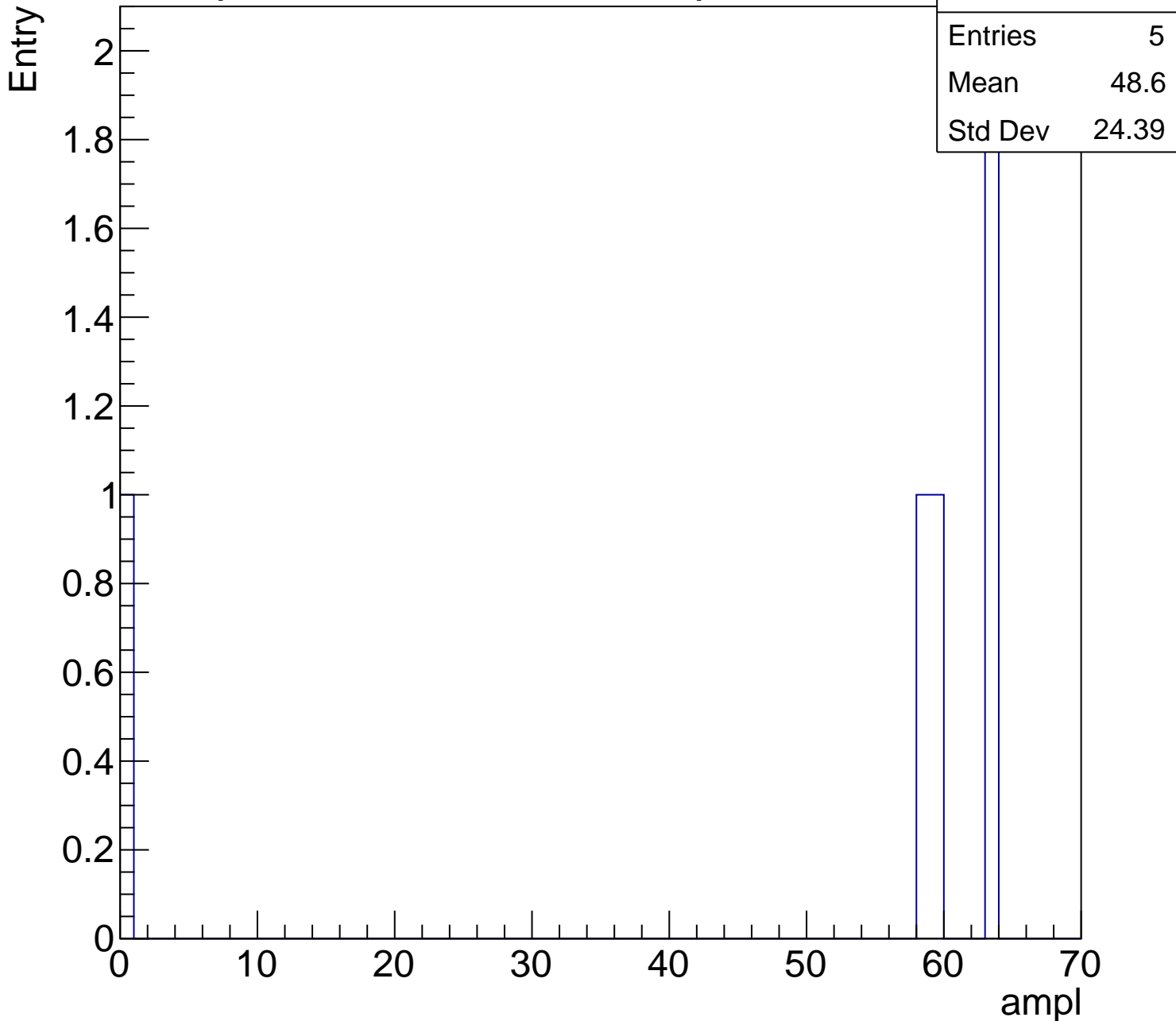
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	48.6
Std Dev	24.39

0 10 20 30 40 50 60 70

ampl



B1L103S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch122, adc0

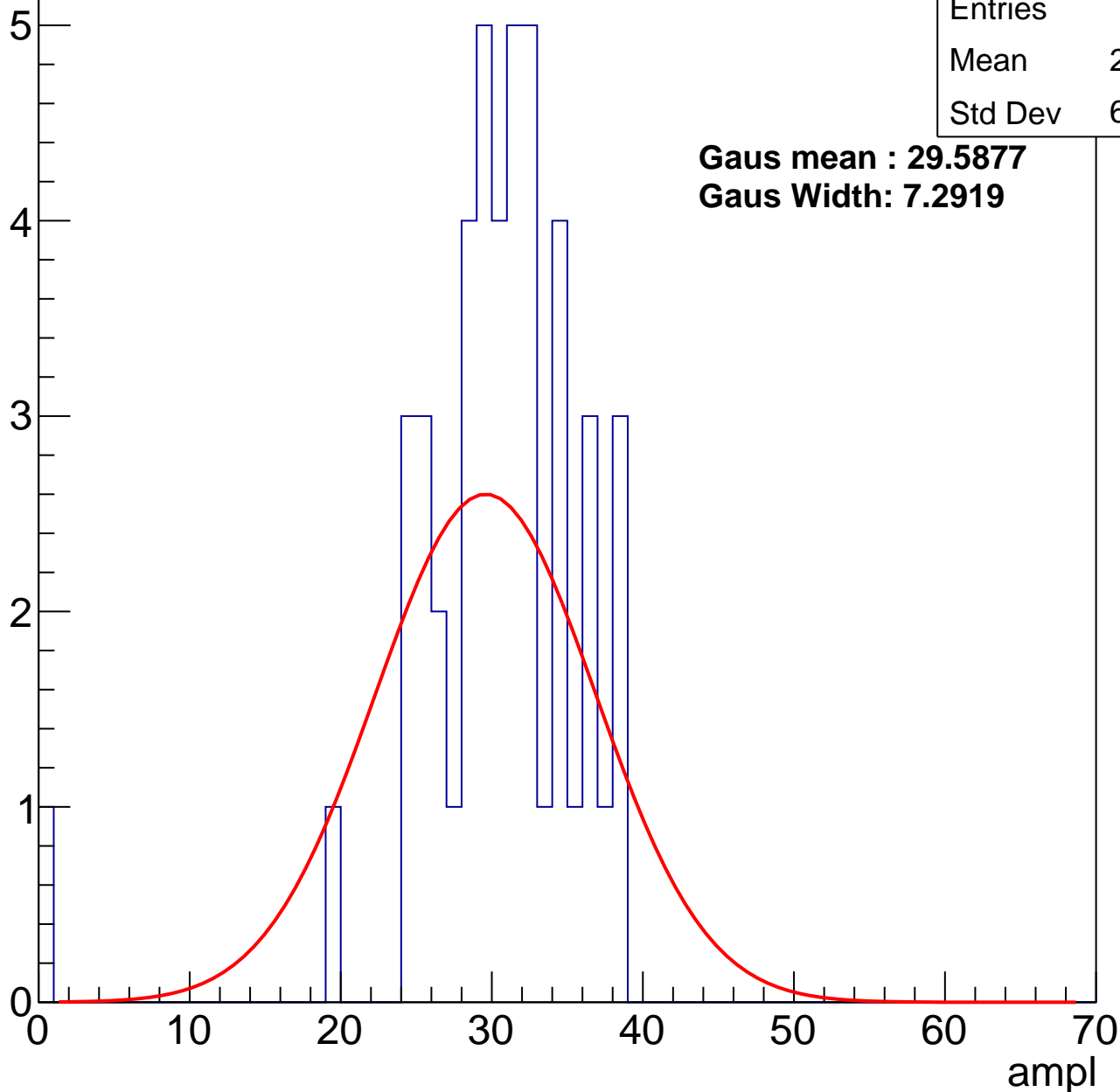
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	29.79
Std Dev	6.077

Gaus mean : 29.5877

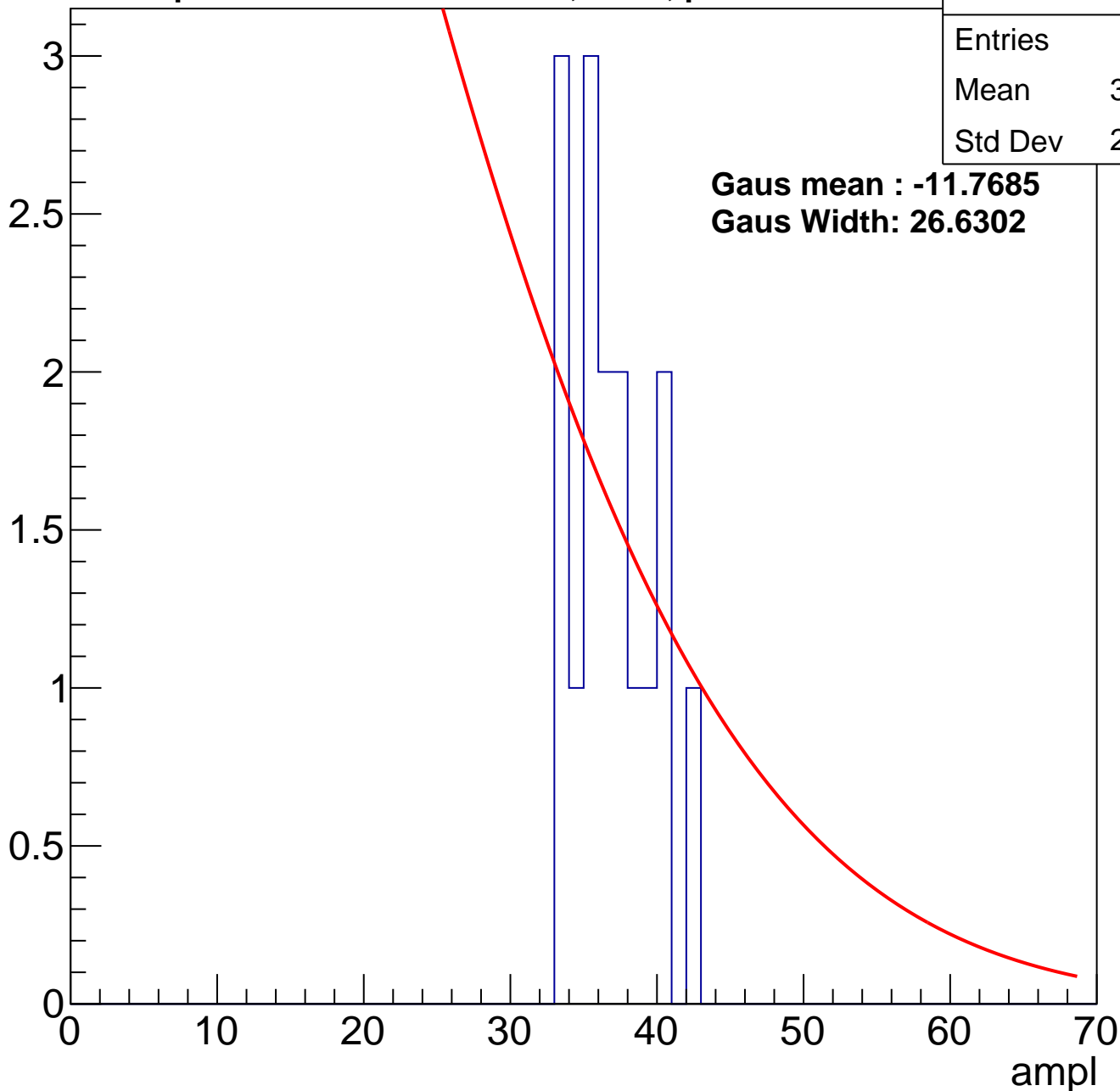
Gaus Width: 7.2919



B1L103S, U15-ch122, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch122, adc2

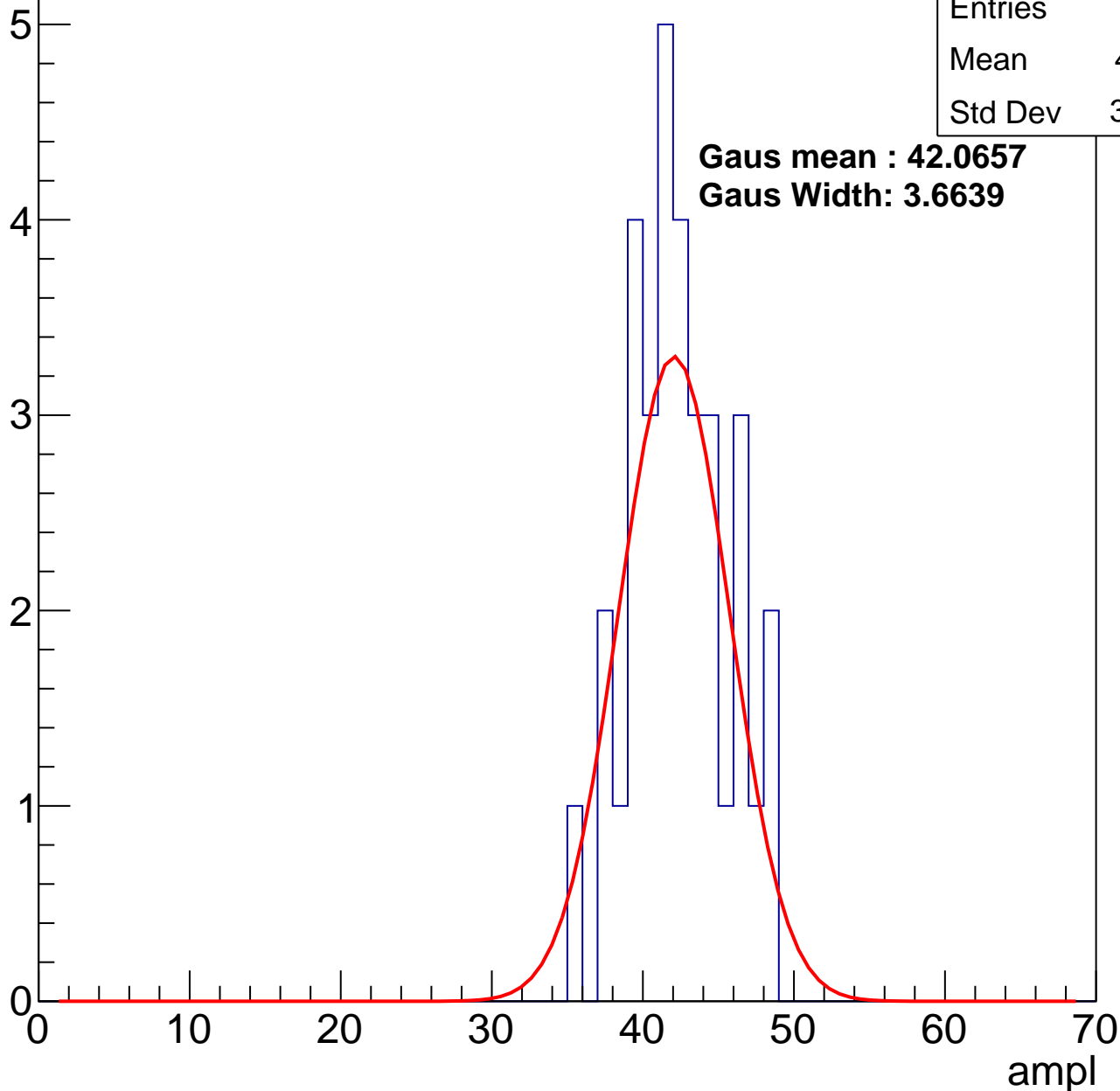
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	41.91
Std Dev	3.194

Gaus mean : 42.0657

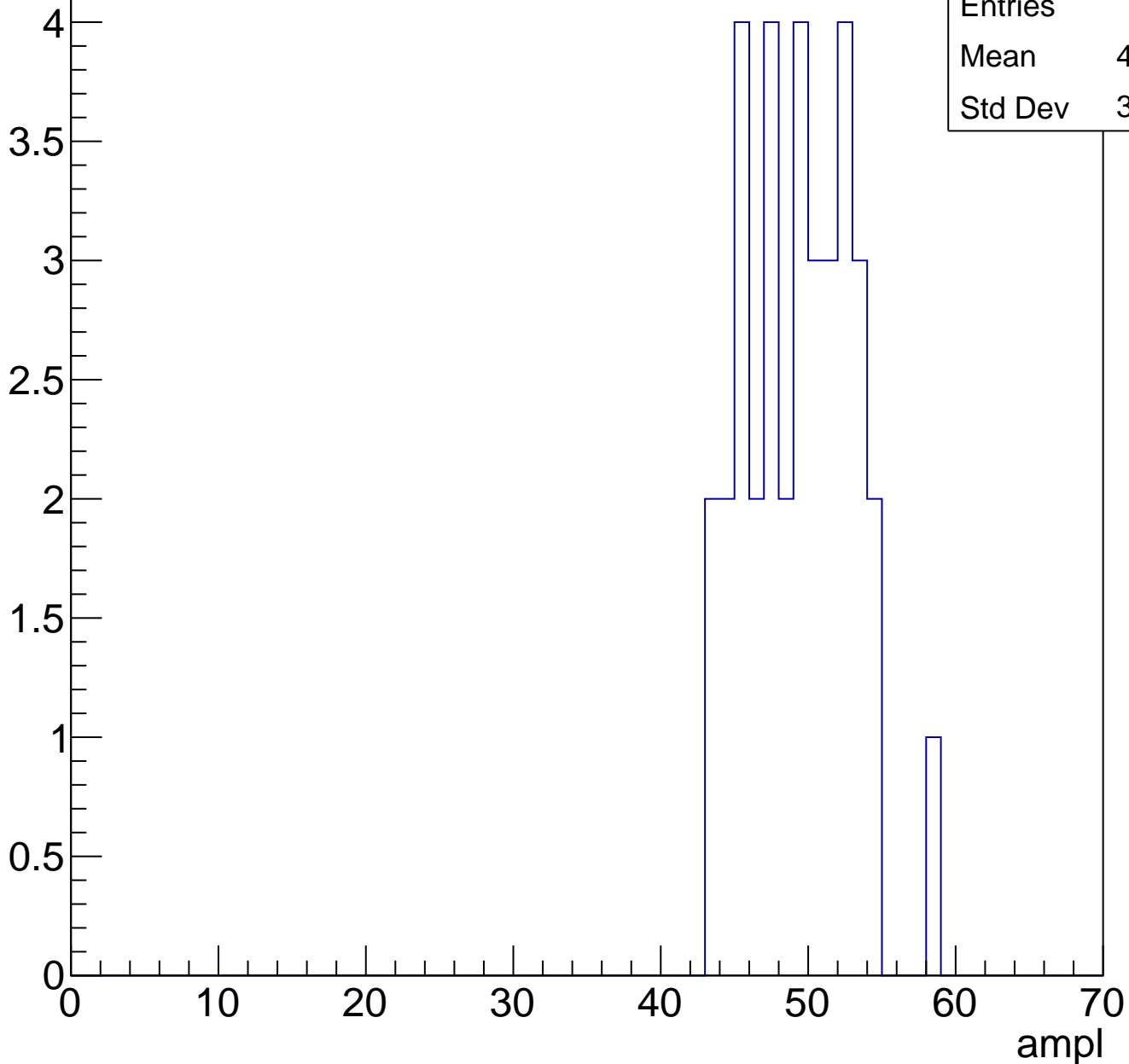
Gaus Width: 3.6639



B1L103S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

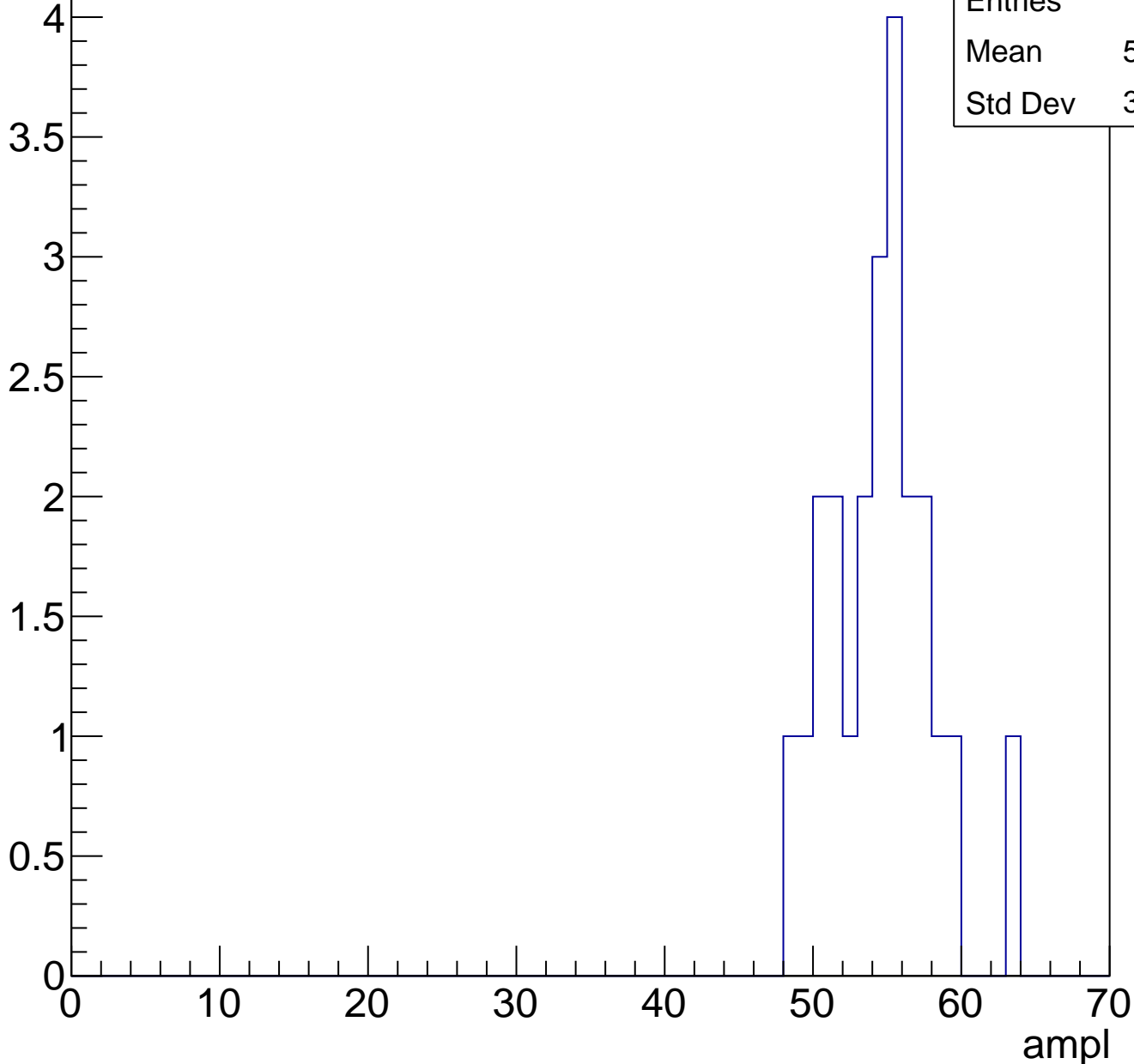
Entry



B1L103S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

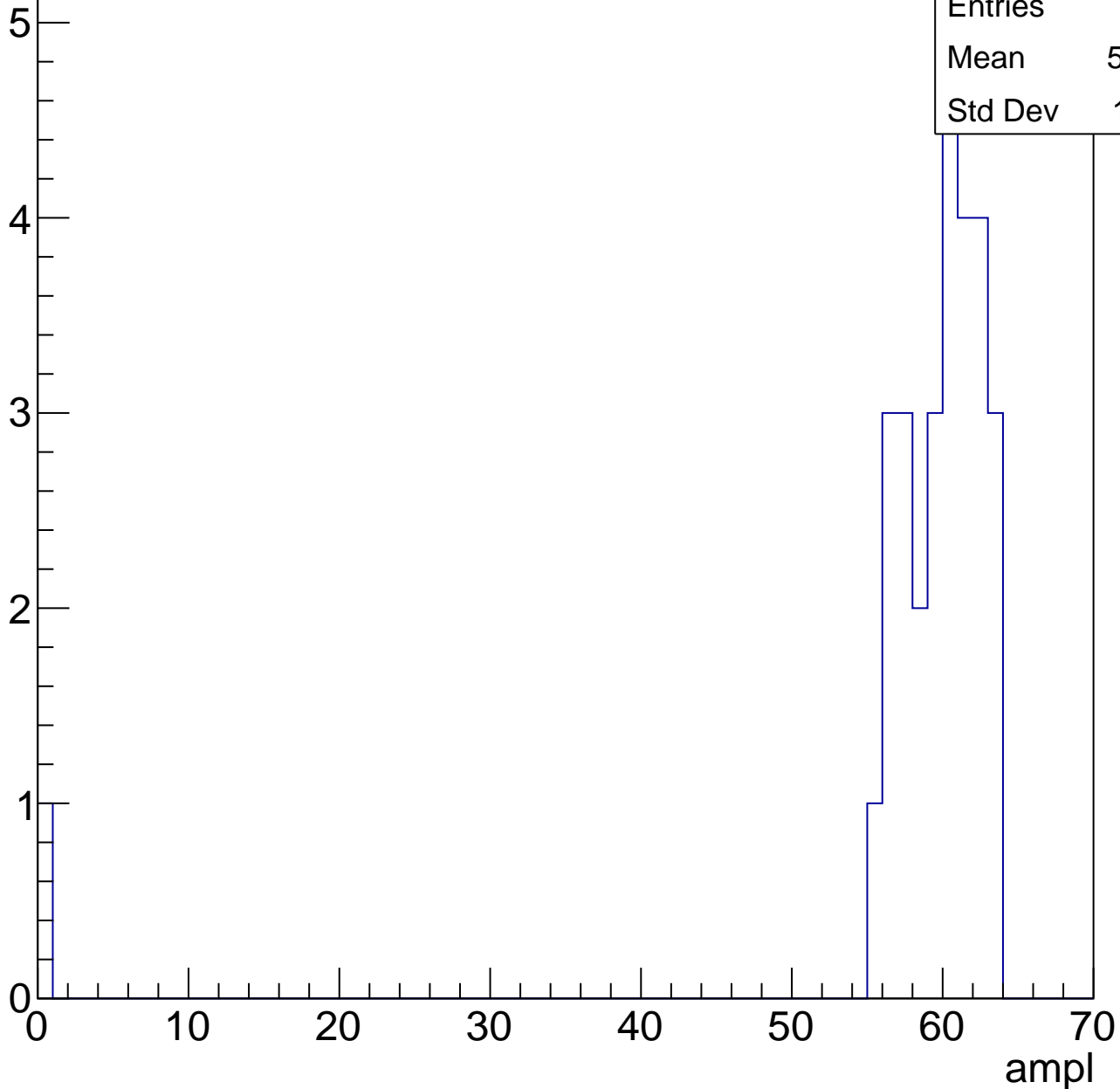


B1L103S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

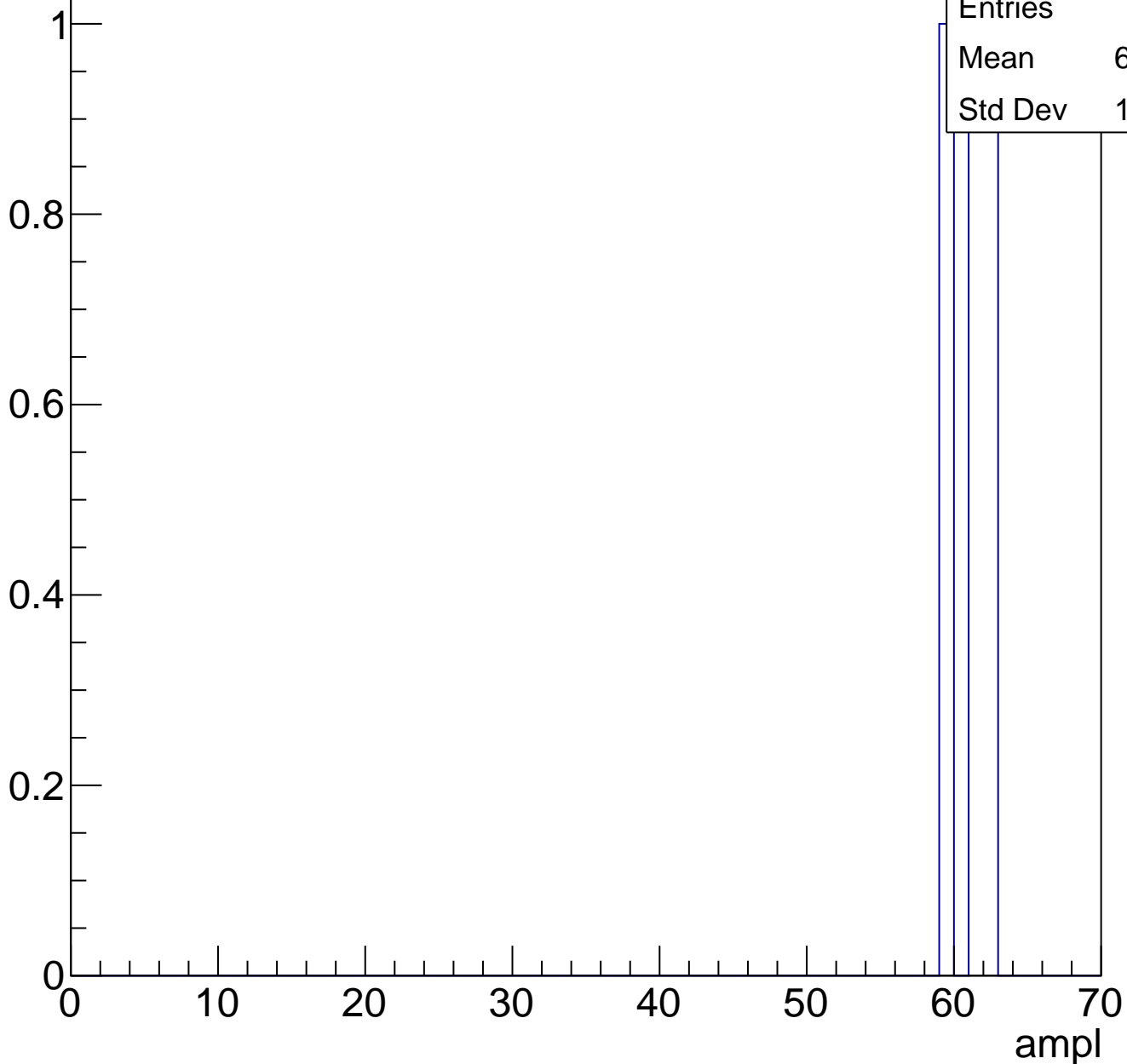
Entries	29
Mean	57.52
Std Dev	11.11



B1L103S, U15-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

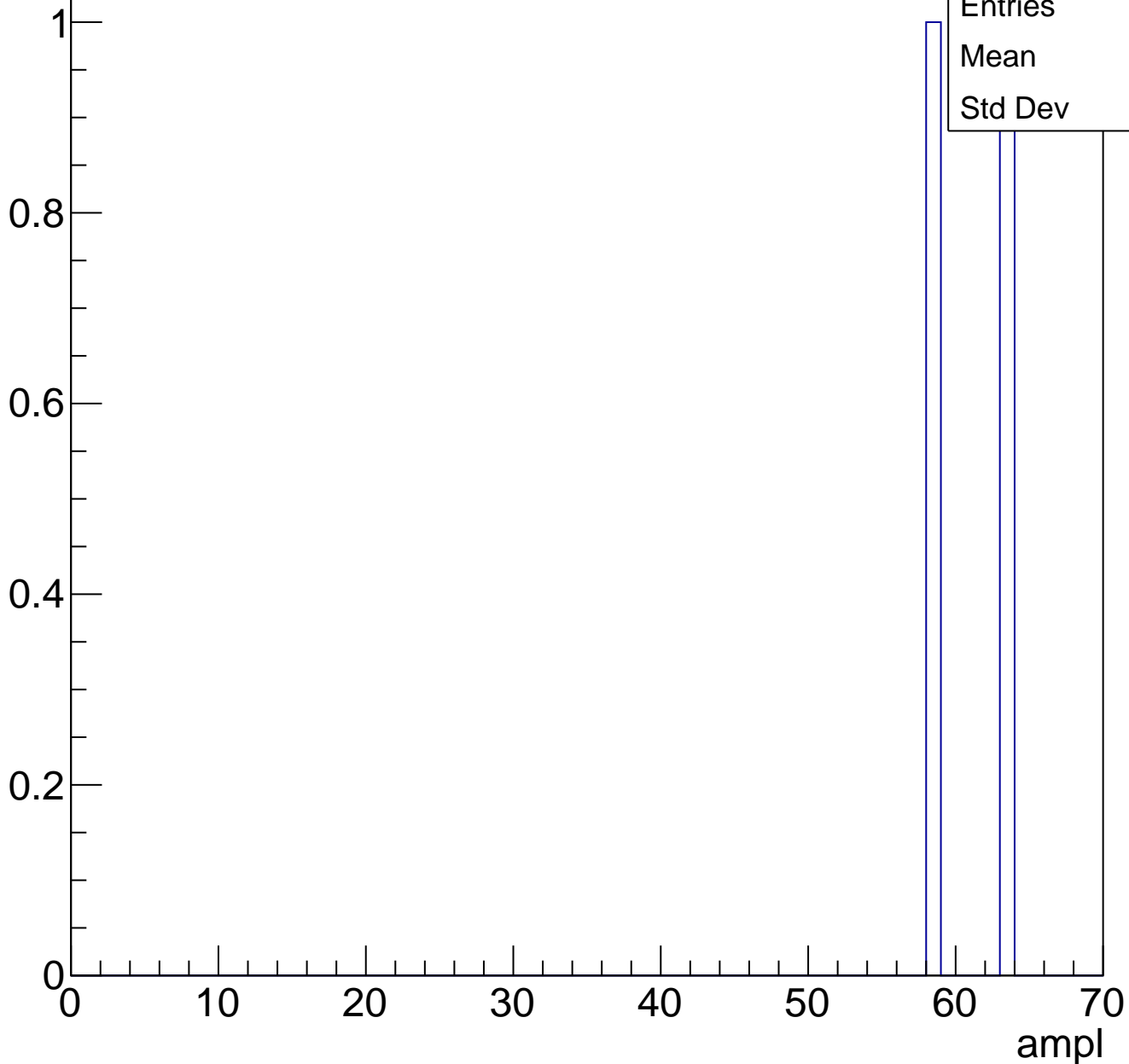
Entry



B1L103S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

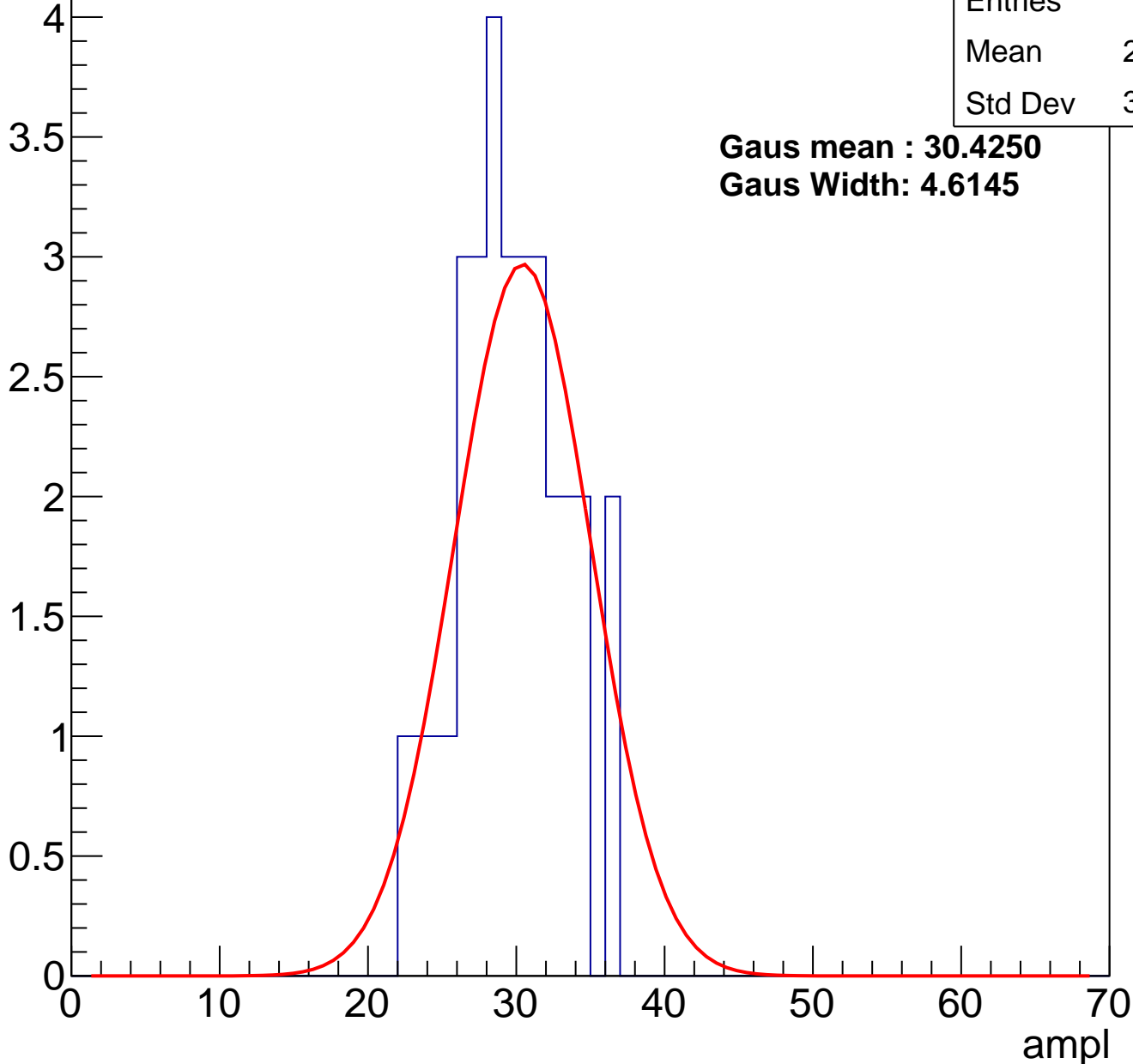
Entry



B1L103S, U15-ch123, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch123, adc1

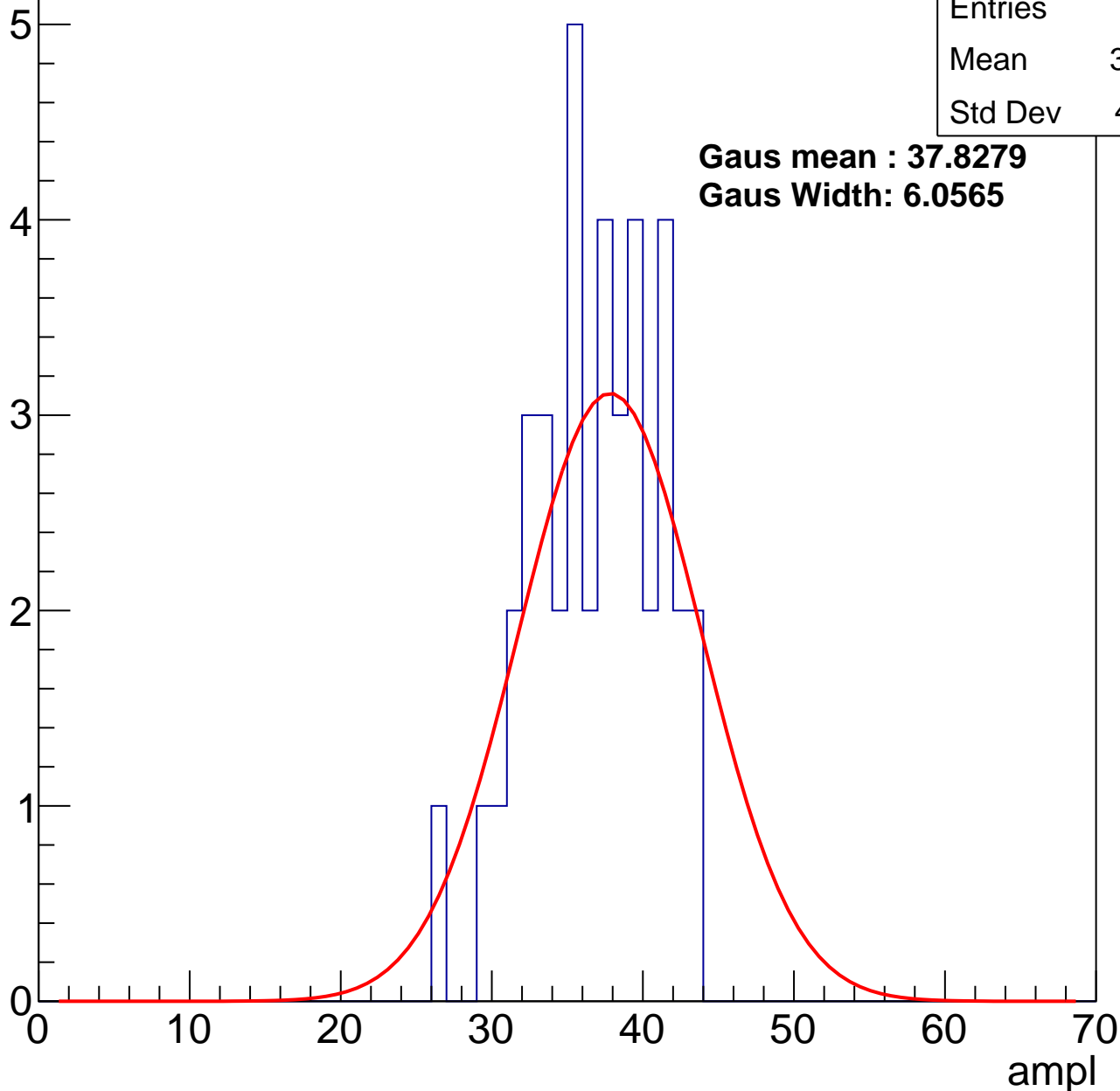
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	36.32
Std Dev	4.051

Gaus mean : 37.8279

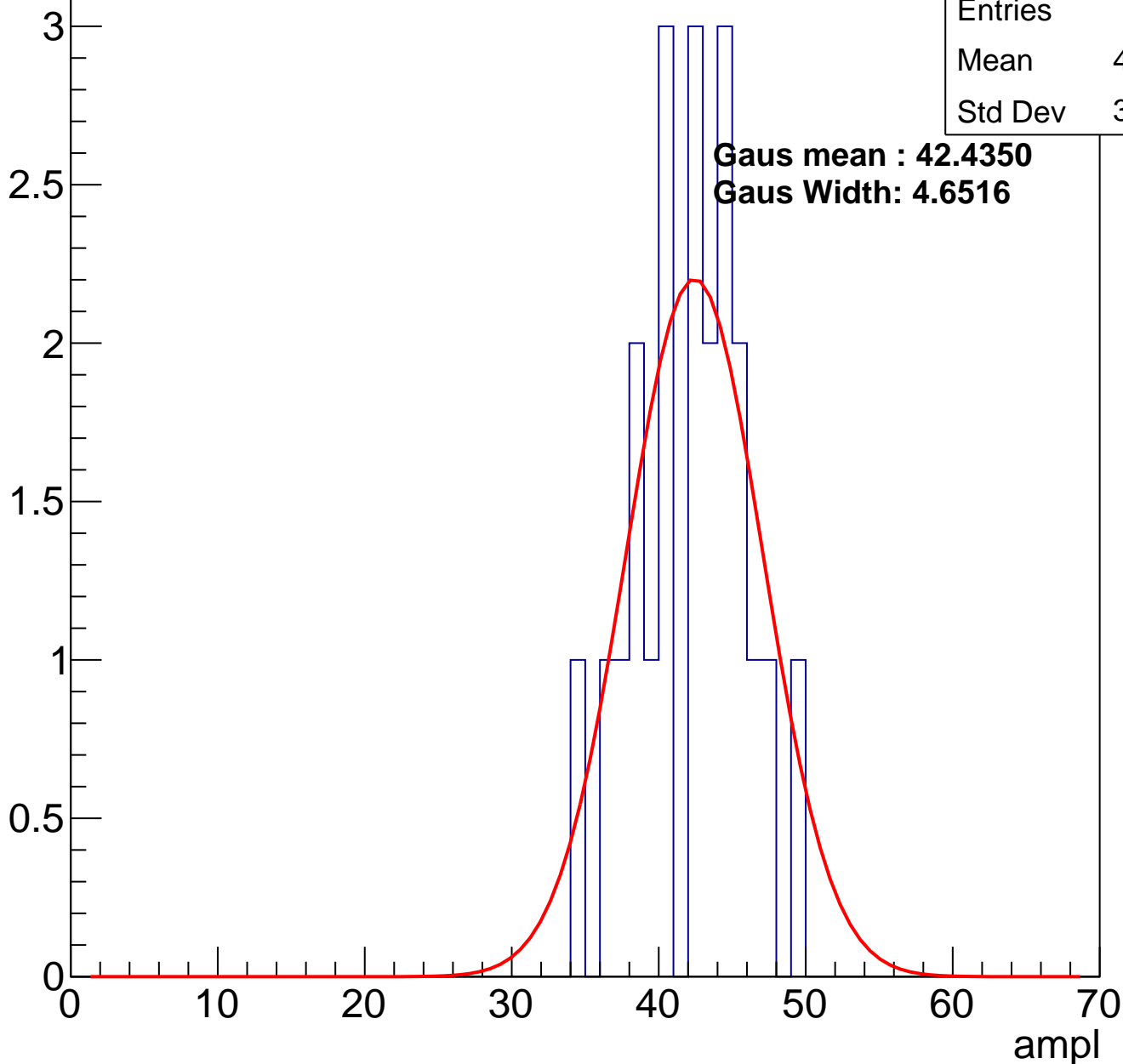
Gaus Width: 6.0565



B1L103S, U15-ch123, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

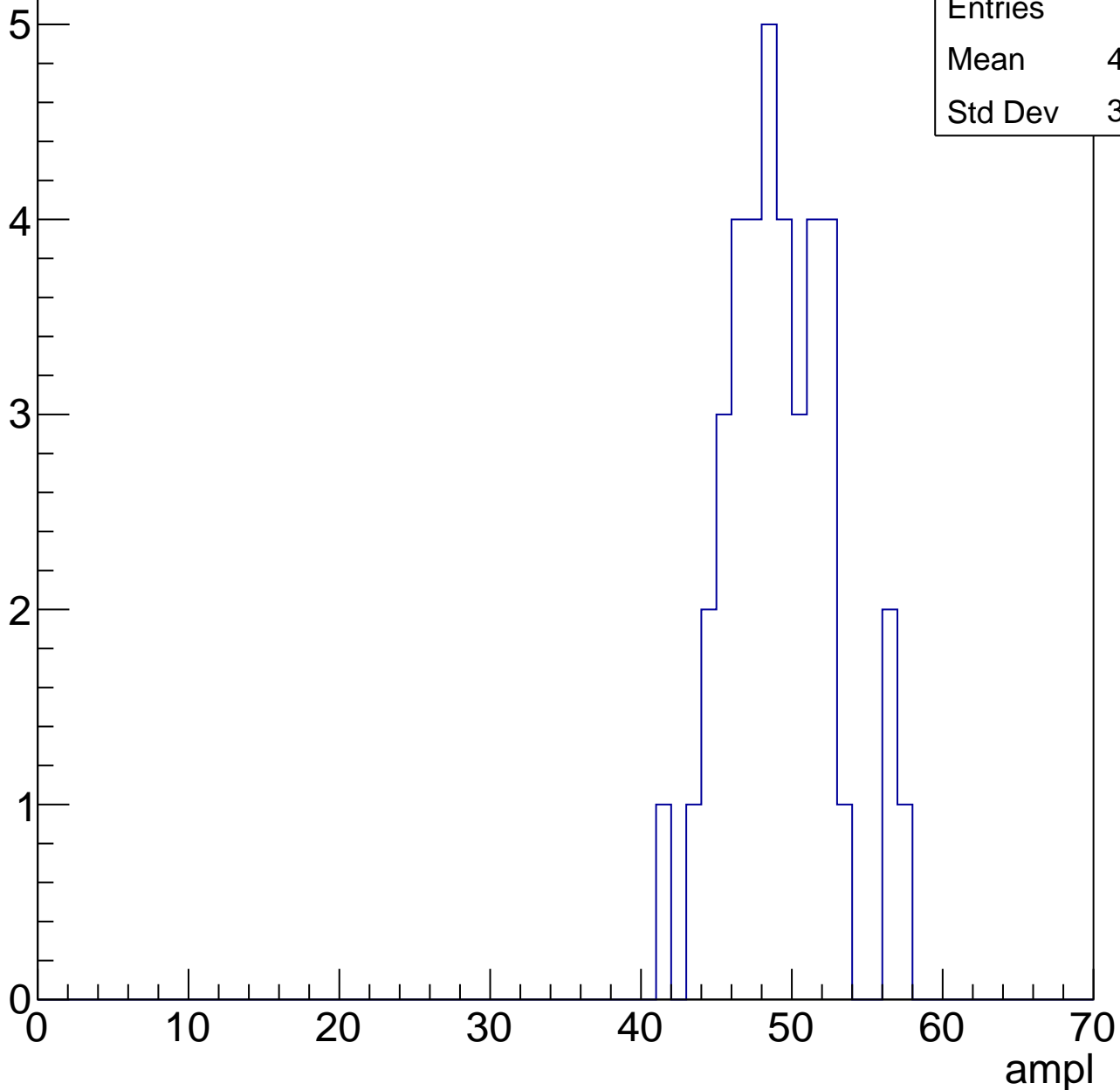


B1L103S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

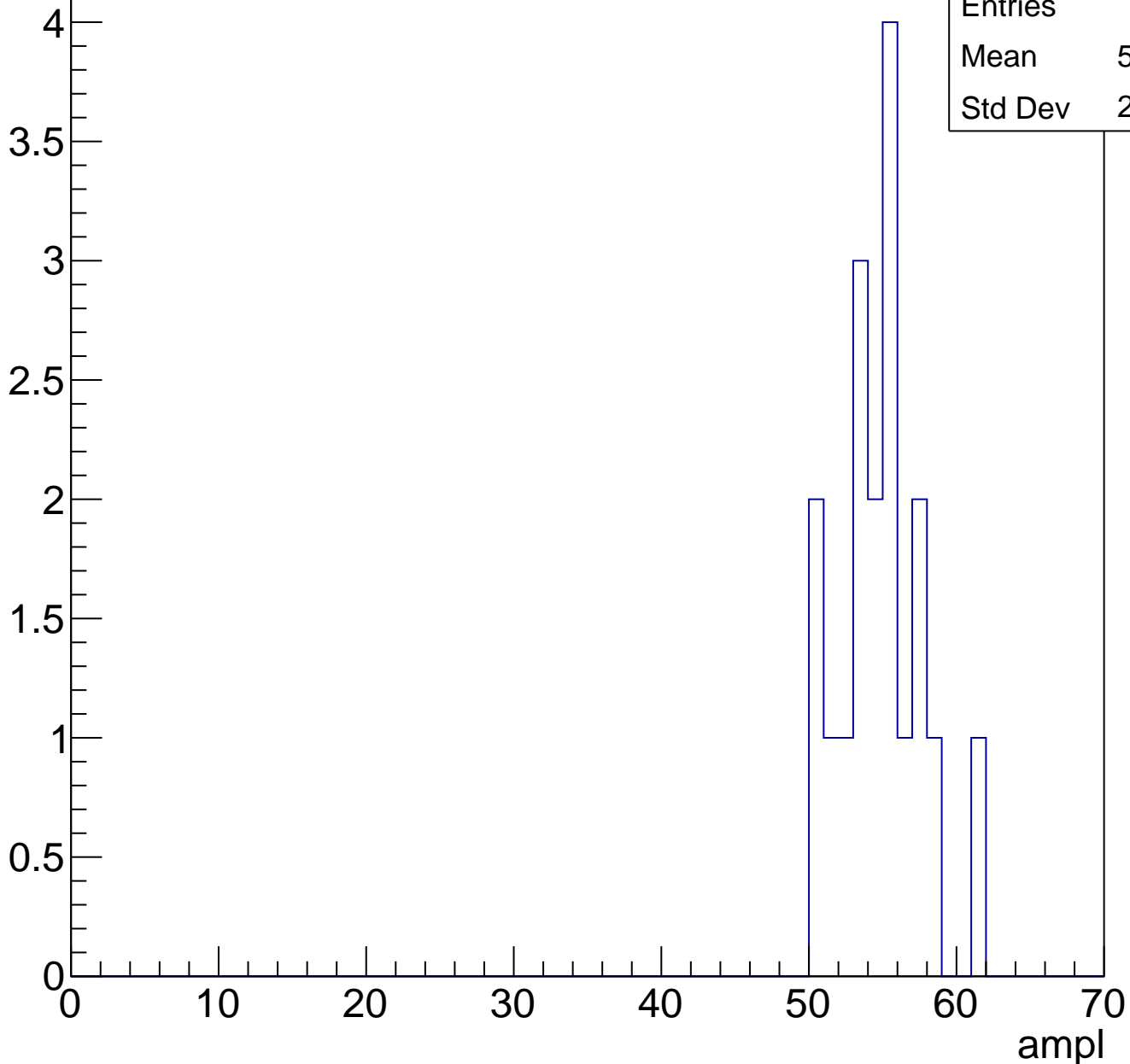
Entries	39
Mean	48.69
Std Dev	3.524



B1L103S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

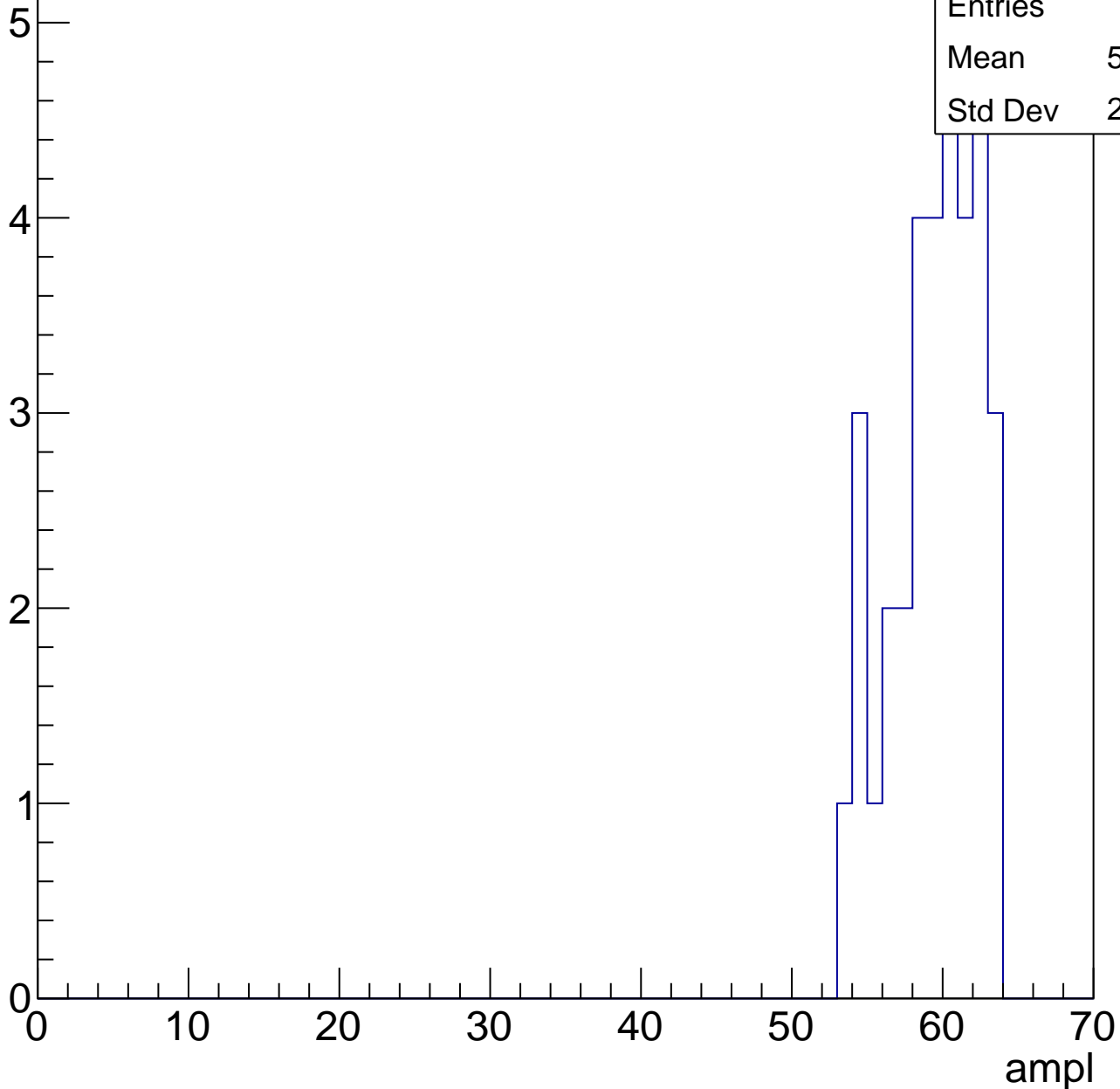


B1L103S, U15-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	59.03
Std Dev	2.823



B1L103S, U15-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

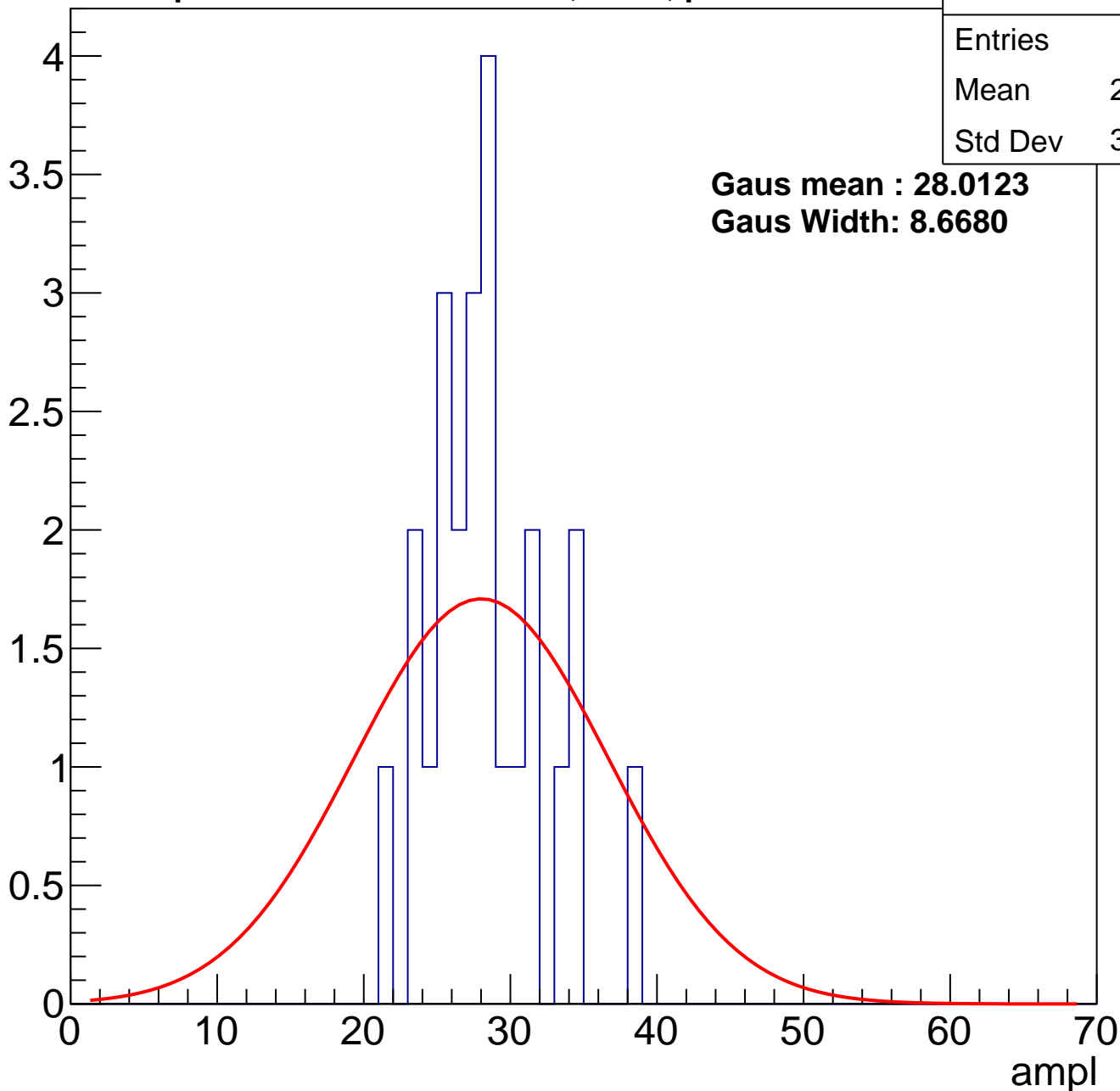
Entry



B1L103S, U15-ch124, adc0

calib_packv5_042523_0143.root, FC#7, port C2

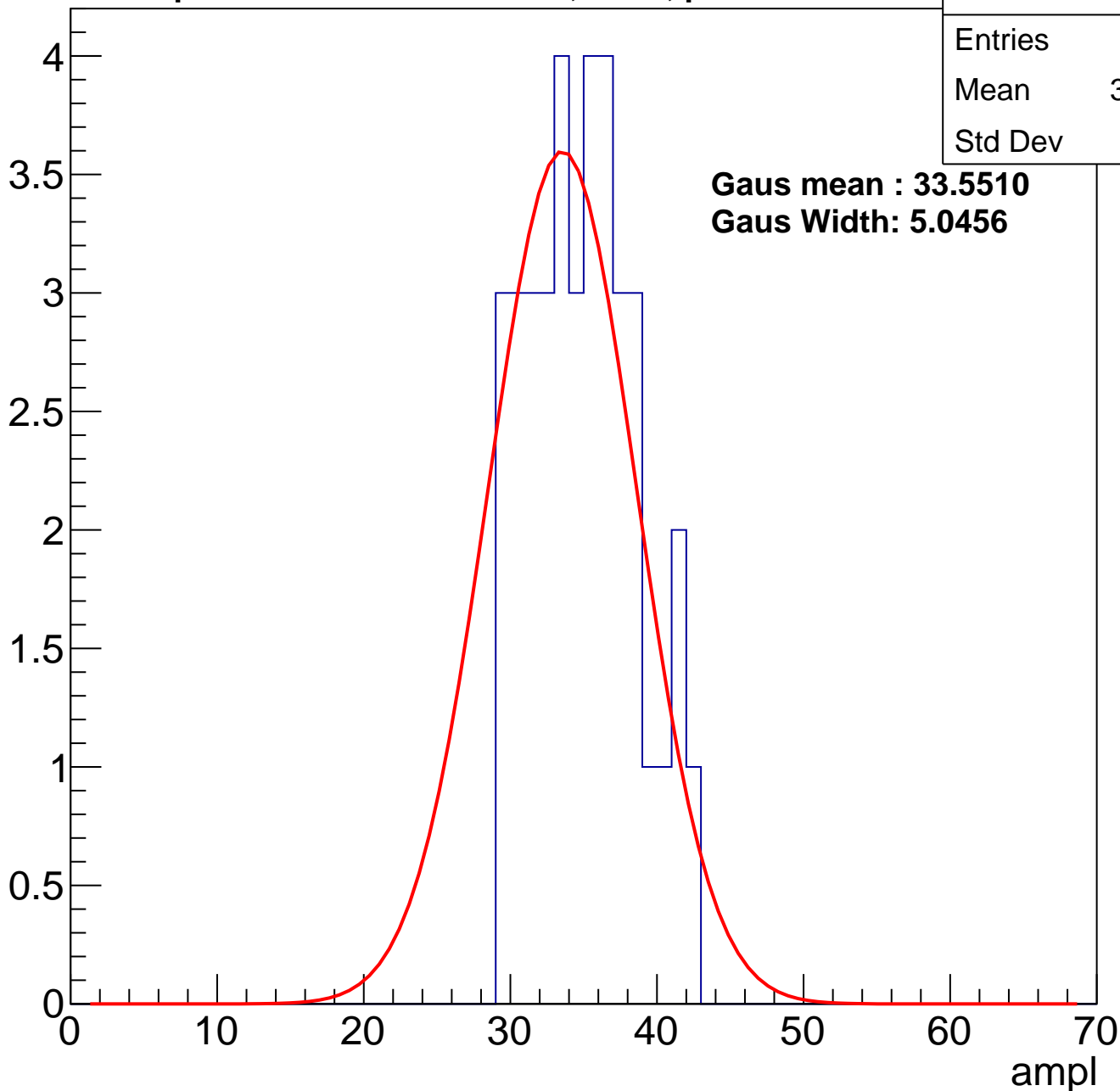
Entry



B1L103S, U15-ch124, adc1

calib_packv5_042523_0143.root, FC#7, port C2

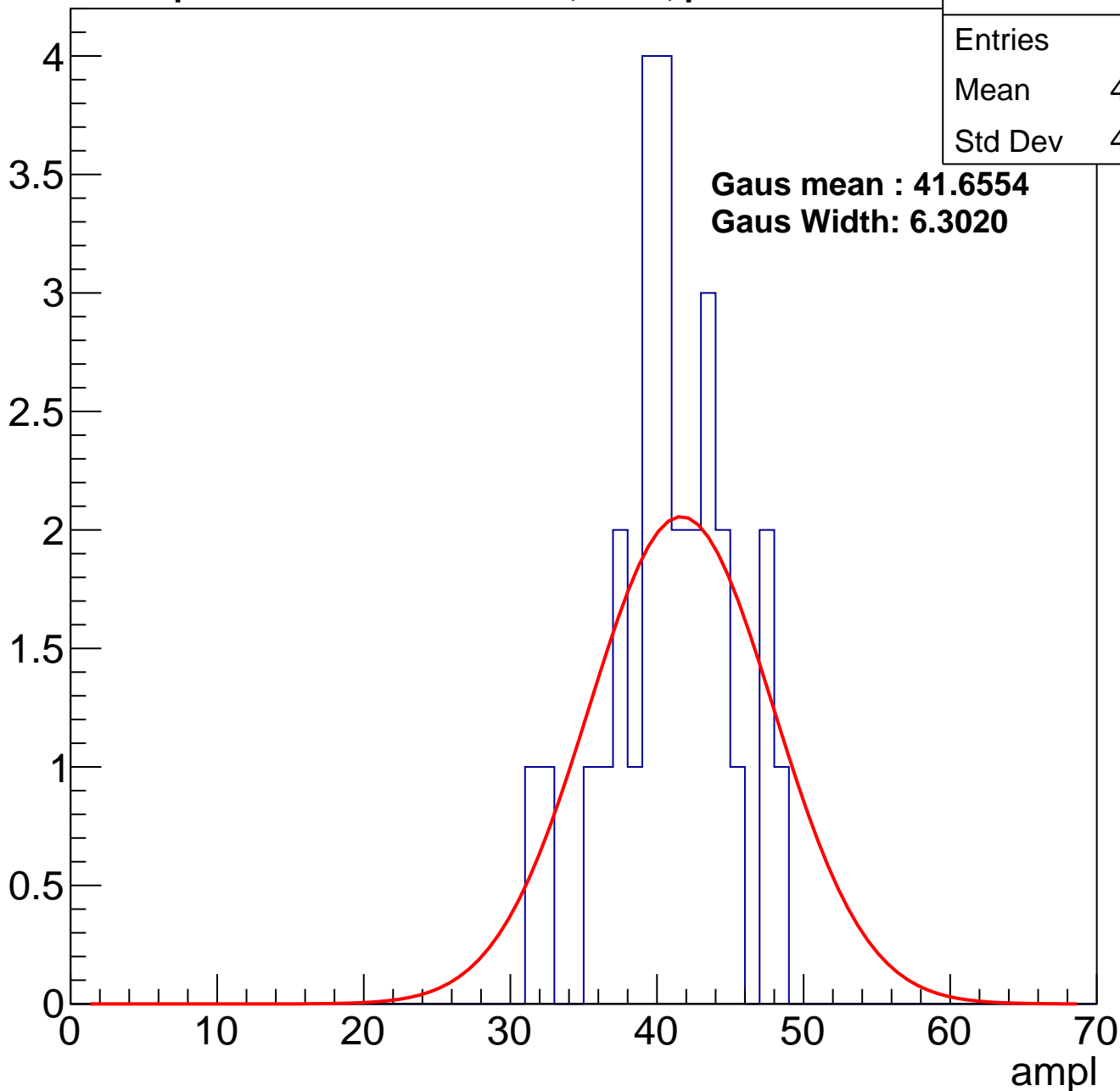
Entry



B1L103S, U15-ch124, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

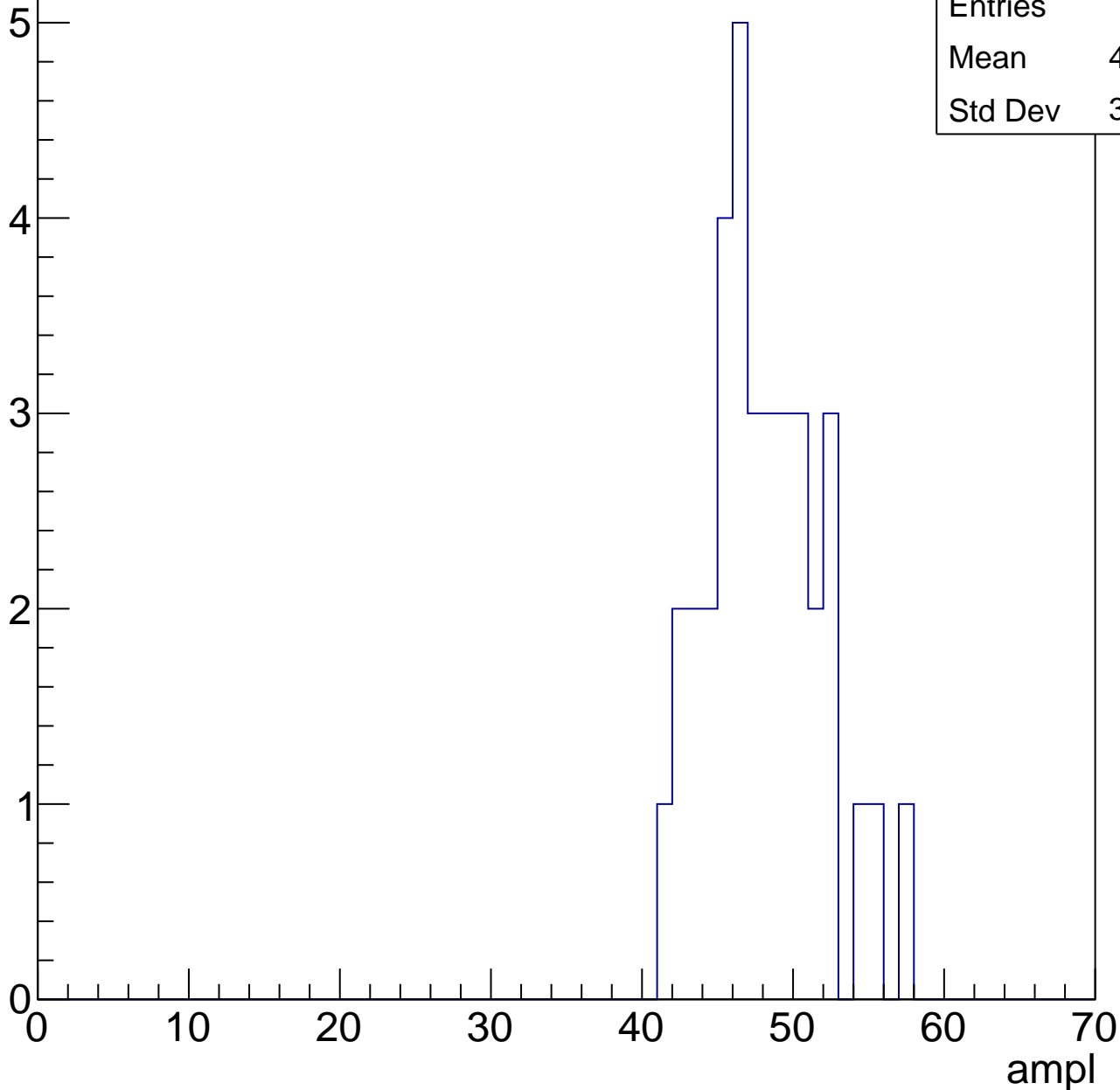


B1L103S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

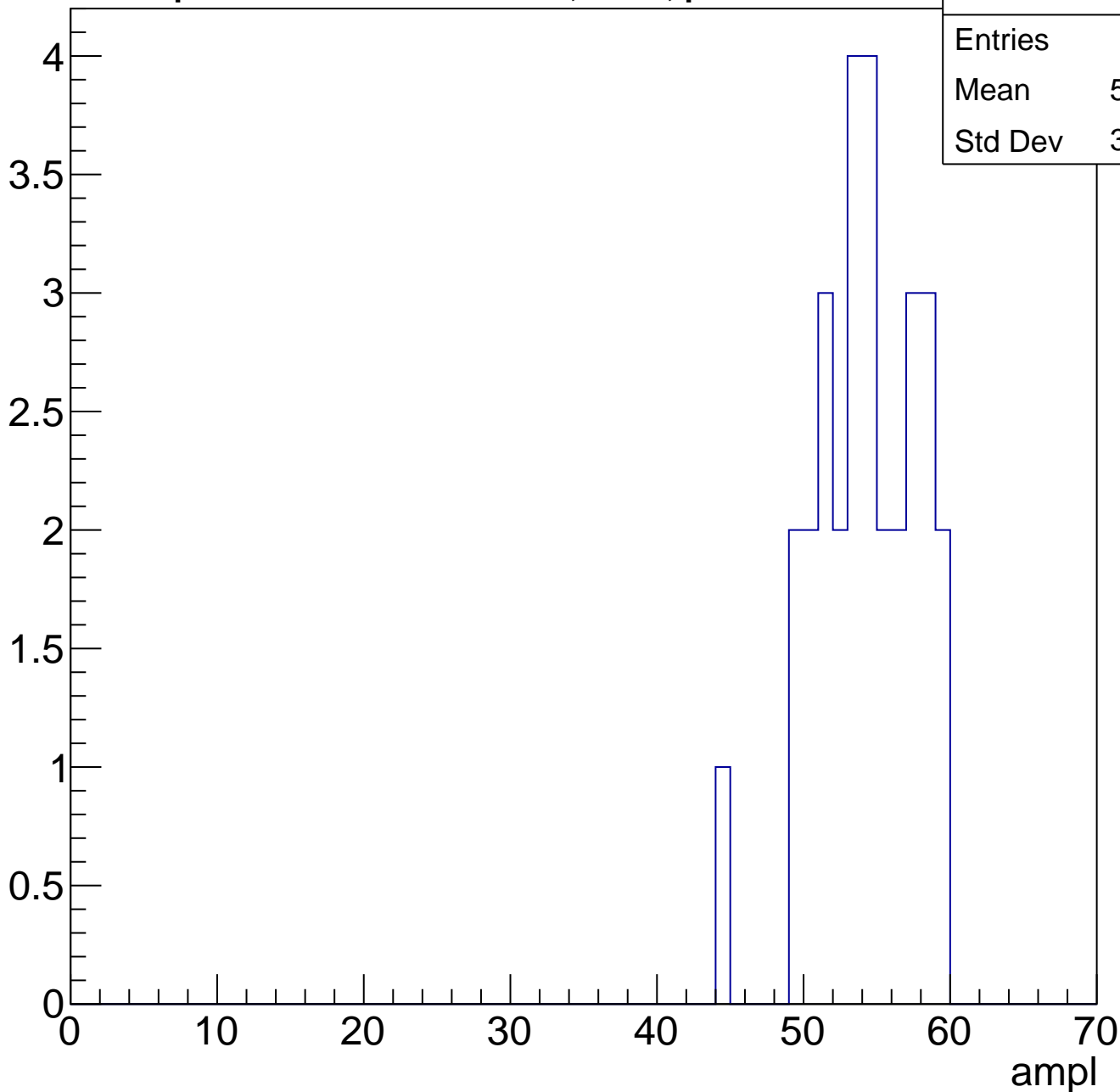
Entries	36
Mean	47.64
Std Dev	3.743



B1L103S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

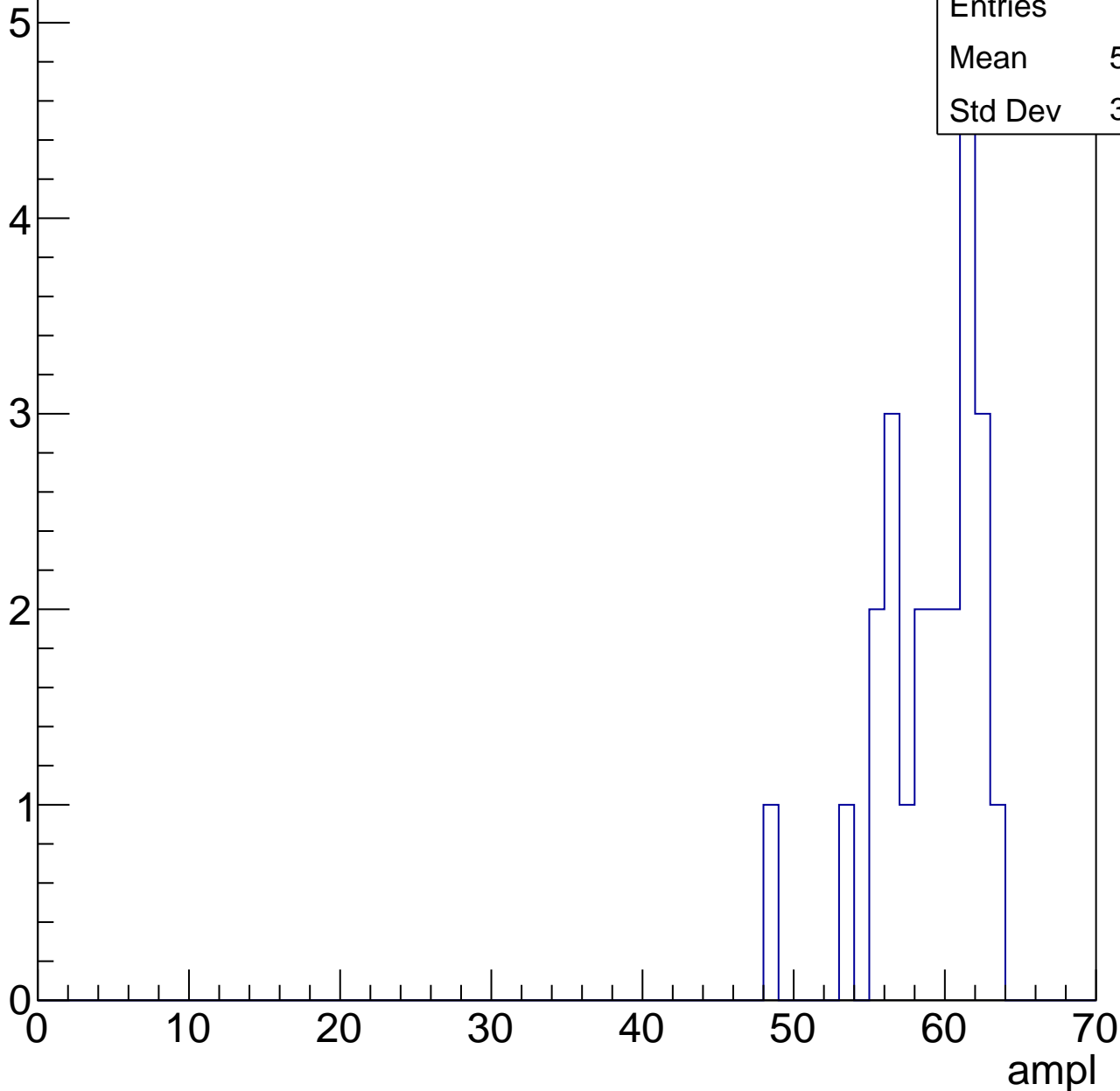


B1L103S, U15-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

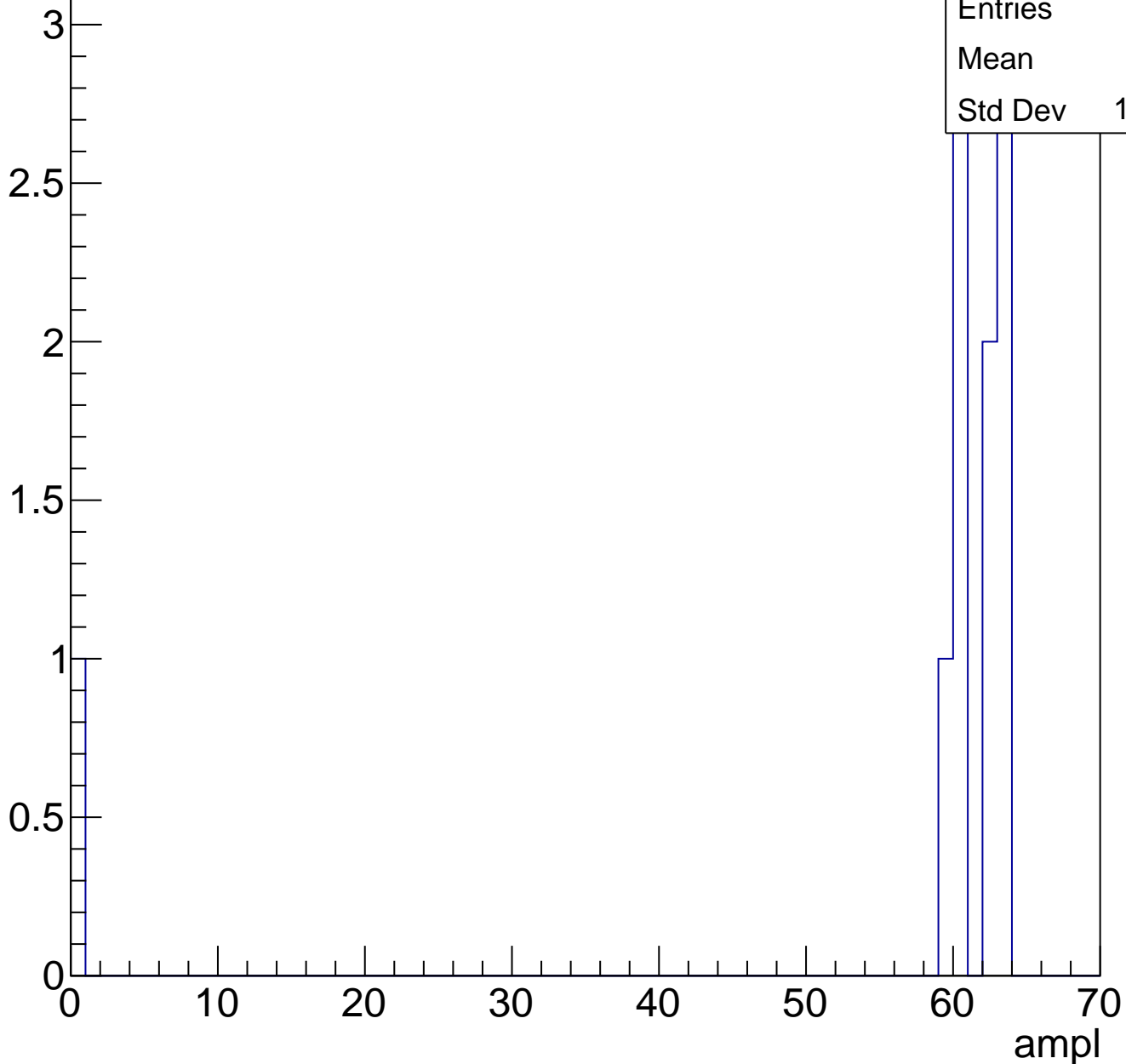
Entries	23
Mean	58.43
Std Dev	3.487



B1L103S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

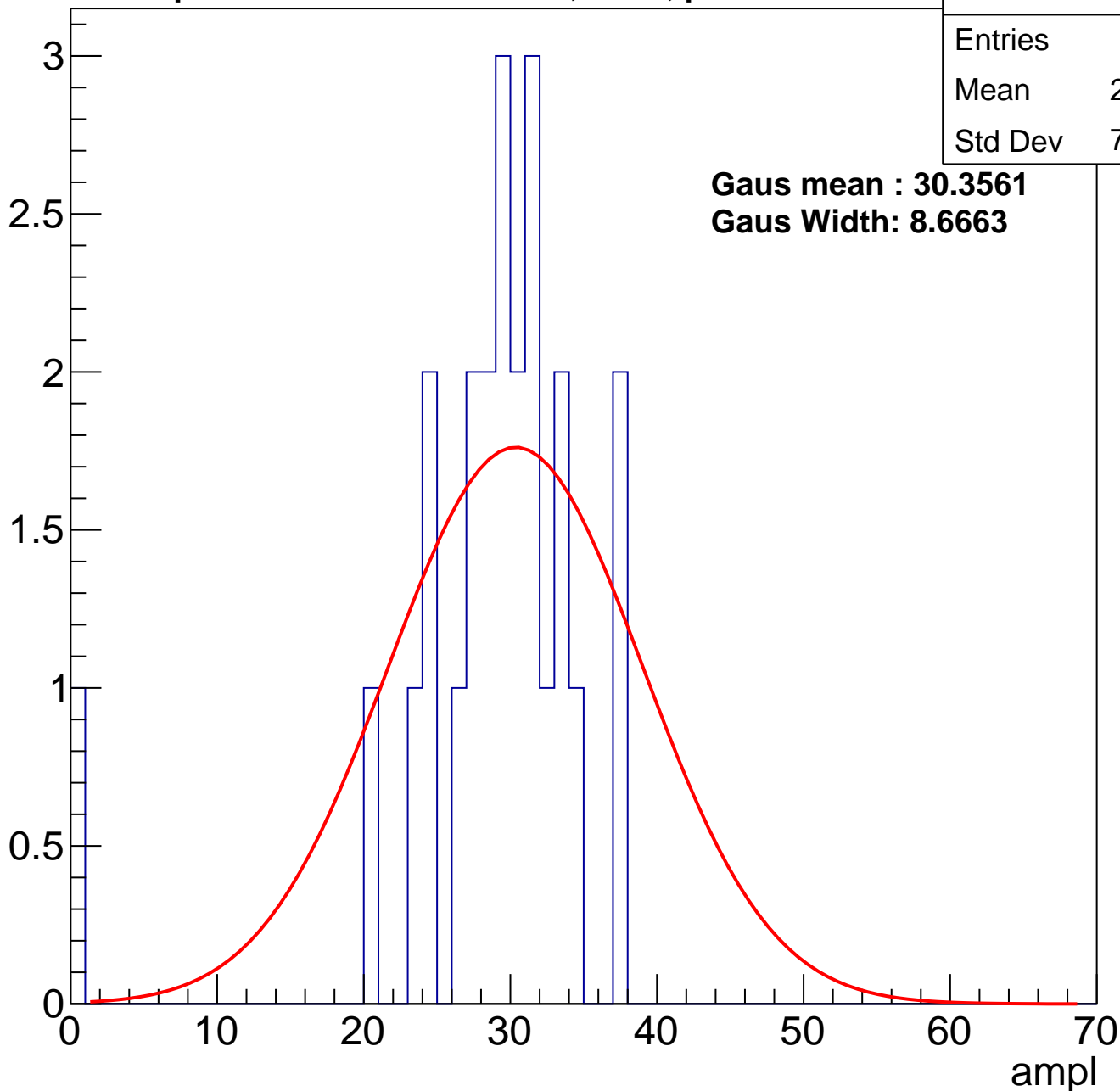
Entry



B1L103S, U15-ch125, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch125, adc1

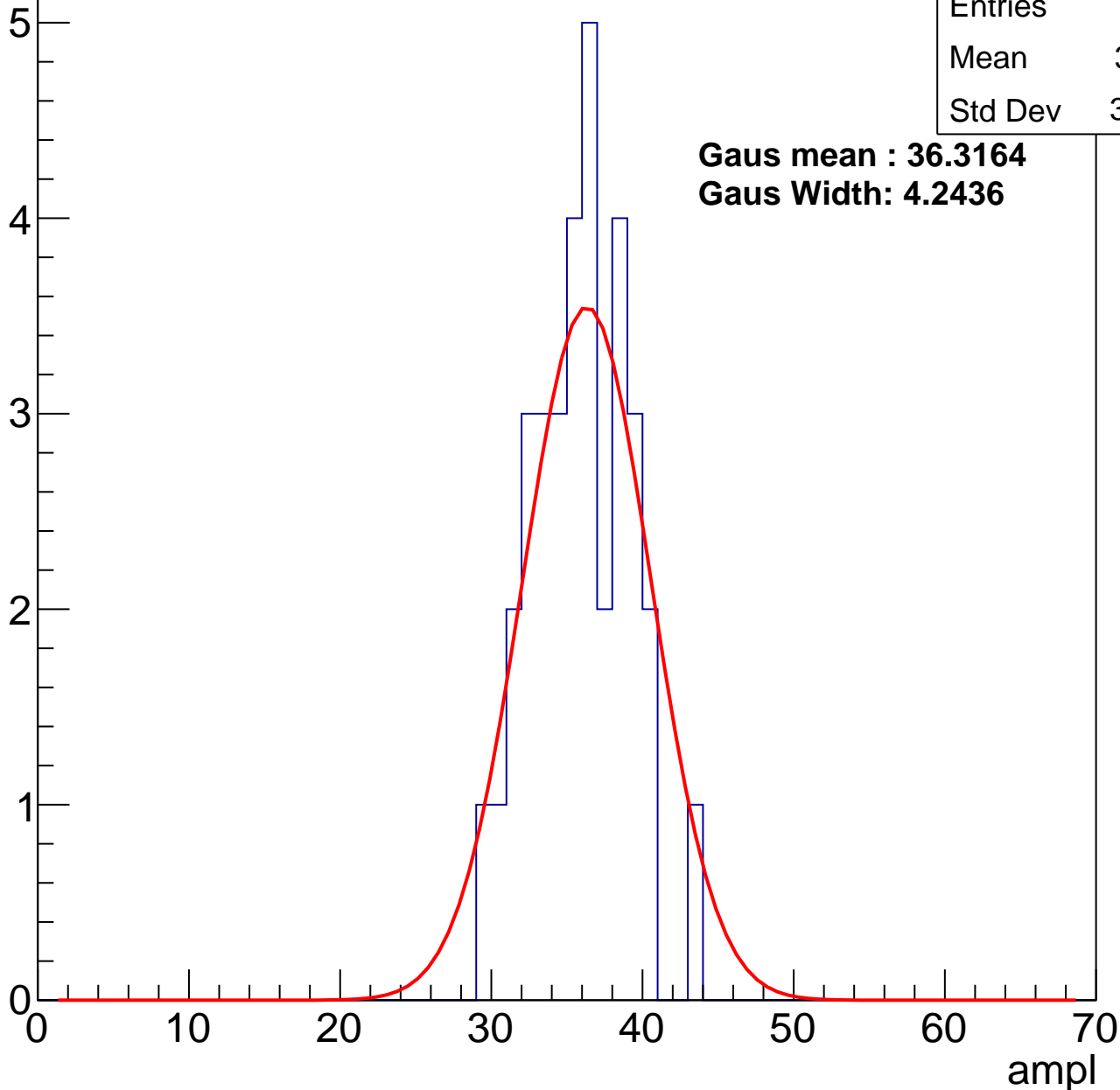
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	35.41
Std Dev	3.163

Gaus mean : 36.3164

Gaus Width: 4.2436



B1L103S, U15-ch125, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

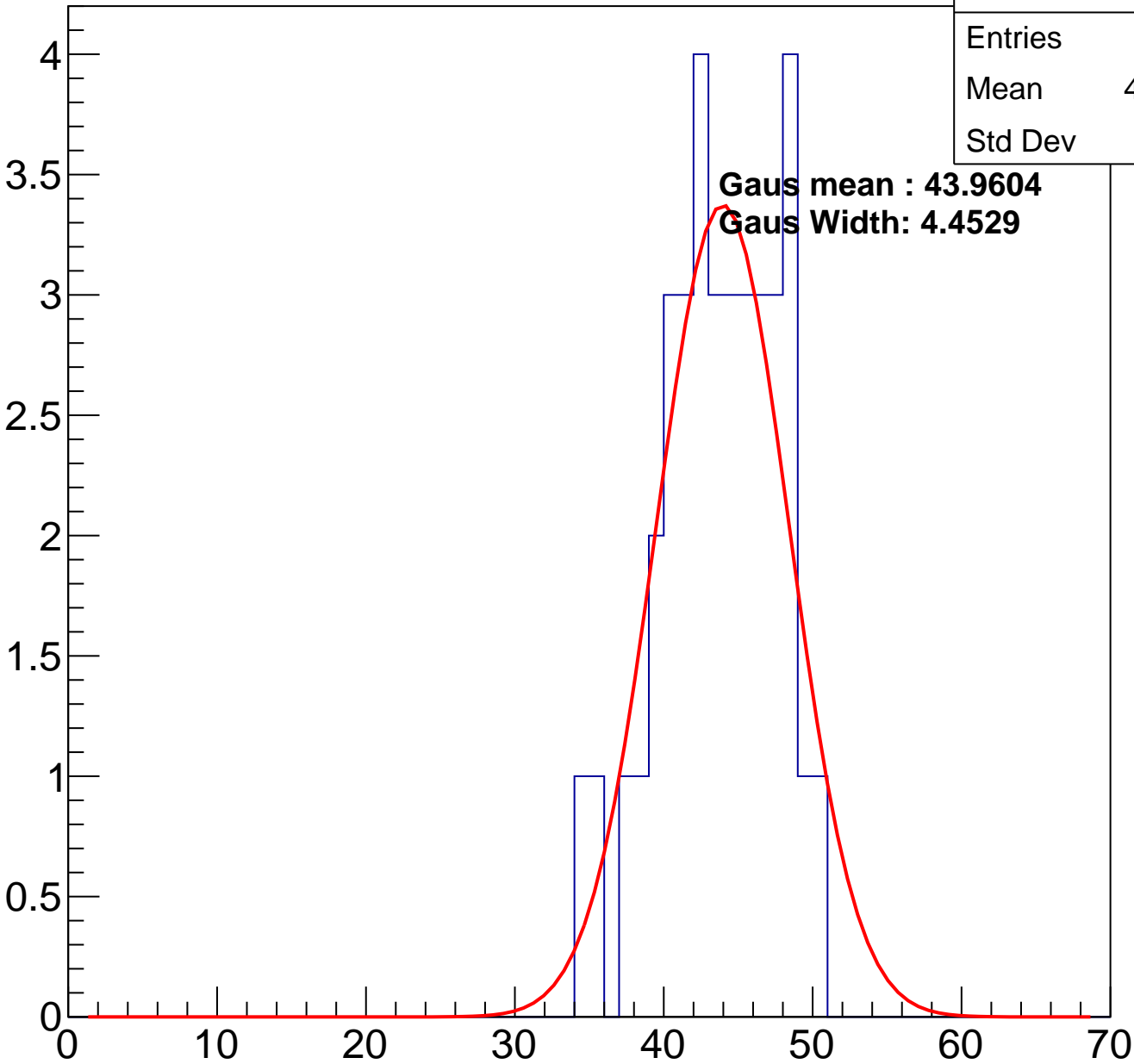
4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	37
Mean	43.22
Std Dev	3.87

Gaus mean : 43.9604

Gaus Width: 4.4529

0 10 20 30 40 50 60 70
ampl

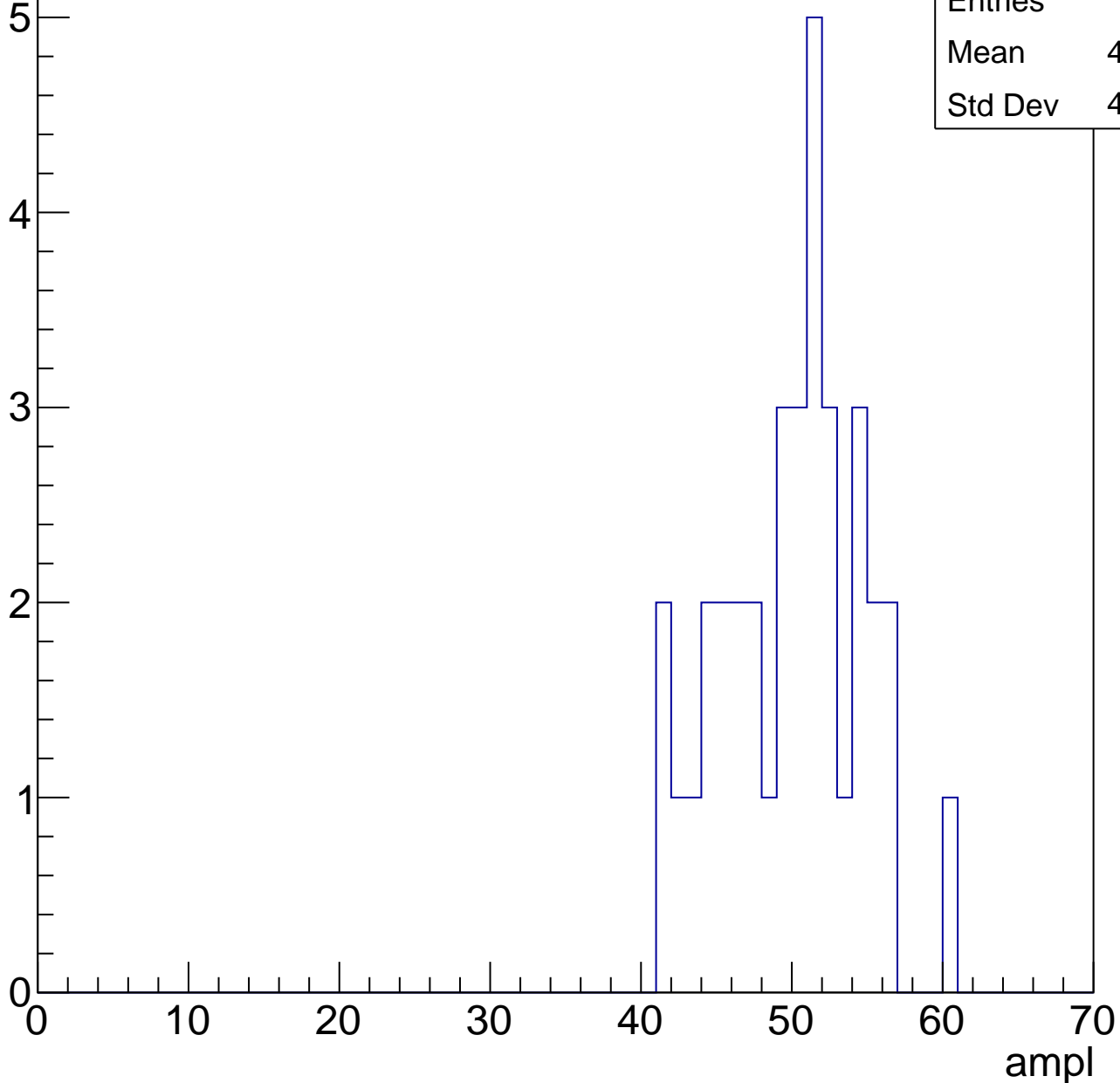


B1L103S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	49.56
Std Dev	4.543

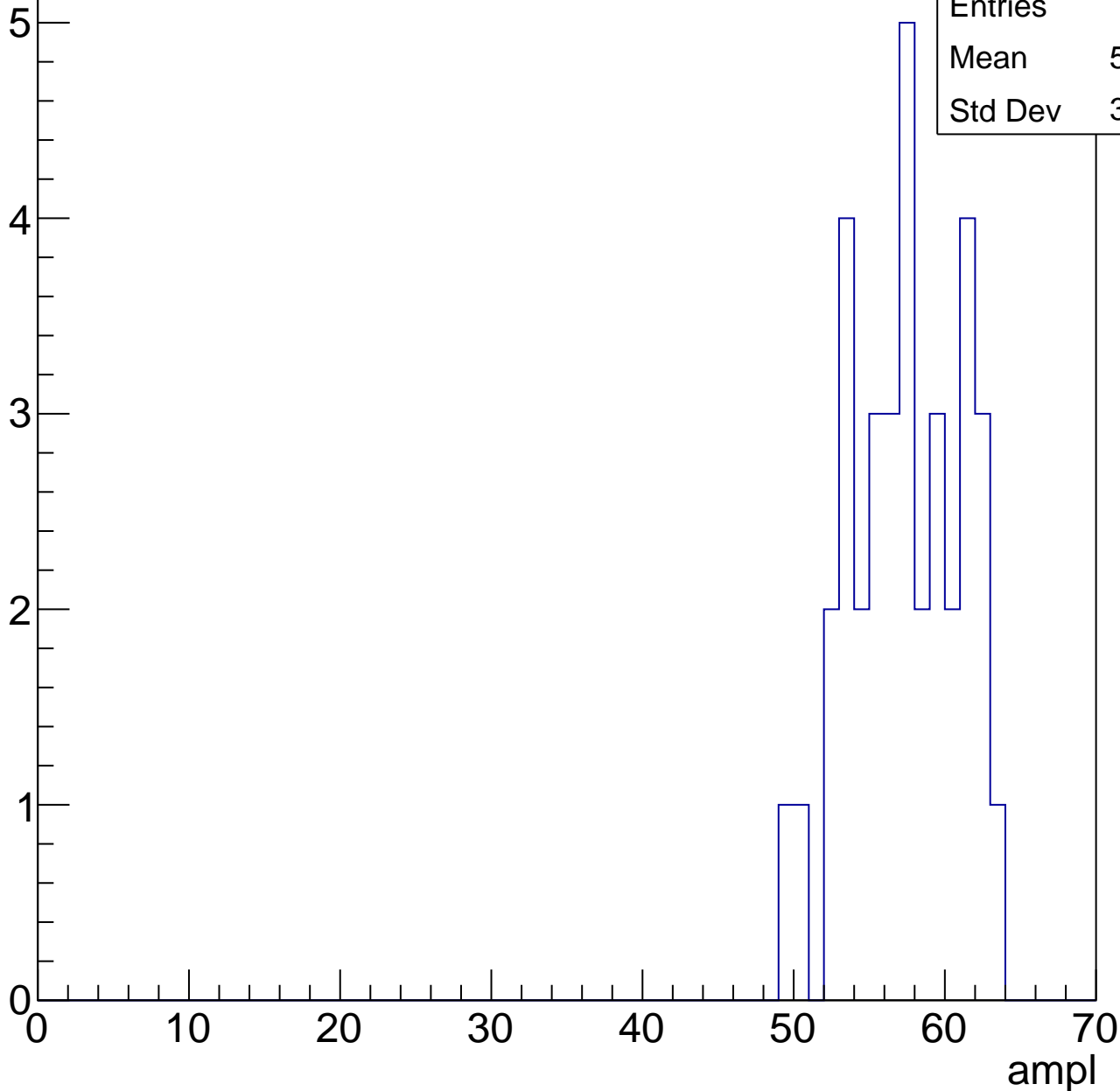


B1L103S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

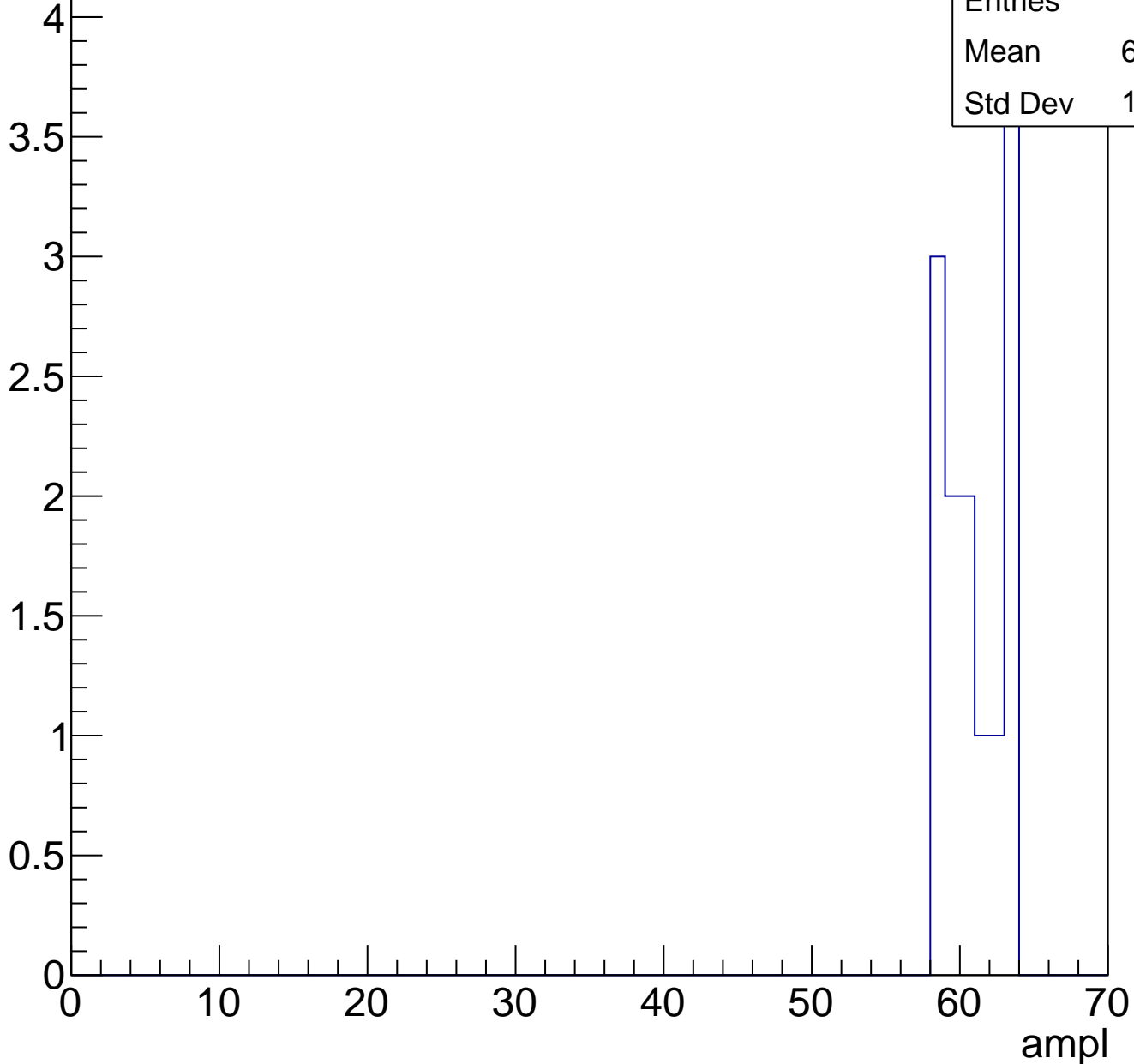
Entries	36
Mean	56.86
Std Dev	3.599



B1L103S, U15-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U15-ch126, adc0

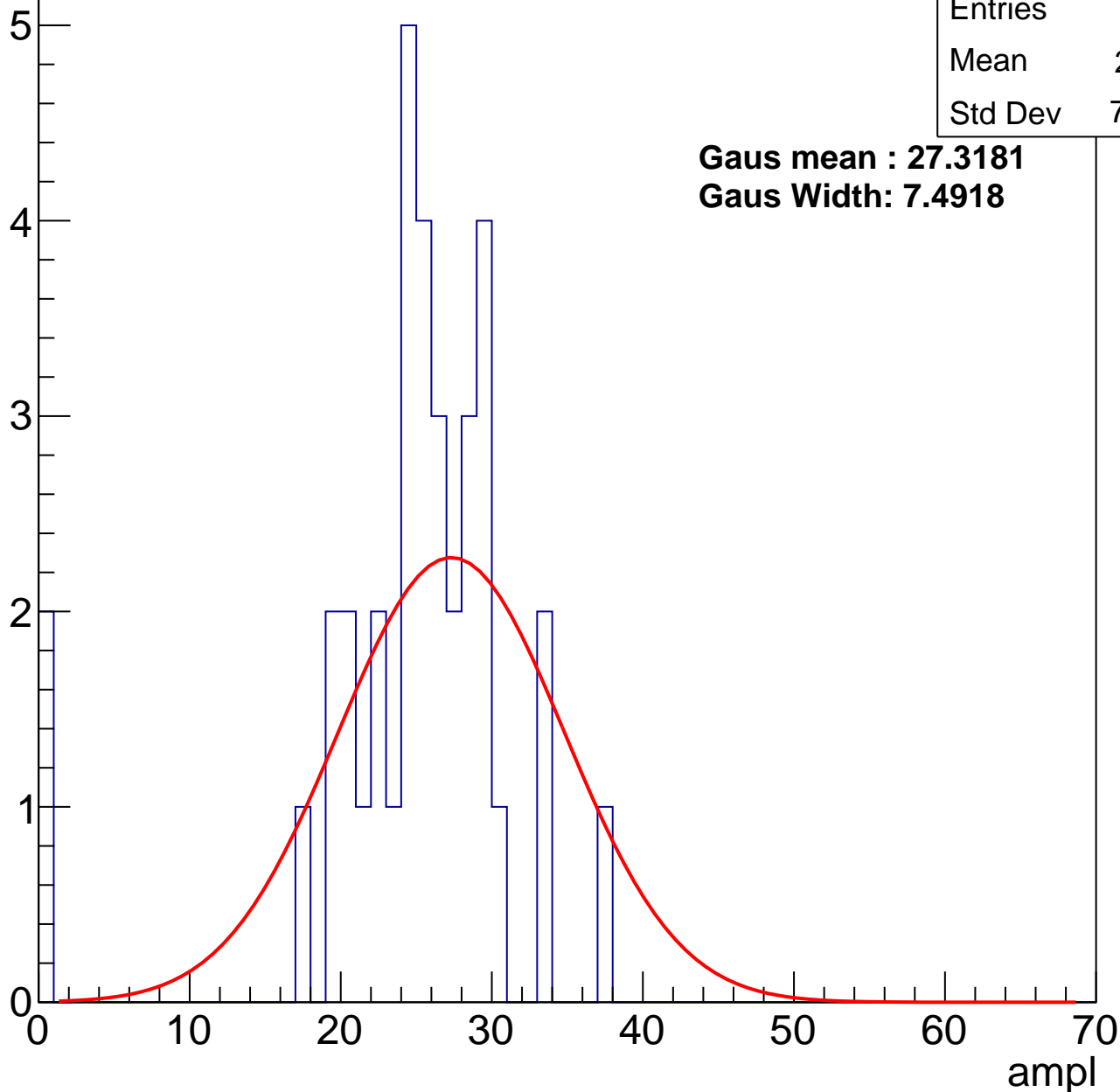
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	24.11
Std Dev	7.168

Gaus mean : 27.3181

Gaus Width: 7.4918



B1L103S, U15-ch126, adc1

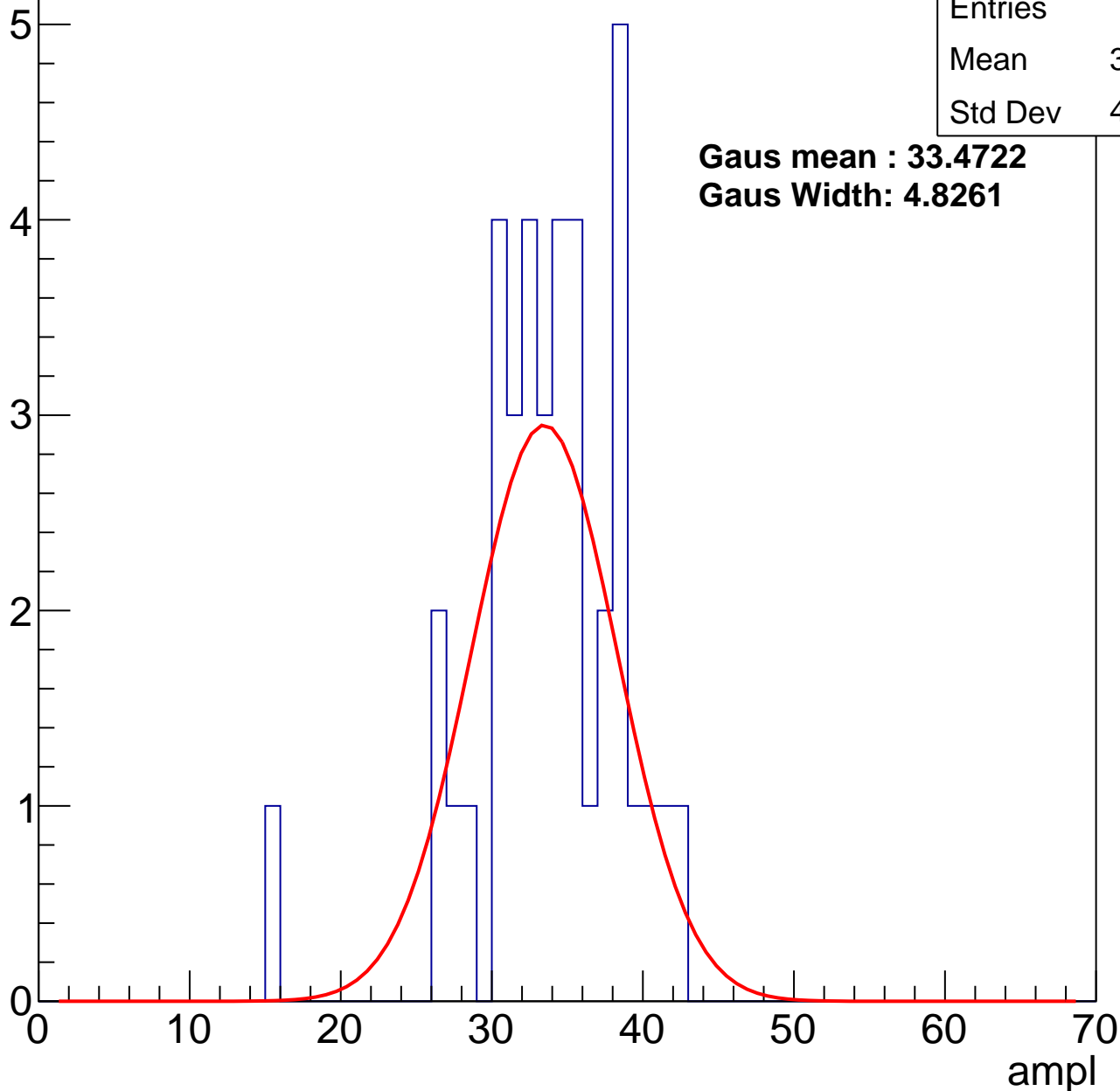
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	33.33
Std Dev	4.927

Gaus mean : 33.4722

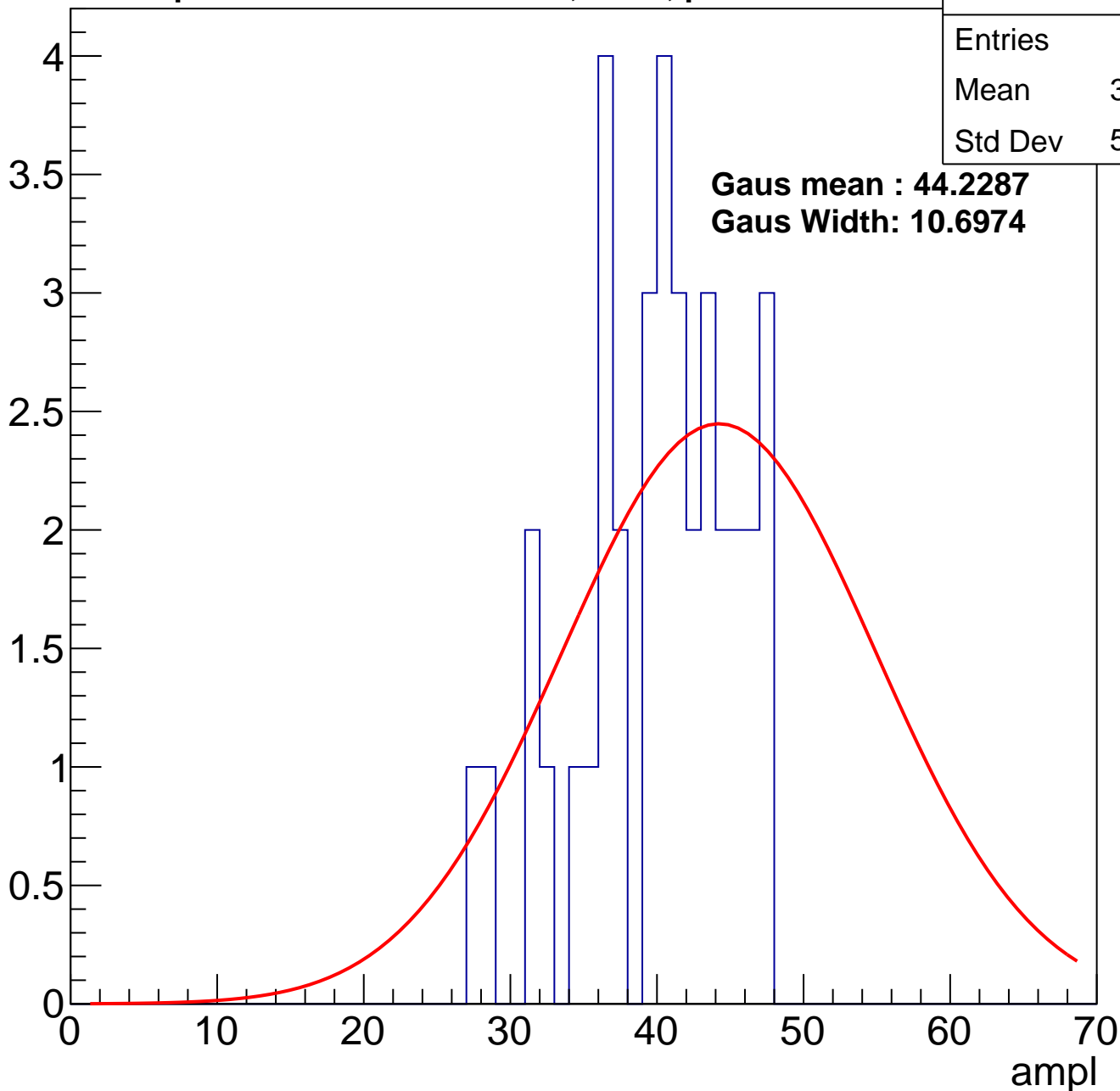
Gaus Width: 4.8261



B1L103S, U15-ch126, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch126, adc3

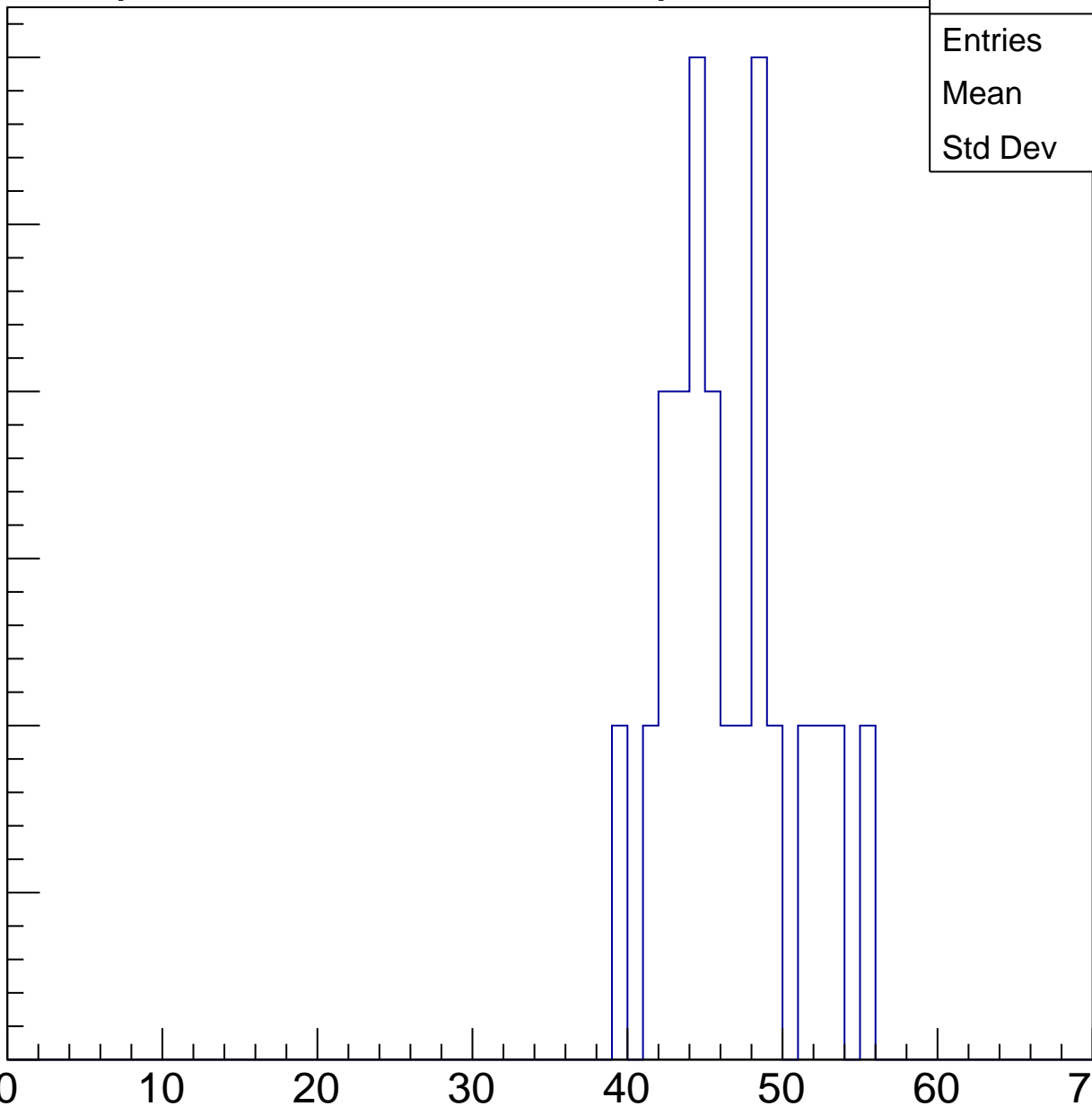
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	21
Mean	46.14
Std Dev	4.086

ampl

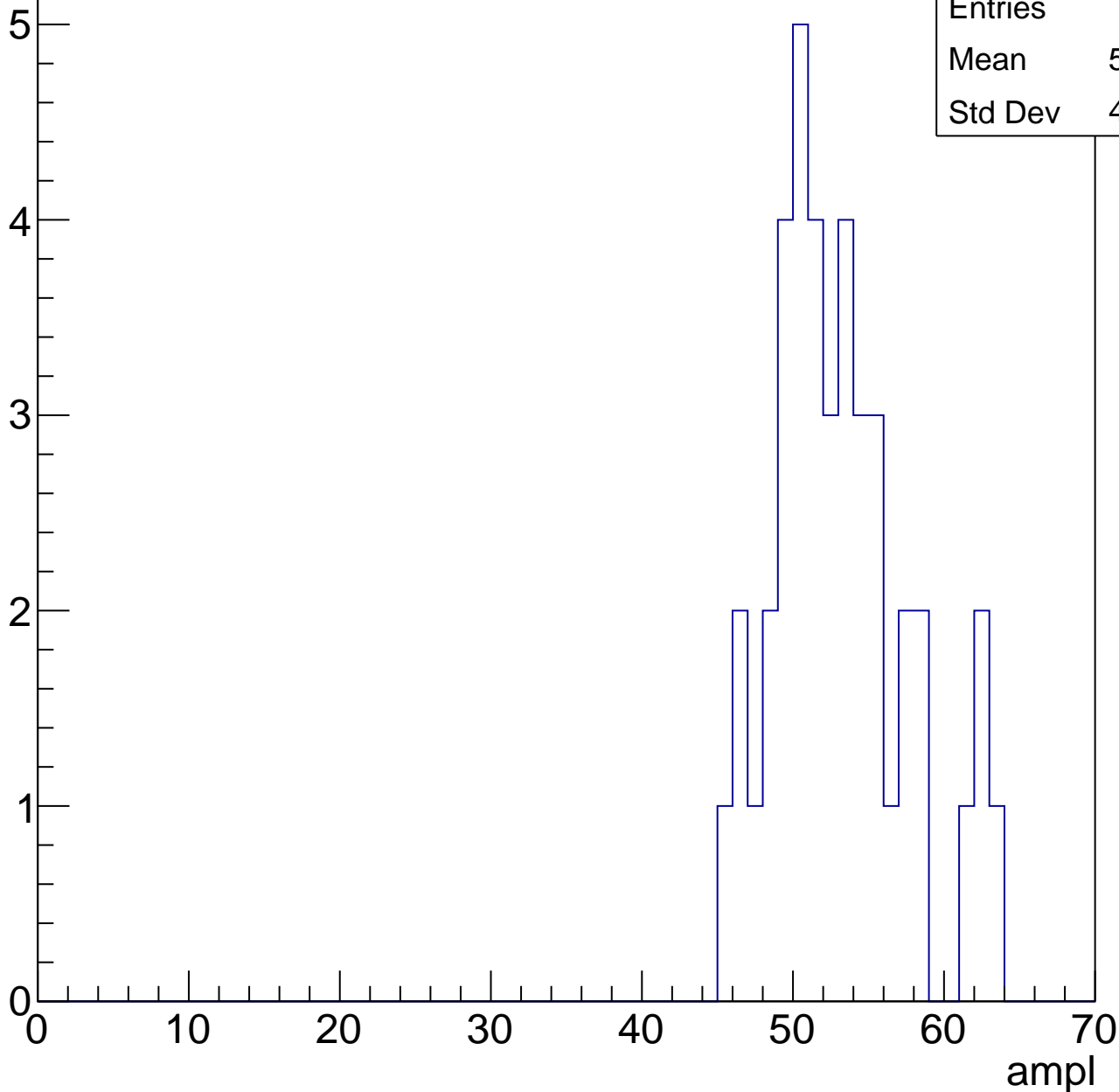


B1L103S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	52.66
Std Dev	4.426

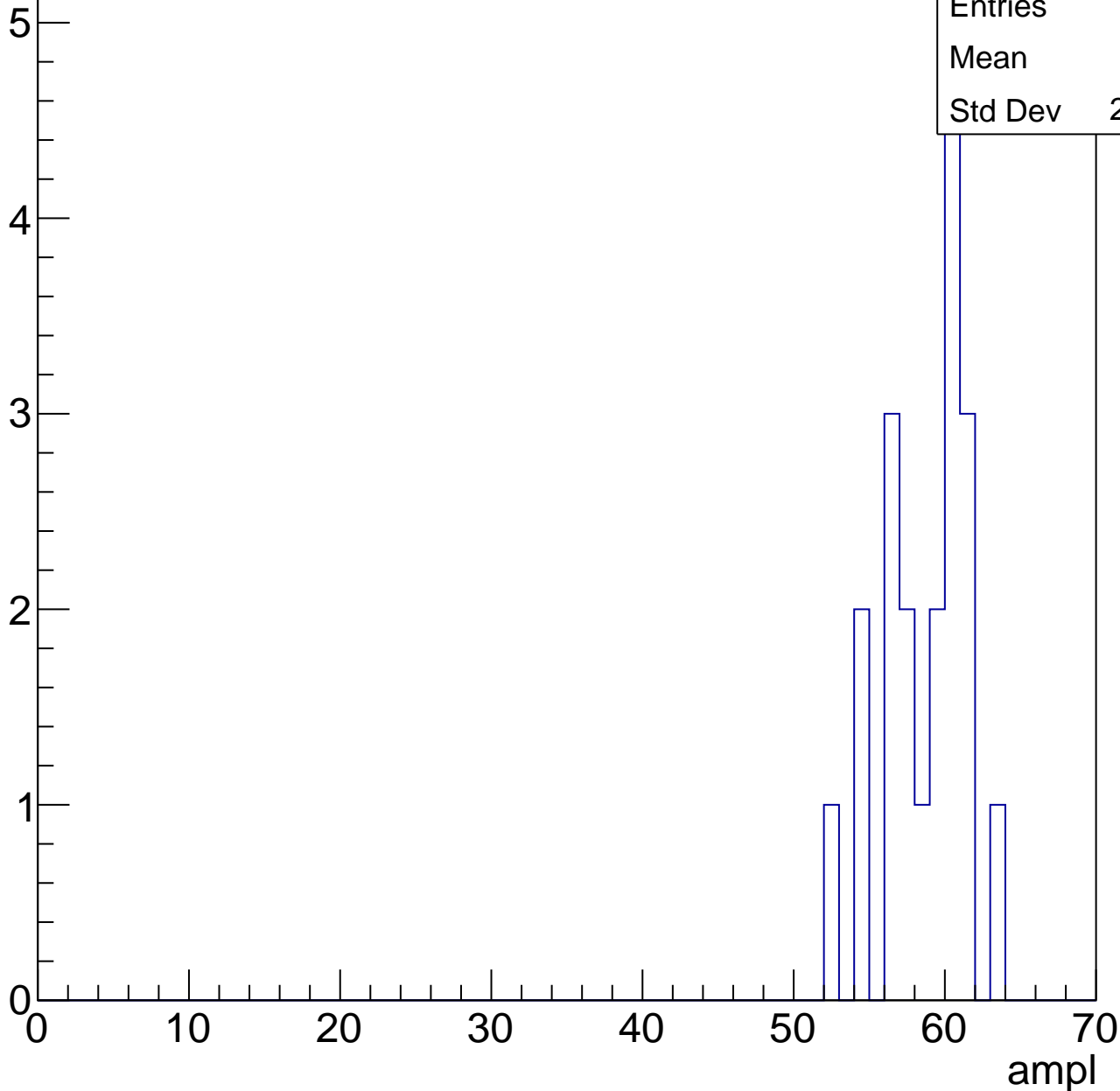


B1L103S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

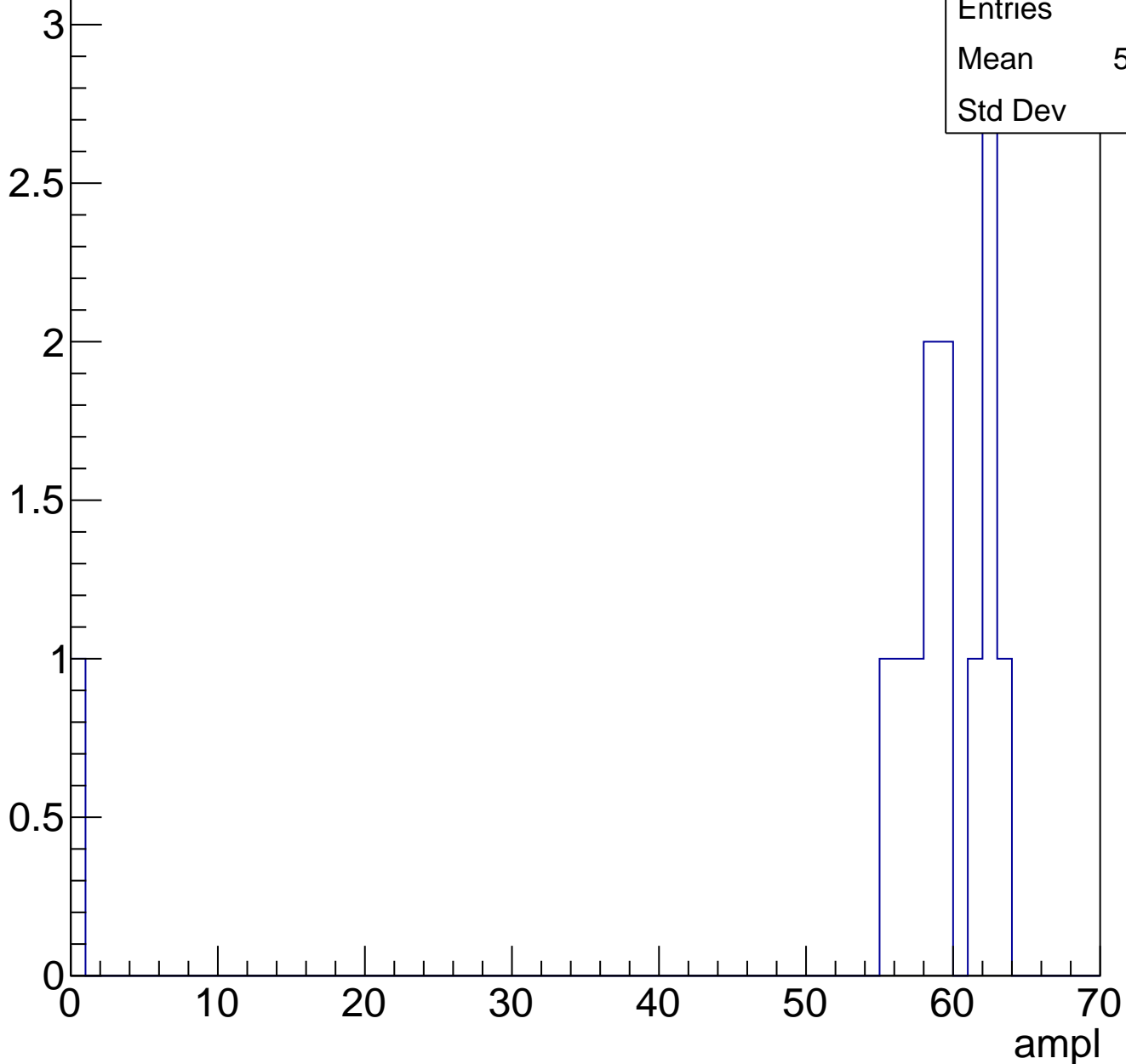
Entries	20
Mean	58.2
Std Dev	2.786



B1L103S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch127, adc0

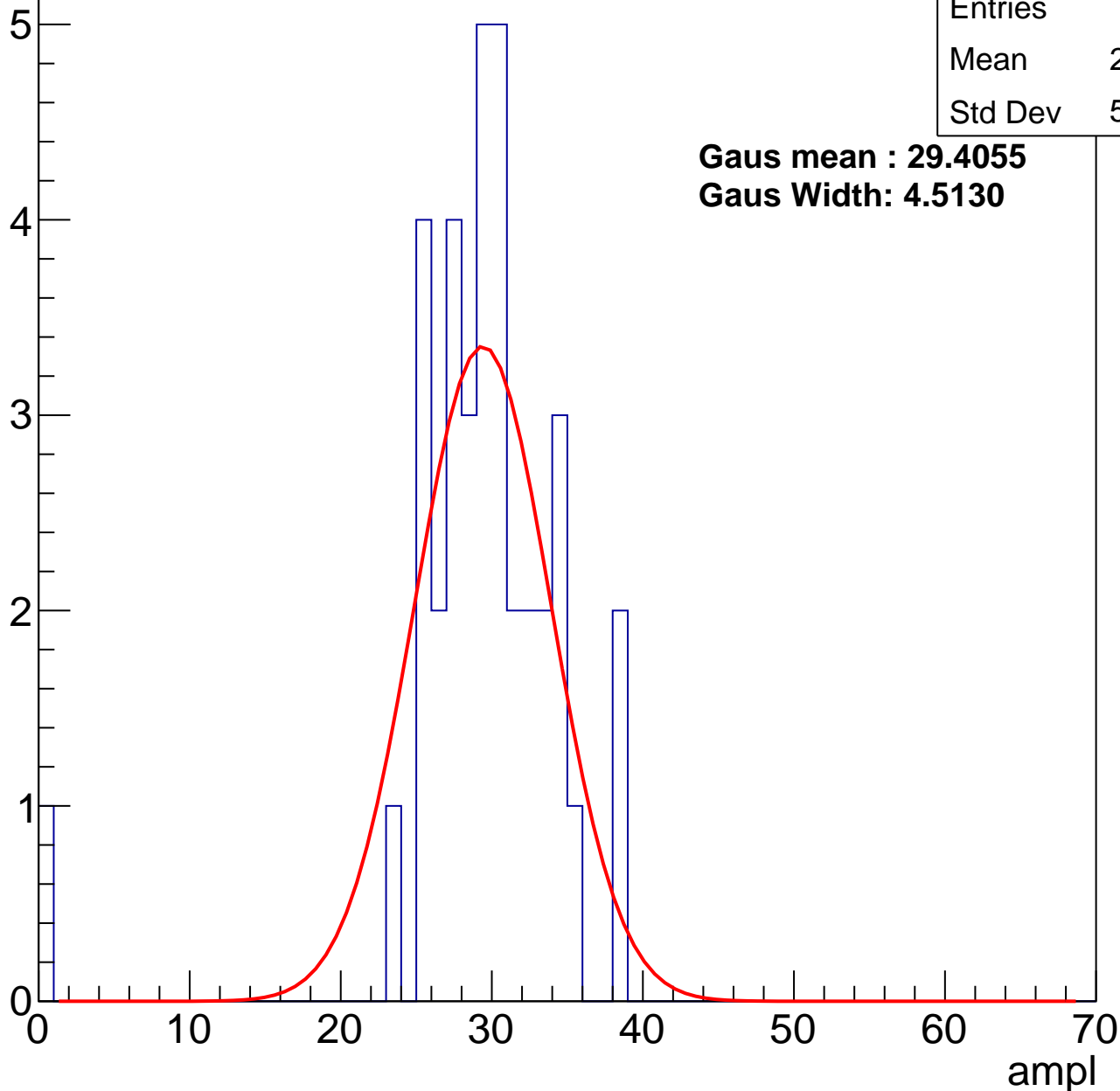
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	28.84
Std Dev	5.957

Gaus mean : 29.4055

Gaus Width: 4.5130



B1L103S, U15-ch127, adc1

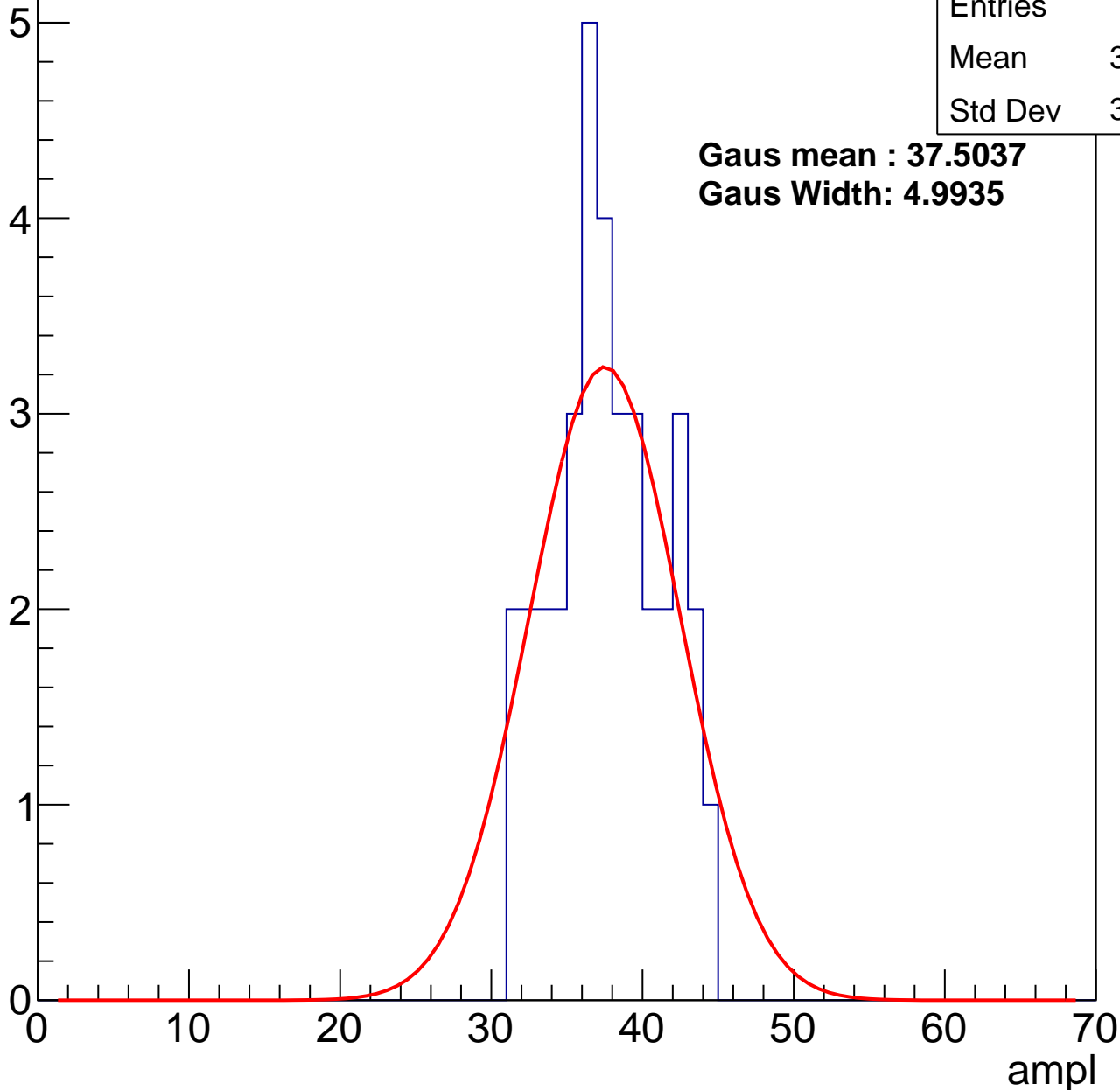
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	37.28
Std Dev	3.525

Gaus mean : 37.5037

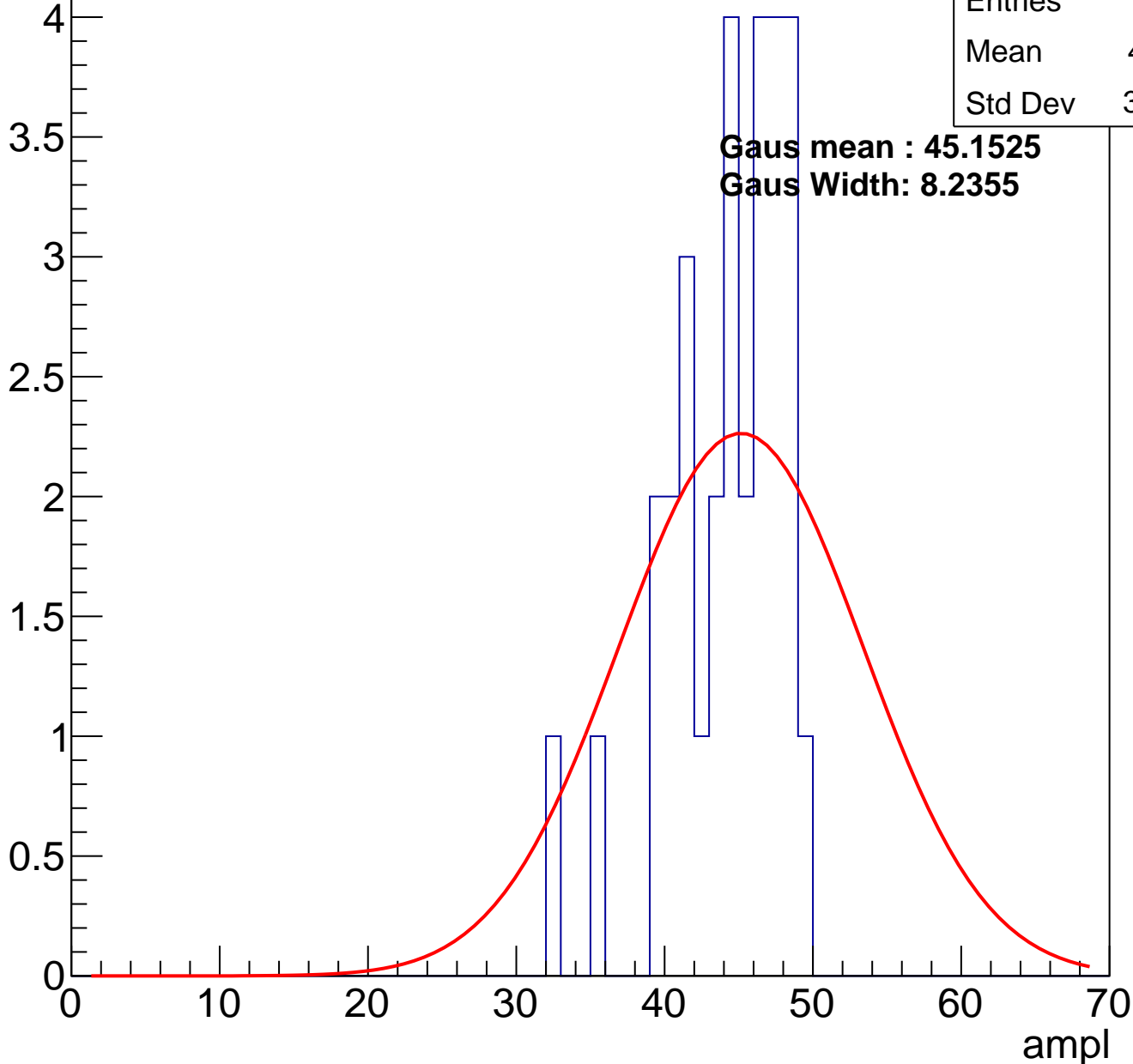
Gaus Width: 4.9935



B1L103S, U15-ch127, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

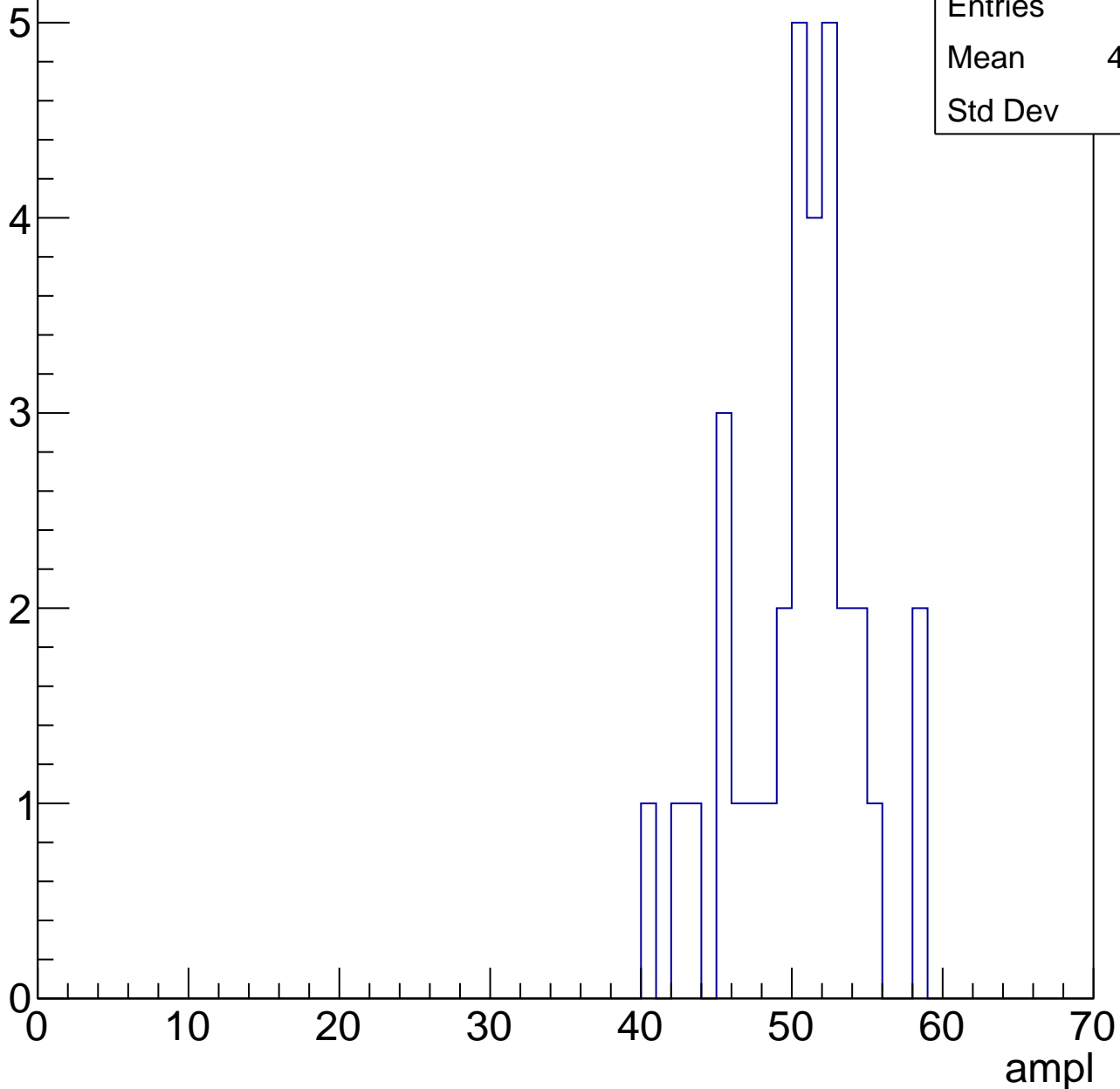


B1L103S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	49.94
Std Dev	4.13

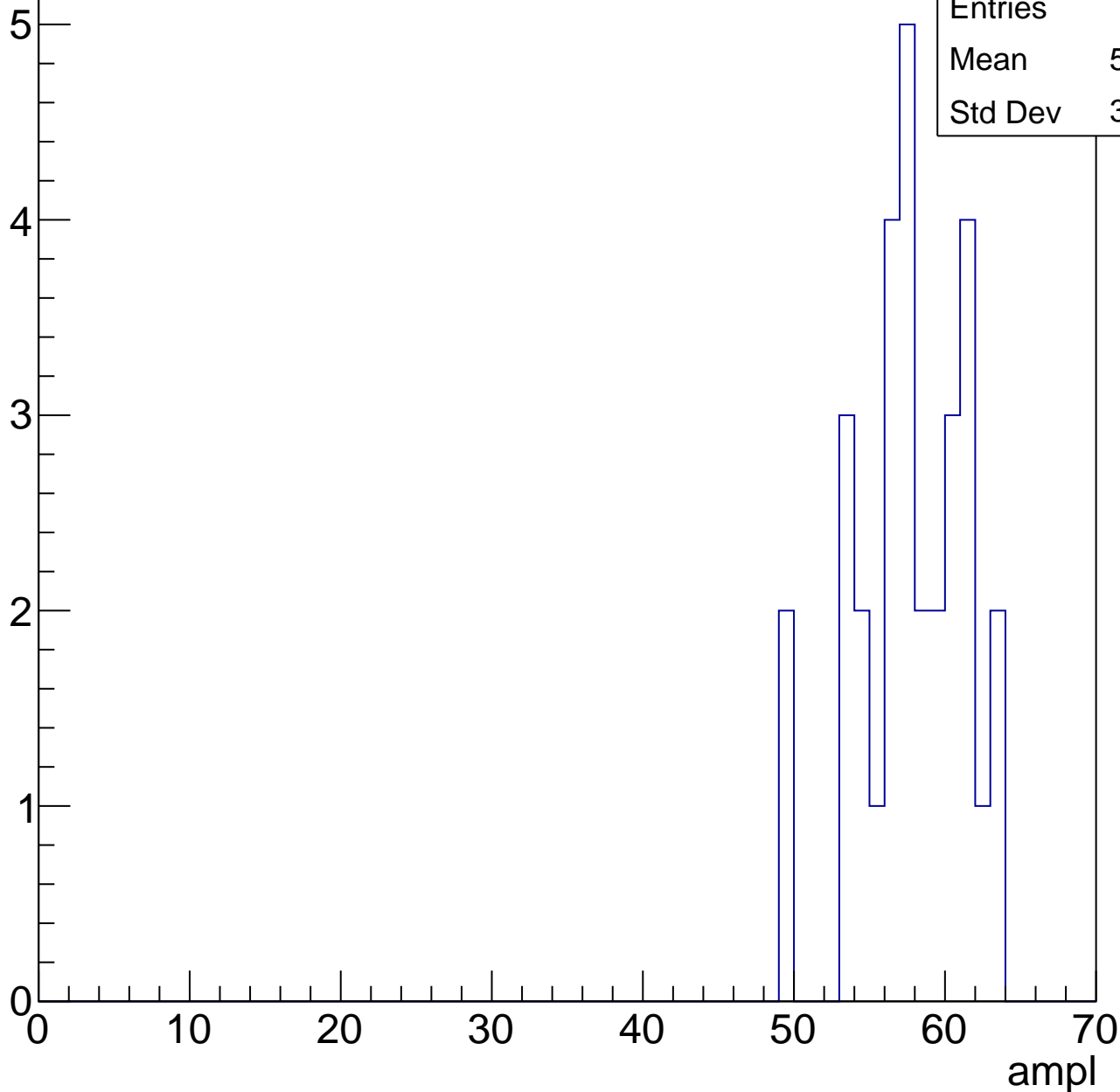


B1L103S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

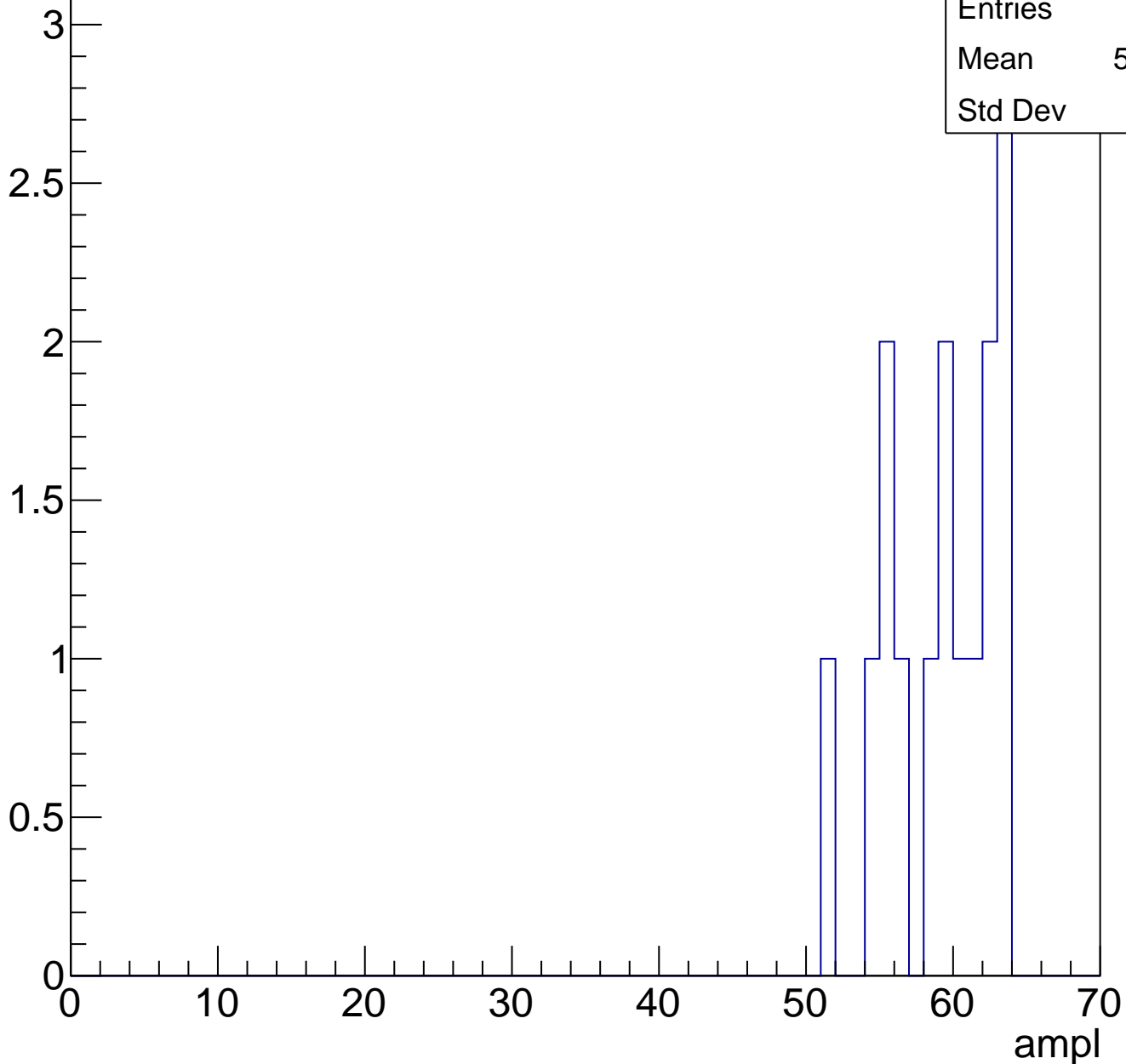
Entries	31
Mean	57.26
Std Dev	3.583



B1L103S, U15-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

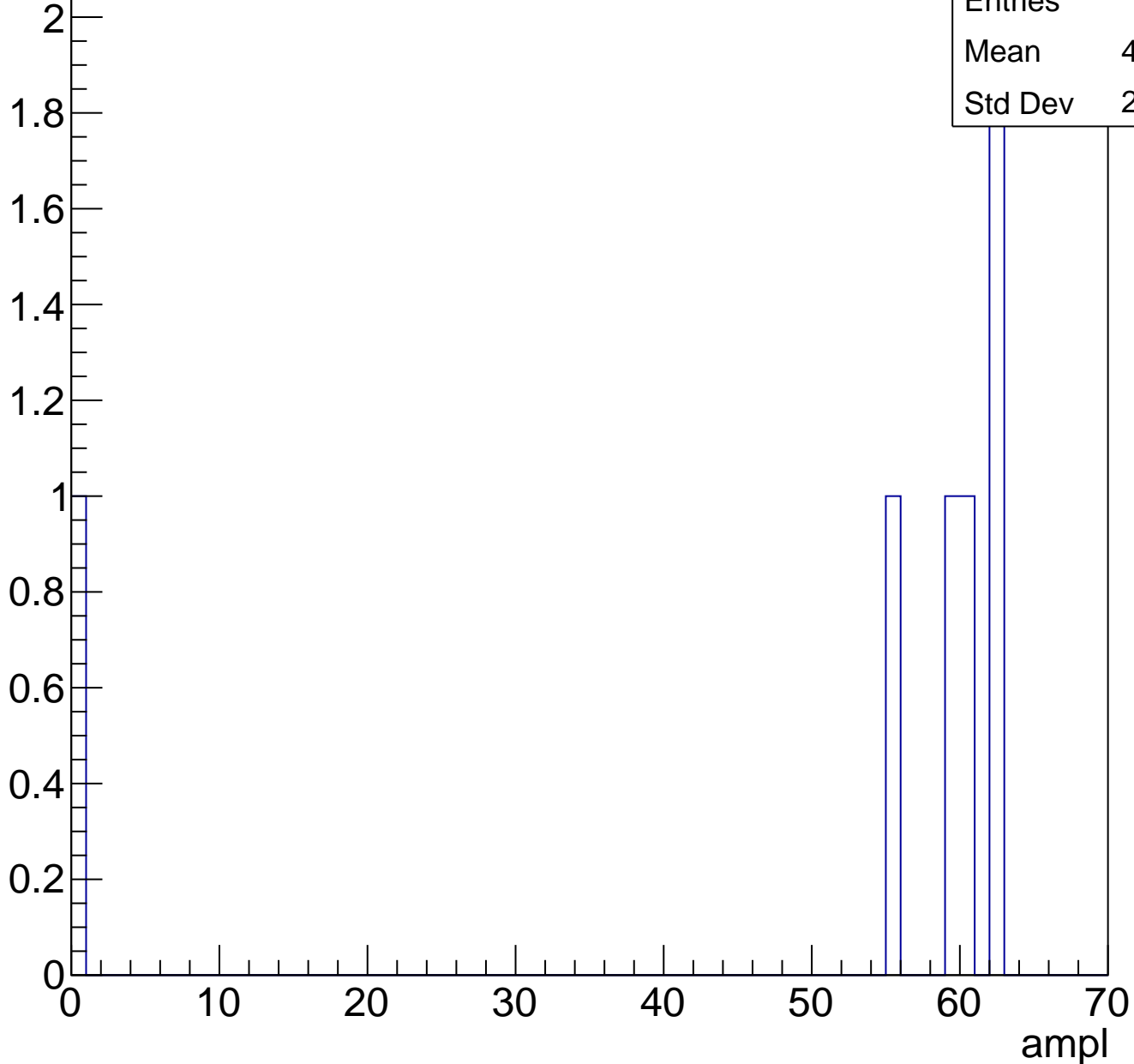
Entry



B1L103S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

