



# B1L003S, U22-ch0

calib\_packv5\_042523\_0143.root, FC#13, port D2

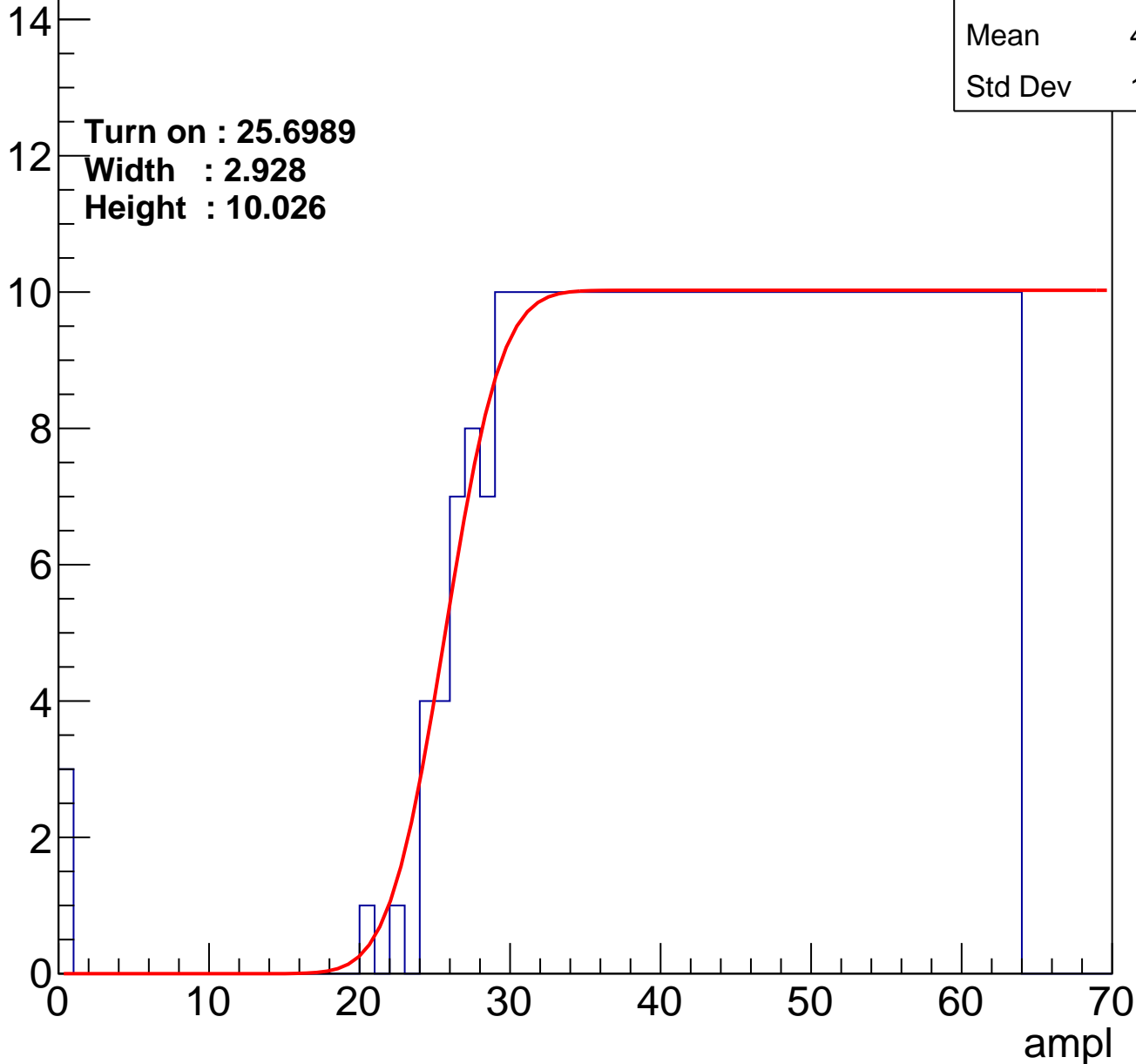
Entry

Entries	385
Mean	43.98
Std Dev	11.78

Turn on : 25.6989

Width : 2.928

Height : 10.026



# B1L003S, U22-ch1

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	381
Mean	44.13
Std Dev	11.75

**Turn on : 26.7118**

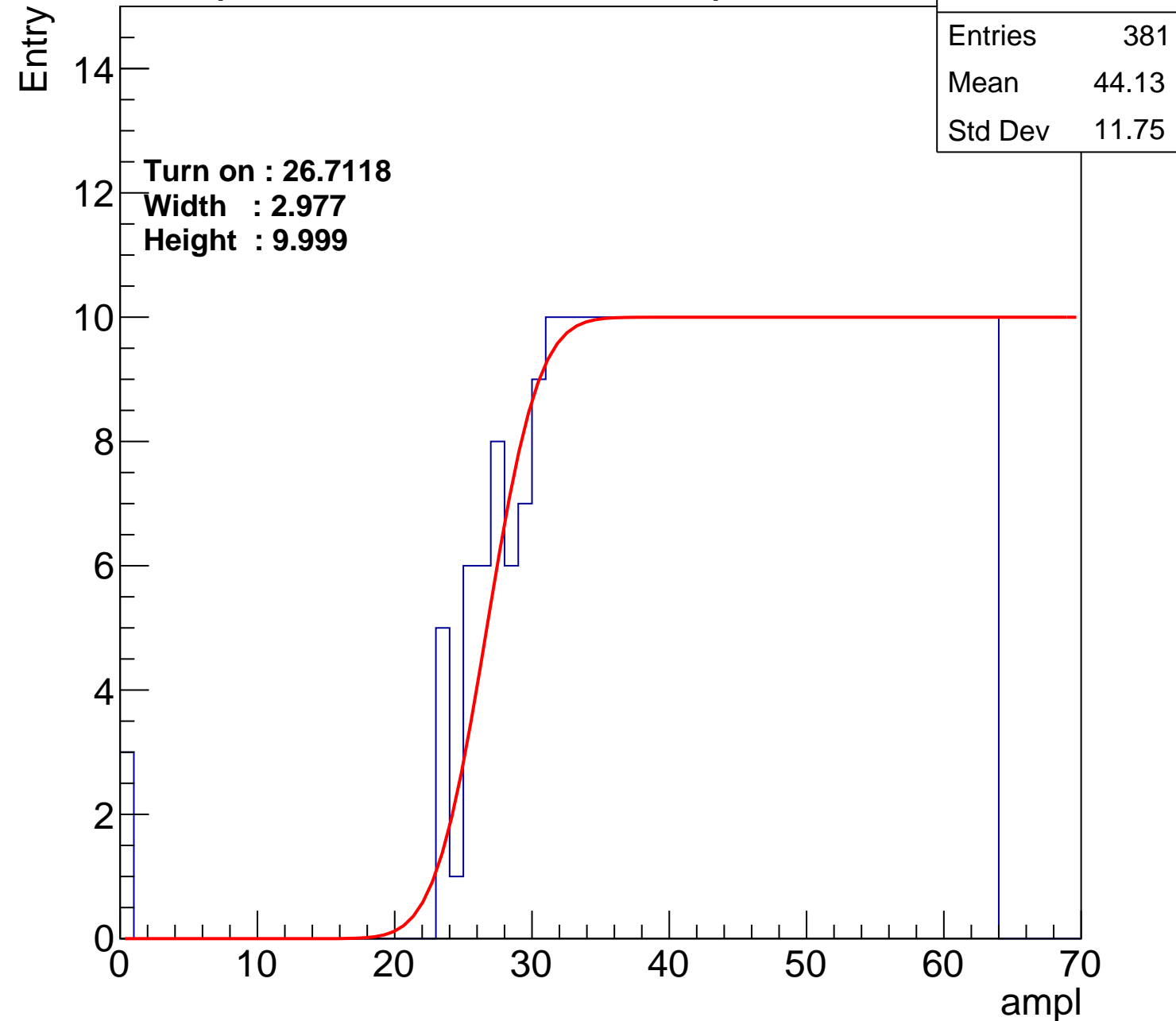
**Width : 2.977**

**Height : 9.999**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch2

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	368
Mean	44.83
Std Dev	11.25

Turn on : 27.4223

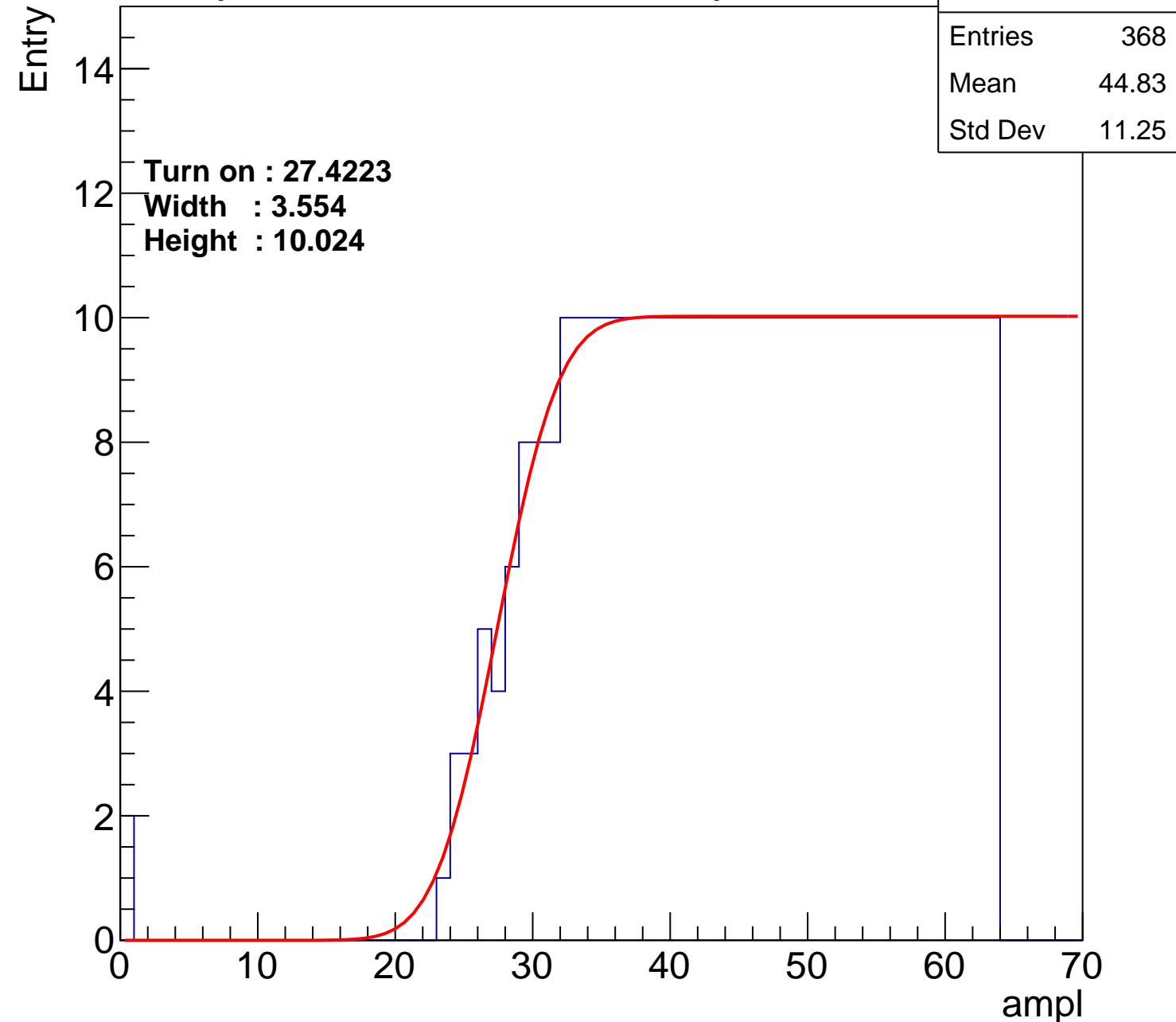
Width : 3.554

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch3

calib\_packv5\_042523\_0143.root, FC#13, port D2

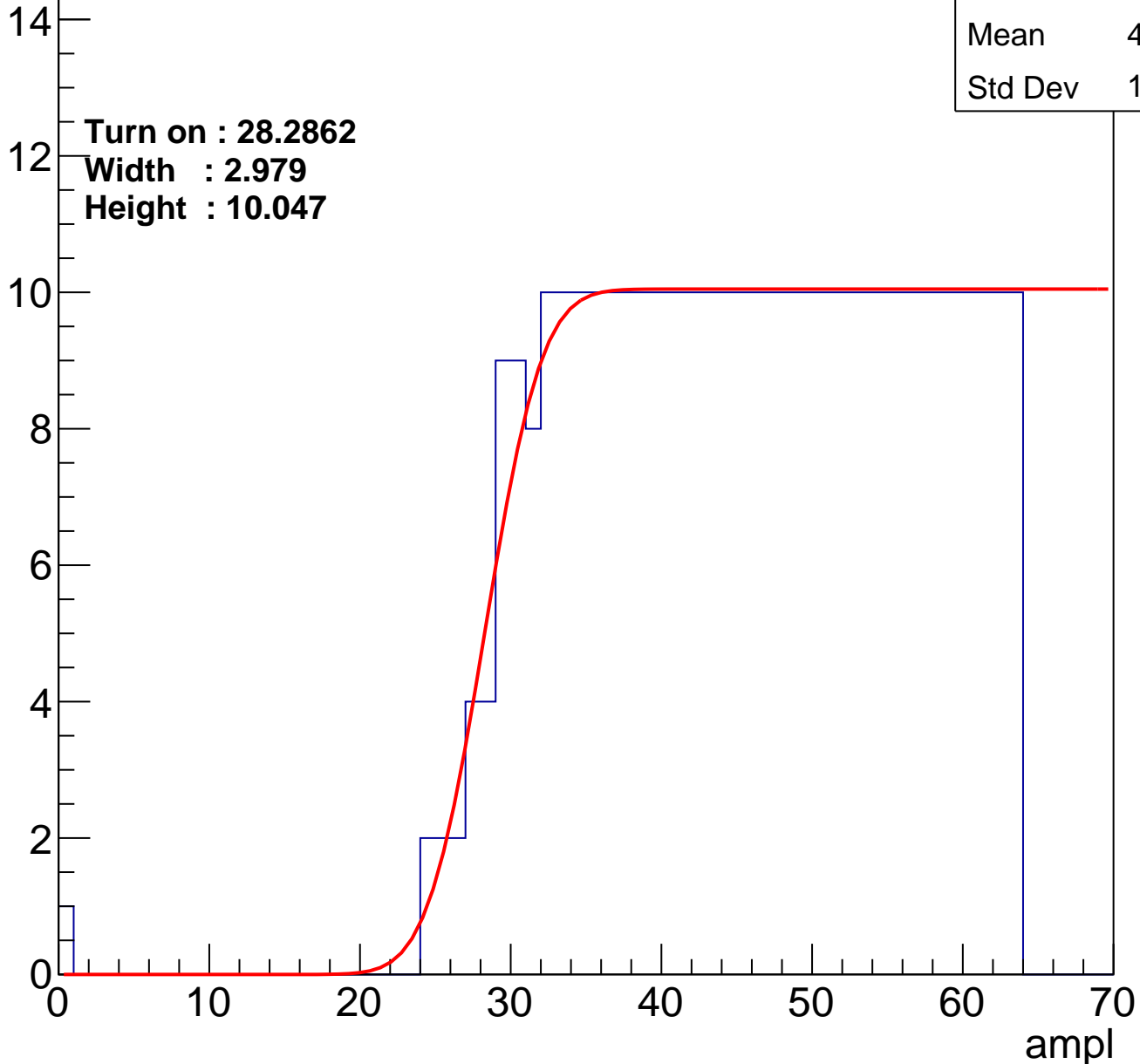
Entries	361
Mean	45.29
Std Dev	10.79

Turn on : 28.2862

Width : 2.979

Height : 10.047

Entry



# B1L003S, U22-ch4

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1094

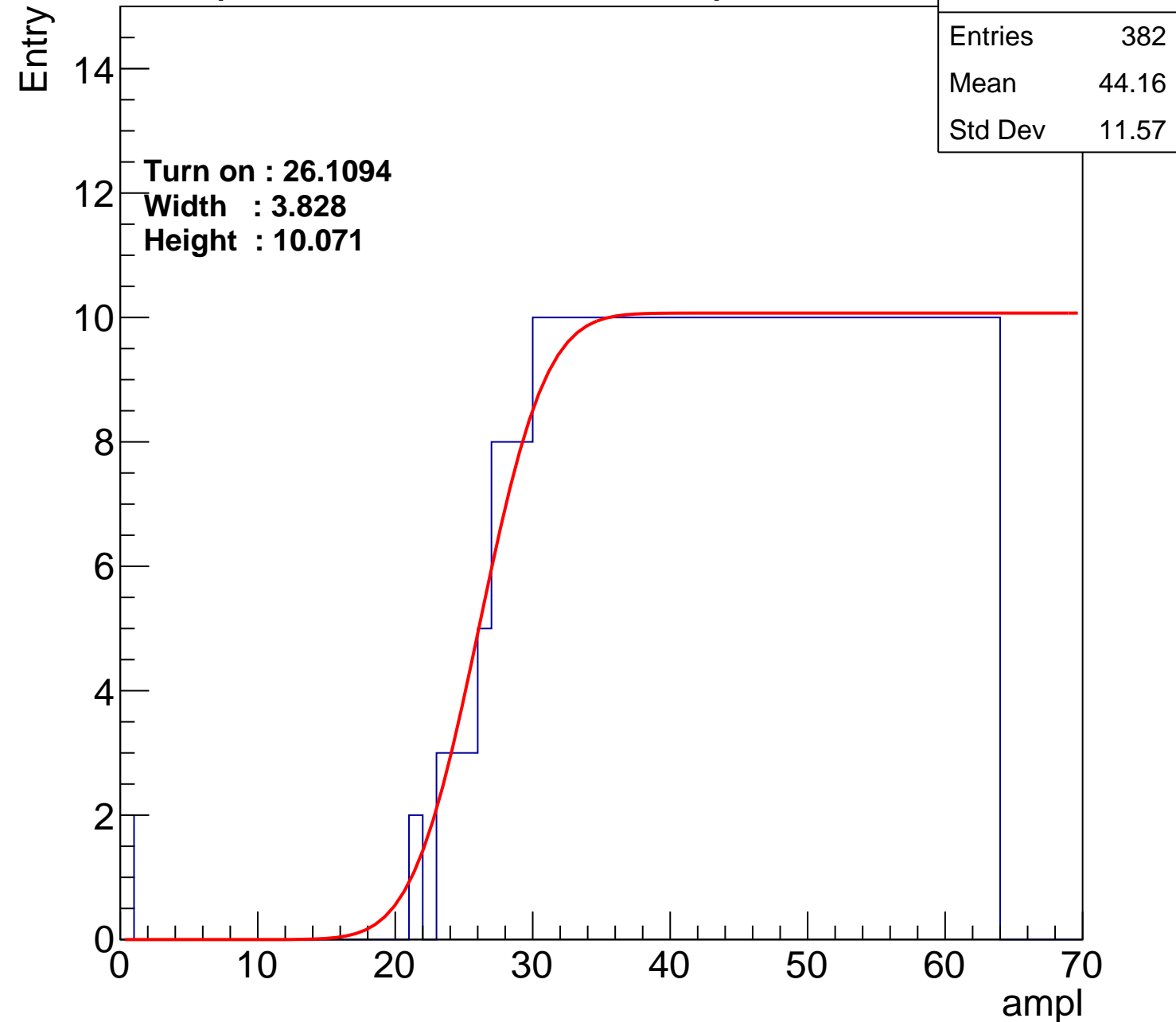
Width : 3.828

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch5

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	366
Mean	44.92
Std Dev	11.3

Turn on : 27.7959

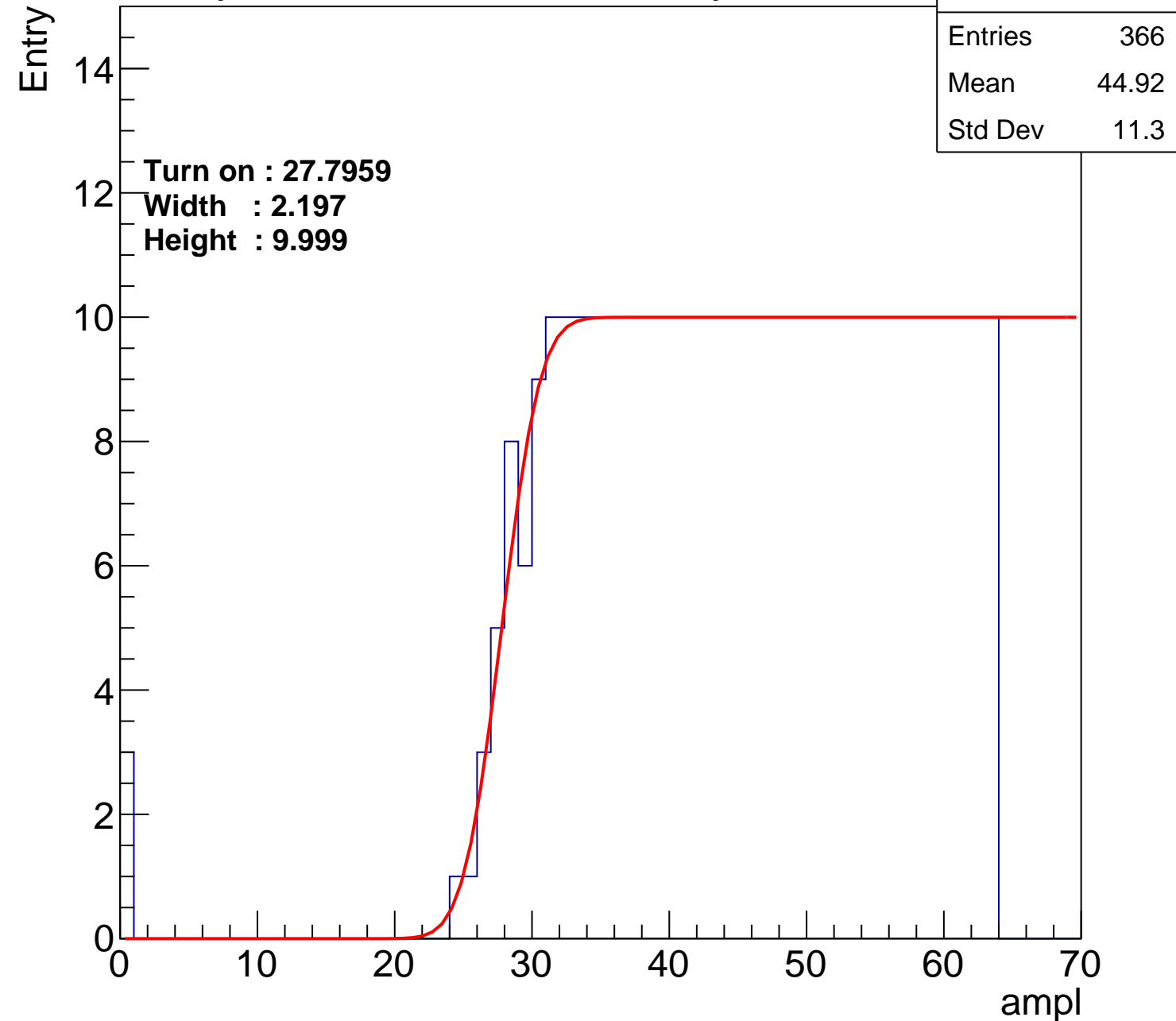
Width : 2.197

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch6

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.3174

Width : 4.053

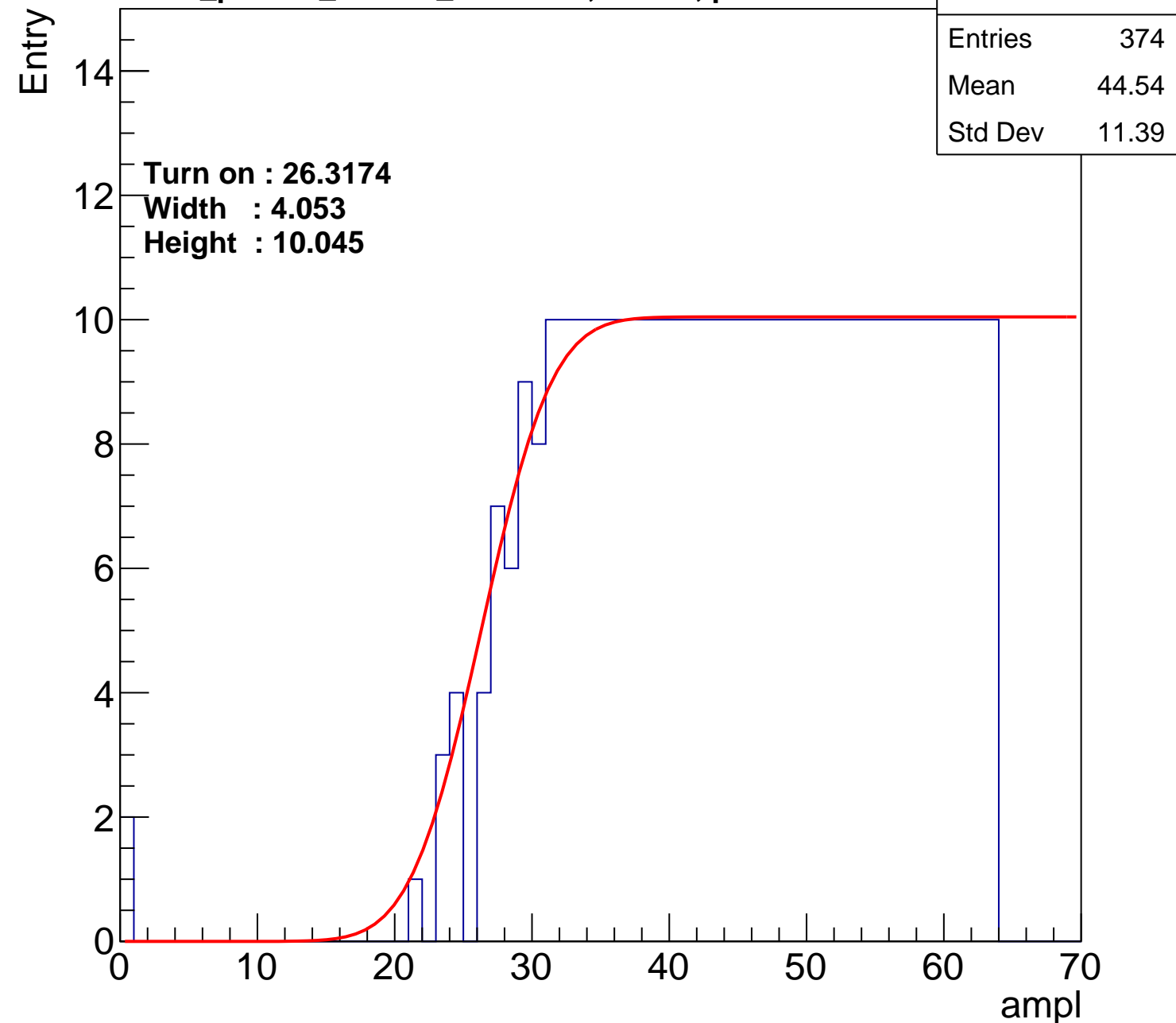
Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B1L003S, U22-ch7

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	362
Mean	45.1
Std Dev	11.13

**Turn on : 27.9177**

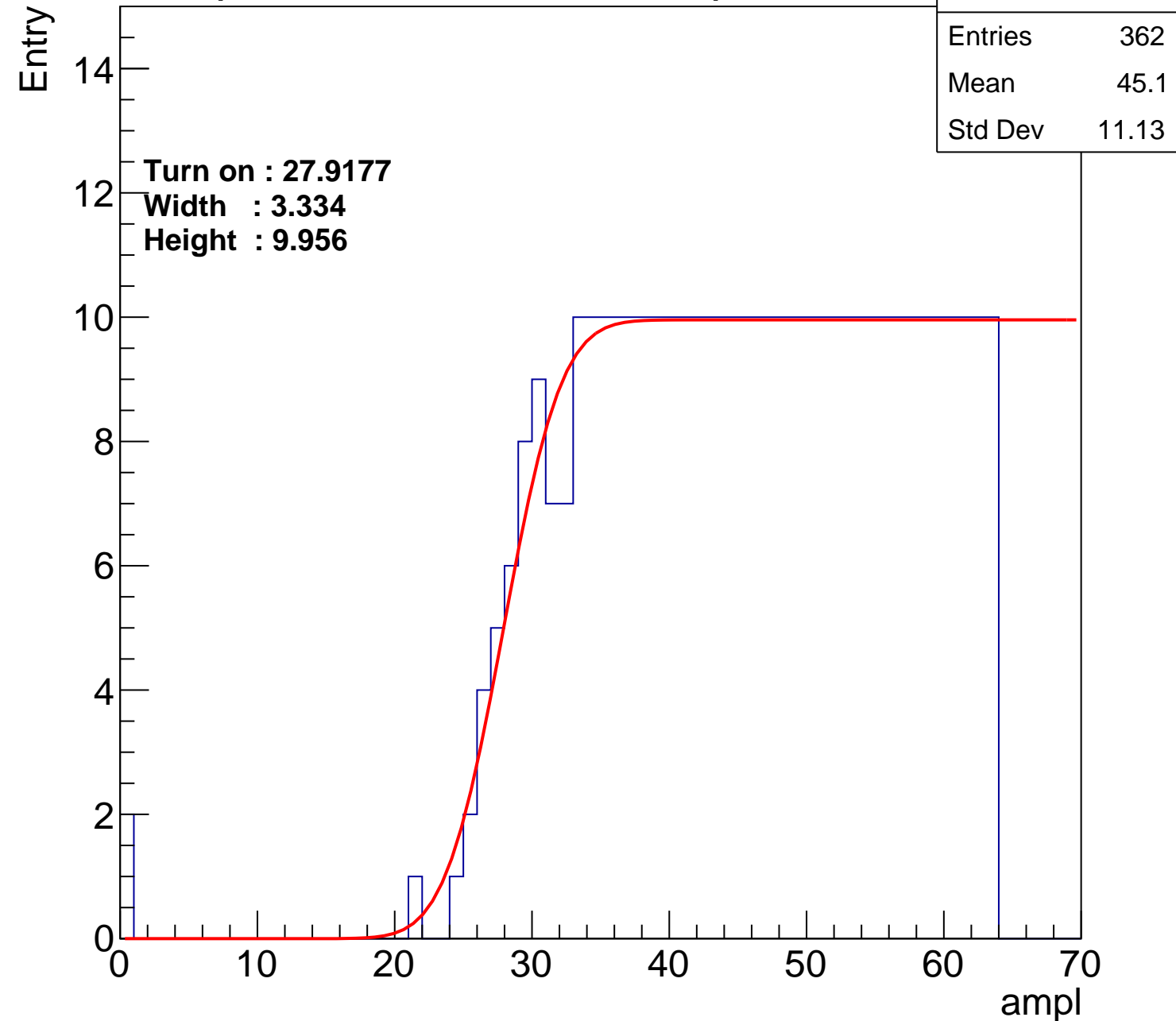
**Width : 3.334**

**Height : 9.956**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch8

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 27.6820

Width : 3.050

Height : 9.992

Entry

14

12

10

8

6

4

2

0

0

10

20

30

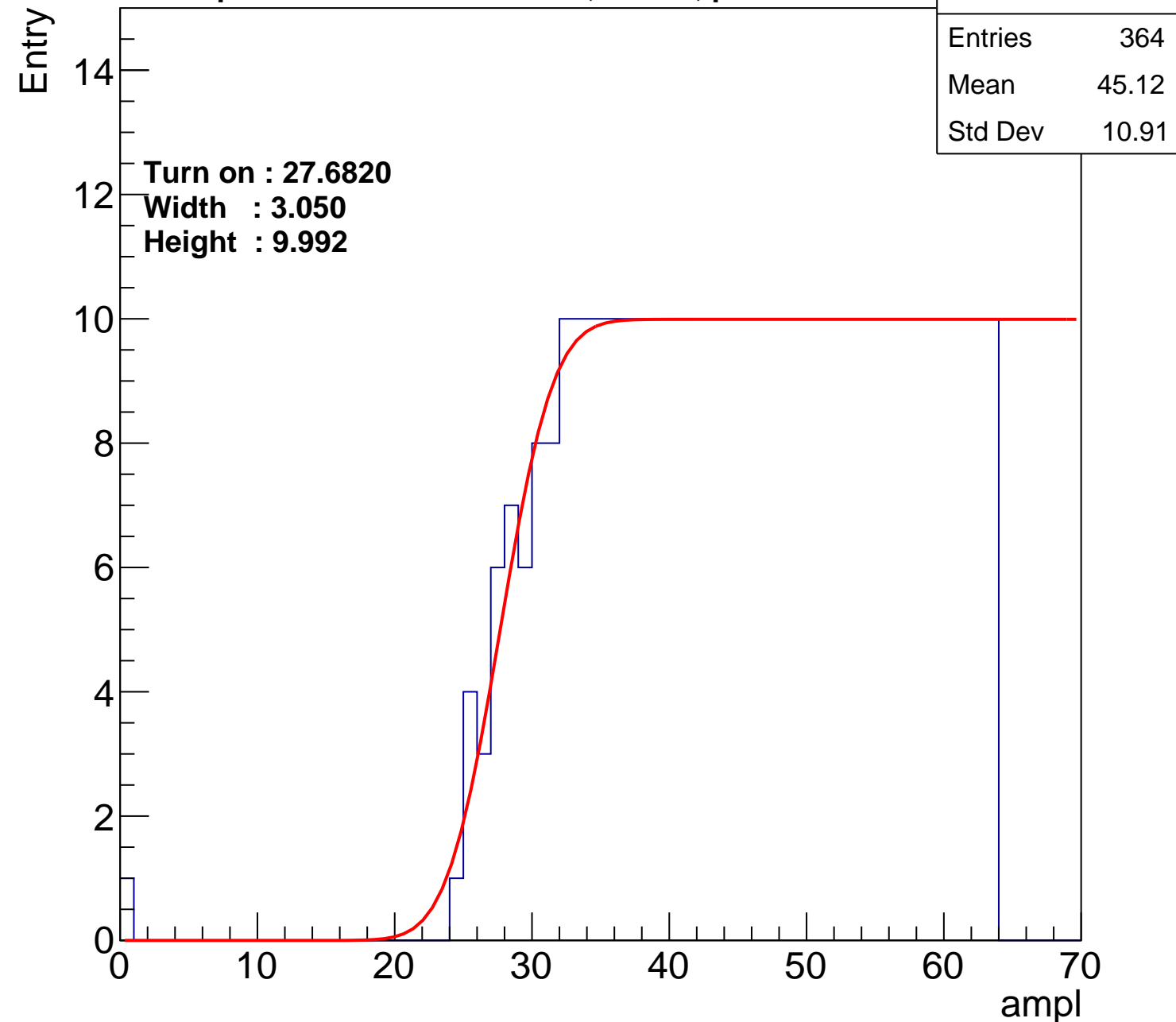
40

50

60

70

ampl



# B1L003S, U22-ch9

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	374
Mean	44.66
Std Dev	11.11

**Turn on : 26.8787**

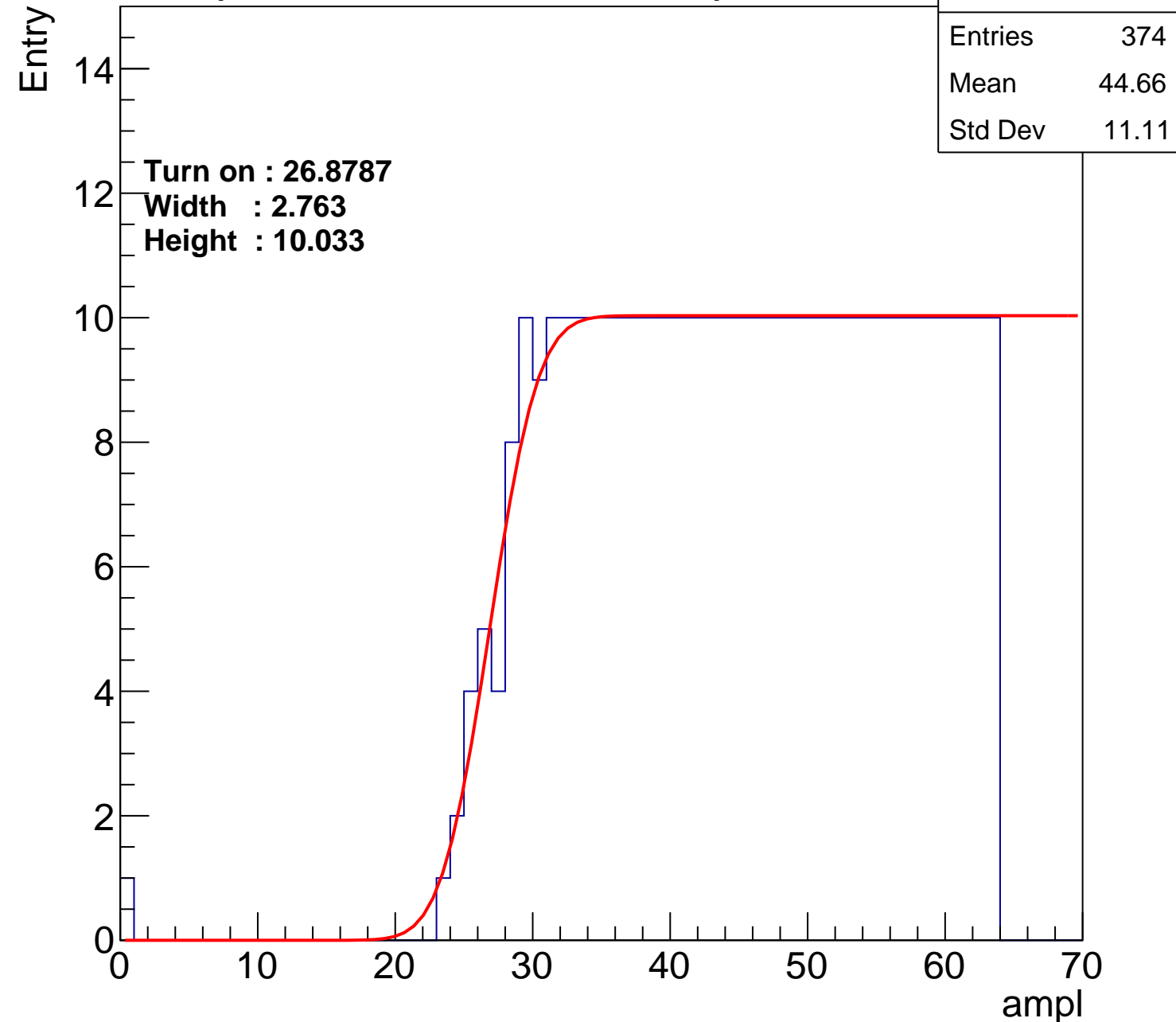
**Width : 2.763**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch10

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	361
Mean	45.27
Std Dev	10.82

**Turn on : 28.1195**

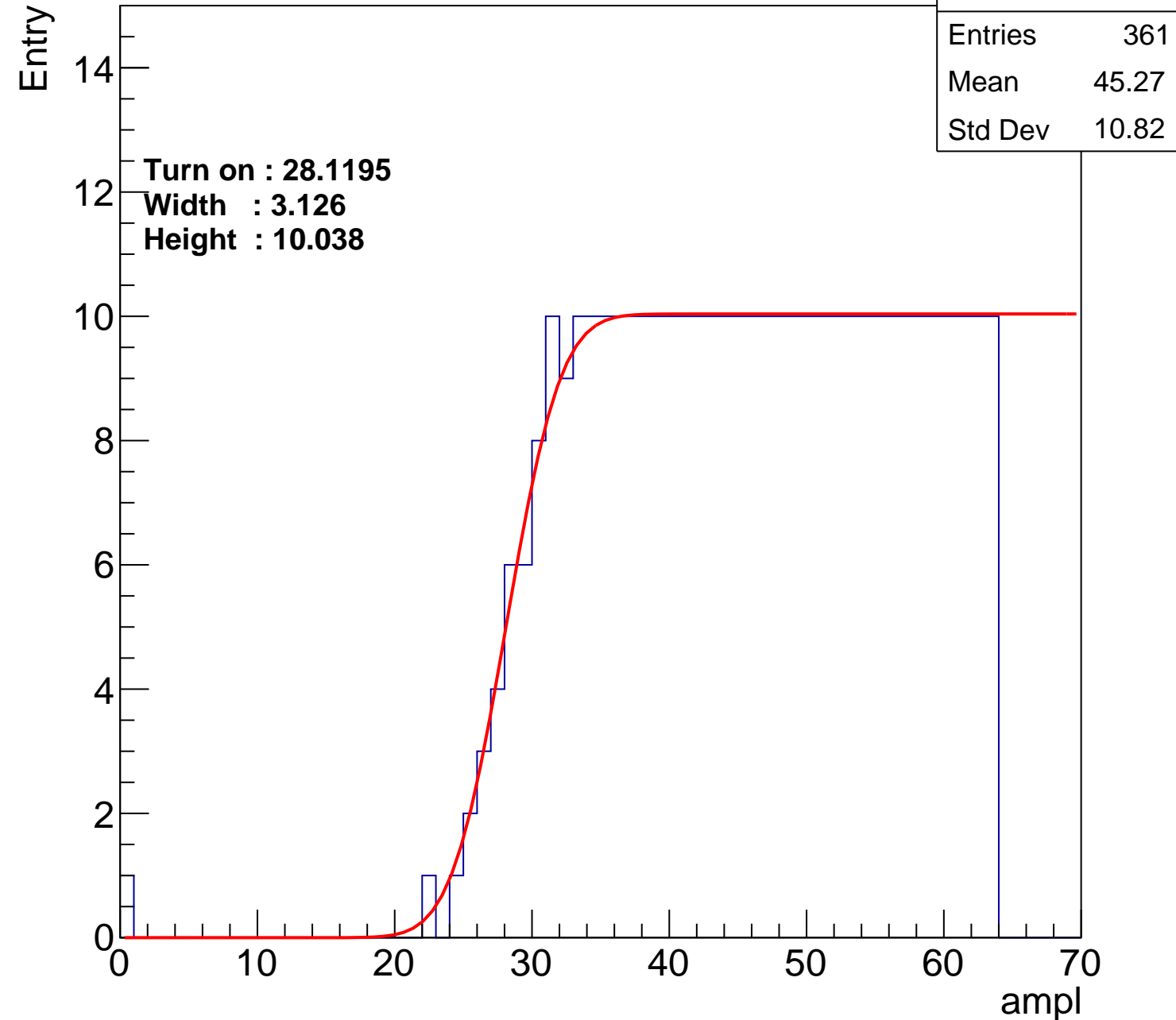
**Width : 3.126**

**Height : 10.038**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch11

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	365
Mean	44.9
Std Dev	11.38

Turn on : 27.8355

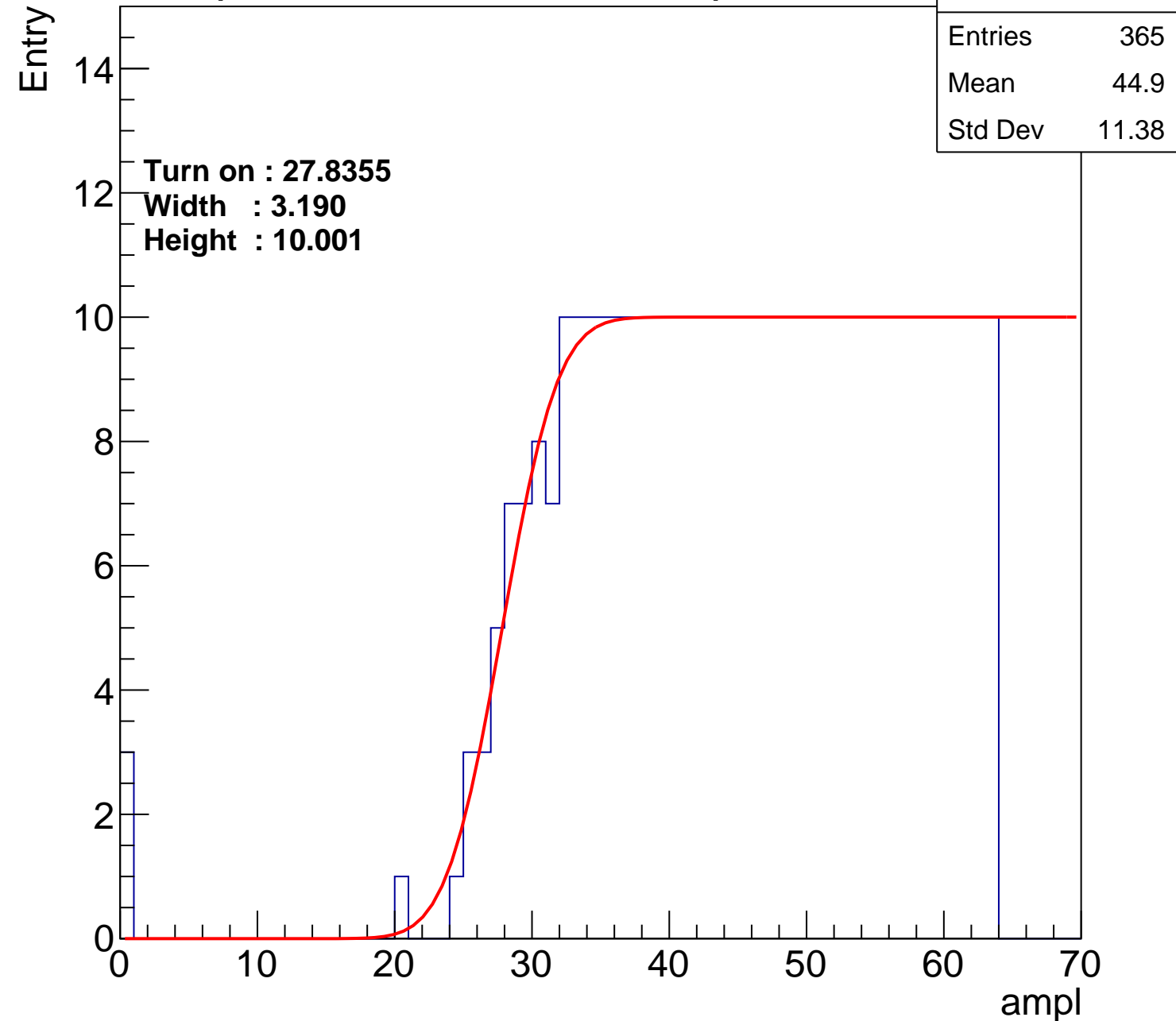
Width : 3.190

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch12

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	44.02
Std Dev	11.46

Turn on : 25.5183

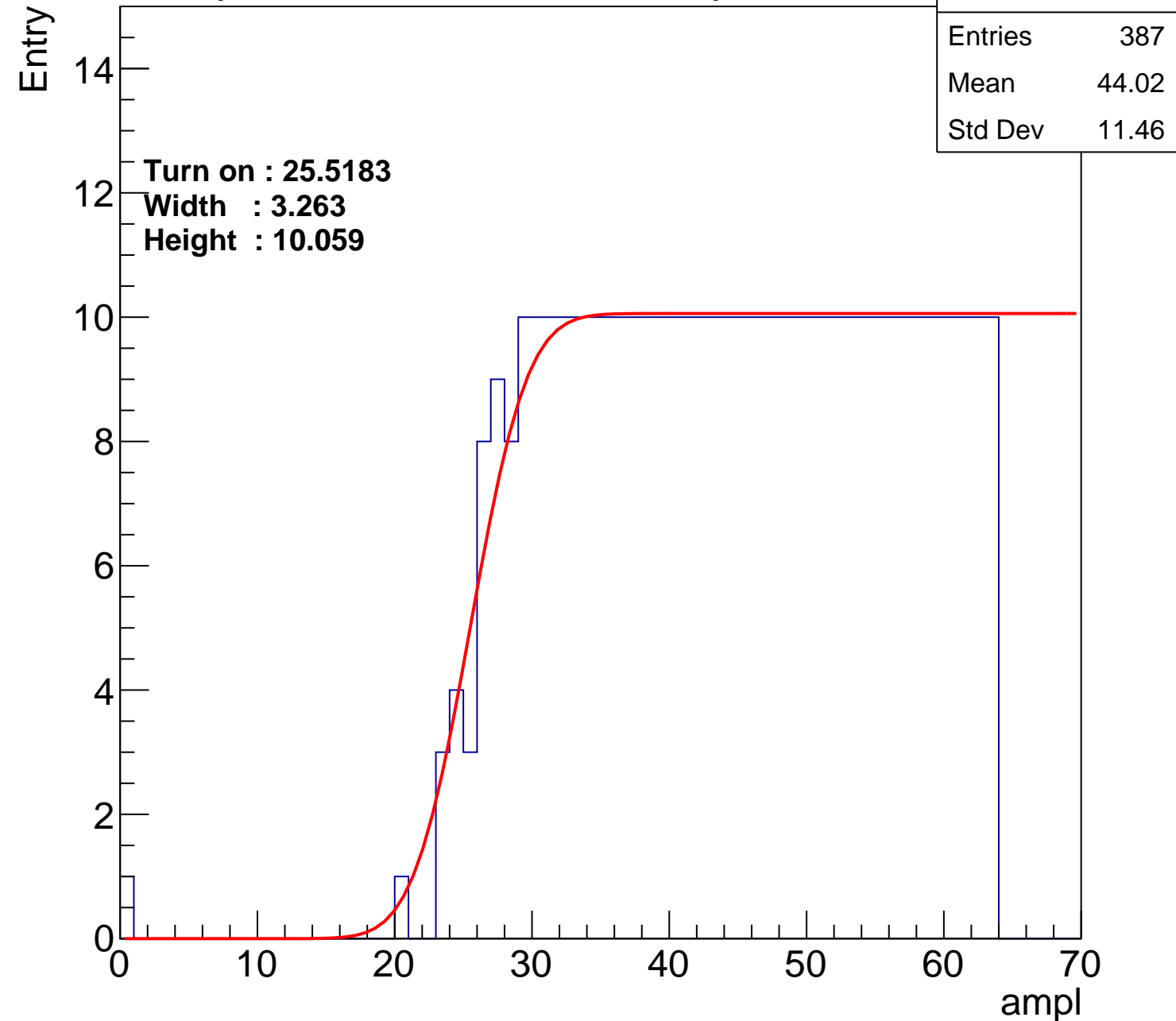
Width : 3.263

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

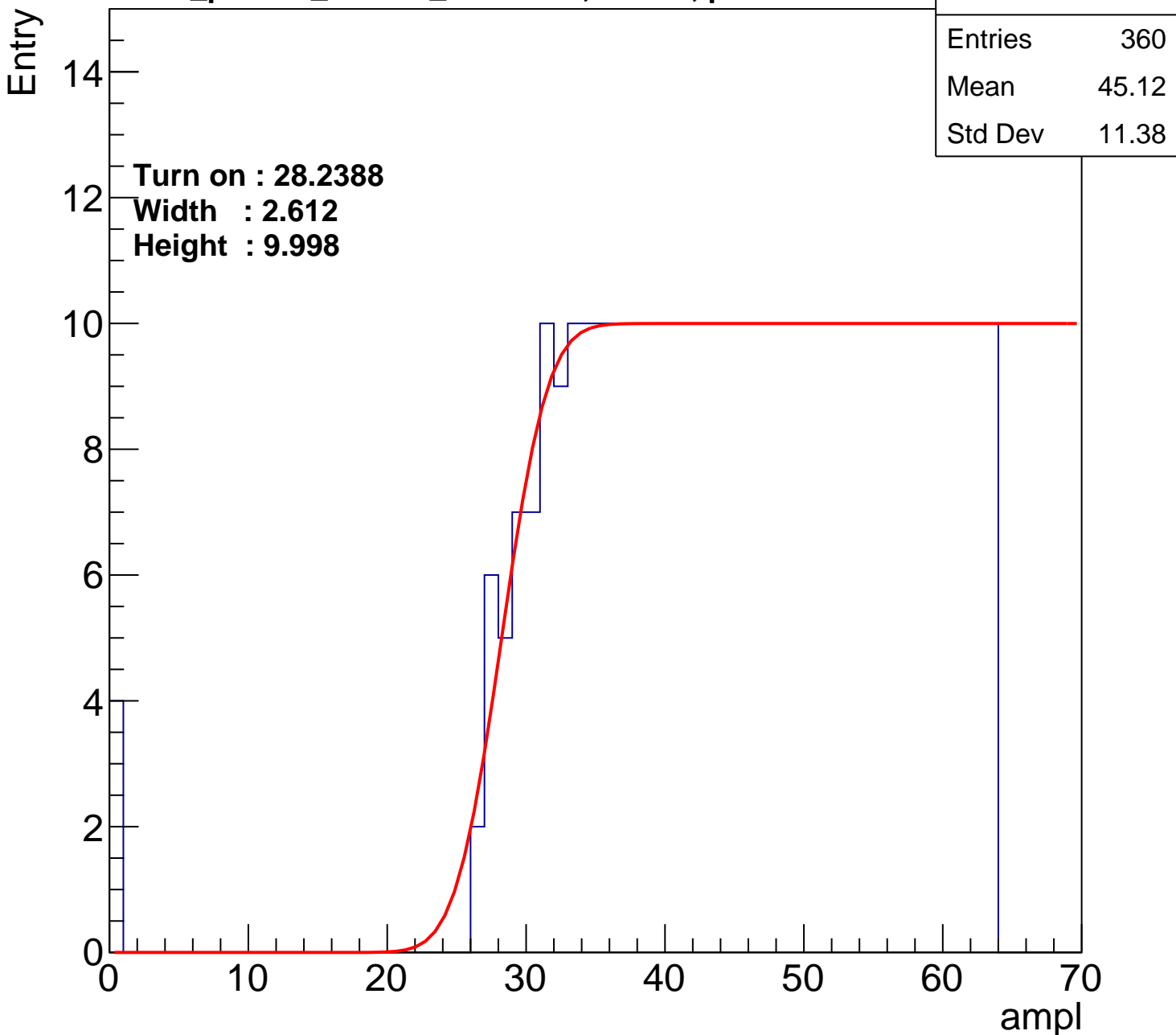


**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Entries	360
Mean	45.12
Std Dev	11.38

**Height : 9.998**



# B1L003S, U22-ch14

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	385
Mean	43.97
Std Dev	11.78

Turn on : 25.7402

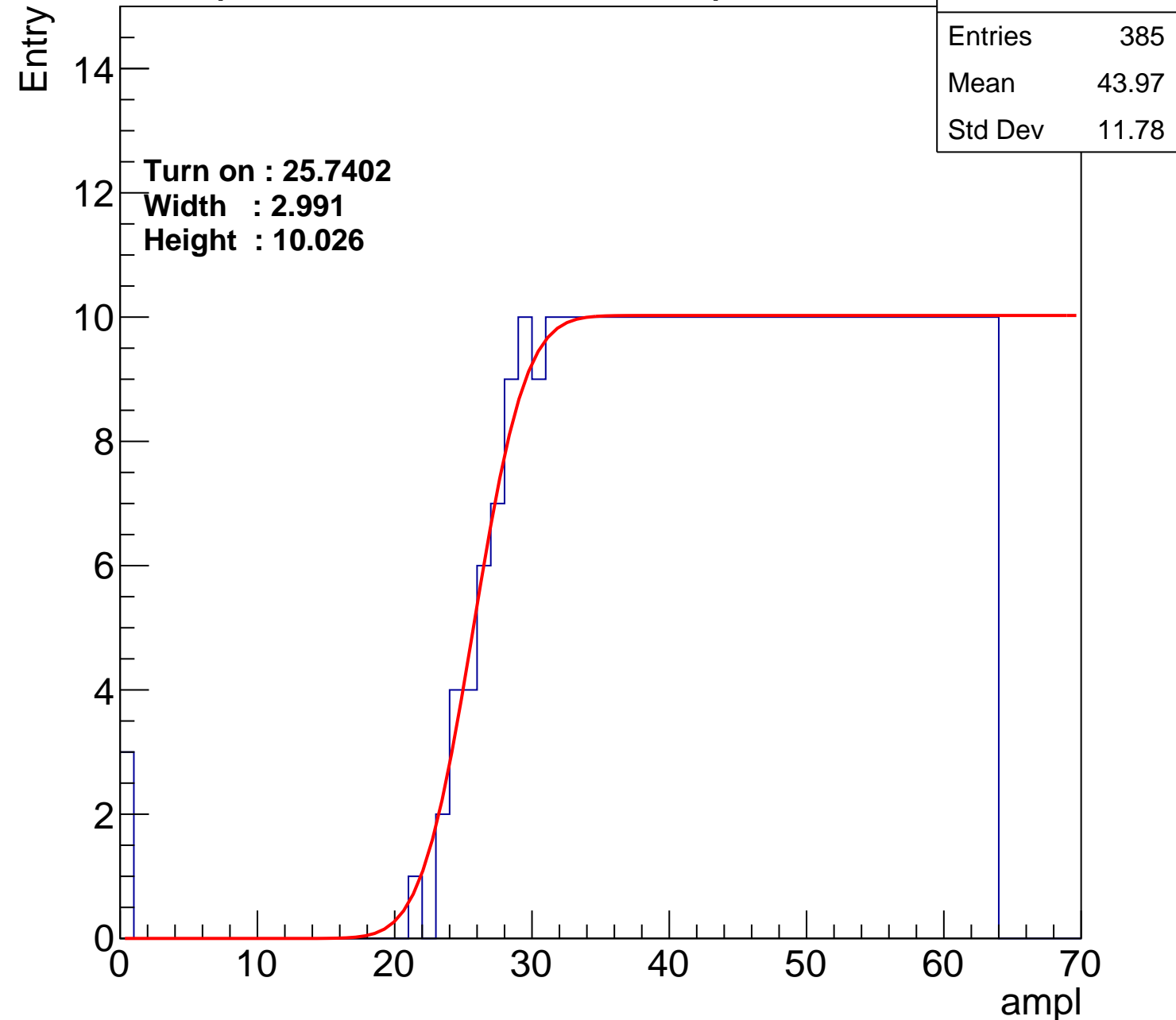
Width : 2.991

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch15

calib\_packv5\_042523\_0143.root, FC#13, port D2

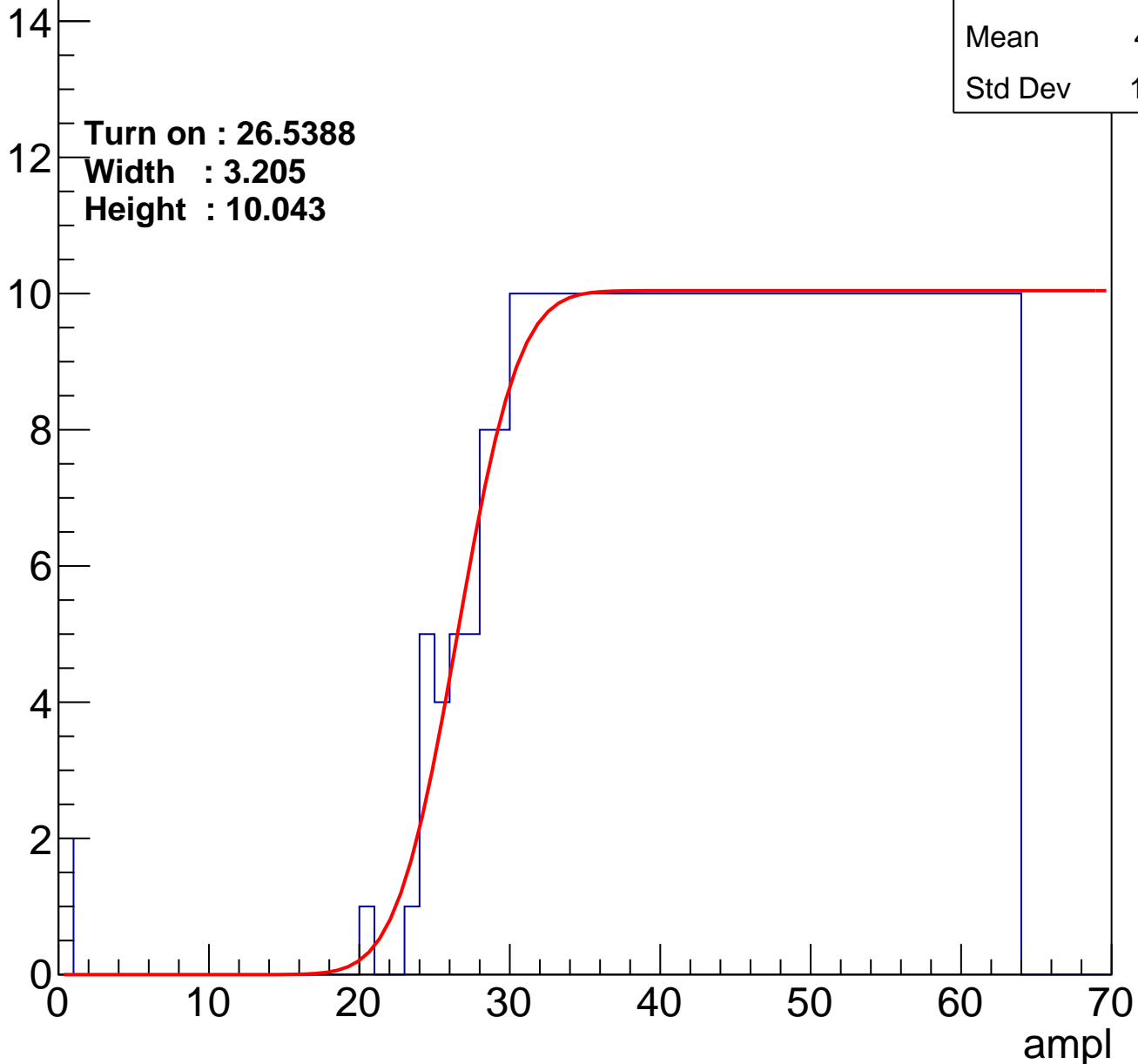
Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.5388

Width : 3.205

Height : 10.043

Entry



# B1L003S, U22-ch16

calib\_packv5\_042523\_0143.root, FC#13, port D2

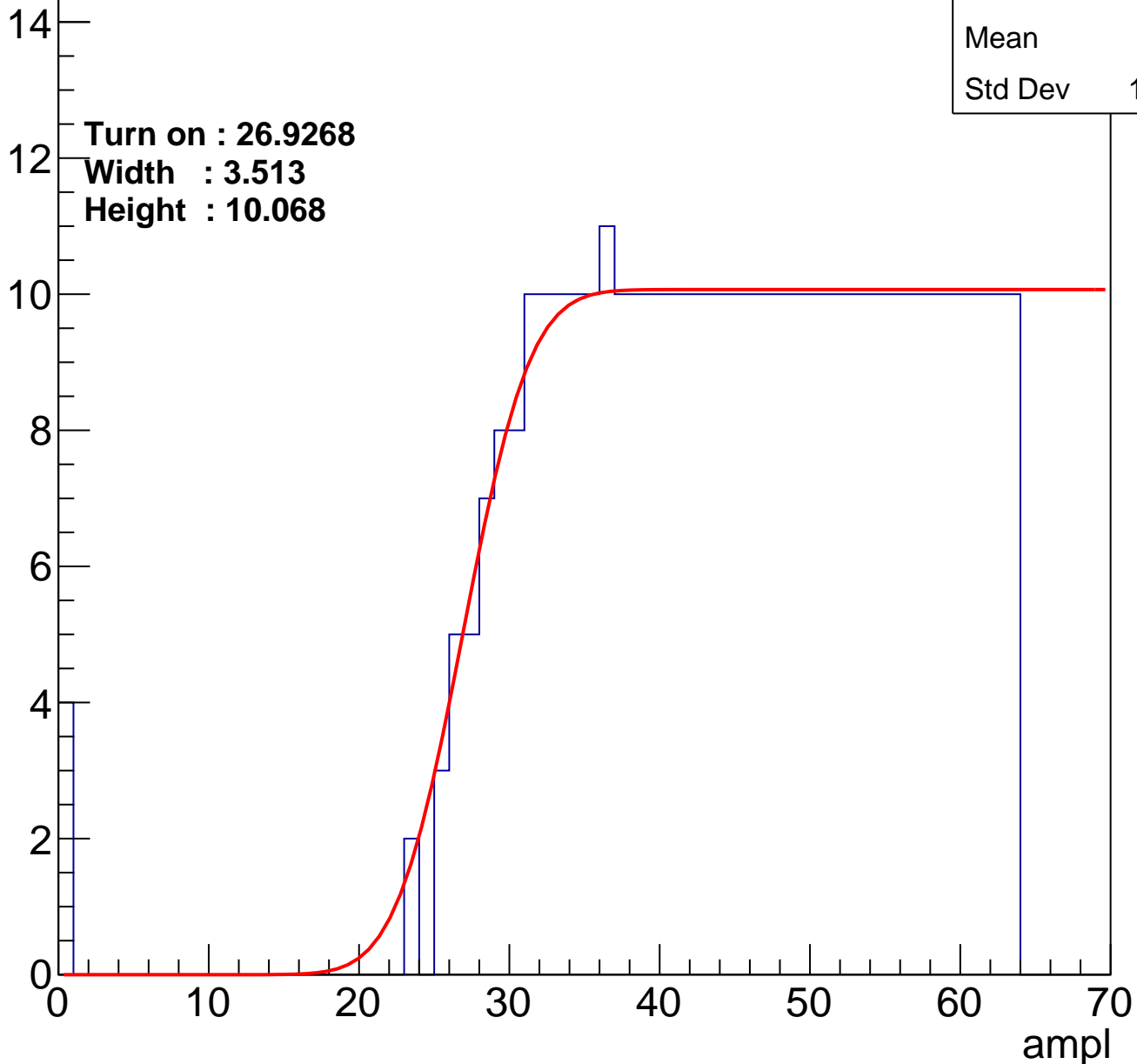
Entries	373
Mean	44.5
Std Dev	11.67

Turn on : 26.9268

Width : 3.513

Height : 10.068

Entry



# B1L003S, U22-ch17

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	366
Mean	44.87
Std Dev	11.37

Turn on : 27.9256

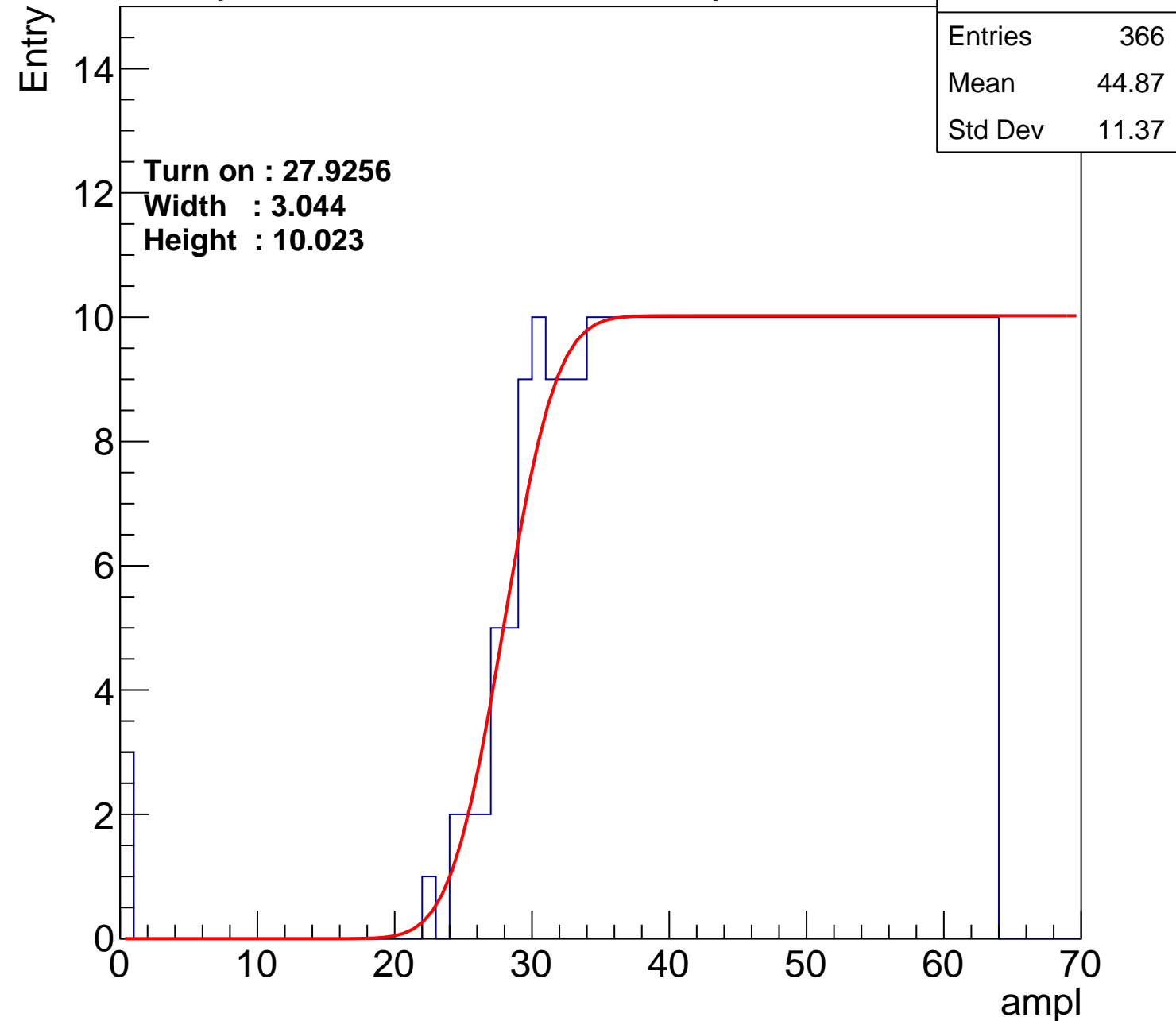
Width : 3.044

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch18

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.3223

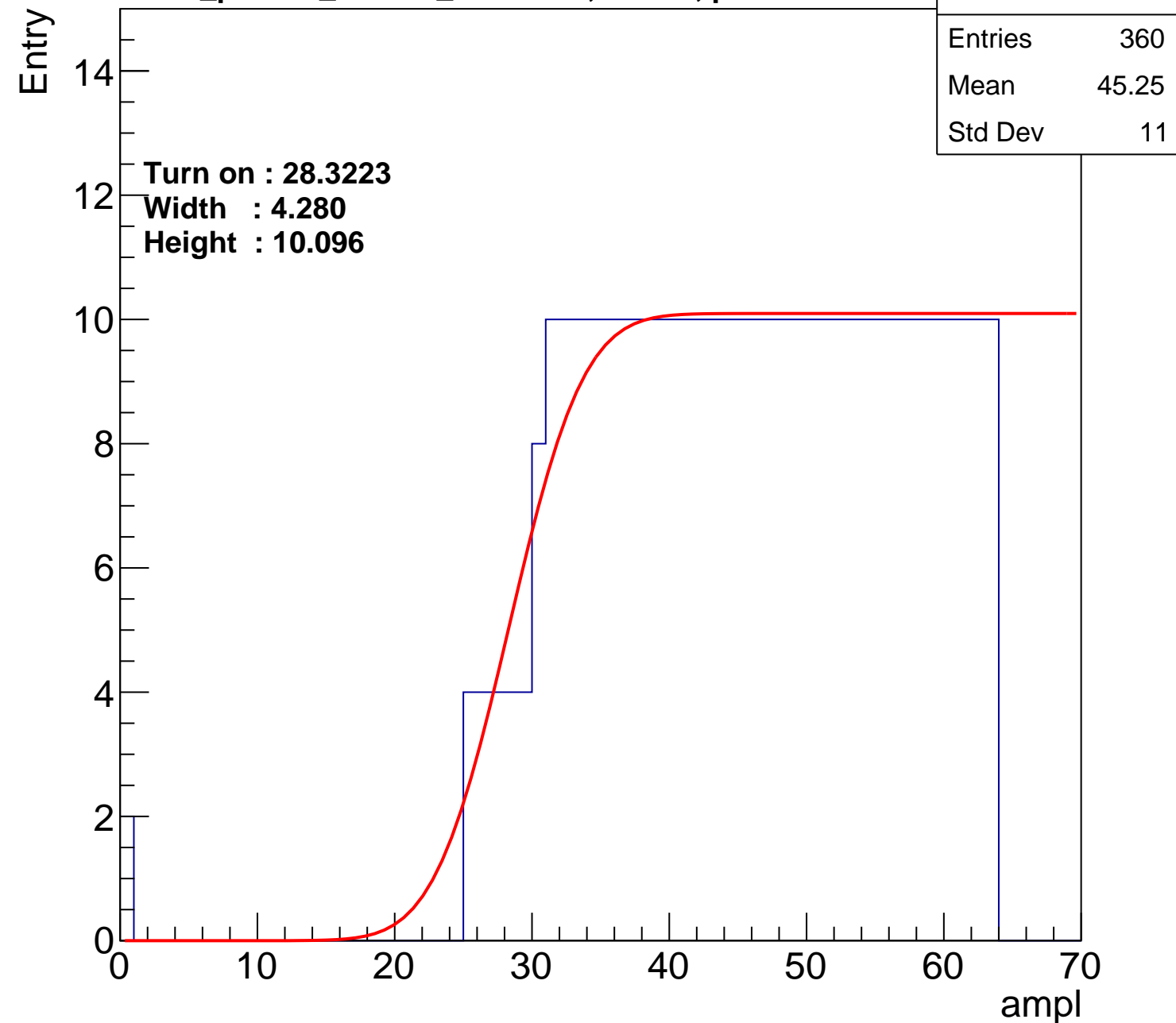
Width : 4.280

Height : 10.096

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch19

calib\_packv5\_042523\_0143.root, FC#13, port D2

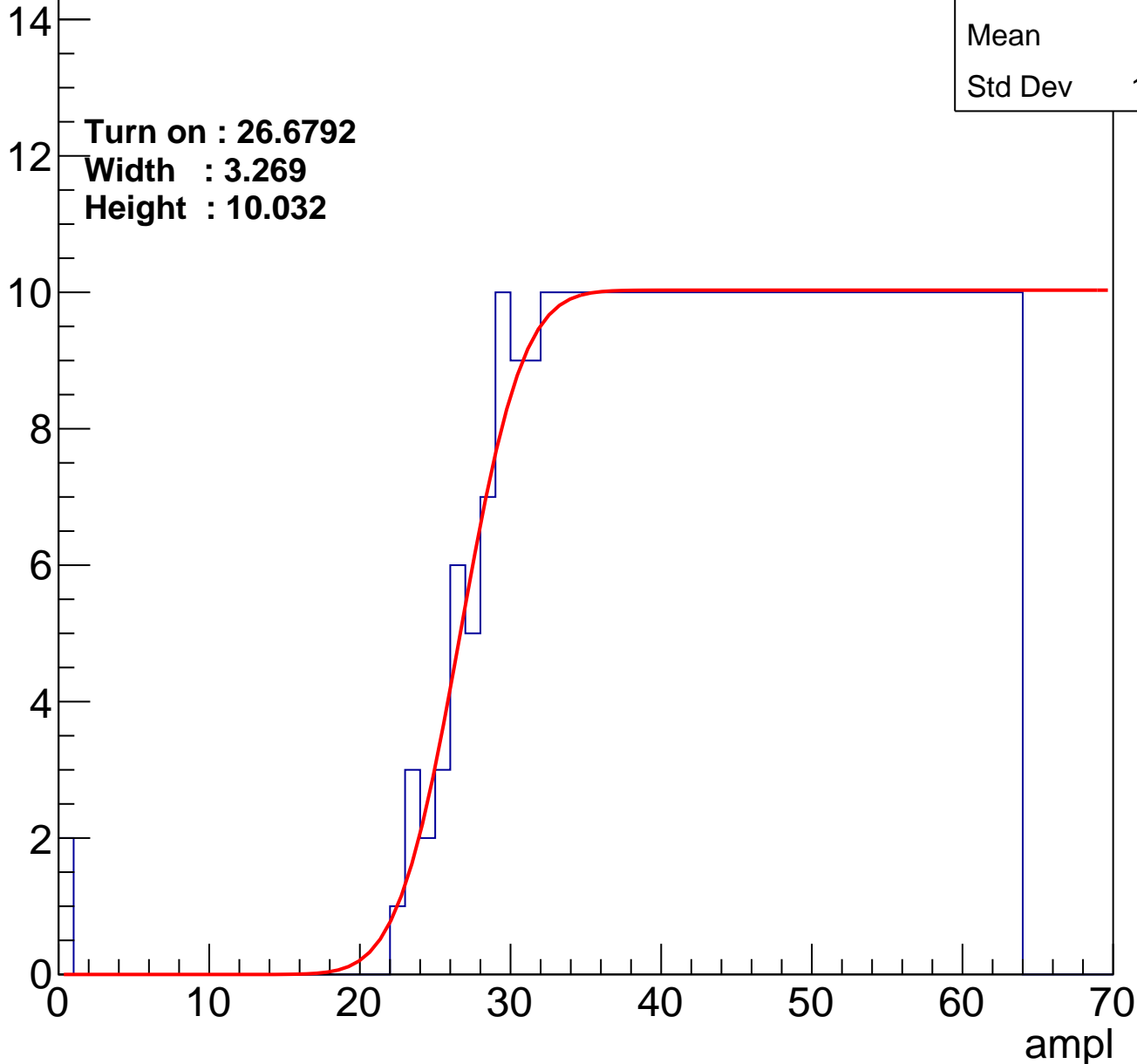
Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.6792

Width : 3.269

Height : 10.032

Entry



# B1L003S, U22-ch20

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	351
Mean	45.73
Std Dev	10.61

Turn on : 29.1835

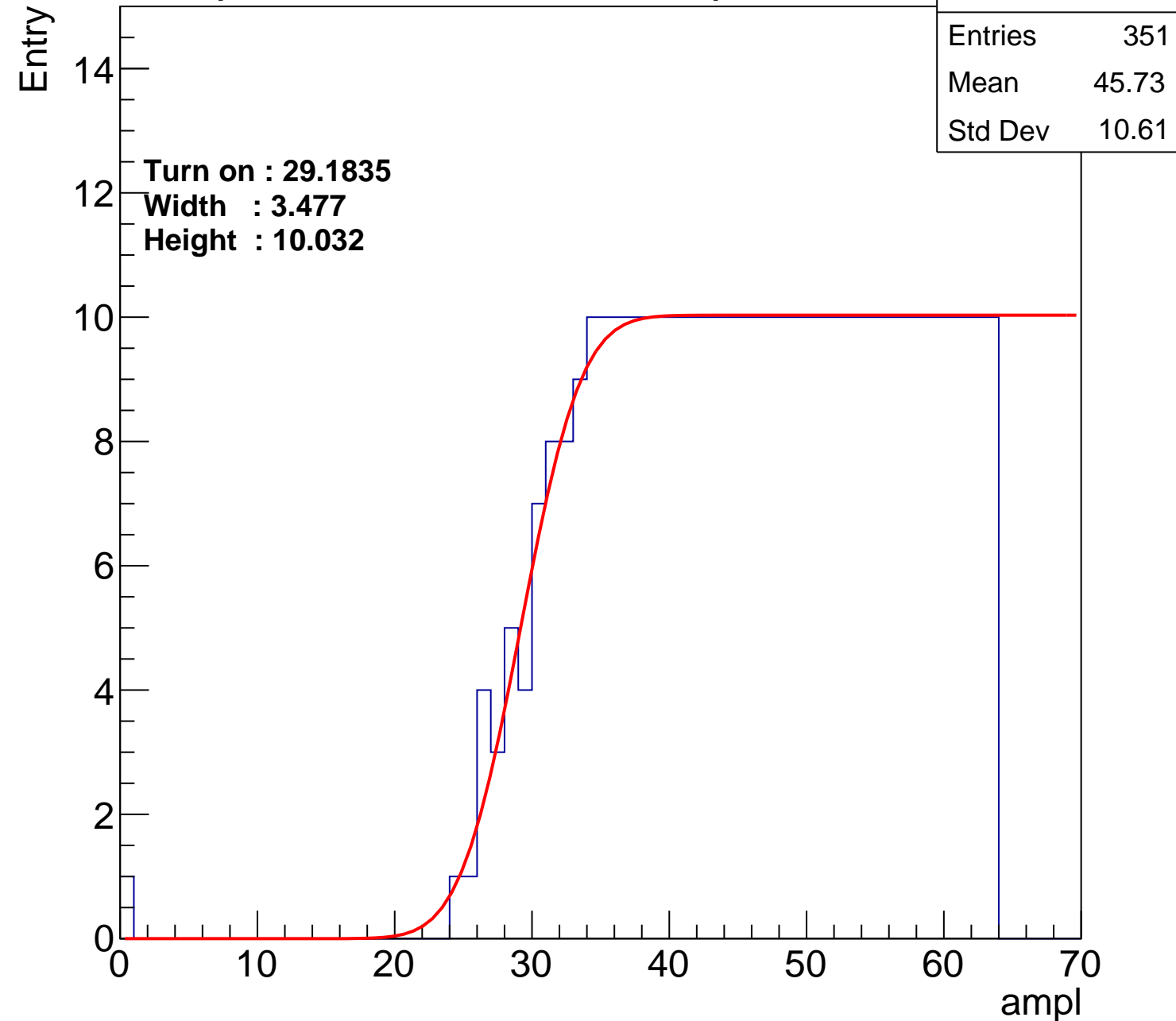
Width : 3.477

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch21

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	43.79
Std Dev	12.03

Turn on : 25.6885

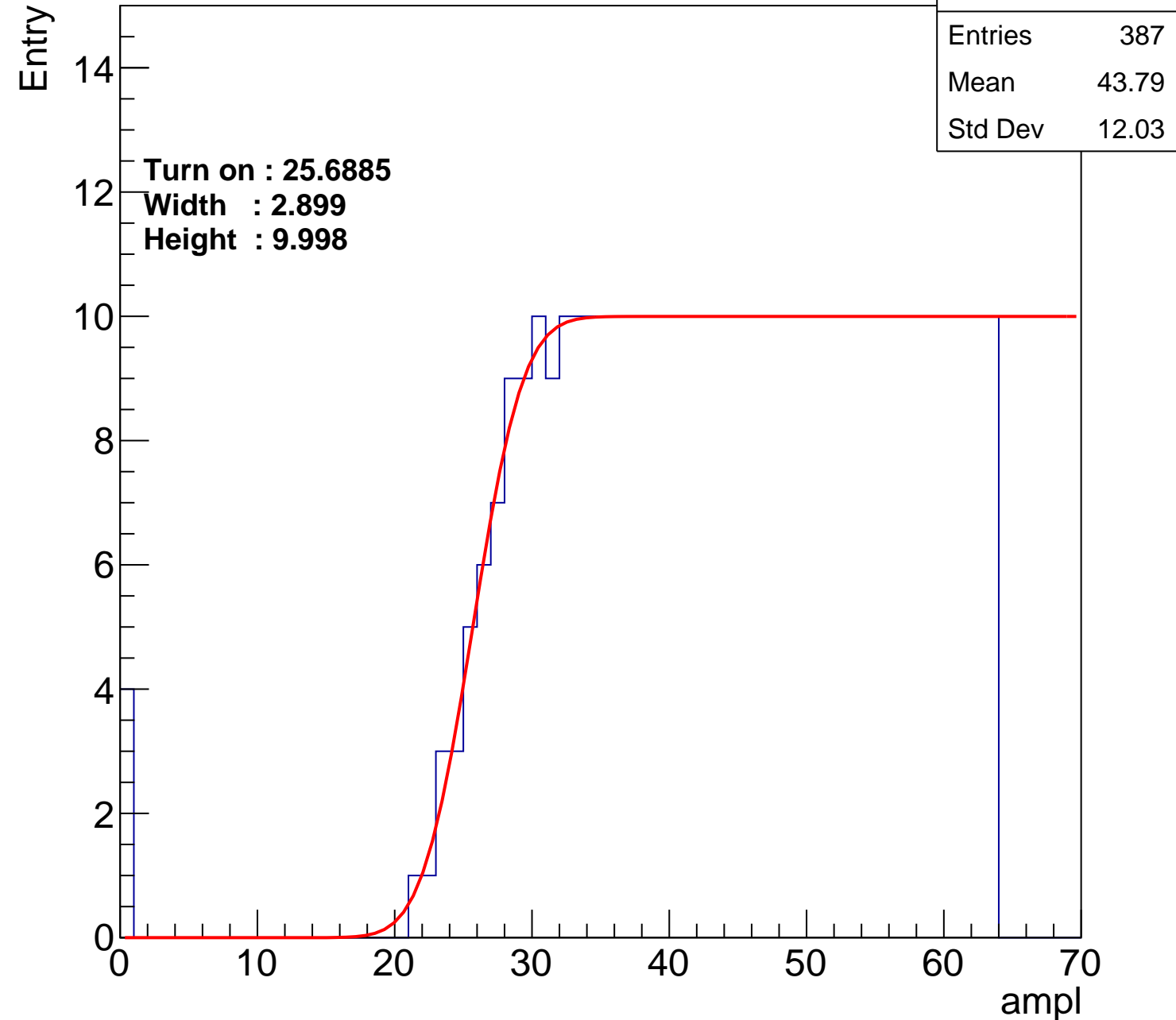
Width : 2.899

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch22

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 27.4787

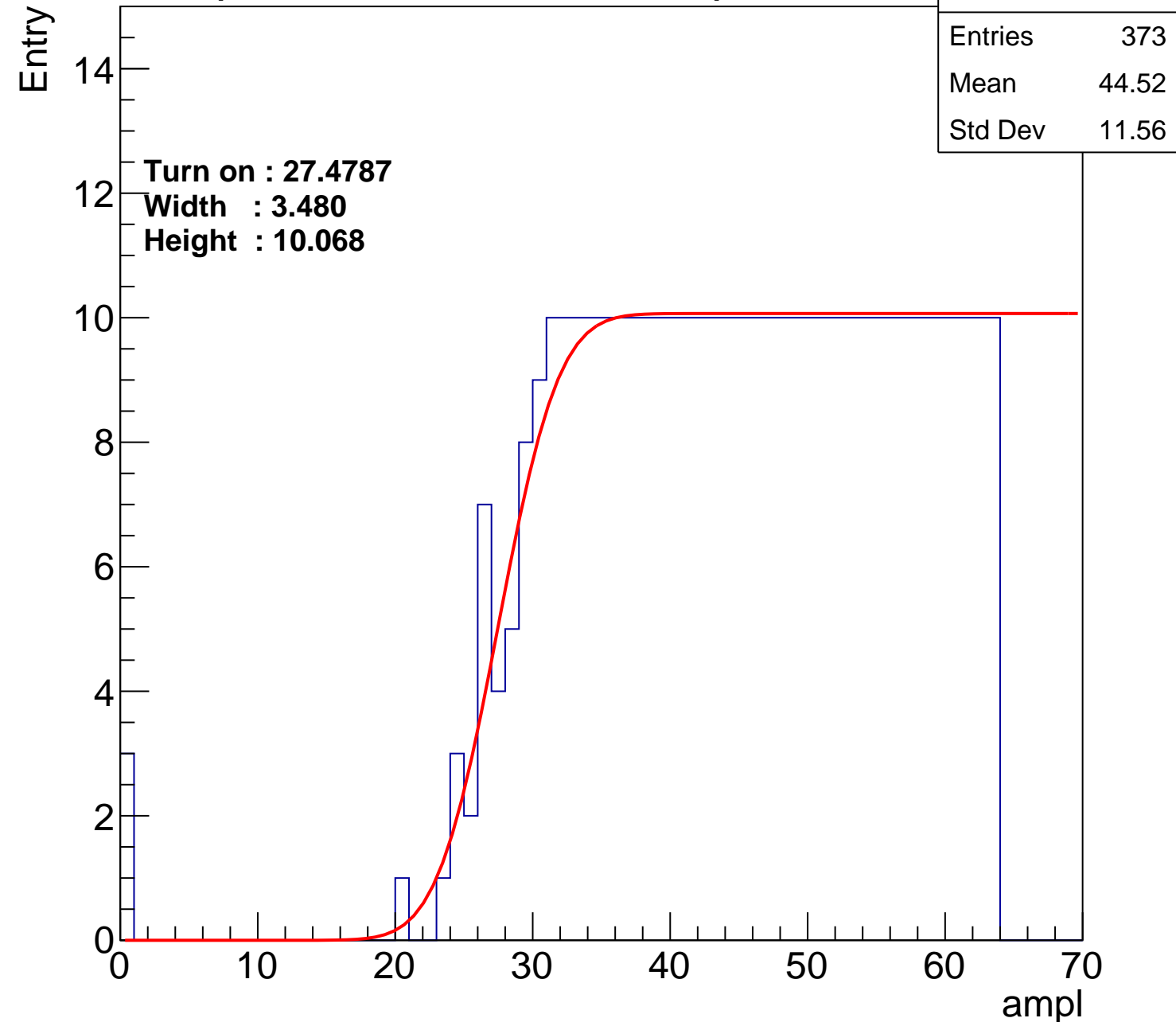
Width : 3.480

Height : 10.068

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch23

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.9133

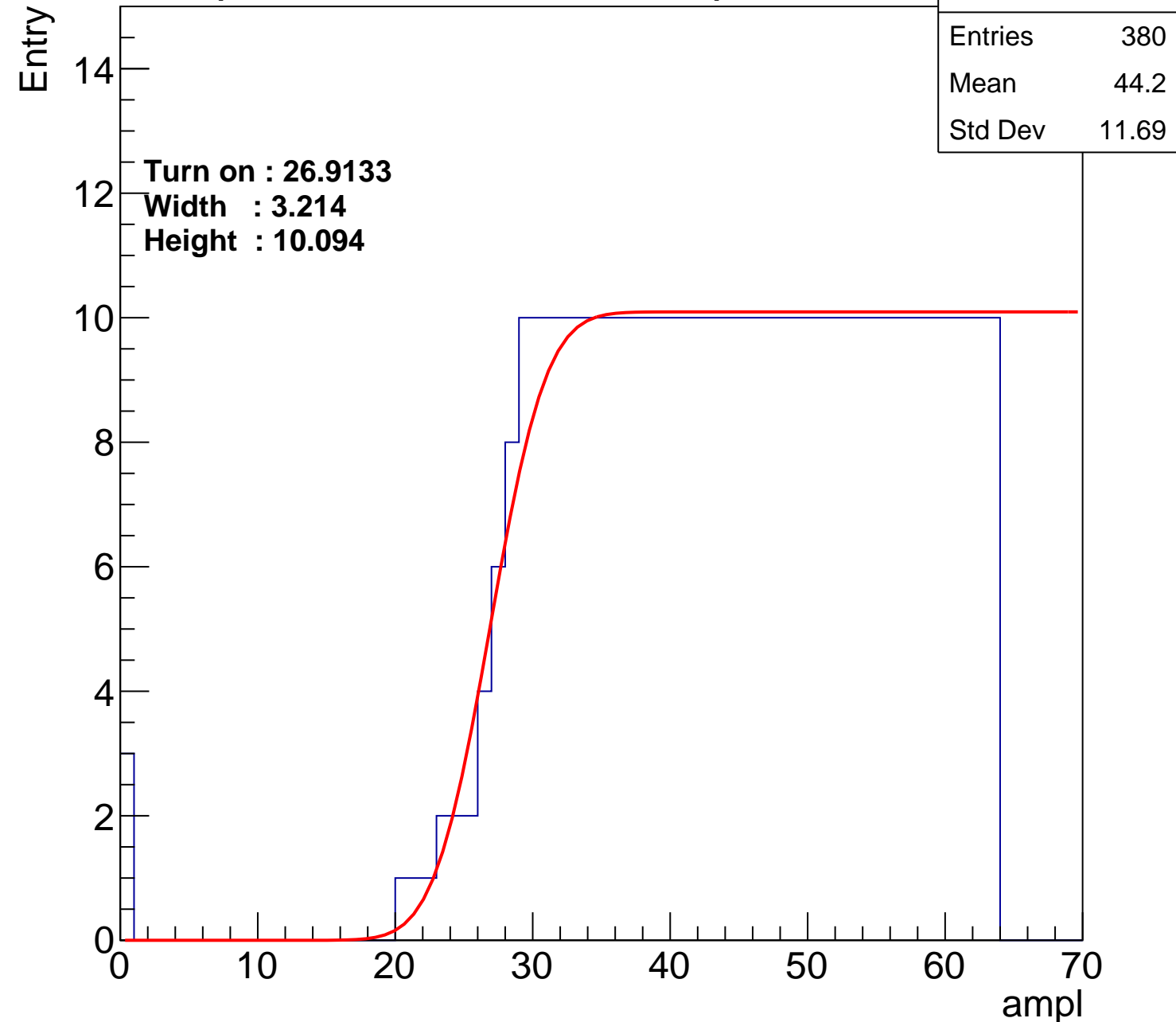
Width : 3.214

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch24

calib\_packv5\_042523\_0143.root, FC#13, port D2

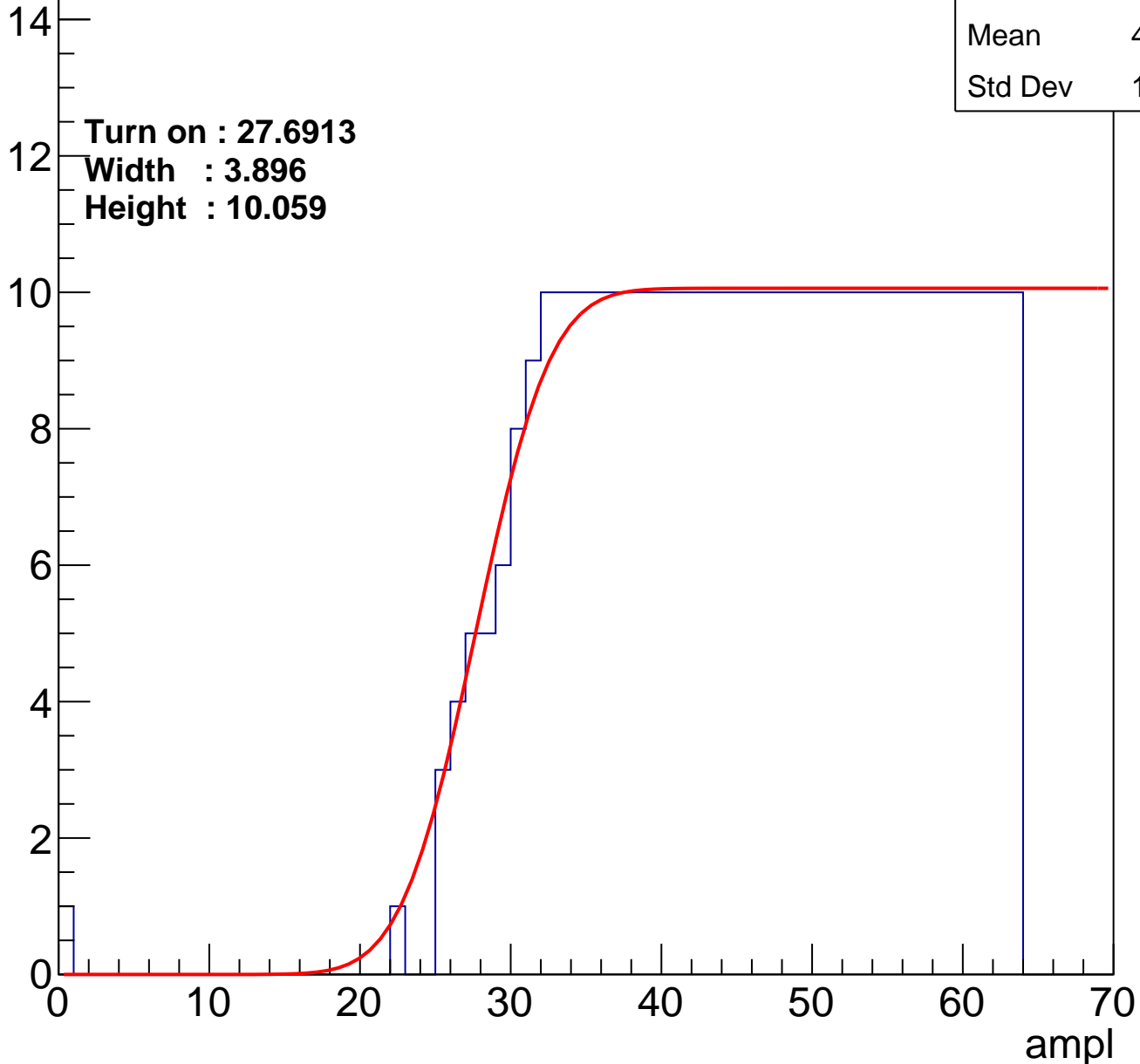
Entries	362
Mean	45.22
Std Dev	10.85

Turn on : 27.6913

Width : 3.896

Height : 10.059

Entry



# B1L003S, U22-ch25

calib\_packv5\_042523\_0143.root, FC#13, port D2

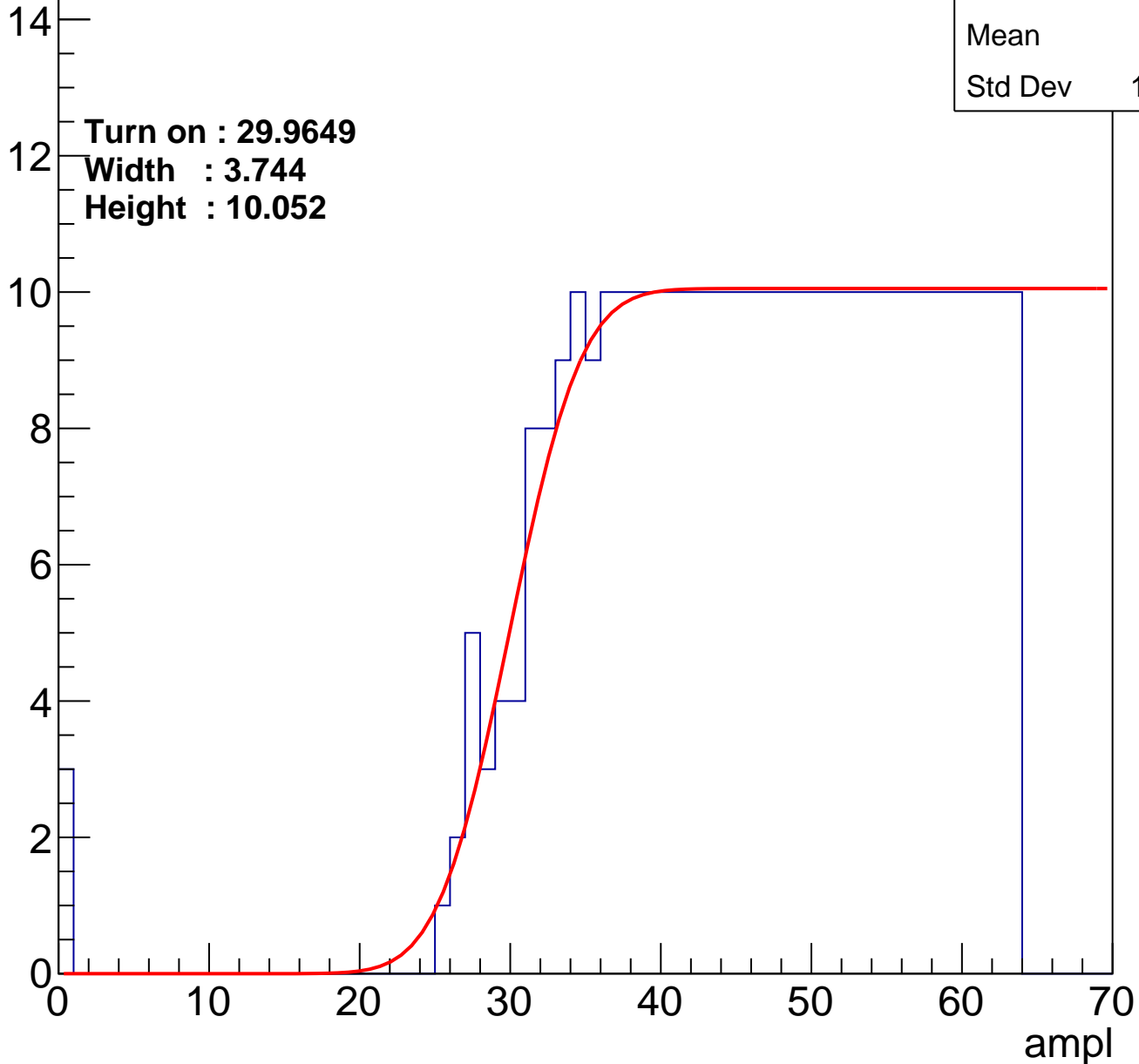
Entries	346
Mean	45.8
Std Dev	10.97

Turn on : 29.9649

Width : 3.744

Height : 10.052

Entry



# B1L003S, U22-ch26

calib\_packv5\_042523\_0143.root, FC#13, port D2

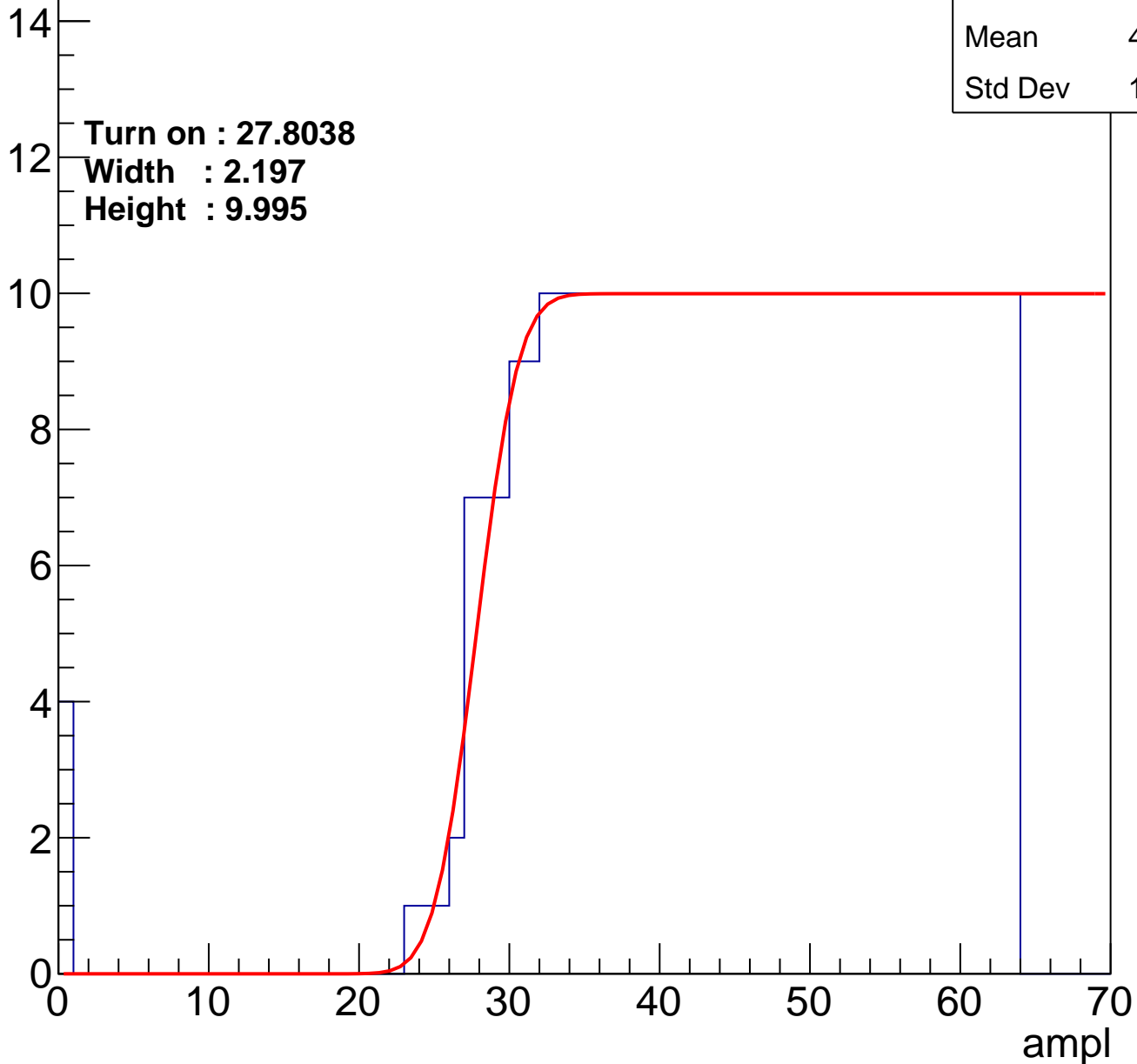
Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.8038

Width : 2.197

Height : 9.995

Entry



# B1L003S, U22-ch27

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	380
Mean	44.29
Std Dev	11.41

**Turn on : 26.5143**

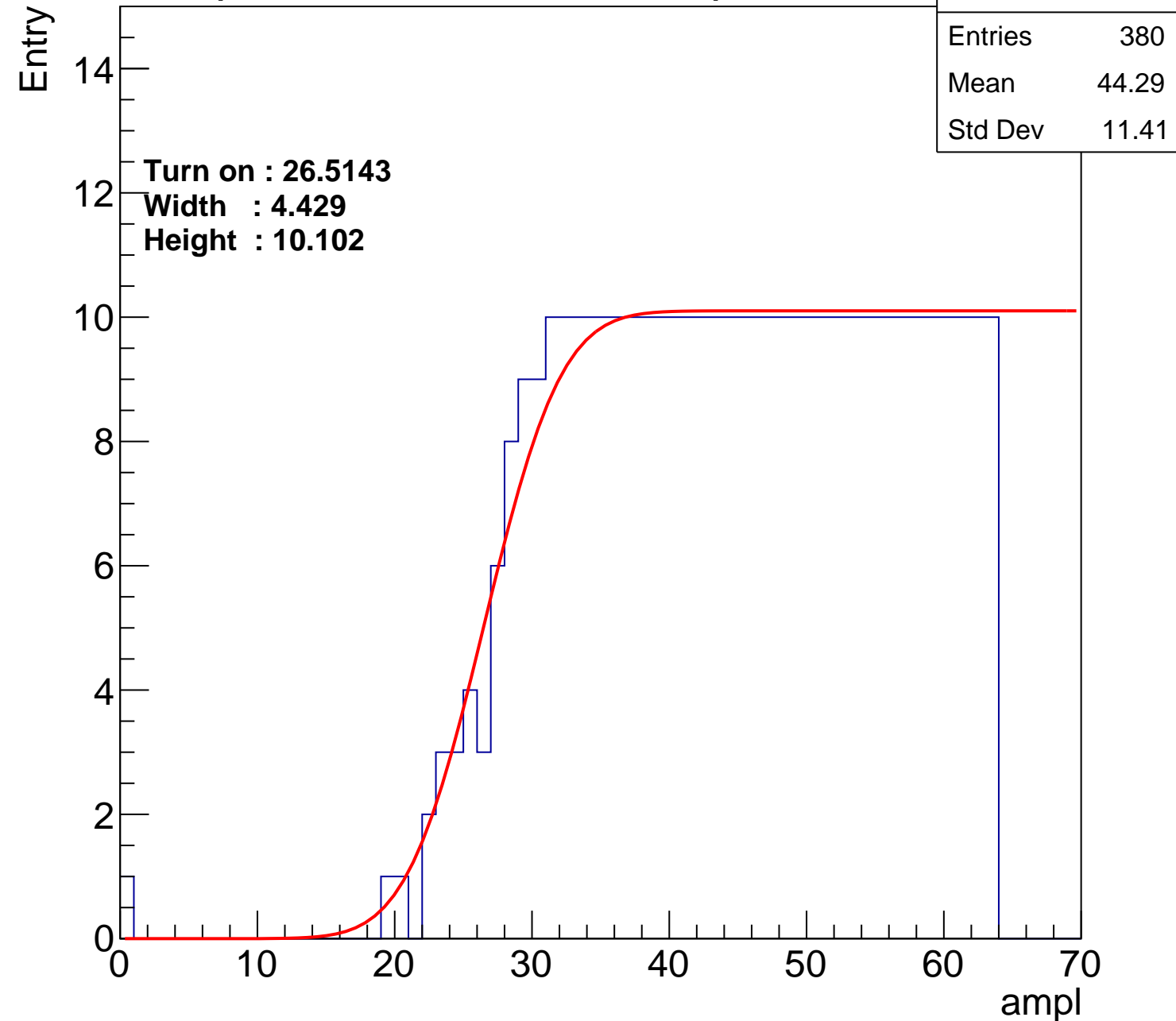
**Width : 4.429**

**Height : 10.102**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch28

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	377
Mean	44.43
Std Dev	11.41

Turn on : 26.7759

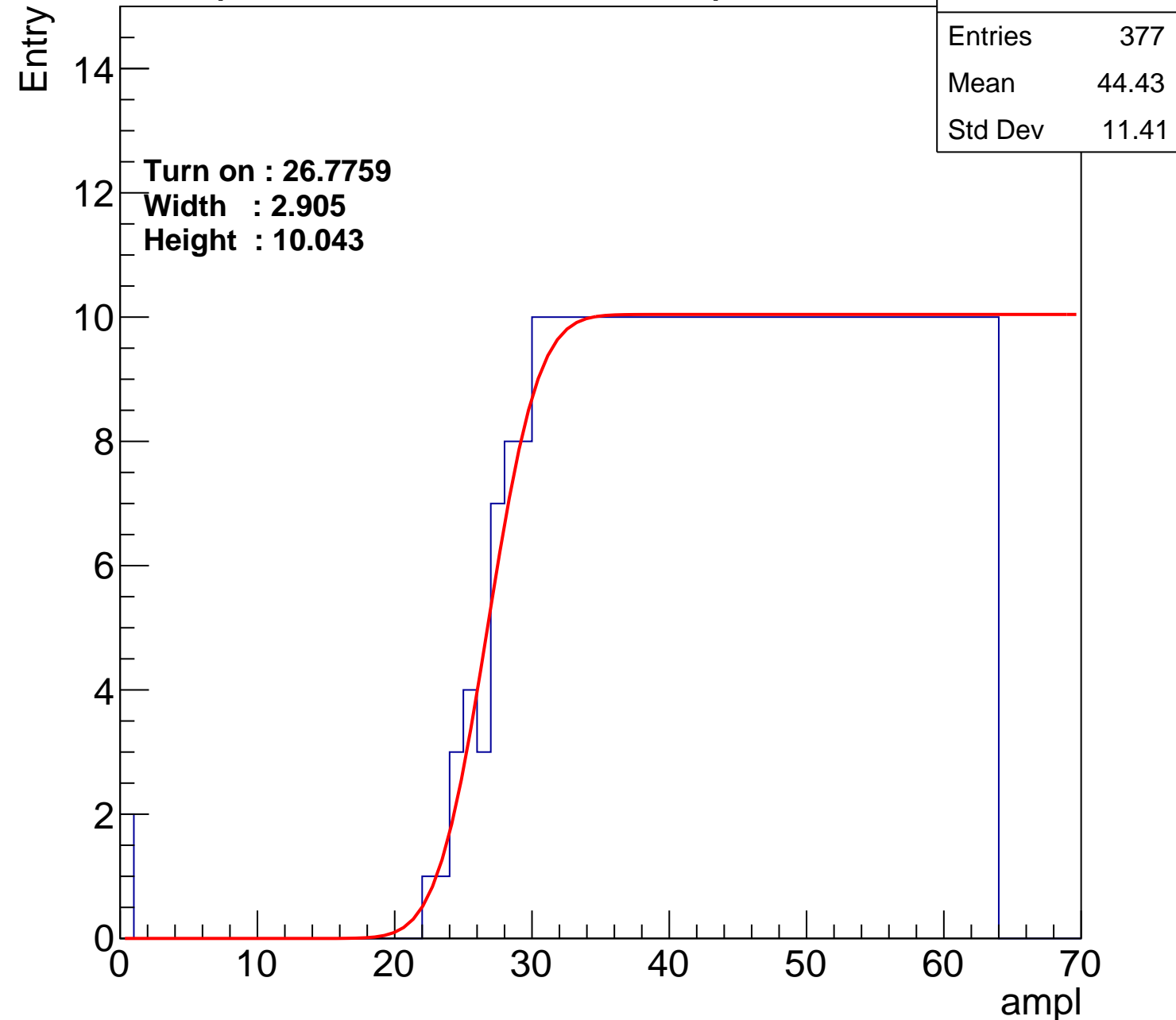
Width : 2.905

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch29

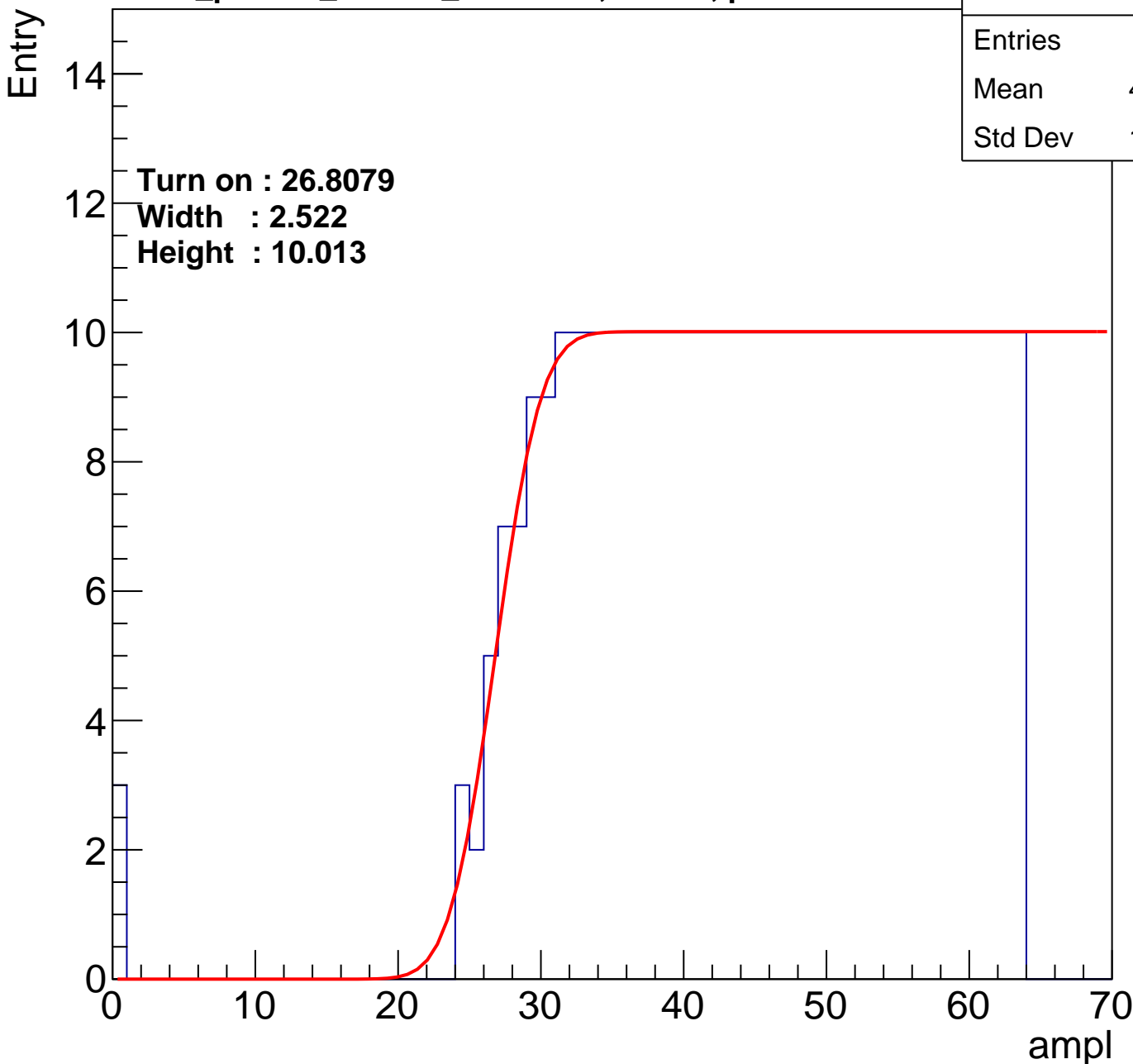
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Entries	375
Mean	44.47
Std Dev	11.52

**Turn on : 26.8079**

**Width : 2.522**

**Height : 10.013**



# B1L003S, U22-ch30

calib\_packv5\_042523\_0143.root, FC#13, port D2

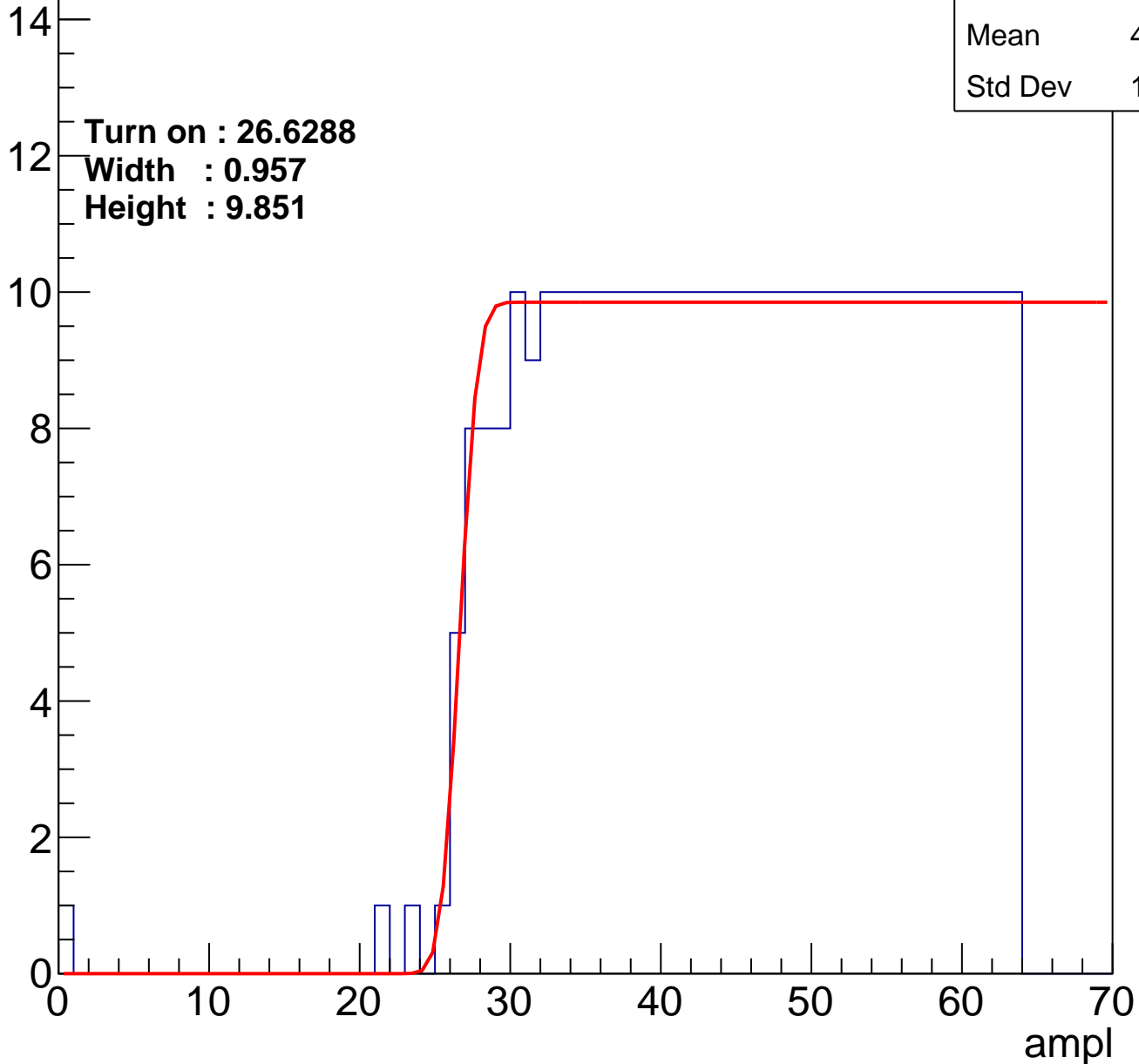
Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.6288

Width : 0.957

Height : 9.851

Entry





# B1L003S, U22-ch31

calib\_packv5\_042523\_0143.root, FC#13, port D2

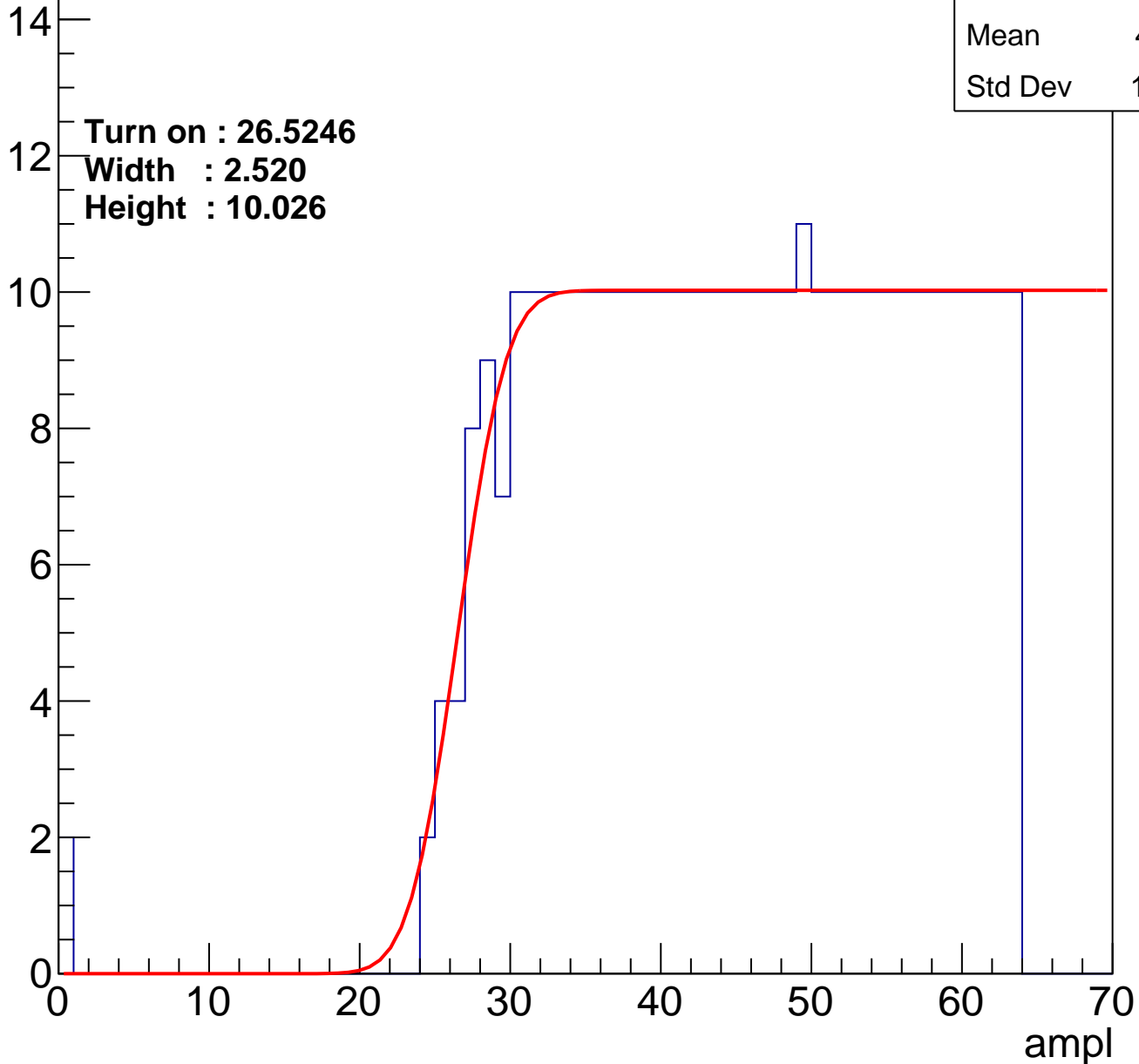
Entries	377
Mean	44.51
Std Dev	11.33

Turn on : 26.5246

Width : 2.520

Height : 10.026

Entry



# B1L003S, U22-ch32

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.1642

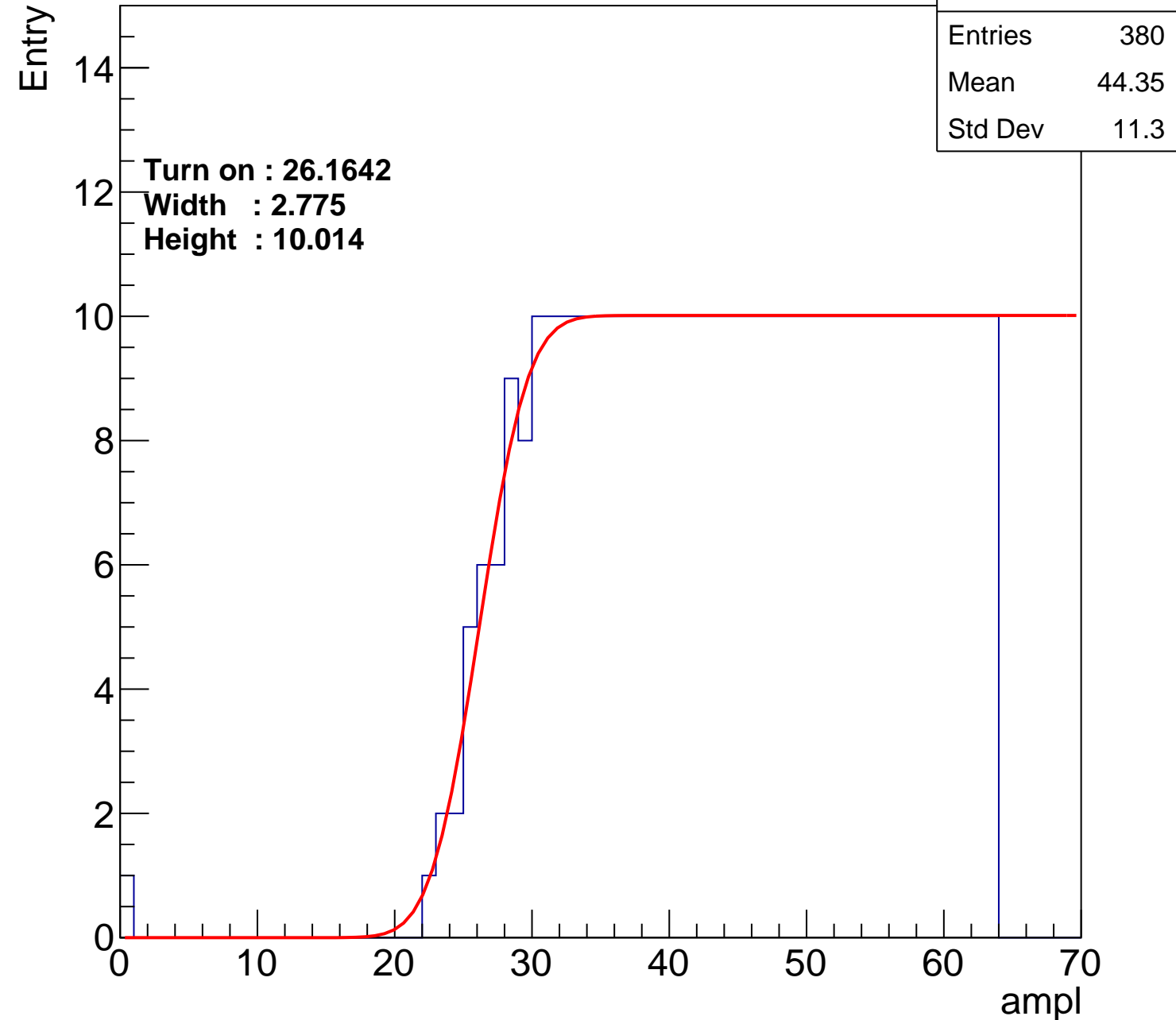
Width : 2.775

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch33

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Entries	362
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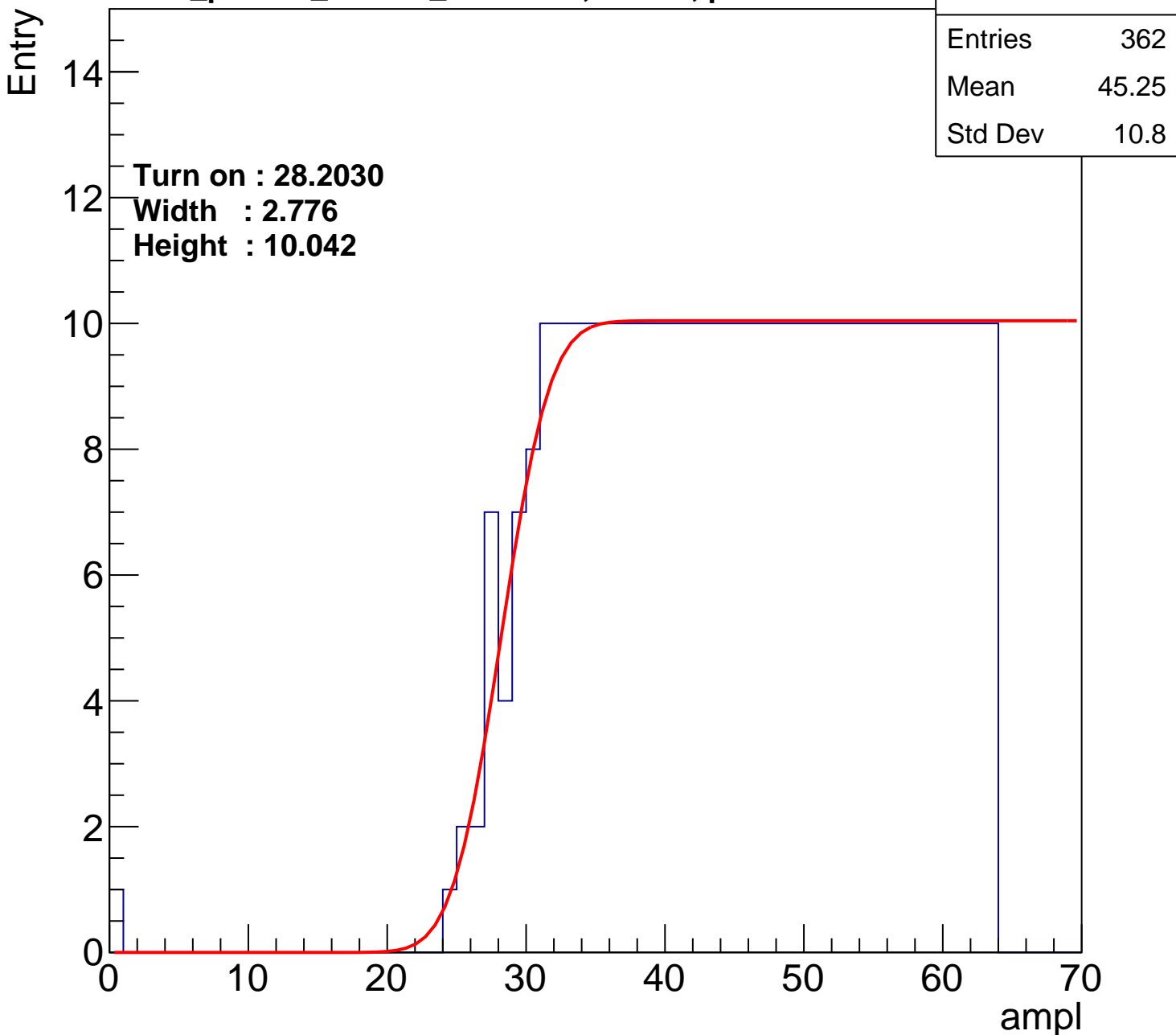
Mean	45.25
------	-------

Std Dev	10.8
---------	------

**Turn on : 28.2030**

**Width : 2.776**

**Height : 10.042**



# B1L003S, U22-ch34

calib\_packv5\_042523\_0143.root, FC#13, port D2

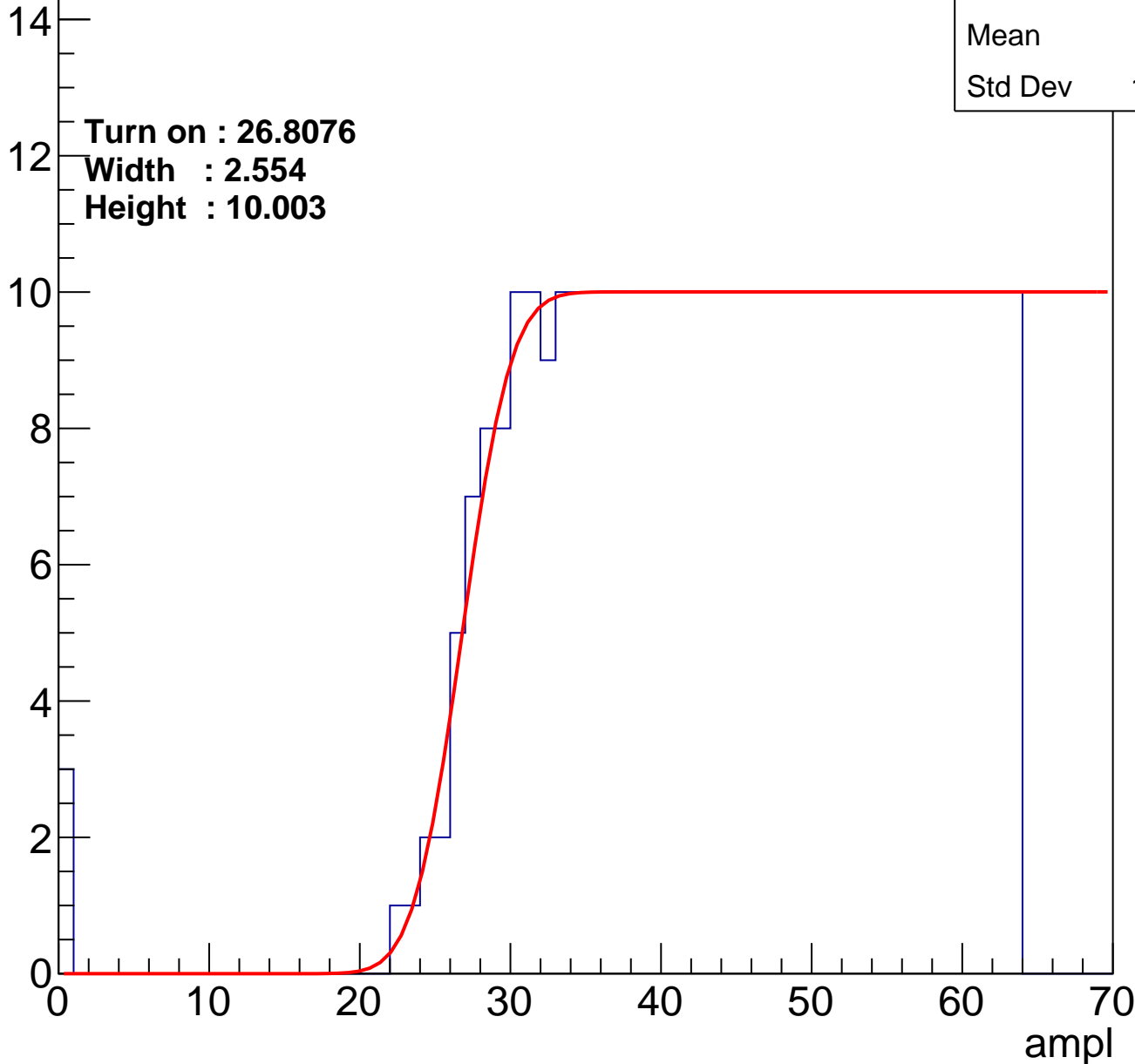
Entries	376
Mean	44.4
Std Dev	11.58

Turn on : 26.8076

Width : 2.554

Height : 10.003

Entry



# B1L003S, U22-ch35

calib\_packv5\_042523\_0143.root, FC#13, port D2

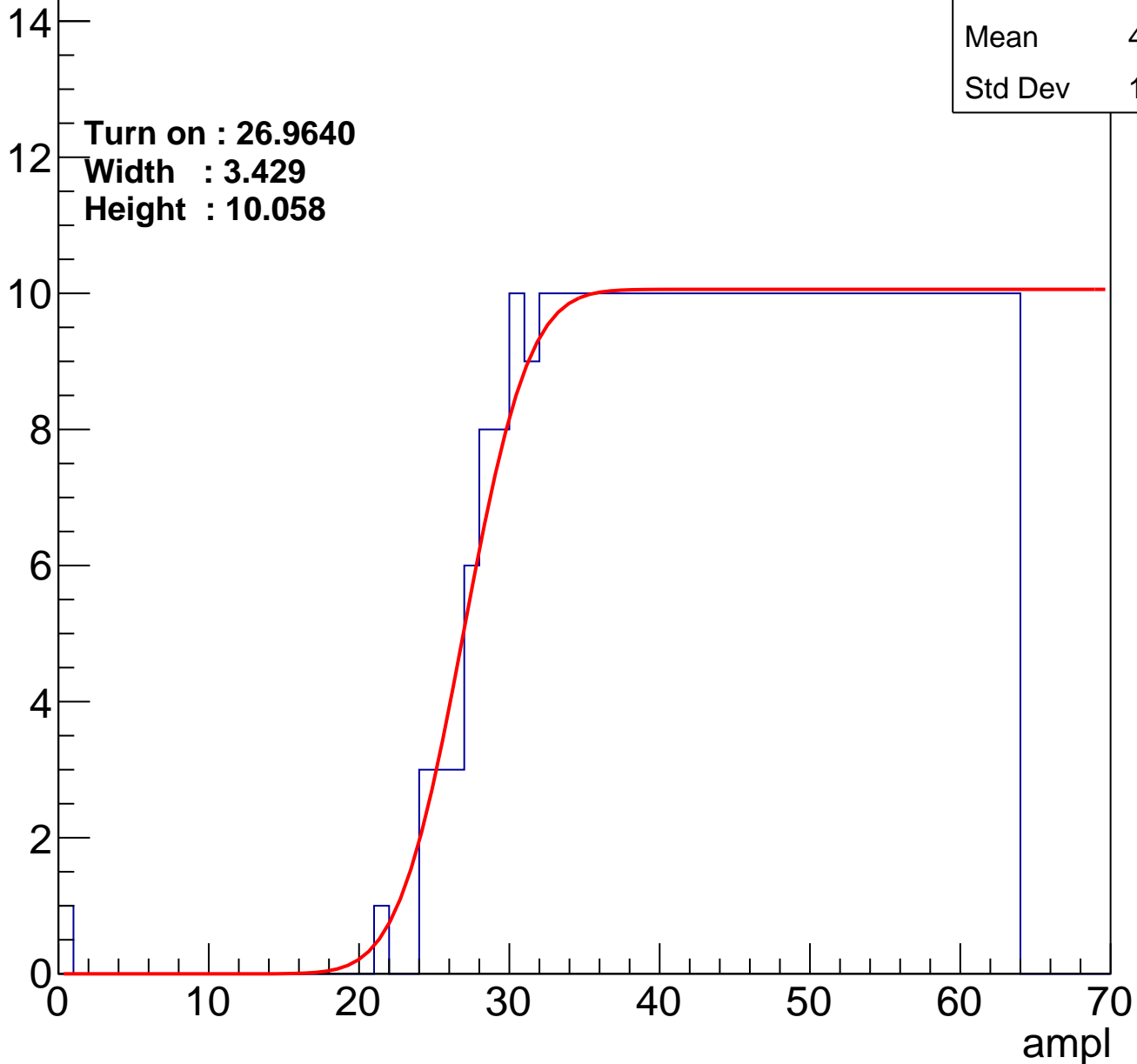
Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 26.9640

Width : 3.429

Height : 10.058

Entry



# B1L003S, U22-ch36

calib\_packv5\_042523\_0143.root, FC#13, port D2

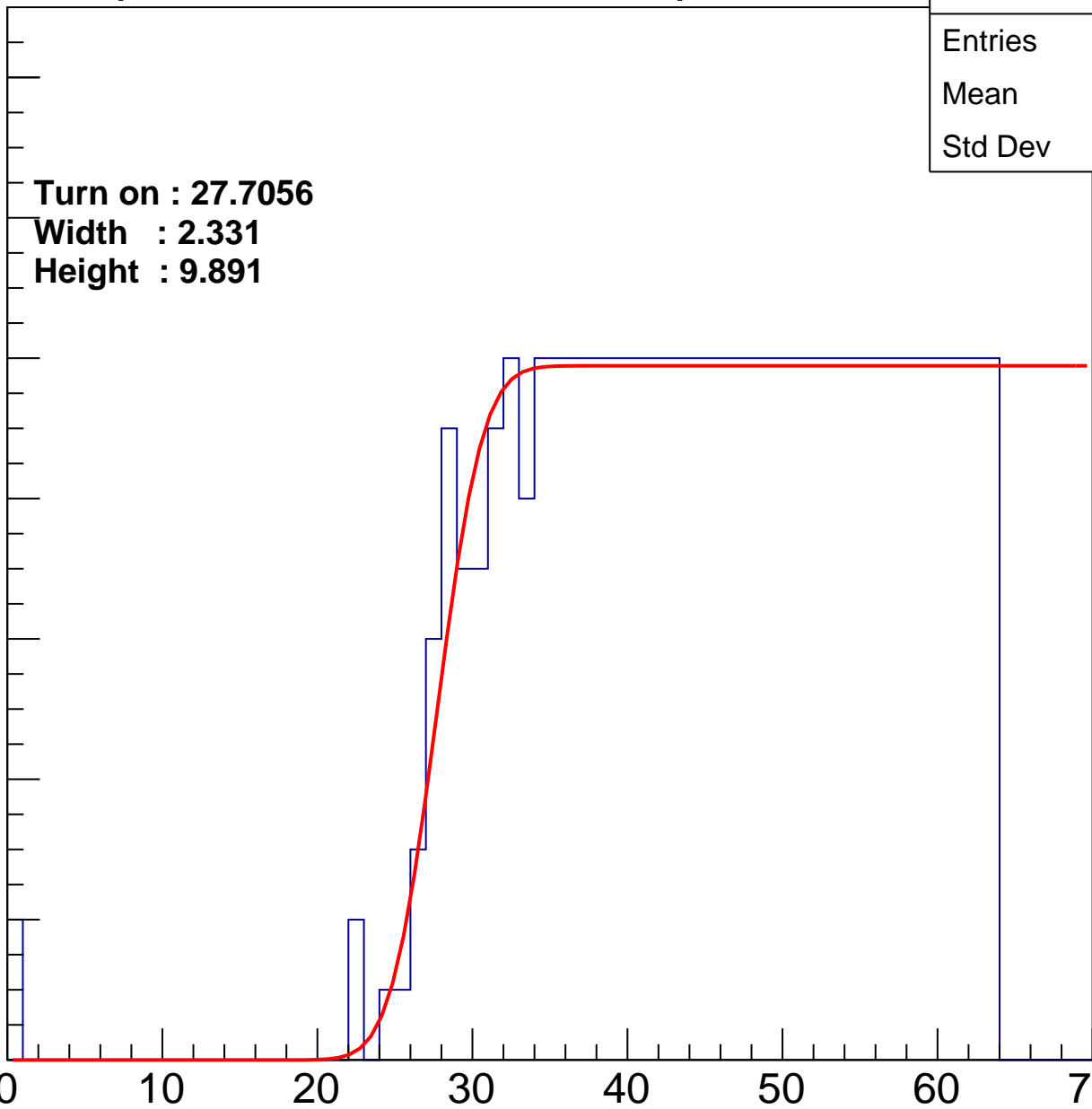
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.7056  
Width : 2.331  
Height : 9.891

Entries	365
Mean	44.96
Std Dev	11.19

ampl



# B1L003S, U22-ch37

calib\_packv5\_042523\_0143.root, FC#13, port D2

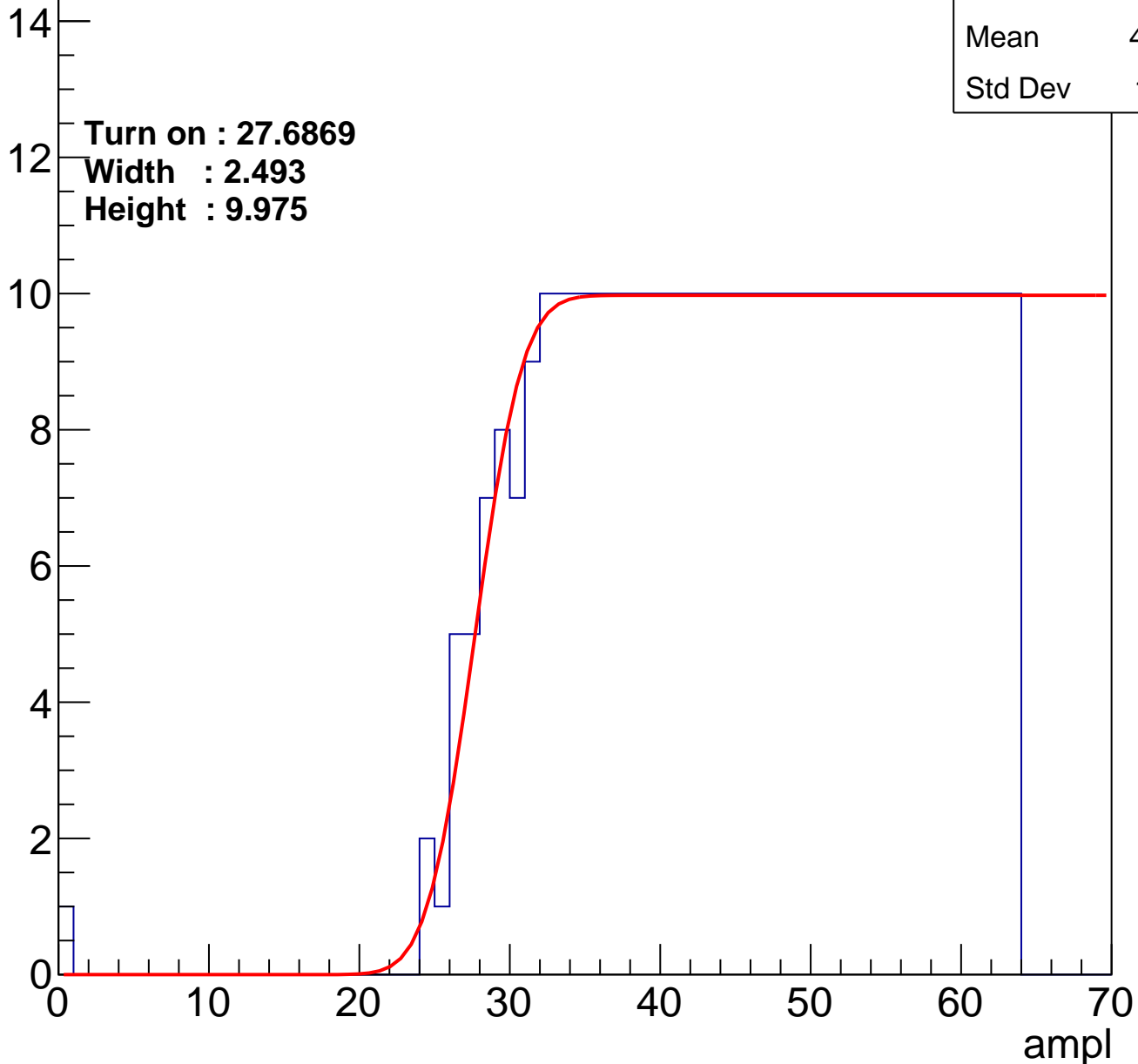
Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.6869

Width : 2.493

Height : 9.975

Entry



# B1L003S, U22-ch38

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 27.5061

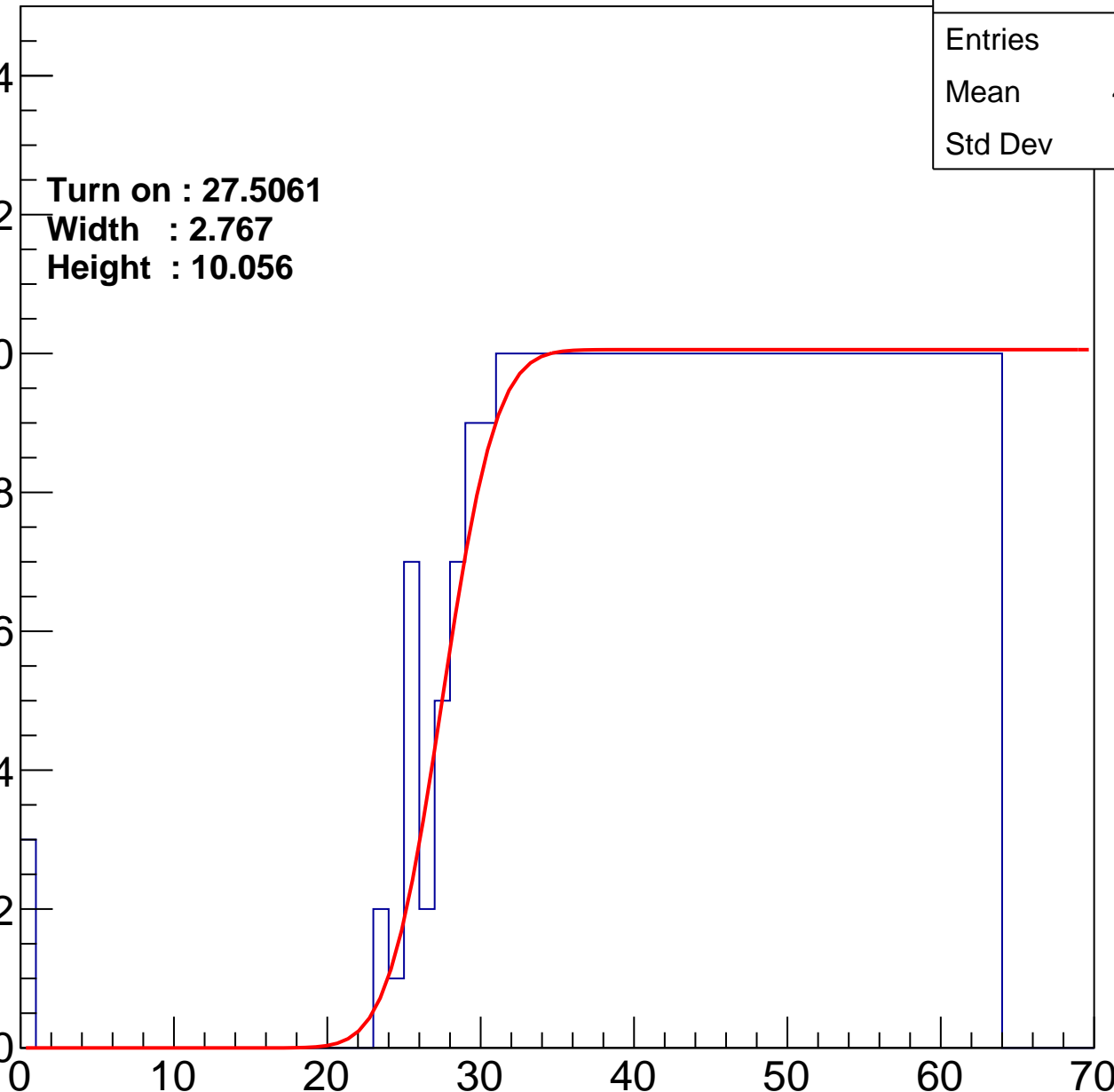
Width : 2.767

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Turn on : 27.2087  
Width : 2.814  
Height : 10.043



# B1L003S, U22-ch40

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	386
Mean	43.99
Std Dev	11.56

Turn on : 25.6248

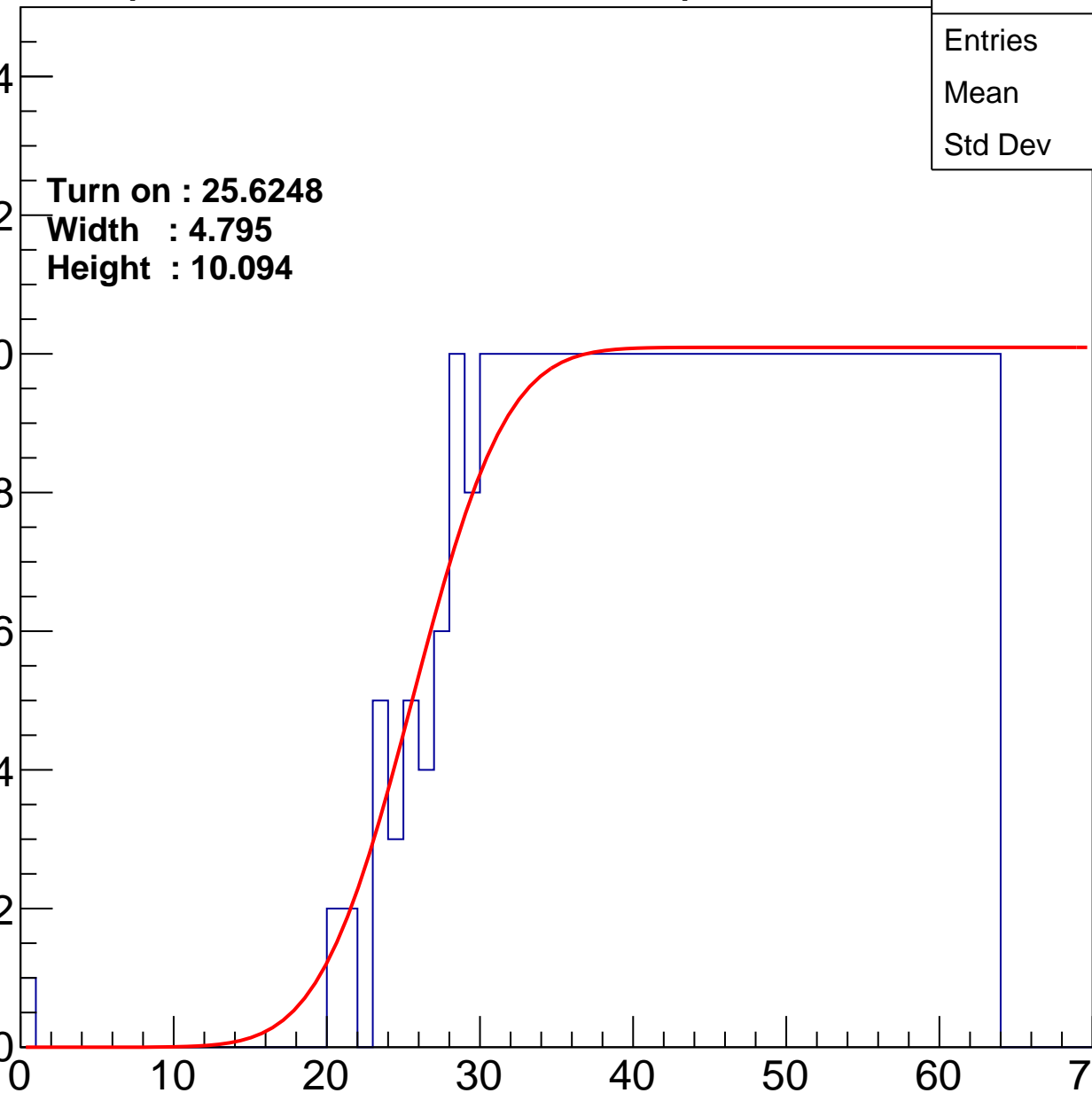
Width : 4.795

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch41

calib\_packv5\_042523\_0143.root, FC#13, port D2

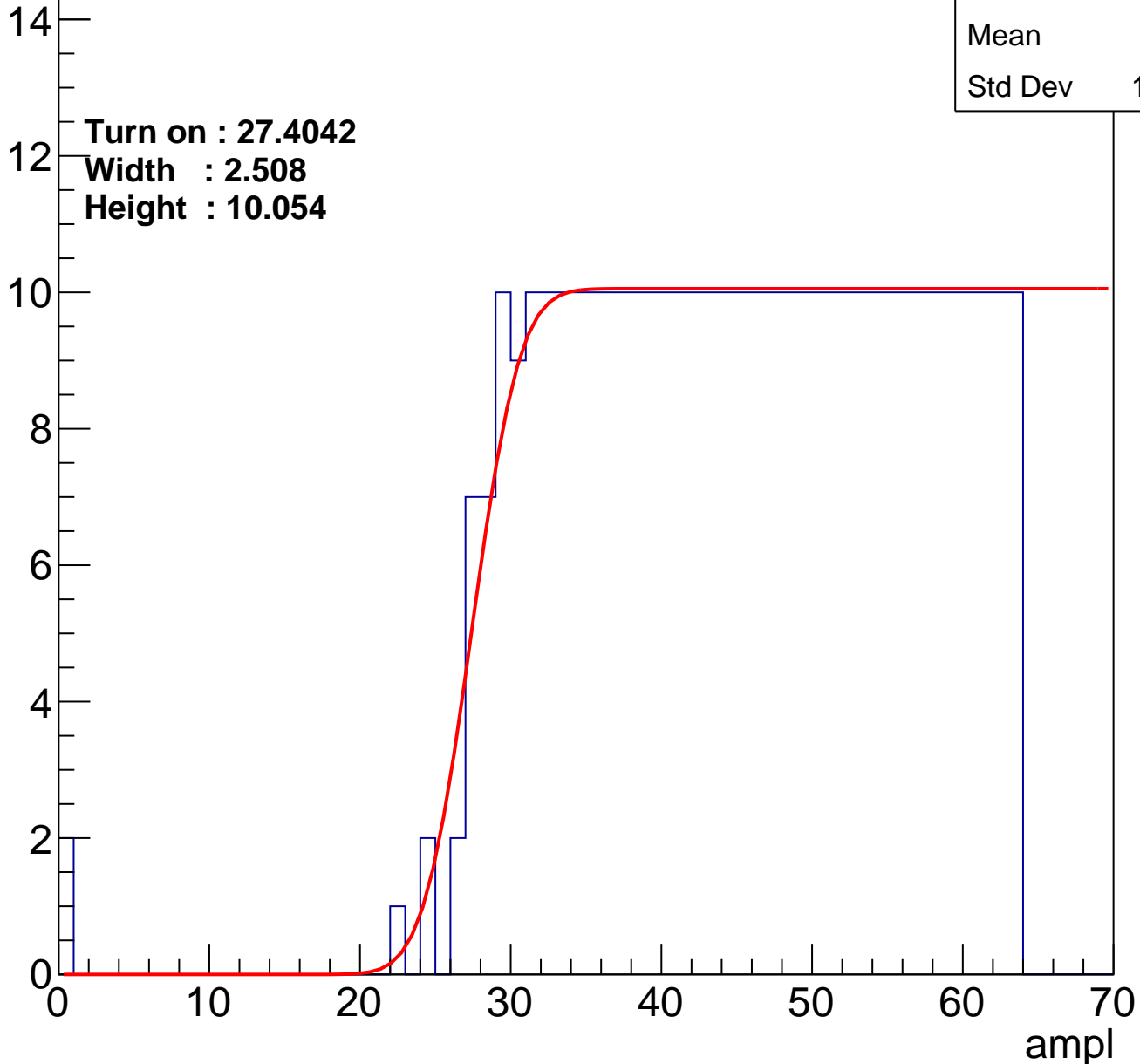
Entries	370
Mean	44.8
Std Dev	11.18

Turn on : 27.4042

Width : 2.508

Height : 10.054

Entry



# B1L003S, U22-ch42

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	388
Mean	43.76
Std Dev	12.03

Turn on : 25.8873

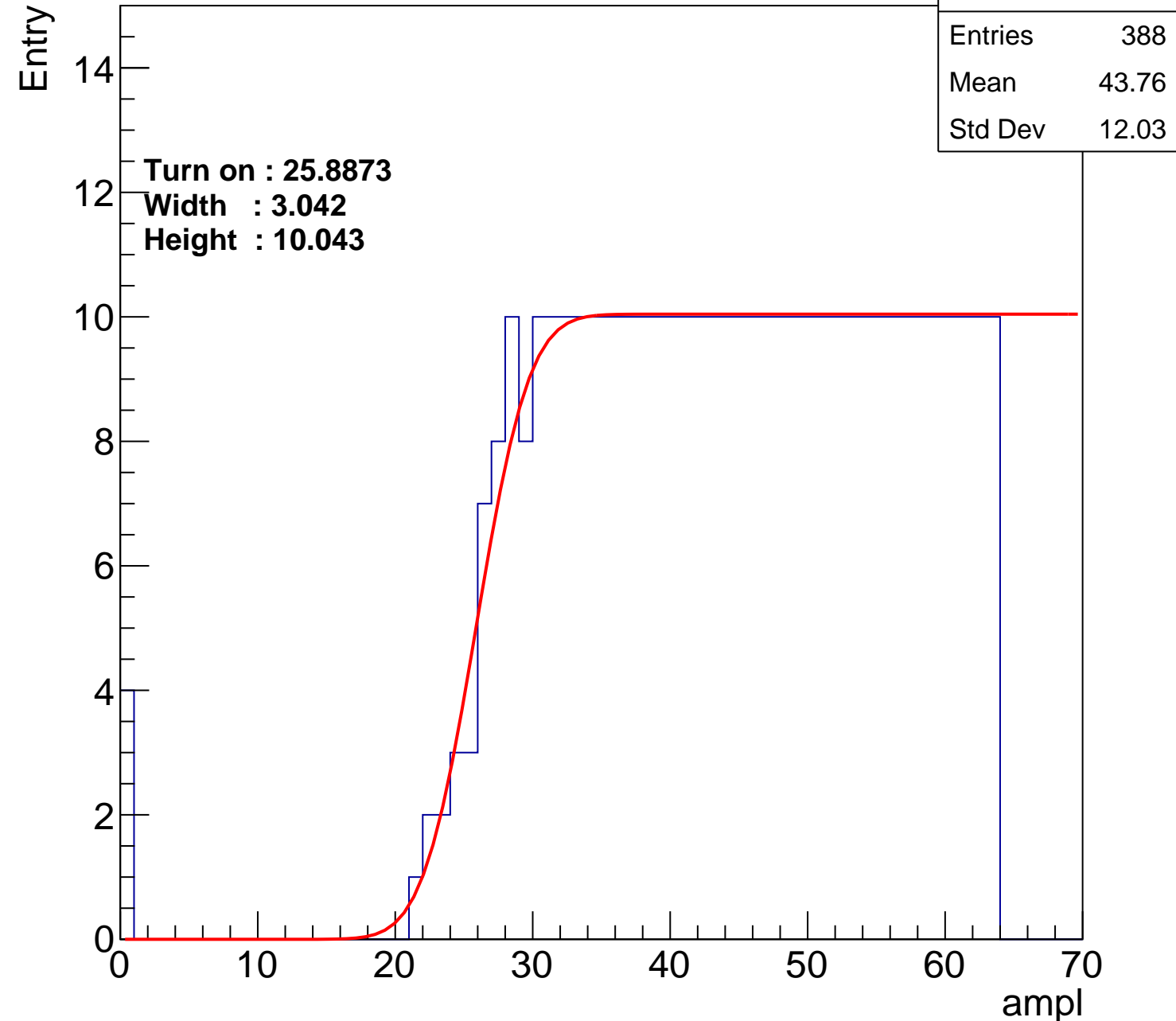
Width : 3.042

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch43

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	367
Mean	44.78
Std Dev	11.55

Turn on : 28.2855

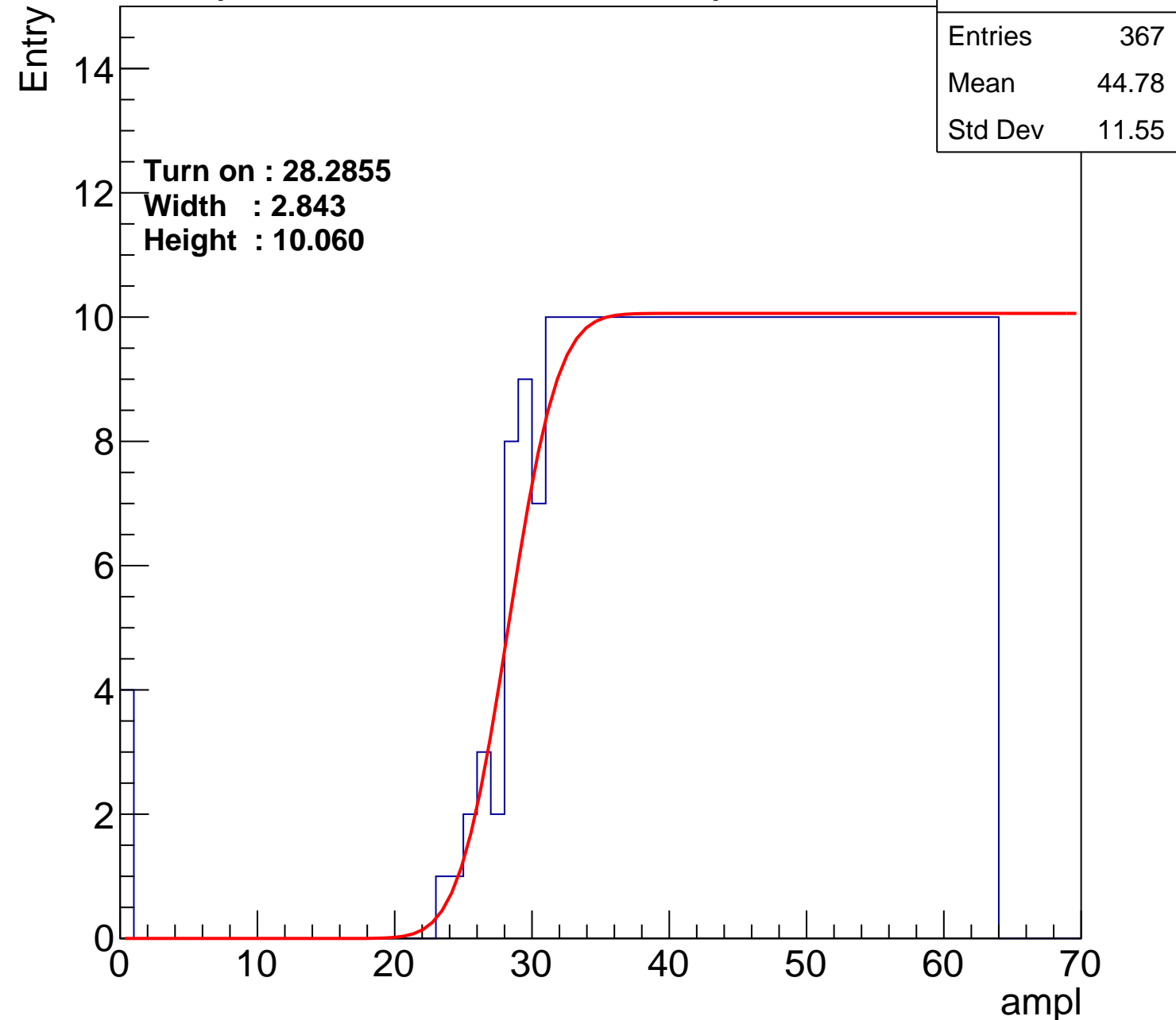
Width : 2.843

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch44

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.7137

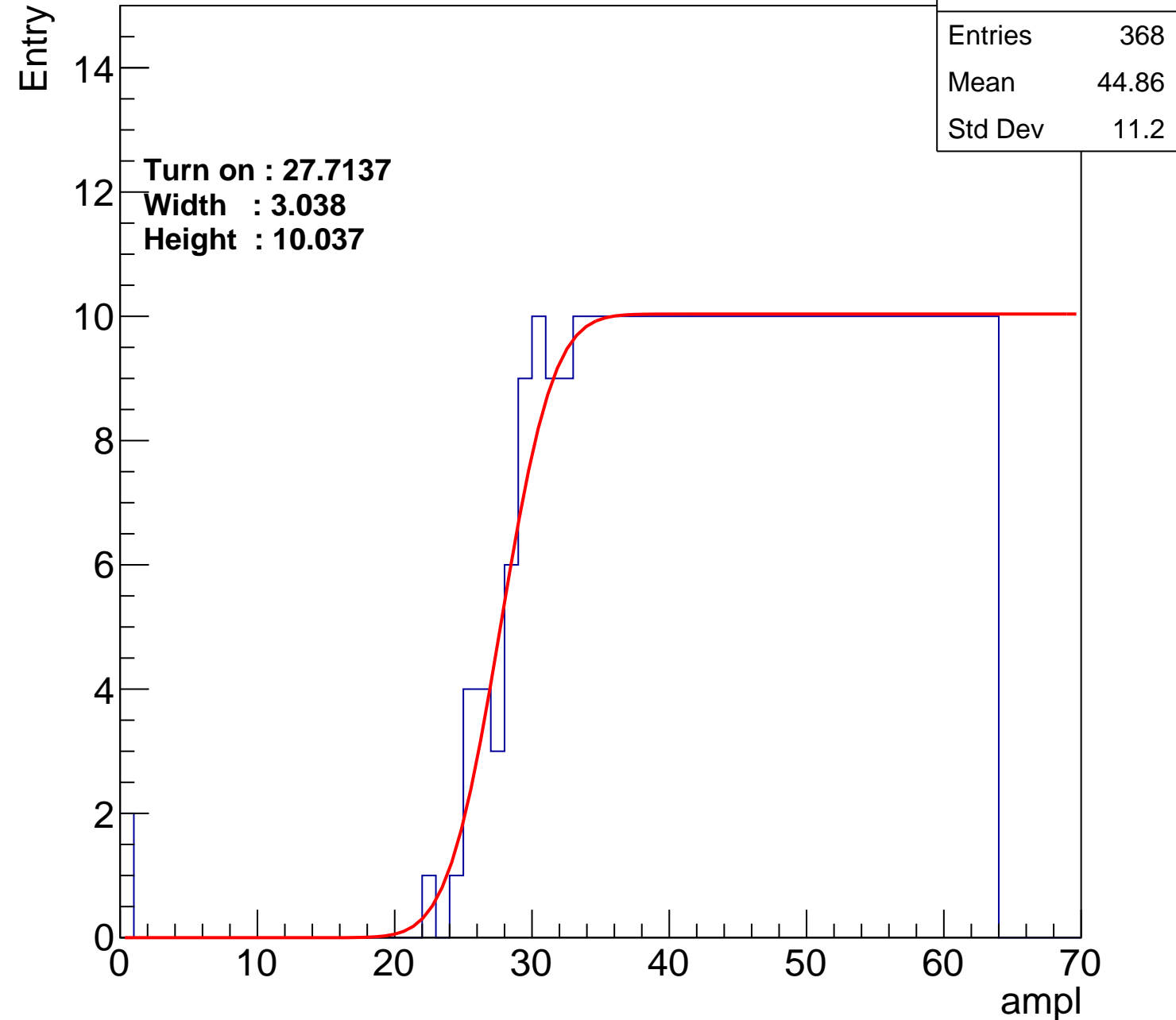
Width : 3.038

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch45

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	389
Mean	43.74
Std Dev	11.94

Turn on : 25.8920

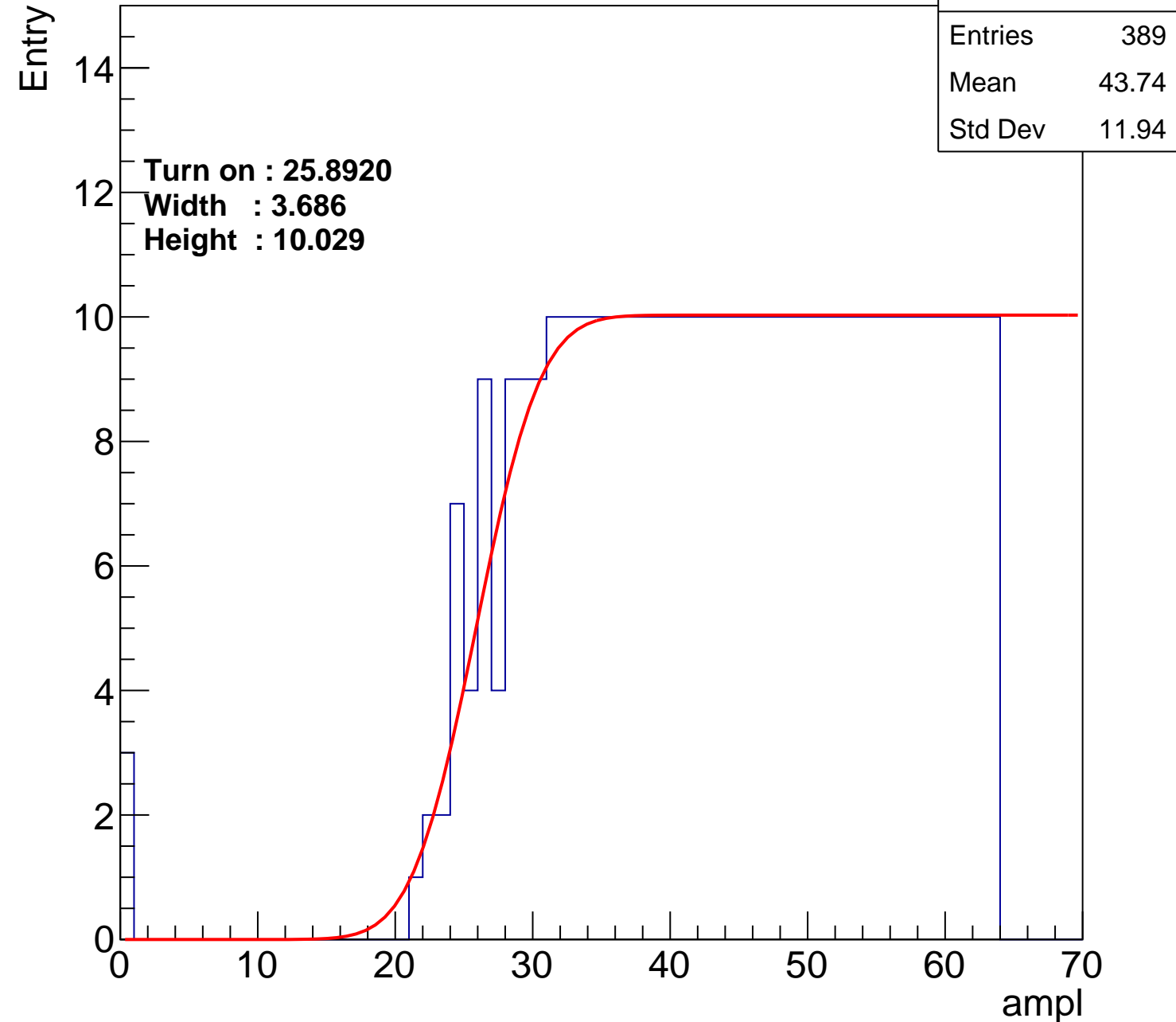
Width : 3.686

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch46

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	363
Mean	44.99
Std Dev	11.44

Turn on : 28.0228

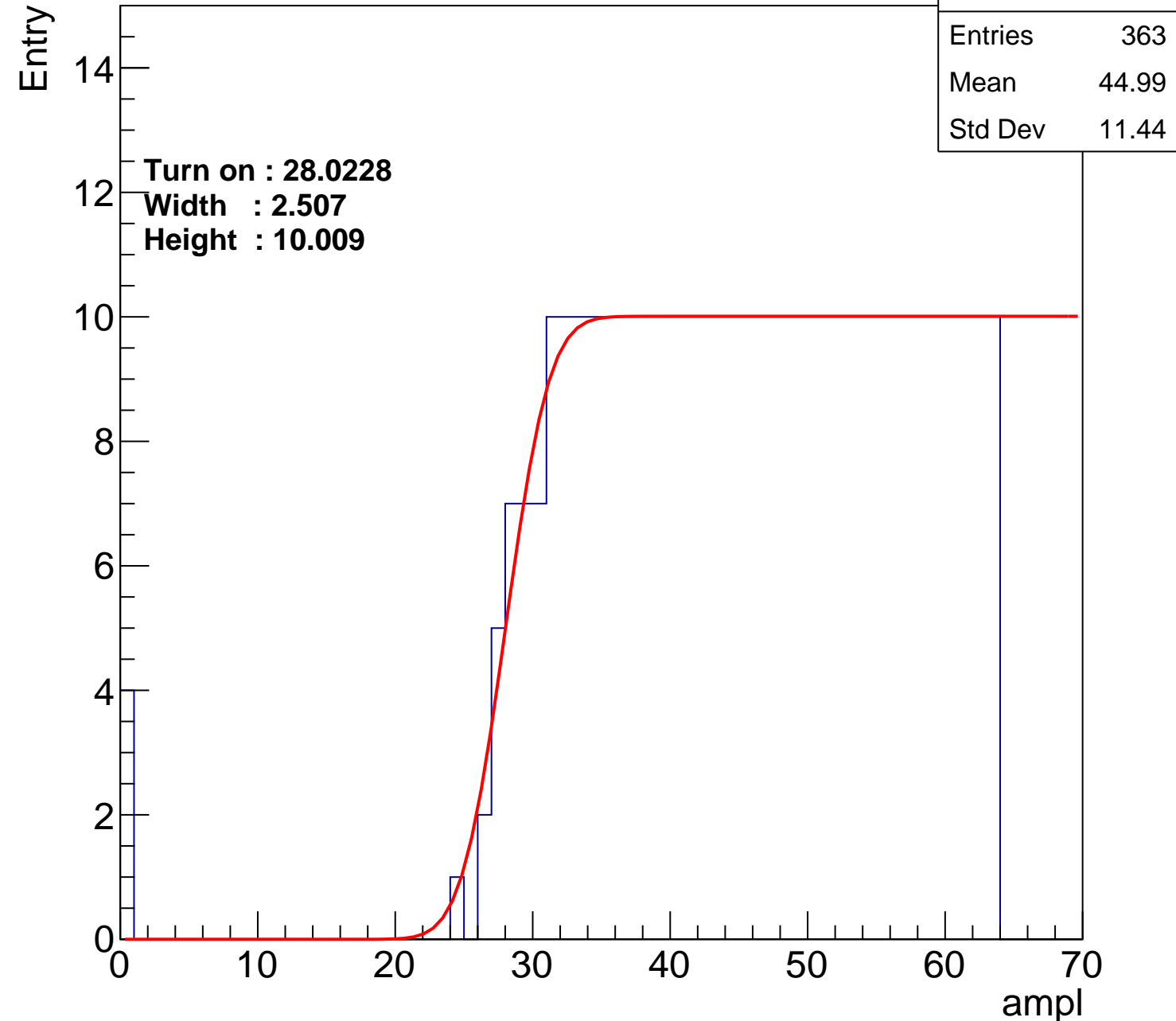
Width : 2.507

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch47

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	395
Mean	43.5
Std Dev	11.93

Turn on : 24.3383

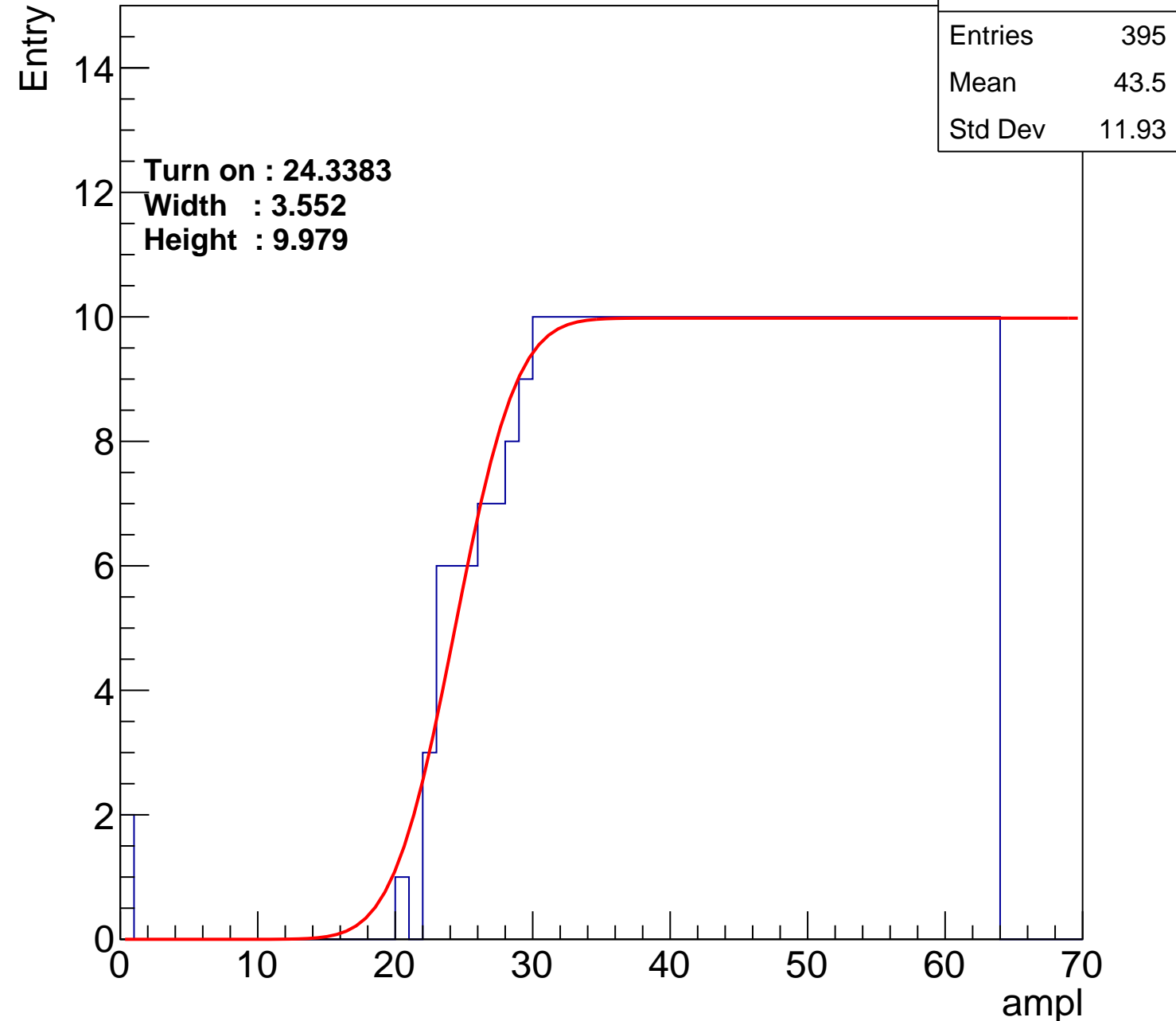
Width : 3.552

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch48

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	365
Mean	44.97
Std Dev	11.19

Turn on : 27.8317

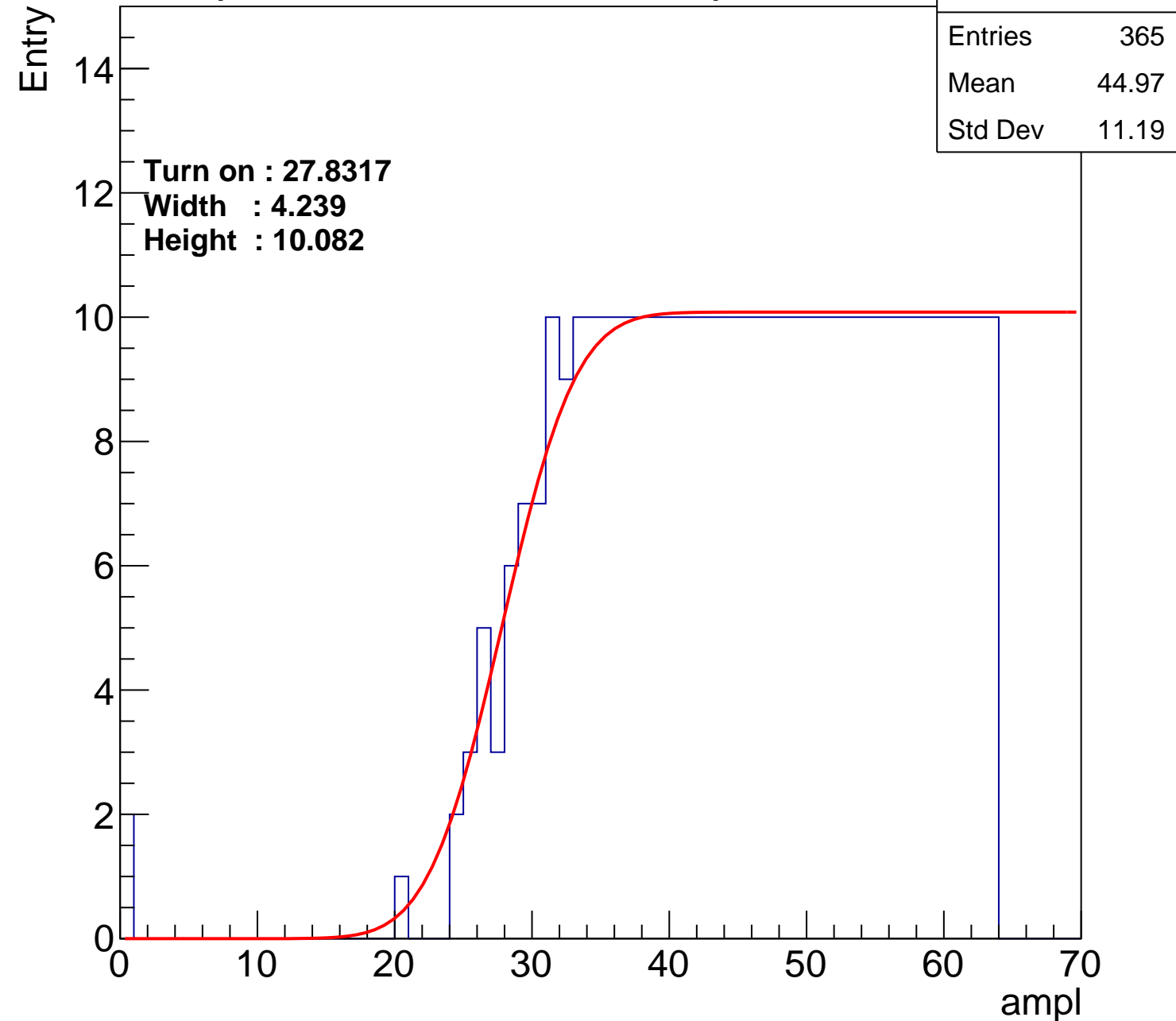
Width : 4.239

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch49

calib\_packv5\_042523\_0143.root, FC#13, port D2

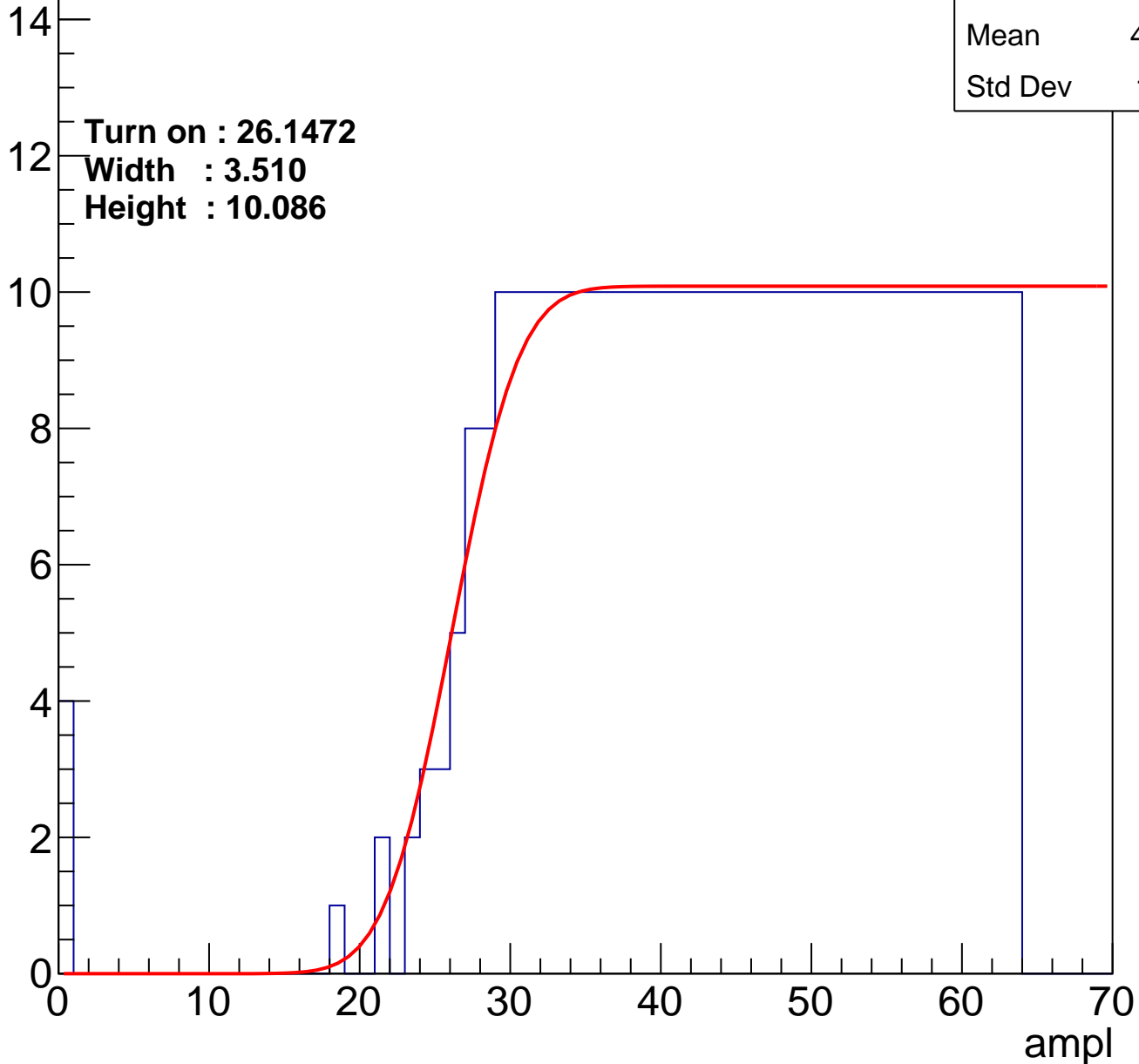
Entries	386
Mean	43.84
Std Dev	12.01

Turn on : 26.1472

Width : 3.510

Height : 10.086

Entry



# B1L003S, U22-ch50

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	357
Mean	45.17
Std Dev	11.56

Turn on : 29.0571

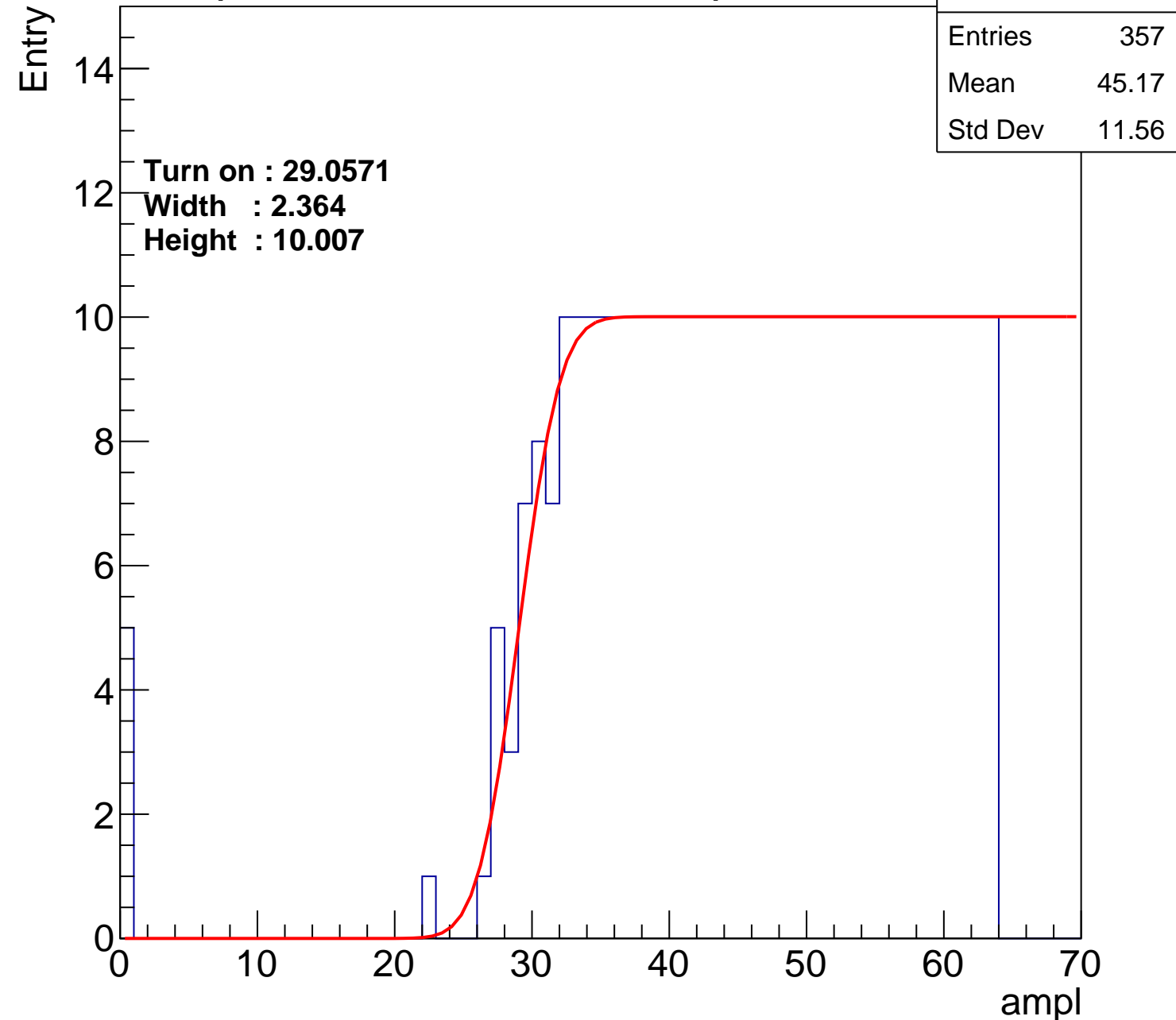
Width : 2.364

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch51

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	360
Mean	45.07
Std Dev	11.46

Turn on : 28.5969

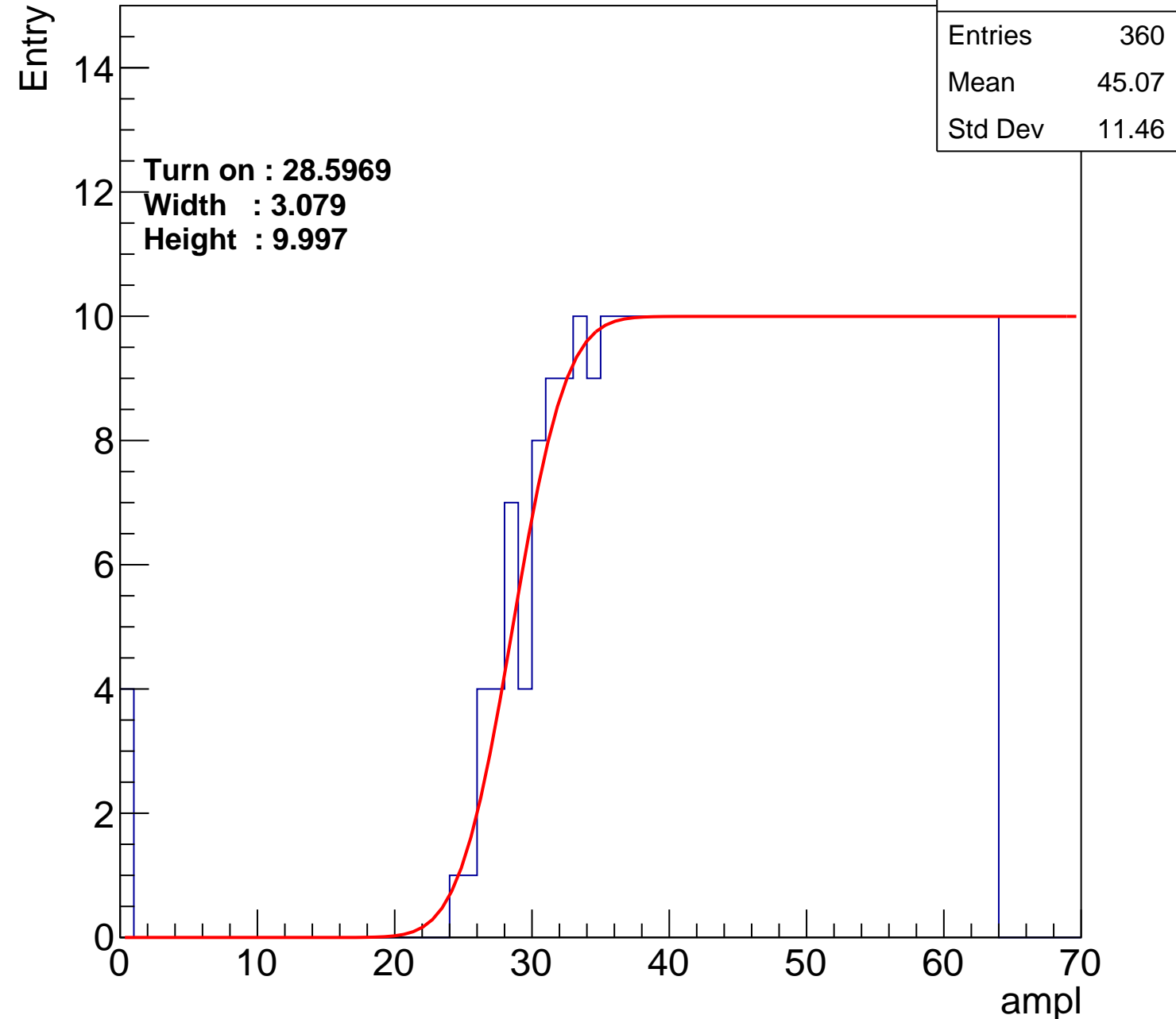
Width : 3.079

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch52

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 26.1258

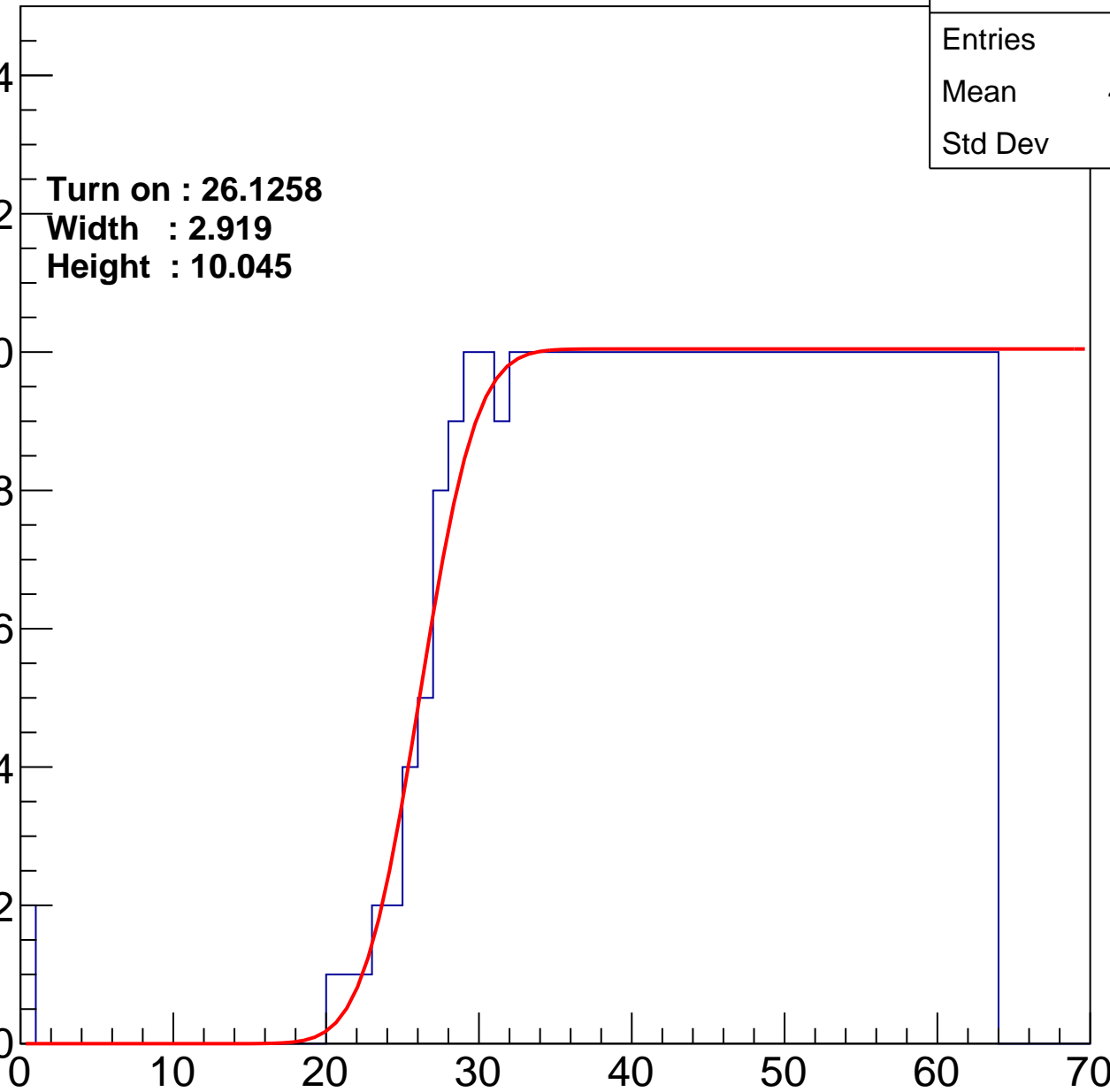
Width : 2.919

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch53

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	360
Mean	45.17
Std Dev	11.23

**Turn on : 28.2565**

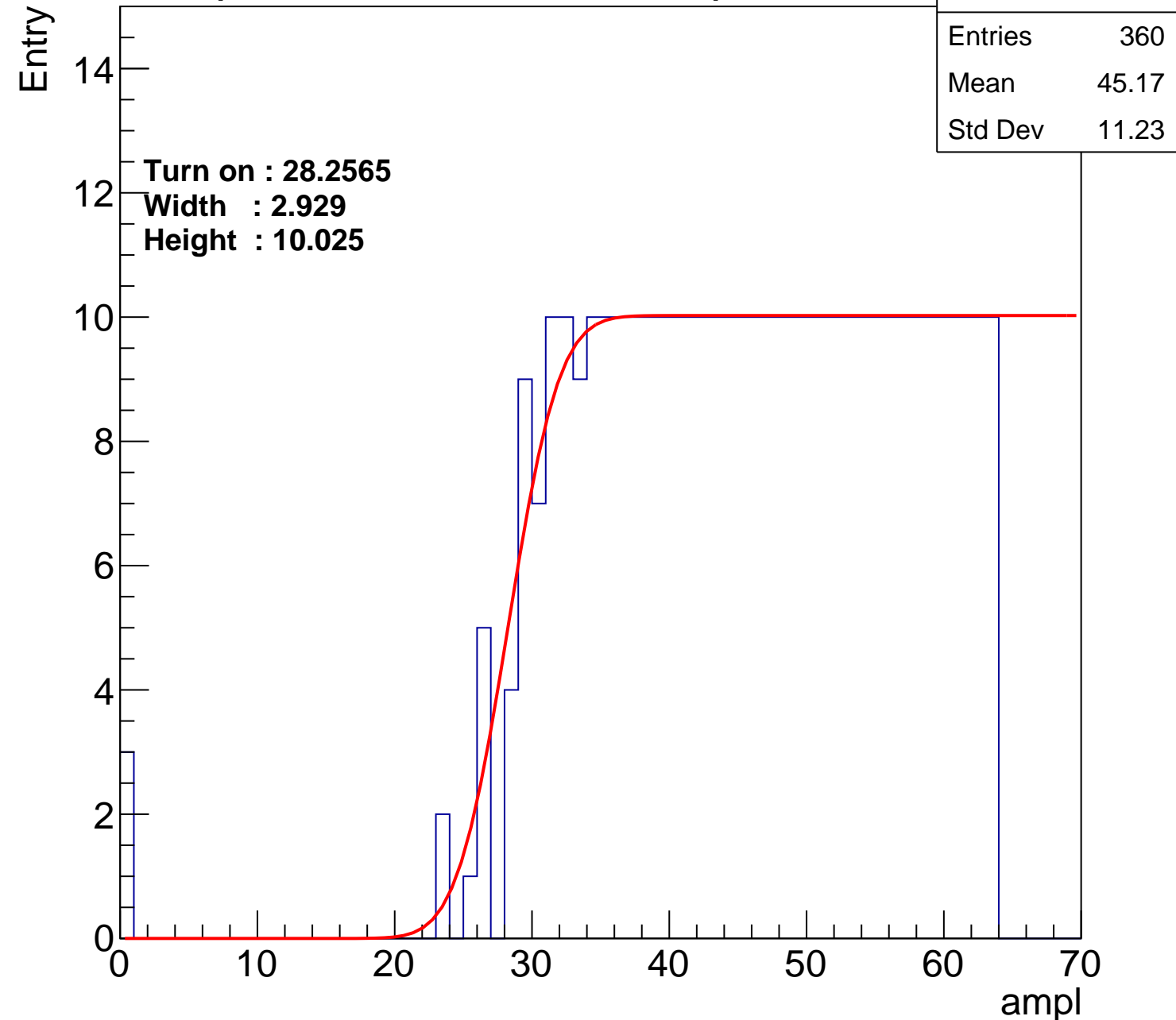
**Width : 2.929**

**Height : 10.025**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch54

calib\_packv5\_042523\_0143.root, FC#13, port D2

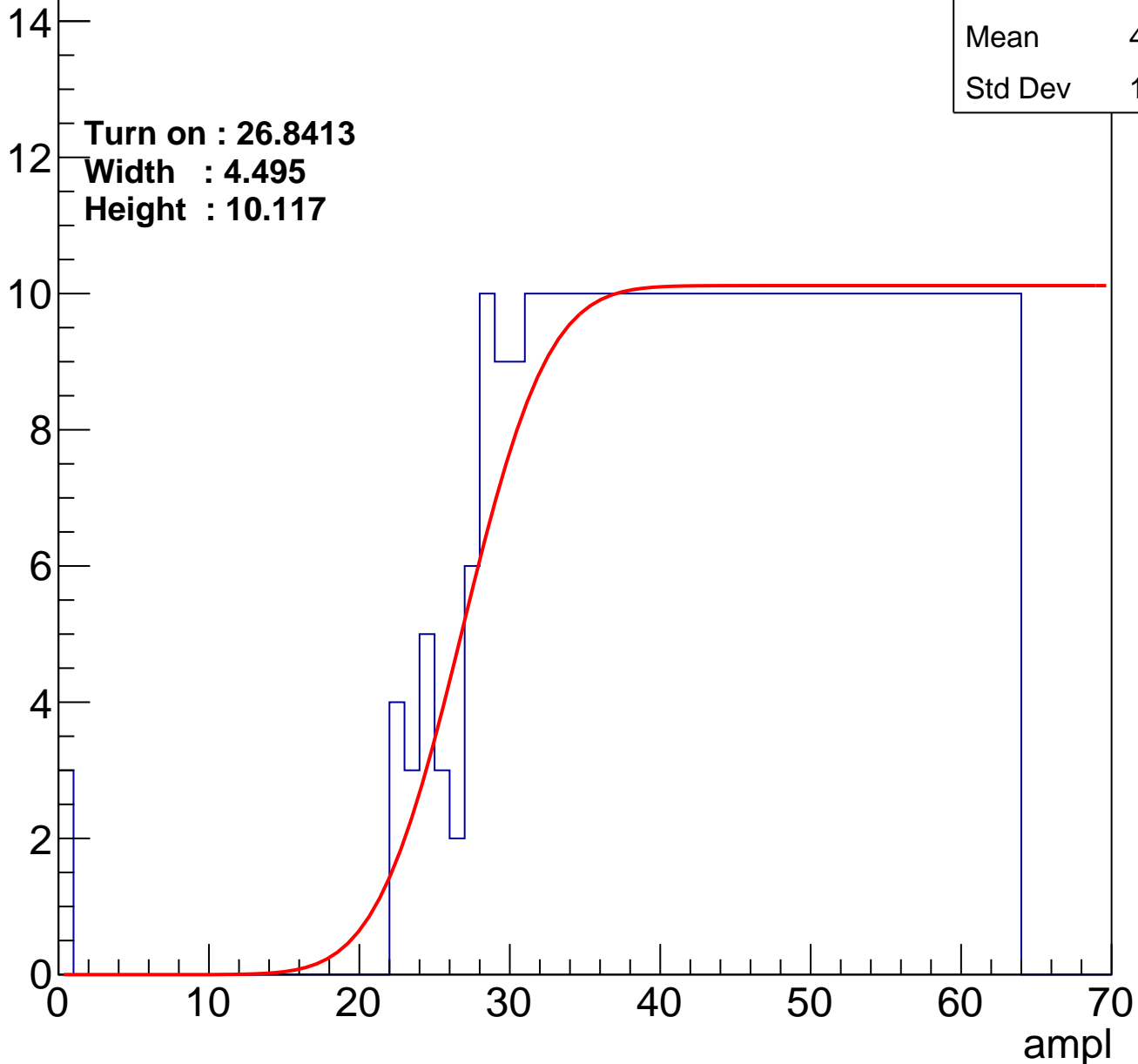
Entries	384
Mean	43.98
Std Dev	11.83

Turn on : 26.8413

Width : 4.495

Height : 10.117

Entry





# B1L003S, U22-ch55

calib\_packv5\_042523\_0143.root, FC#13, port D2

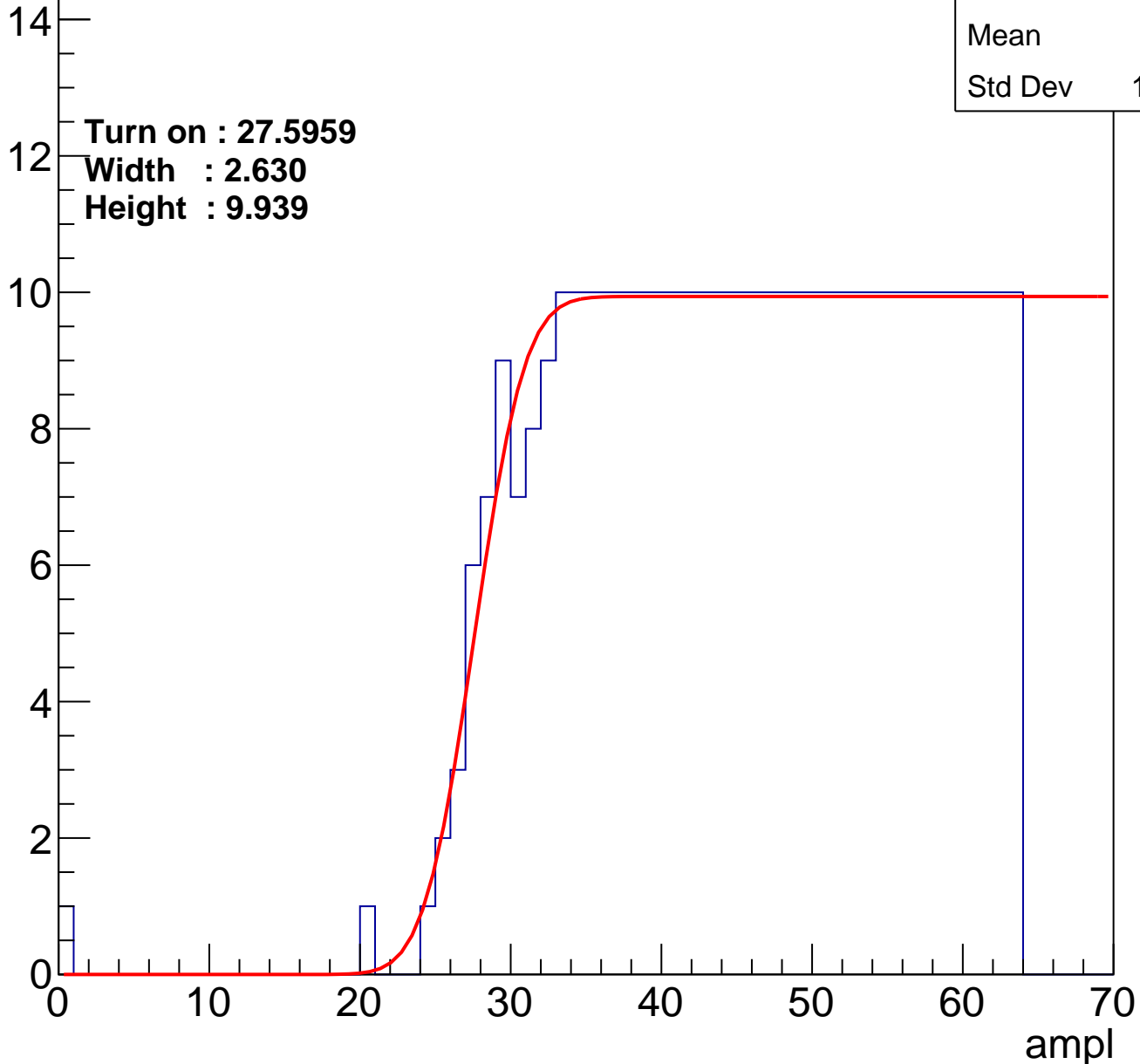
Entries	364
Mean	45.1
Std Dev	10.93

Turn on : 27.5959

Width : 2.630

Height : 9.939

Entry



# B1L003S, U22-ch56

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	377
Mean	44.31
Std Dev	11.67

Turn on : 26.9524

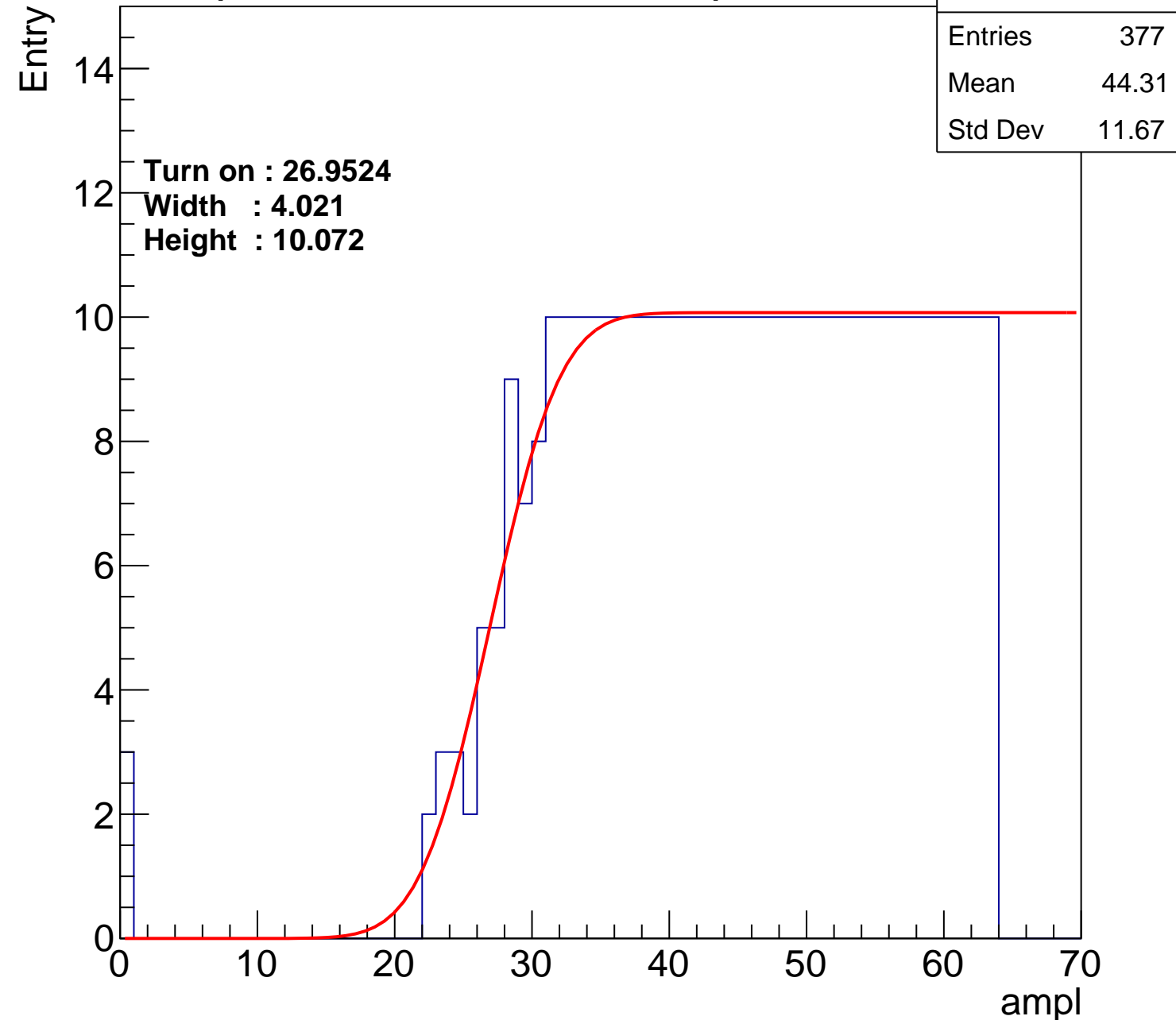
Width : 4.021

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch57

calib\_packv5\_042523\_0143.root, FC#13, port D2

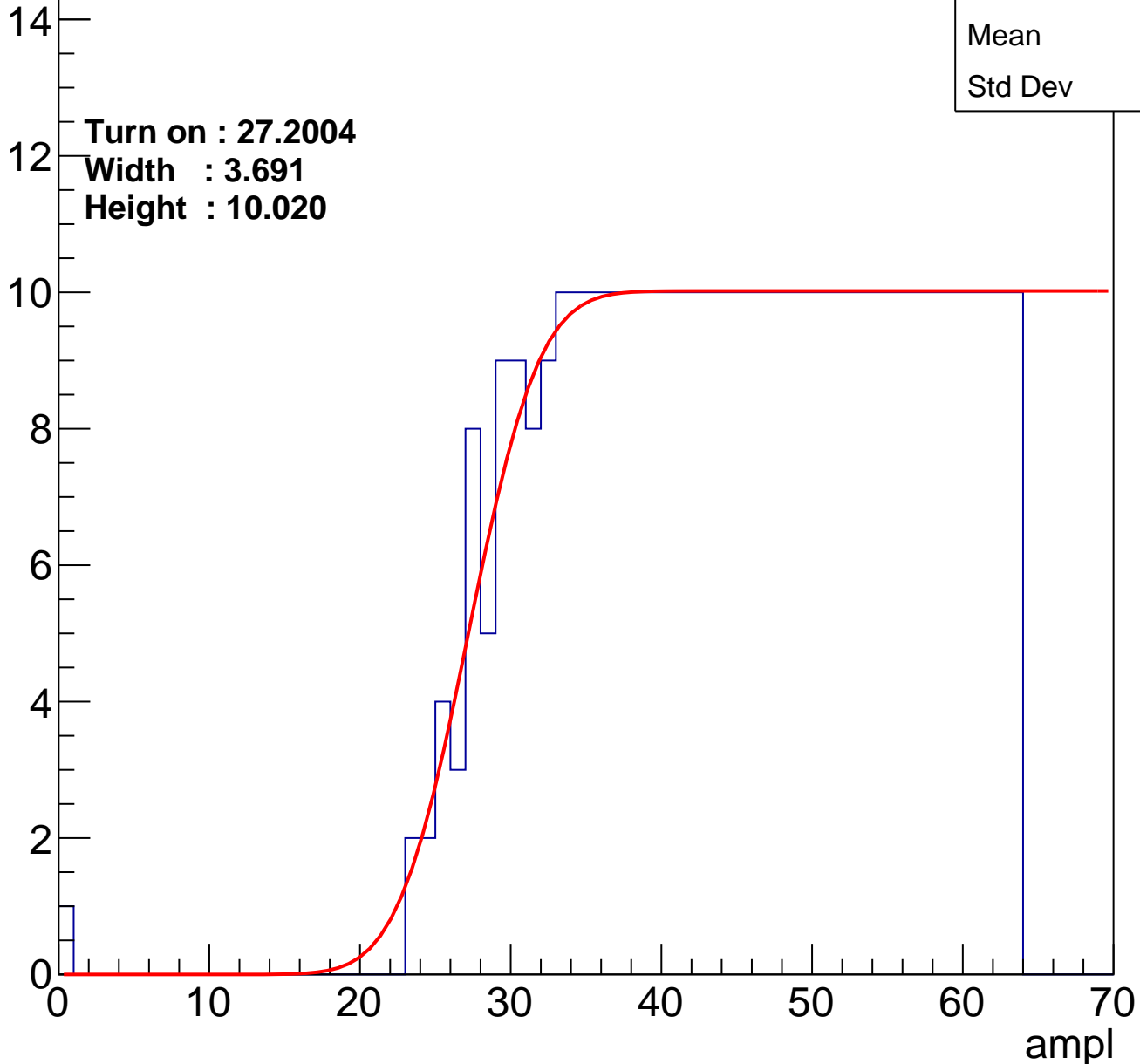
Entries	370
Mean	44.8
Std Dev	11.1

Turn on : 27.2004

Width : 3.691

Height : 10.020

Entry



# B1L003S, U22-ch58

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	390
Mean	43.84
Std Dev	11.6

Turn on : 25.5511

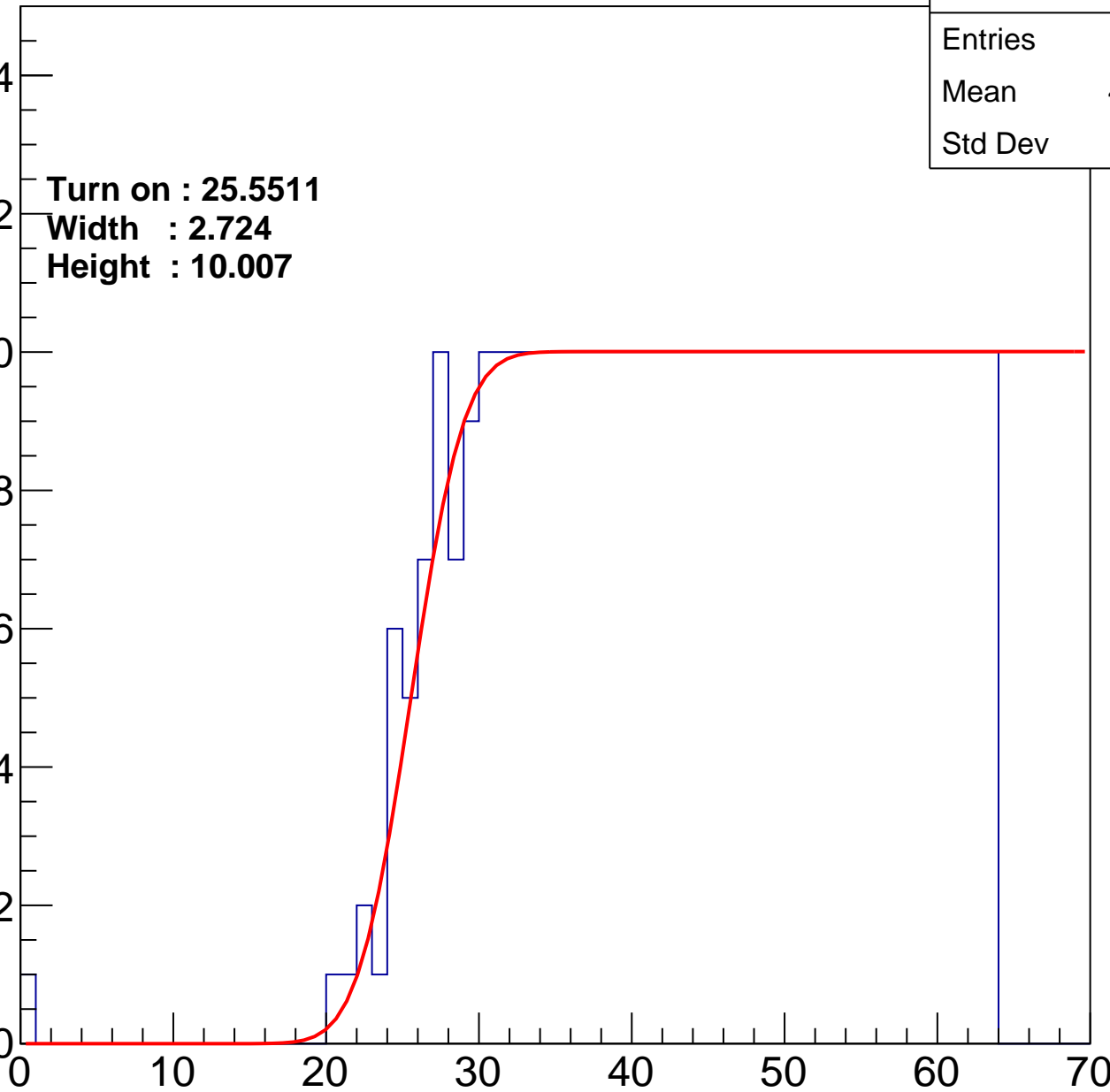
Width : 2.724

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch59

calib\_packv5\_042523\_0143.root, FC#13, port D2

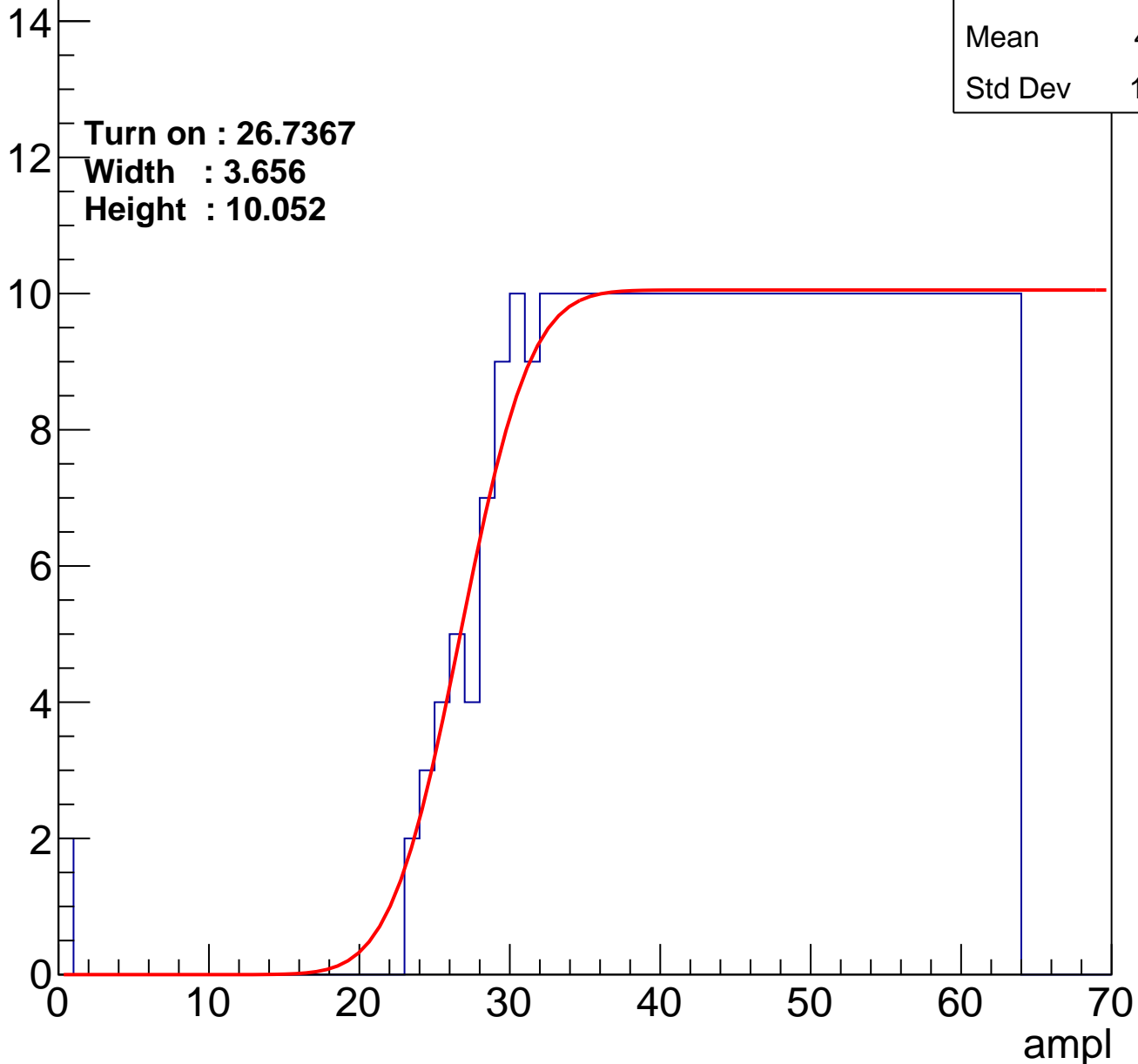
Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.7367

Width : 3.656

Height : 10.052

Entry



# B1L003S, U22-ch60

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	389
Mean	43.73
Std Dev	11.96

Turn on : 25.8901

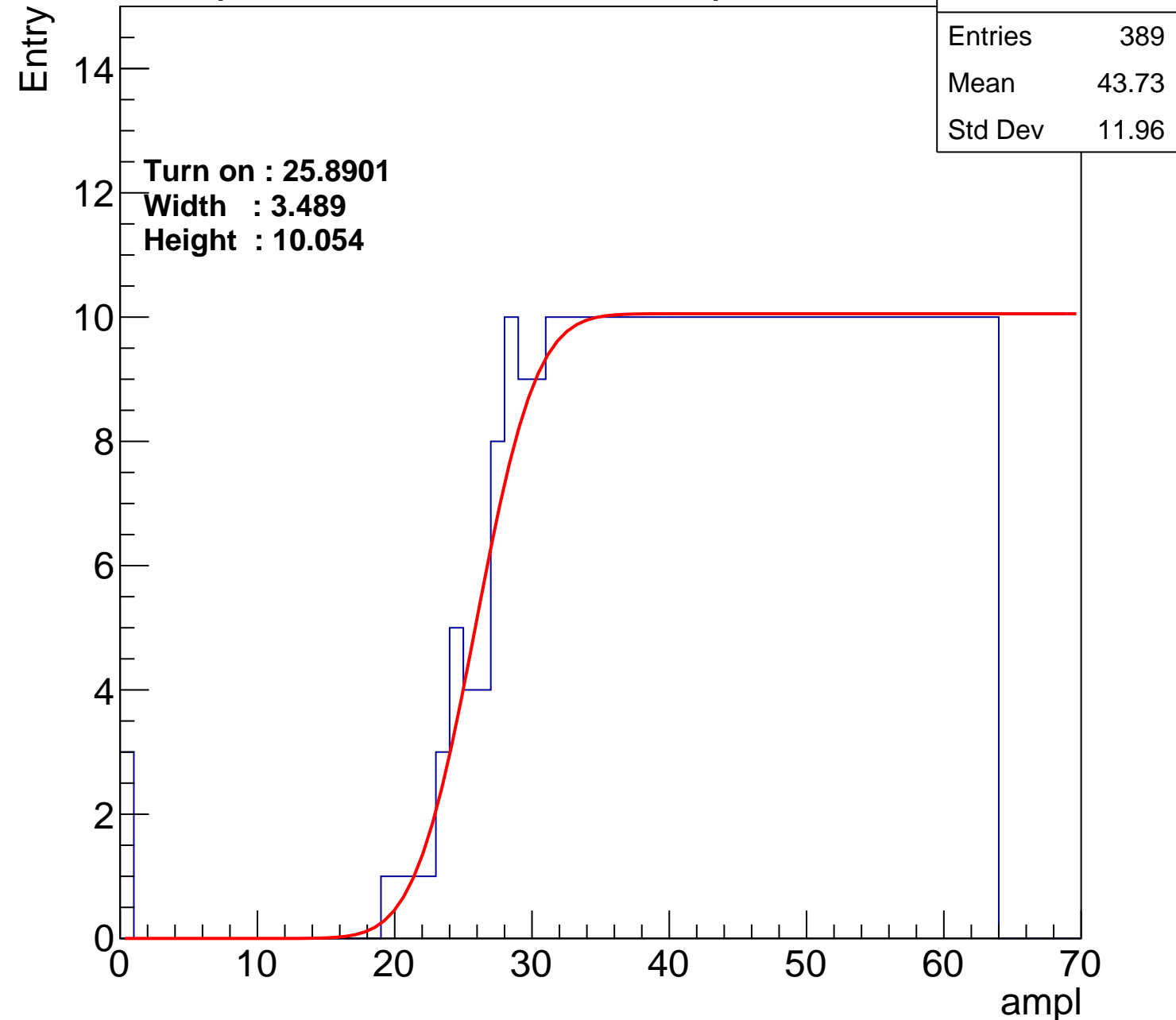
Width : 3.489

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch61

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	351
Mean	45.68
Std Dev	10.81

Turn on : 29.7071

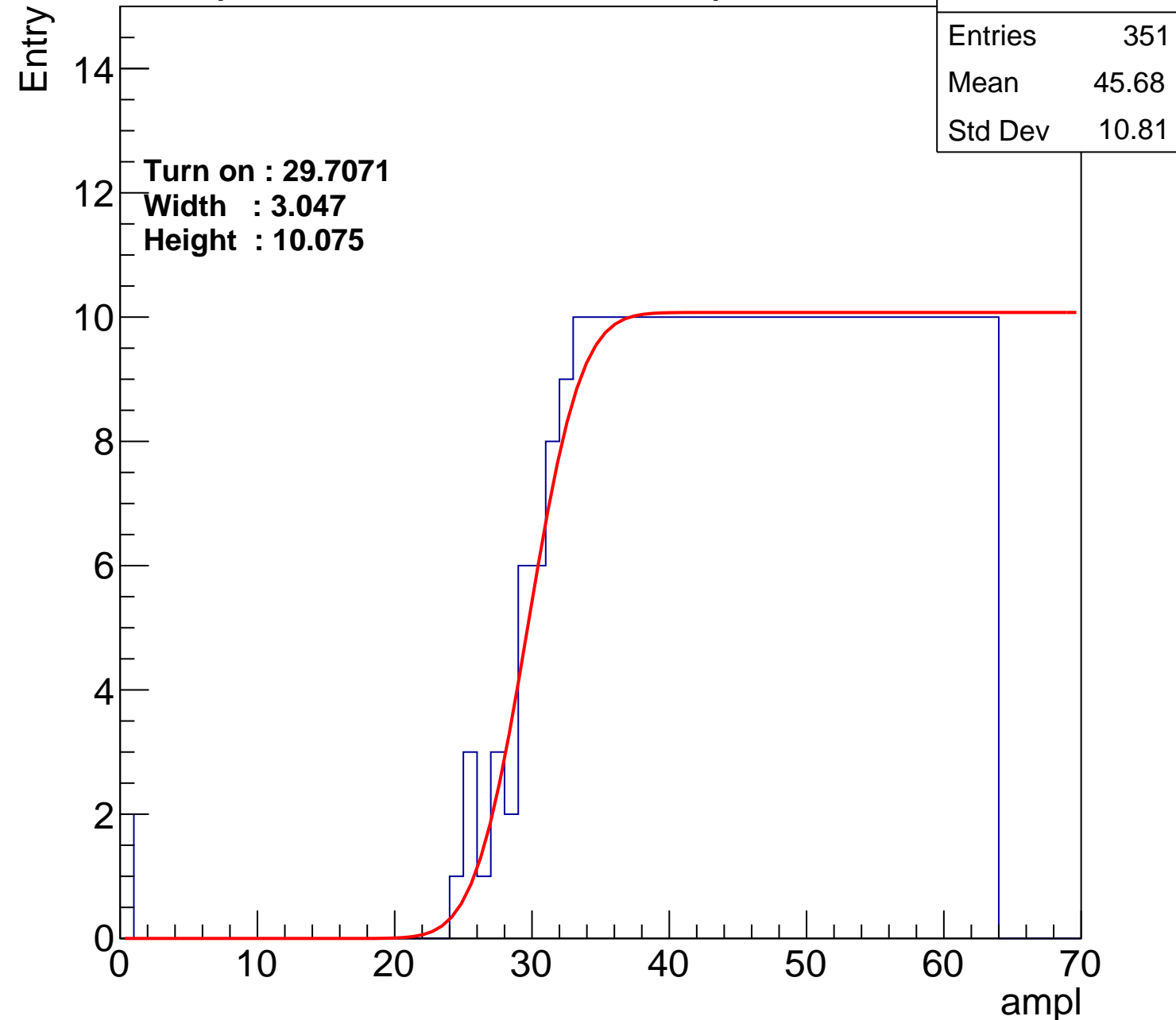
Width : 3.047

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch62

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	392
Mean	43.64
Std Dev	11.89

Turn on : 24.9326

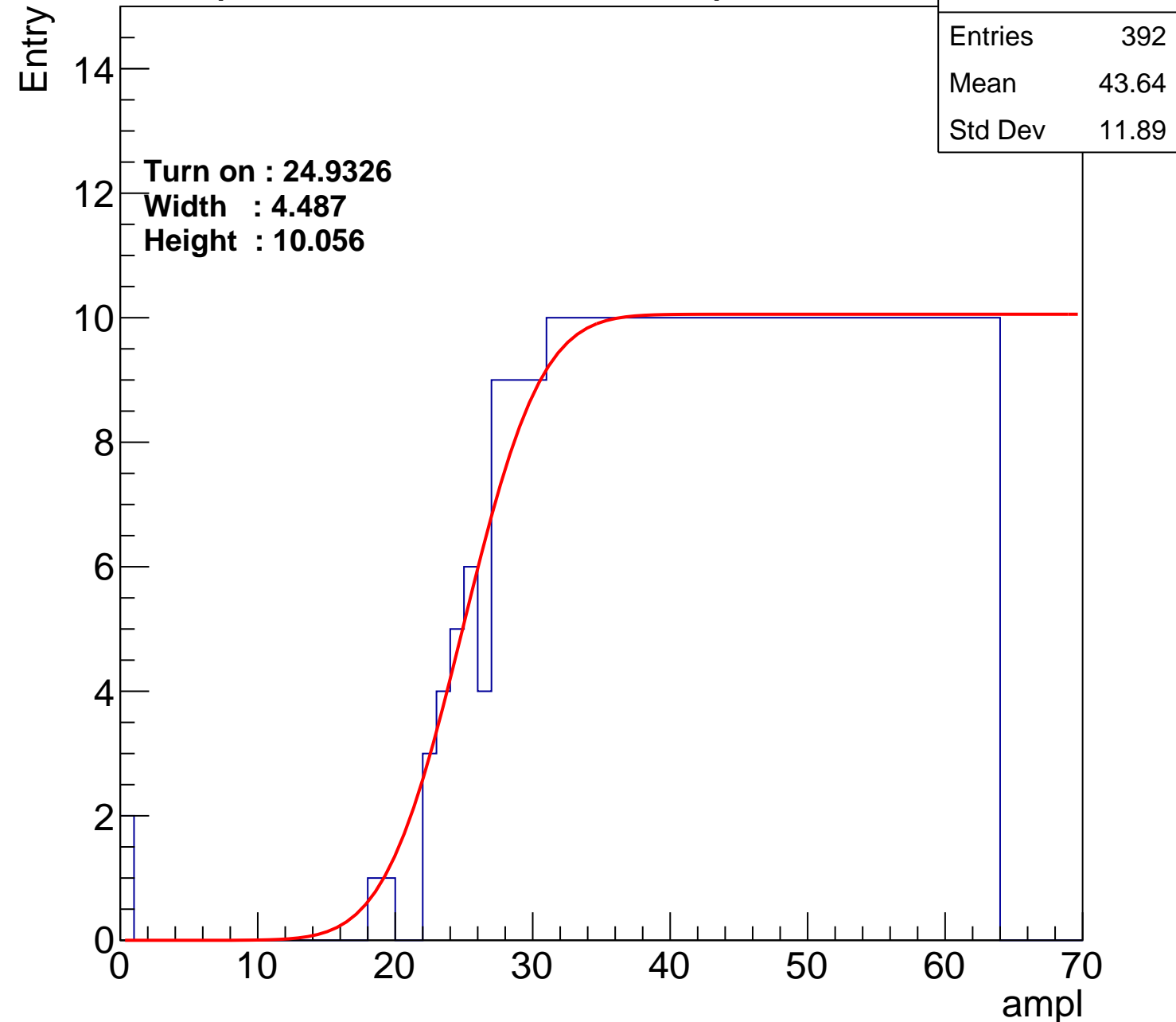
Width : 4.487

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch63

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	368
Mean	44.77
Std Dev	11.43

Turn on : 27.8253

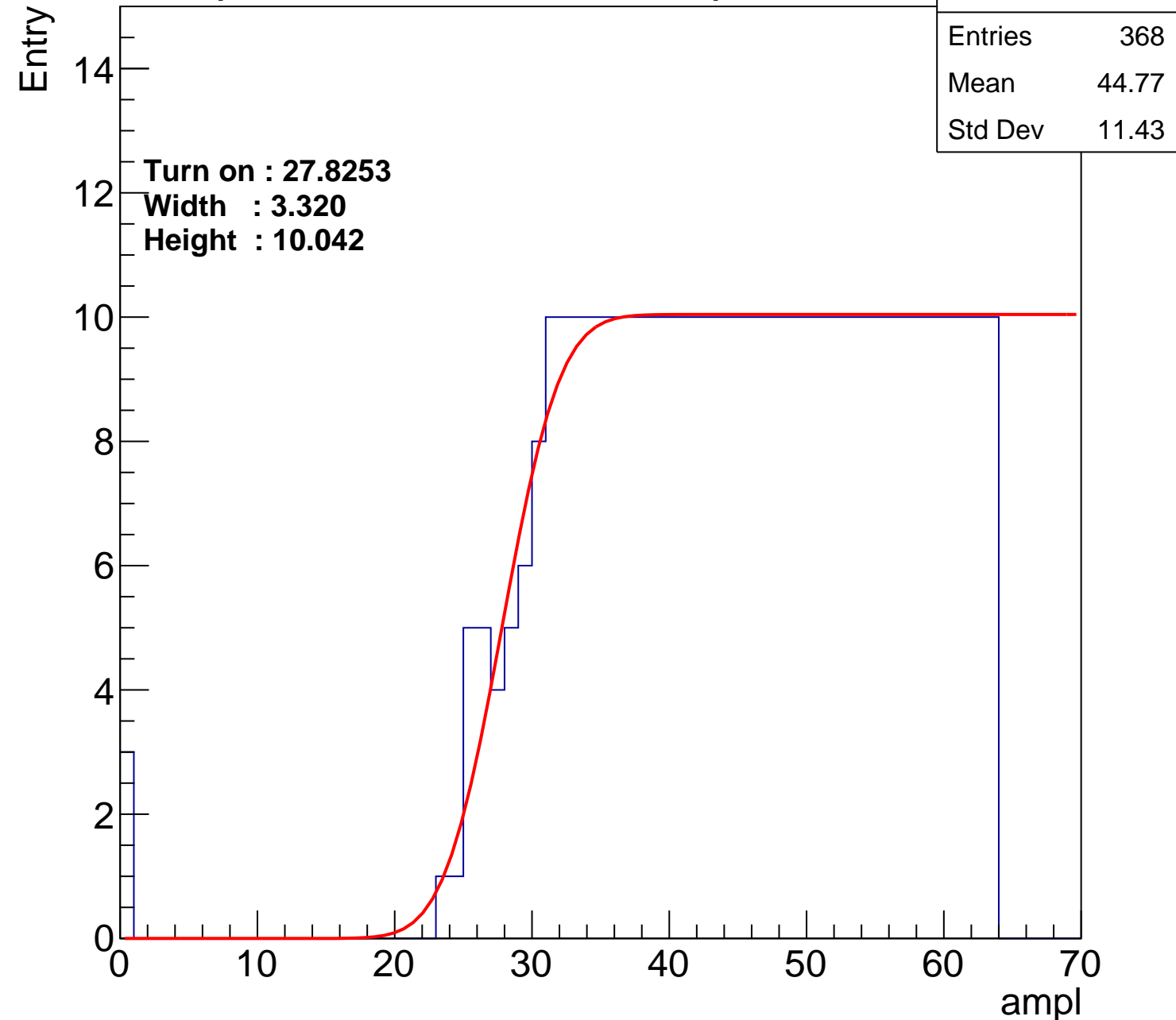
Width : 3.320

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch64

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	386
Mean	43.92
Std Dev	11.82

Turn on : 25.8308

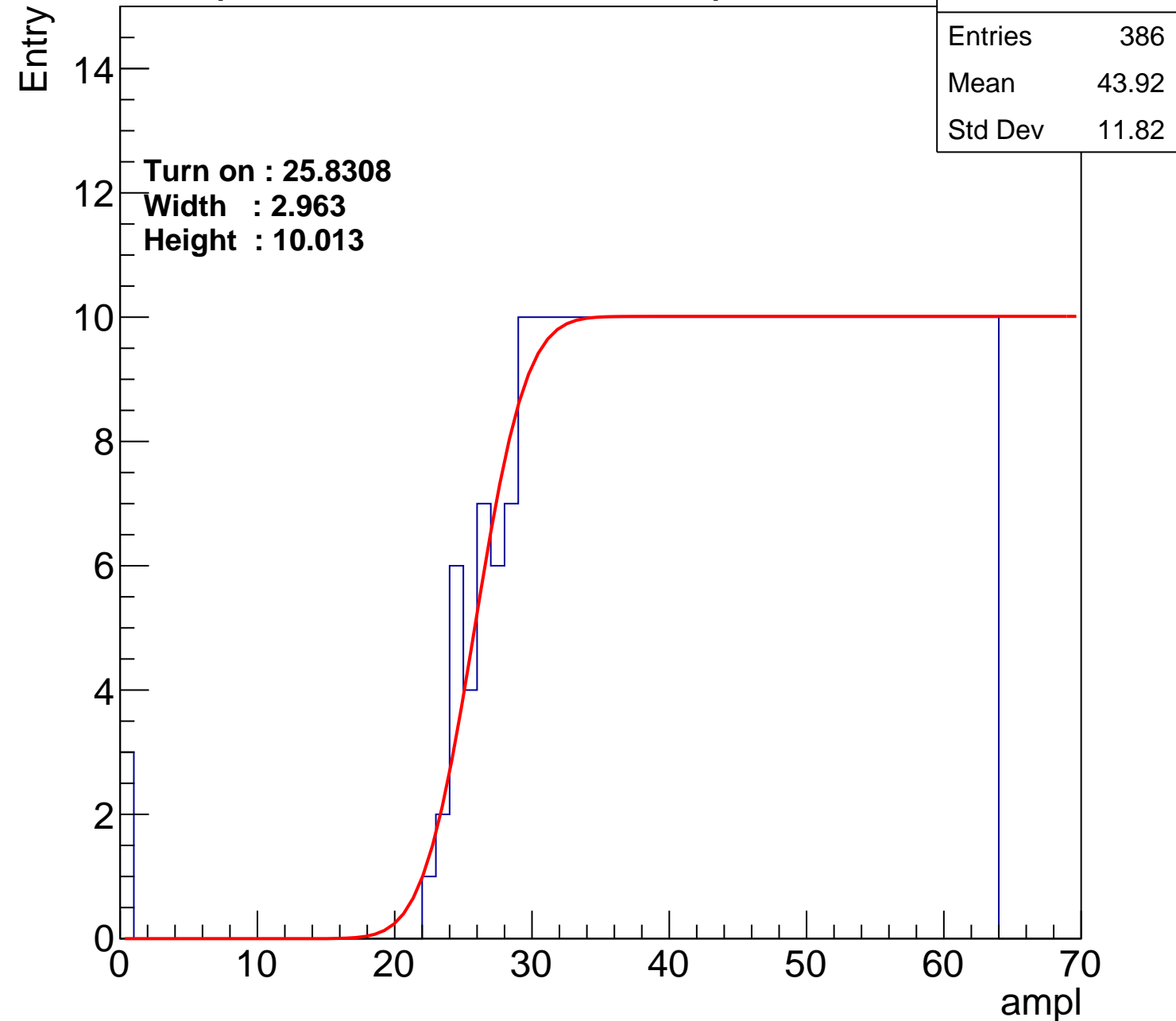
Width : 2.963

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch65

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	379
Mean	44.32
Std Dev	11.46

Turn on : 26.1347

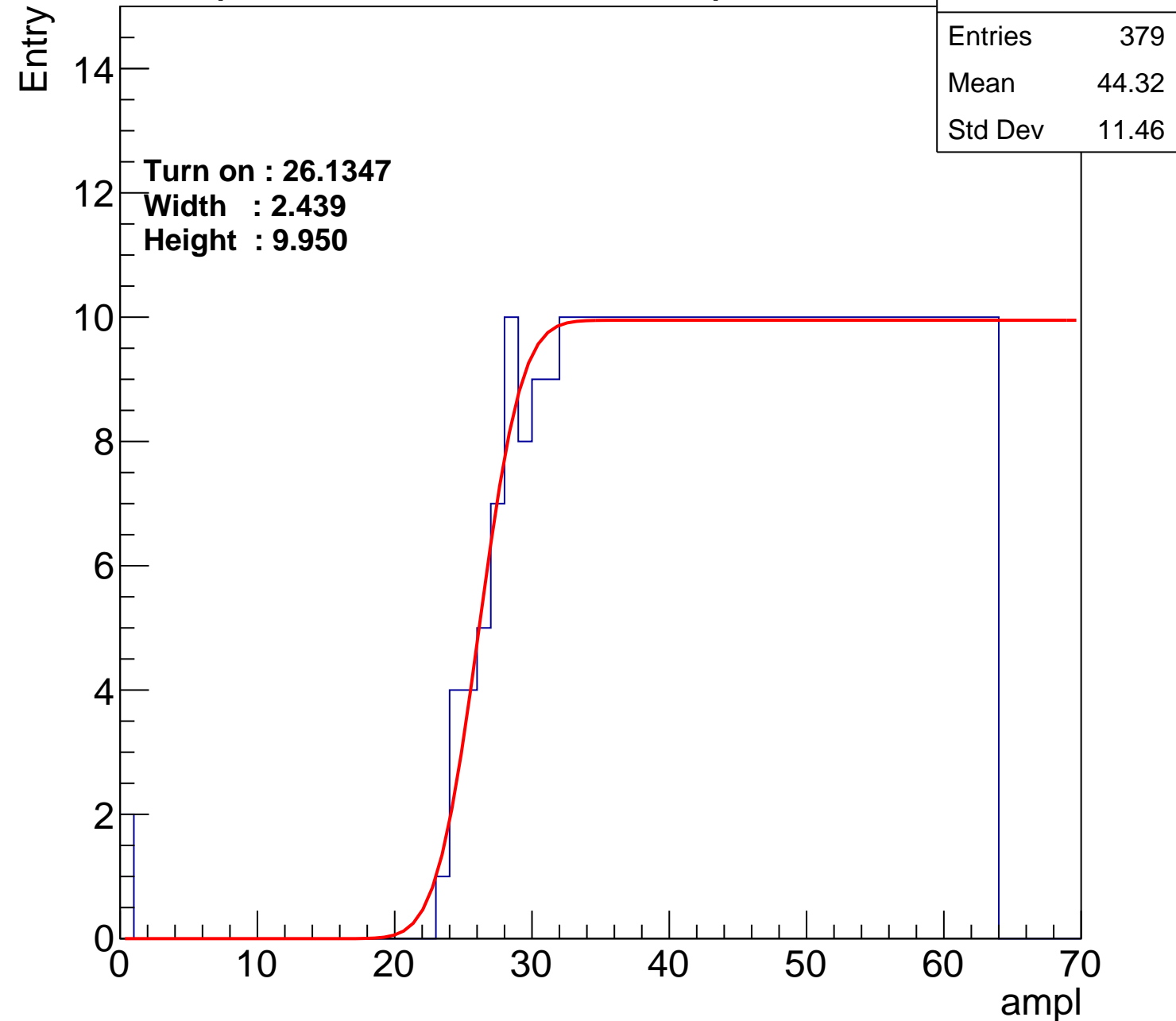
Width : 2.439

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch66

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.2601

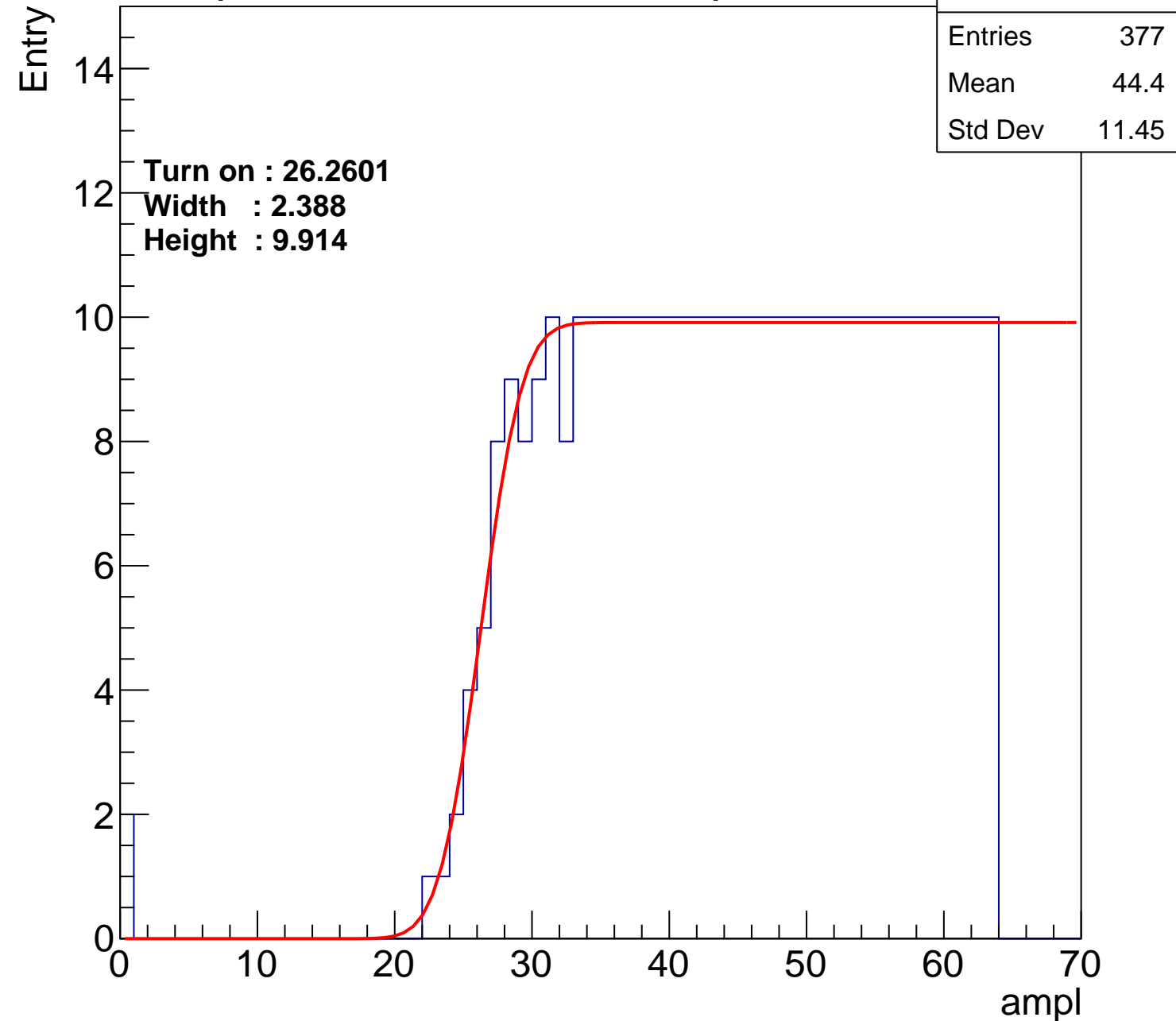
Width : 2.388

Height : 9.914

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch67

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.0704

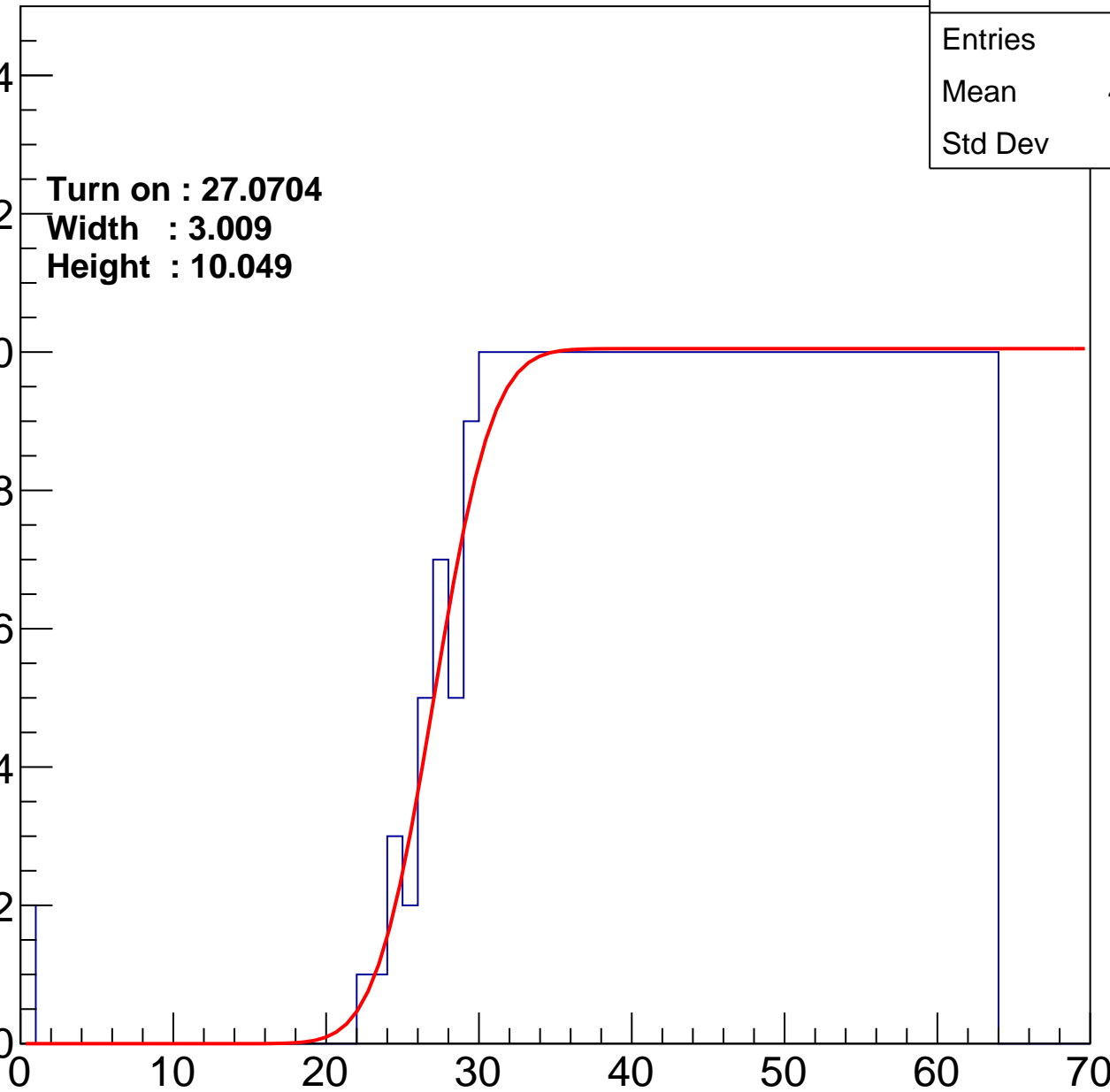
Width : 3.009

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch68

calib\_packv5\_042523\_0143.root, FC#13, port D2

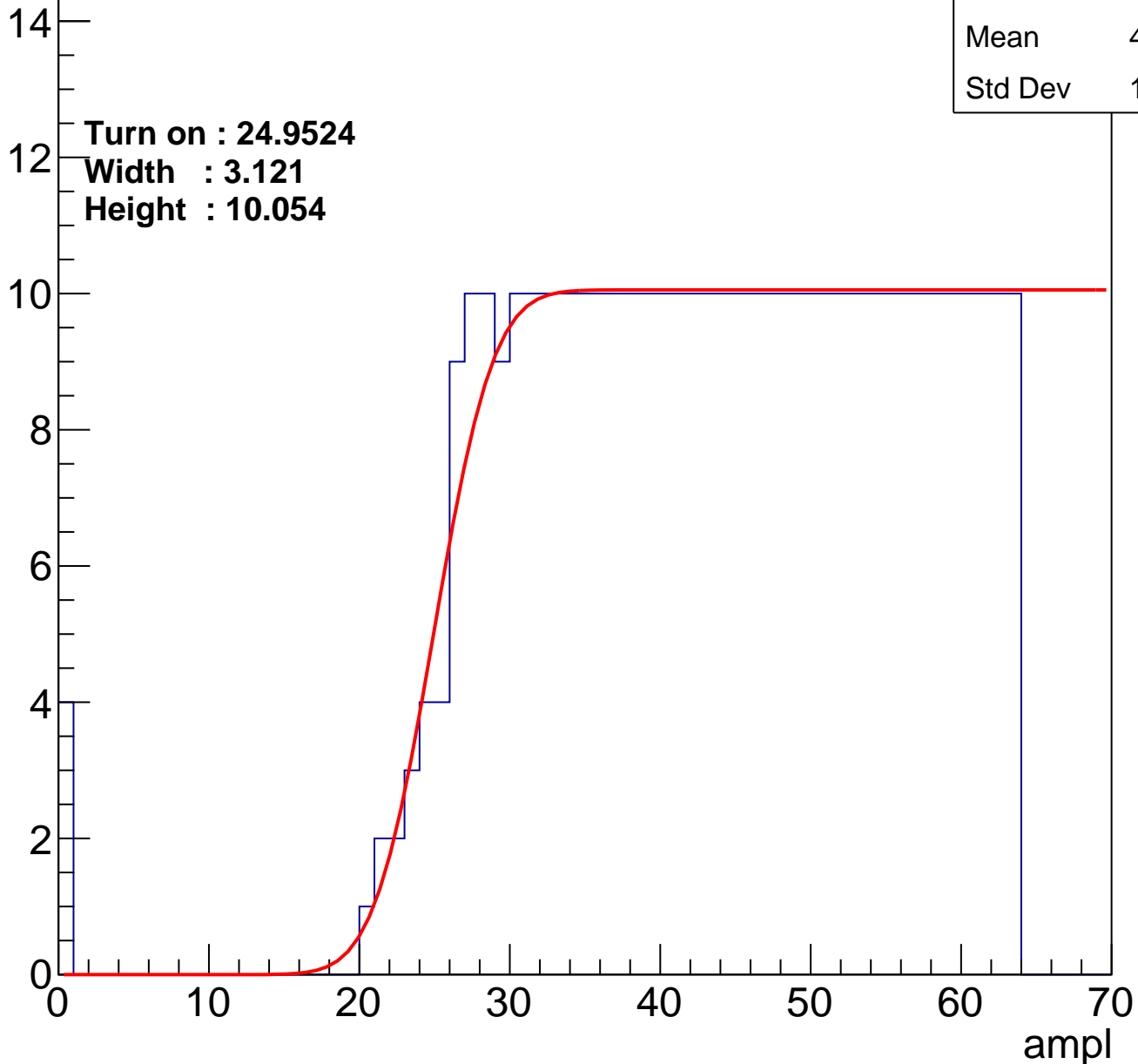
Entries	398
Mean	43.28
Std Dev	12.25

Turn on : 24.9524

Width : 3.121

Height : 10.054

Entry



# B1L003S, U22-ch69

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	368
Mean	44.8
Std Dev	11.37

Turn on : 27.7639

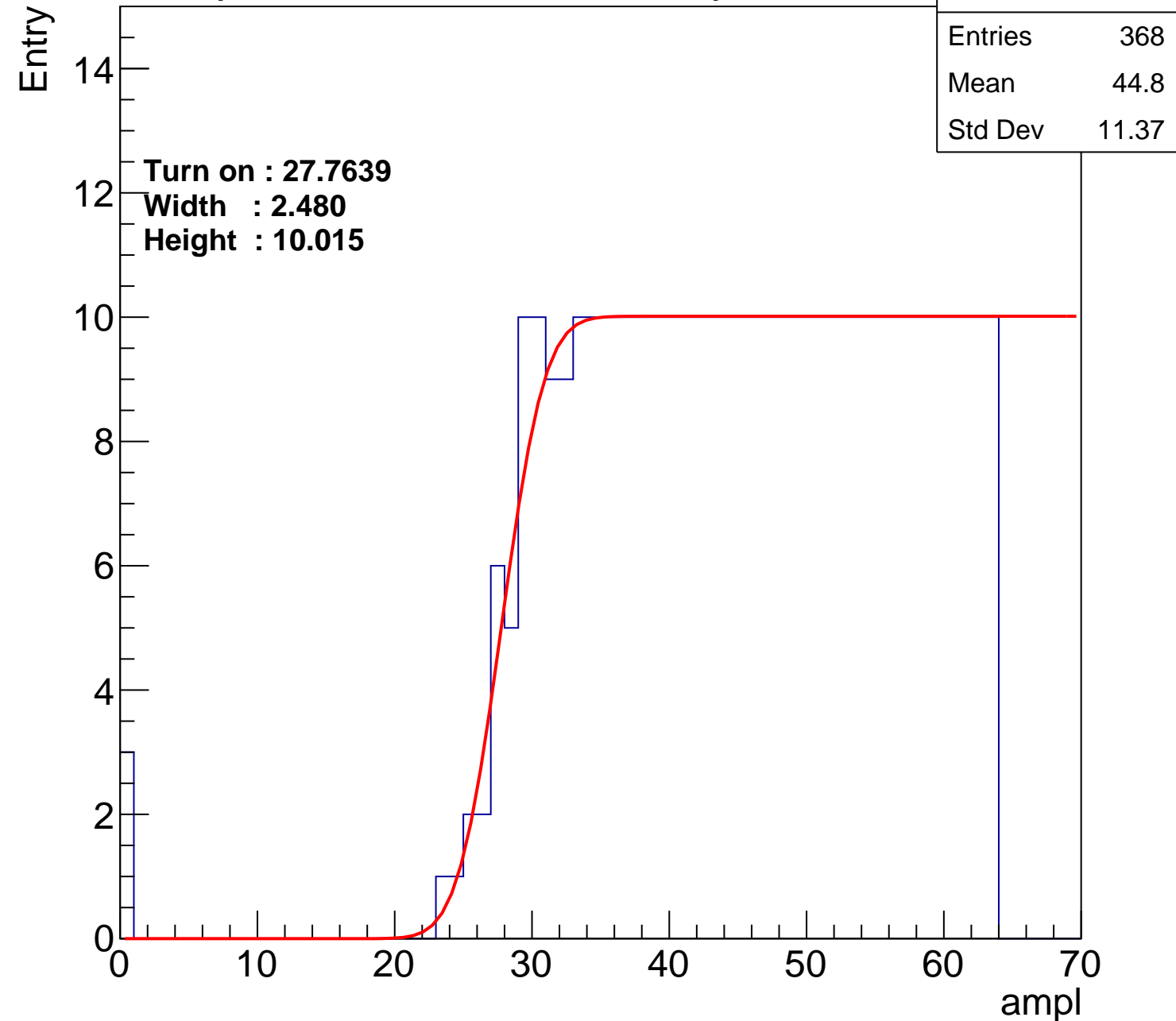
Width : 2.480

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch70

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.7109

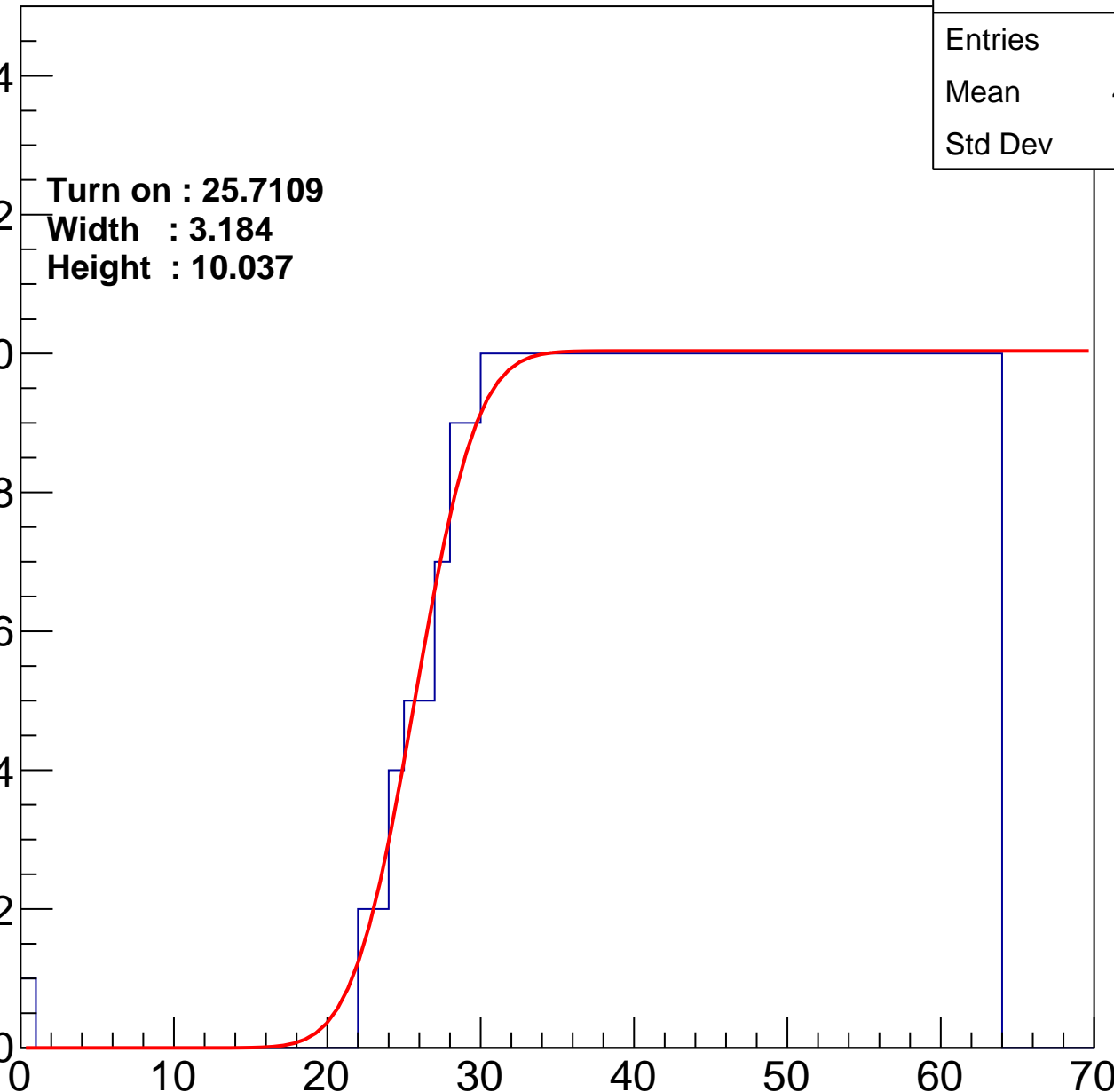
Width : 3.184

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch71

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	362
Mean	45.12
Std Dev	11.1

Turn on : 28.2020

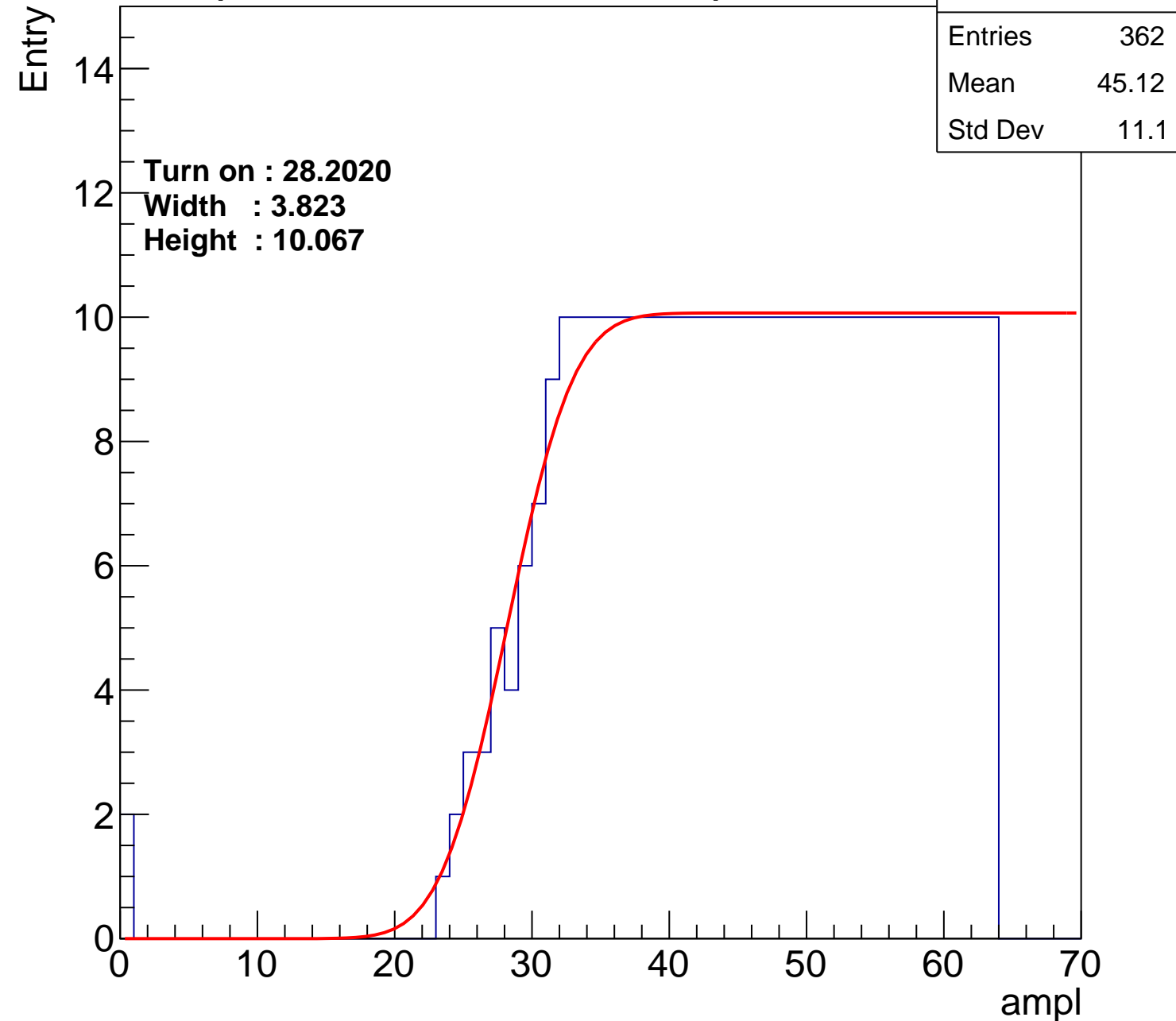
Width : 3.823

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch72

calib\_packv5\_042523\_0143.root, FC#13, port D2

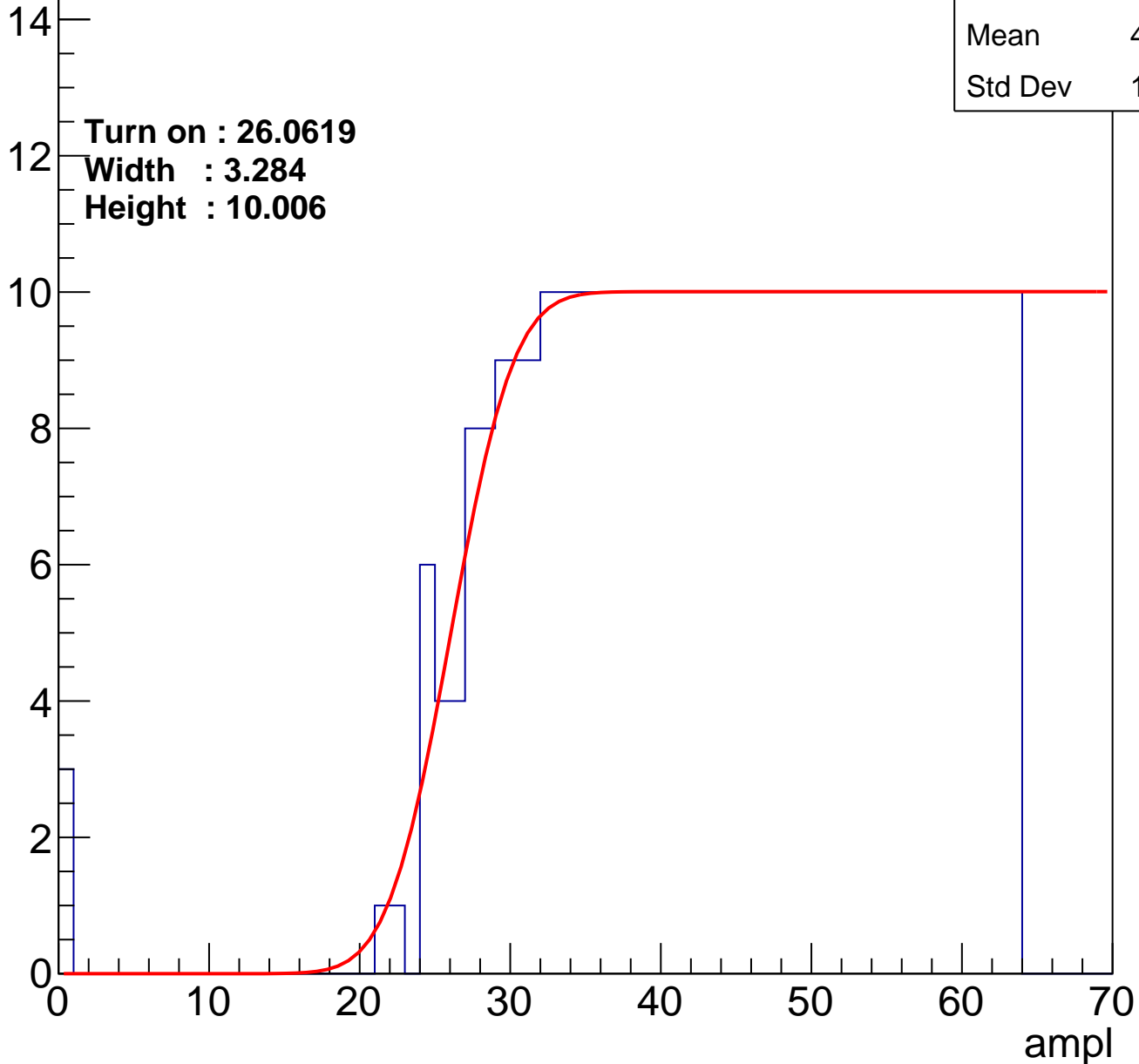
Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 26.0619

Width : 3.284

Height : 10.006

Entry



# B1L003S, U22-ch73

calib\_packv5\_042523\_0143.root, FC#13, port D2

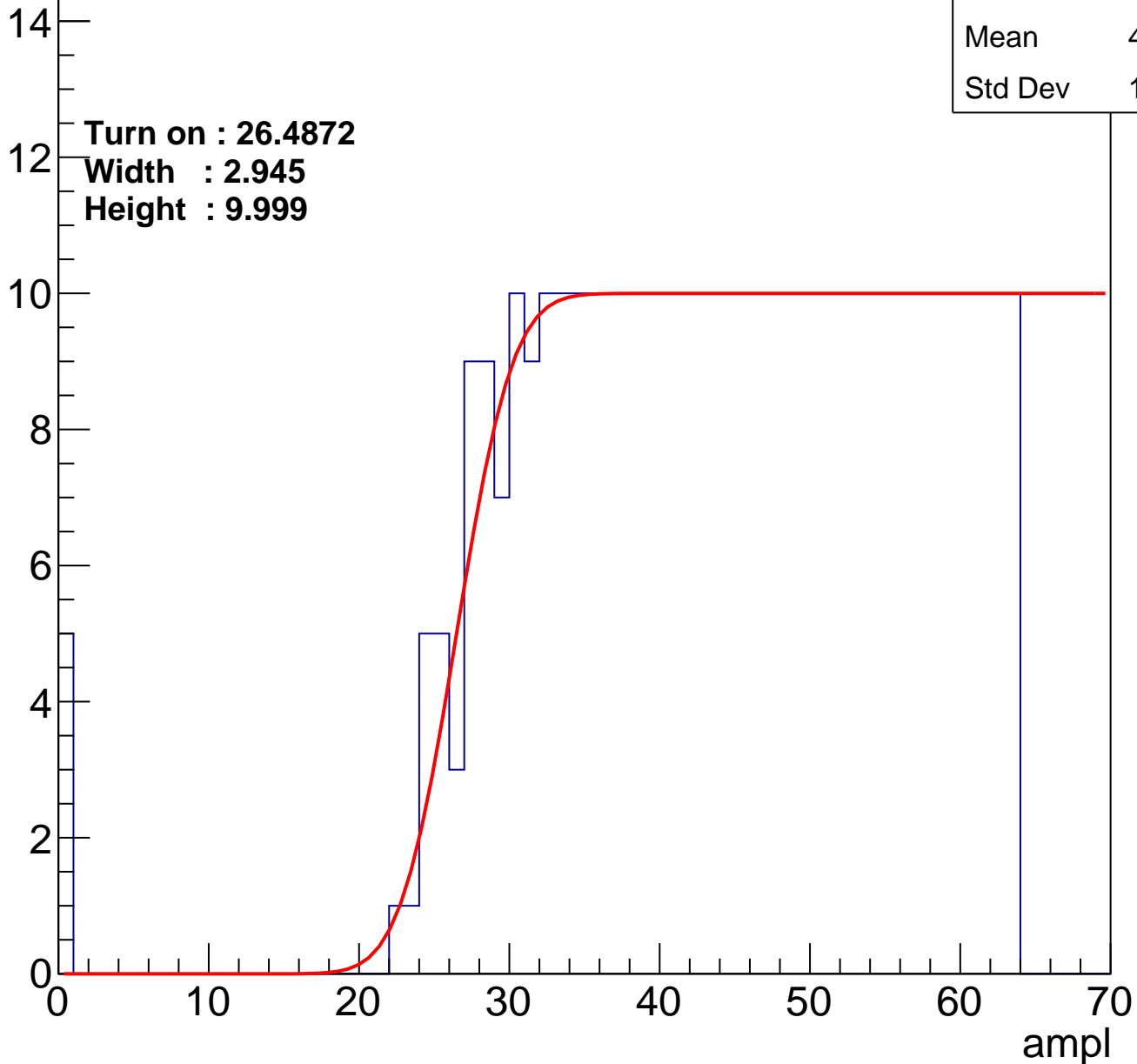
Entry

Entries	384
Mean	43.87
Std Dev	12.13

Turn on : 26.4872

Width : 2.945

Height : 9.999



# B1L003S, U22-ch74

calib\_packv5\_042523\_0143.root, FC#13, port D2

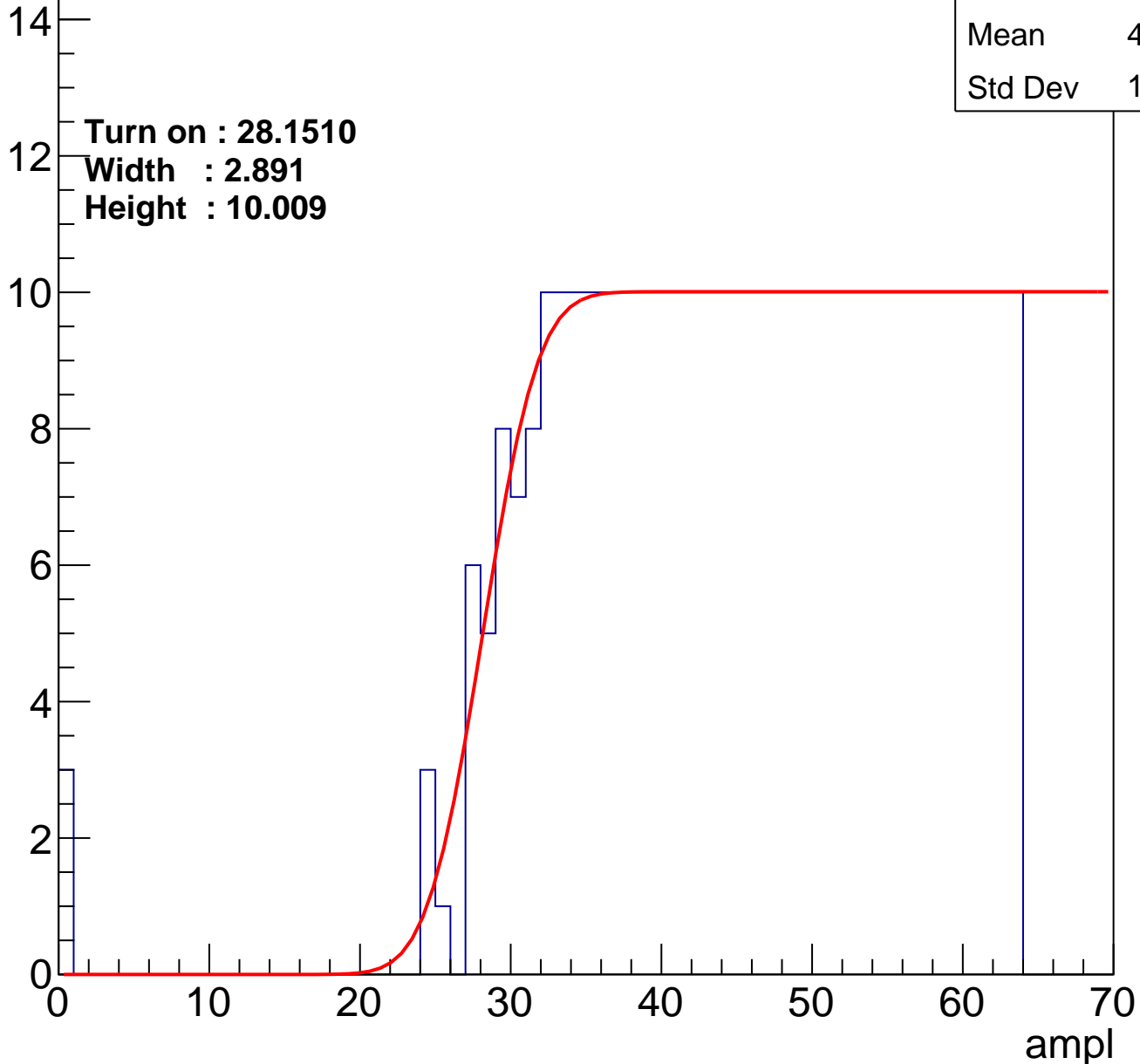
Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.1510

Width : 2.891

Height : 10.009

Entry



# B1L003S, U22-ch75

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	373
Mean	44.73
Std Dev	11.04

Turn on : 26.8400

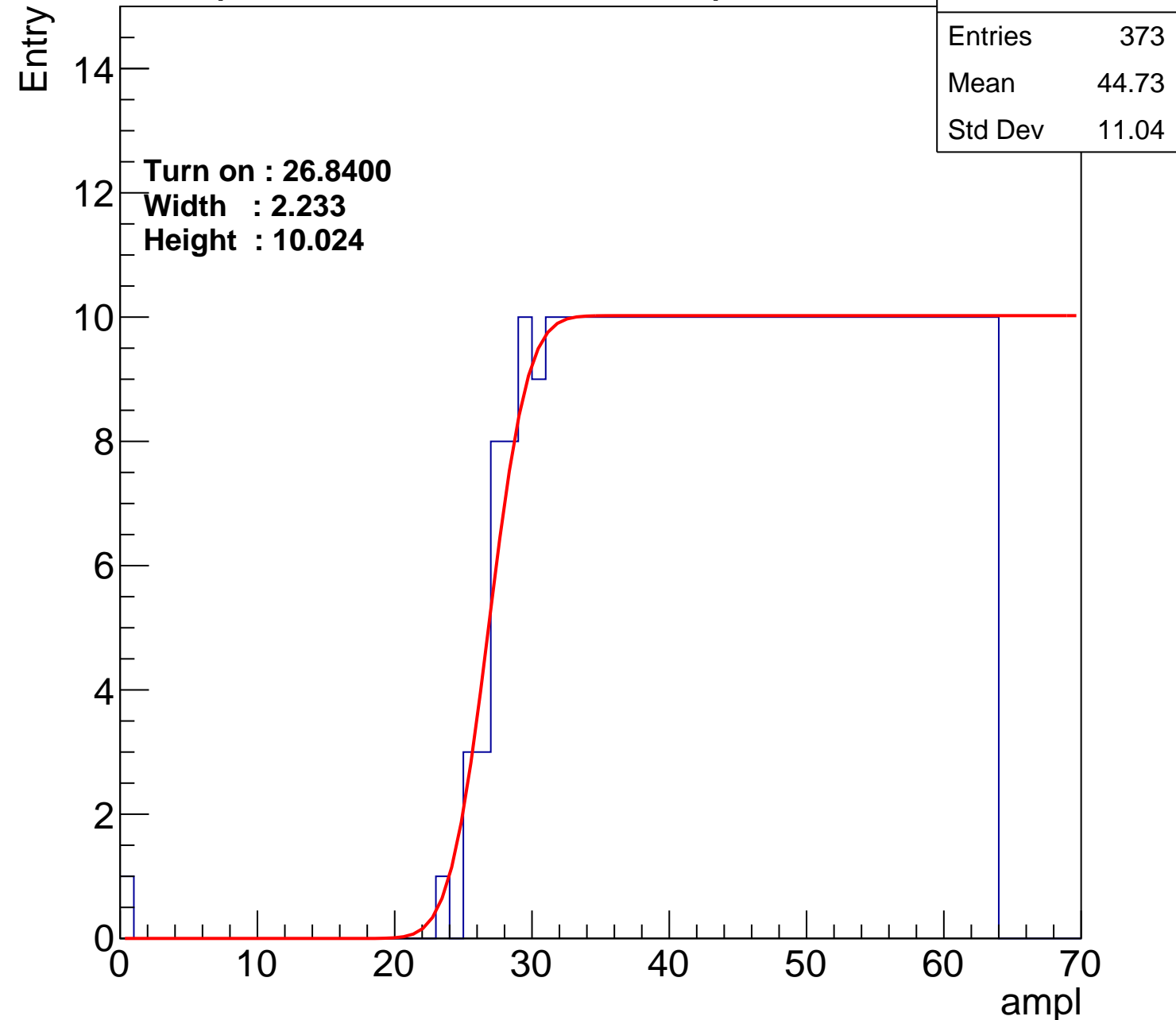
Width : 2.233

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch76

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	359
Mean	45.34
Std Dev	10.91

Turn on : 27.9981

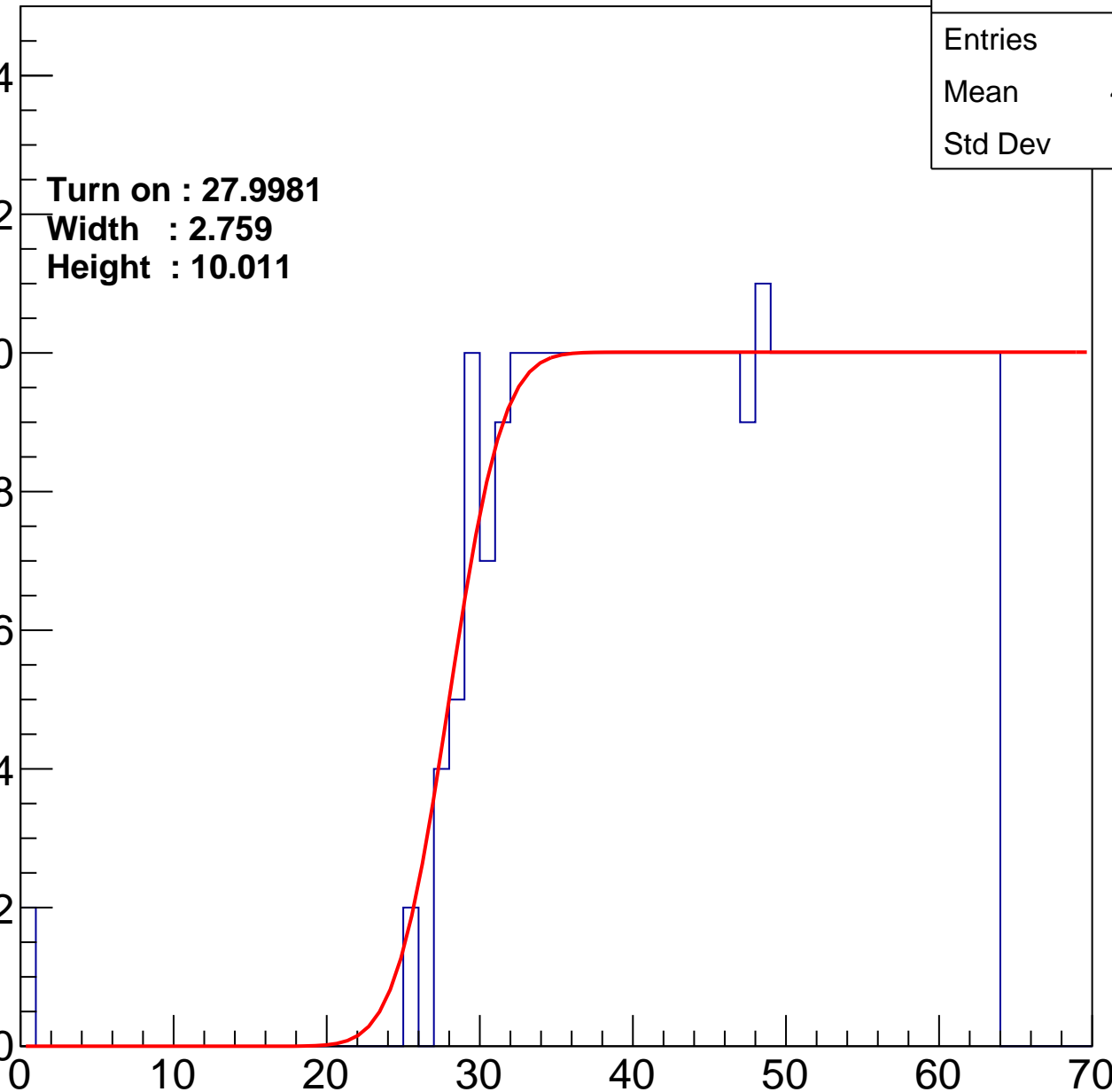
Width : 2.759

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch77

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	386
Mean	44.05
Std Dev	11.45

Turn on : 25.3450

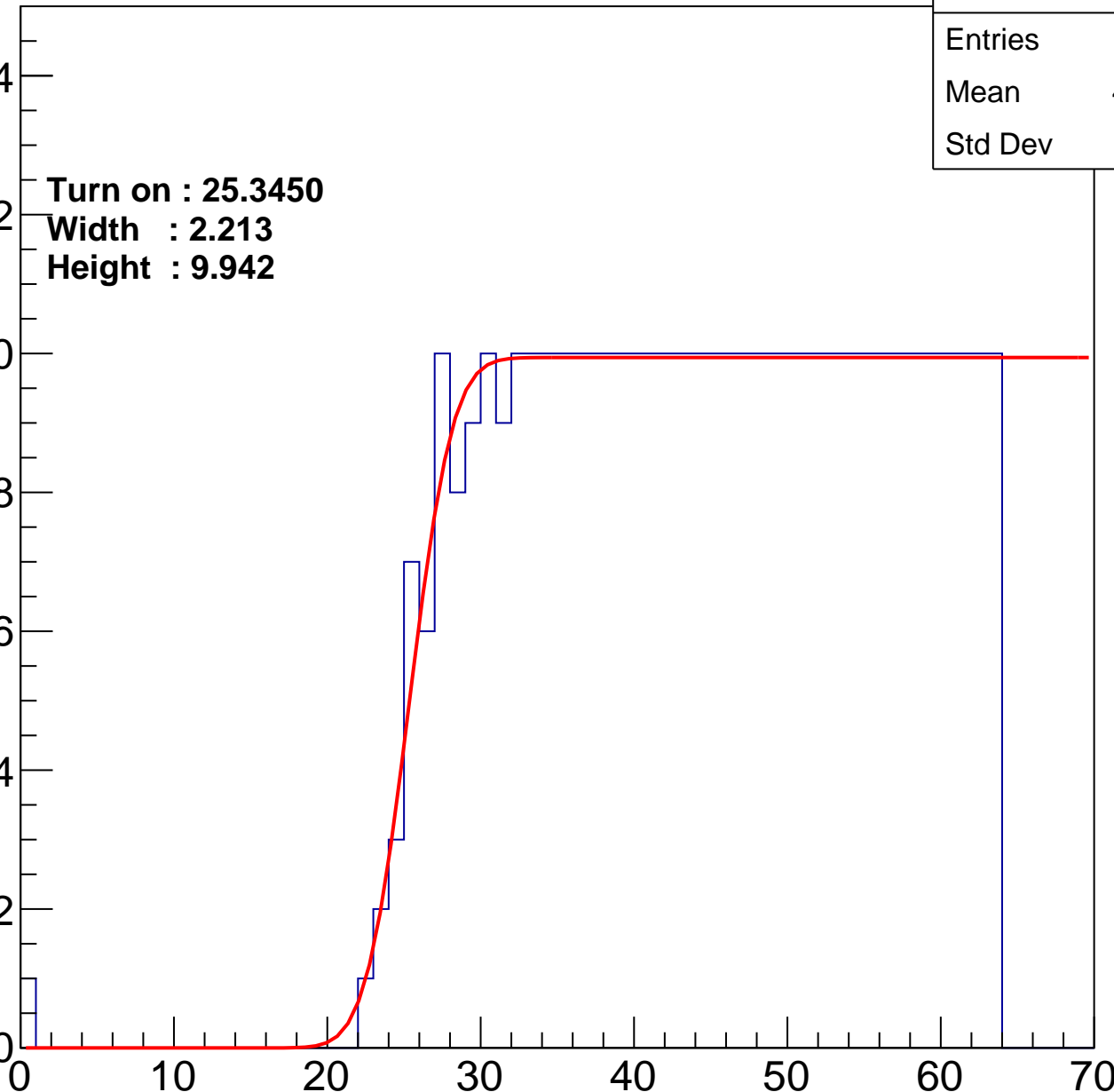
Width : 2.213

Height : 9.942

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch78

calib\_packv5\_042523\_0143.root, FC#13, port D2

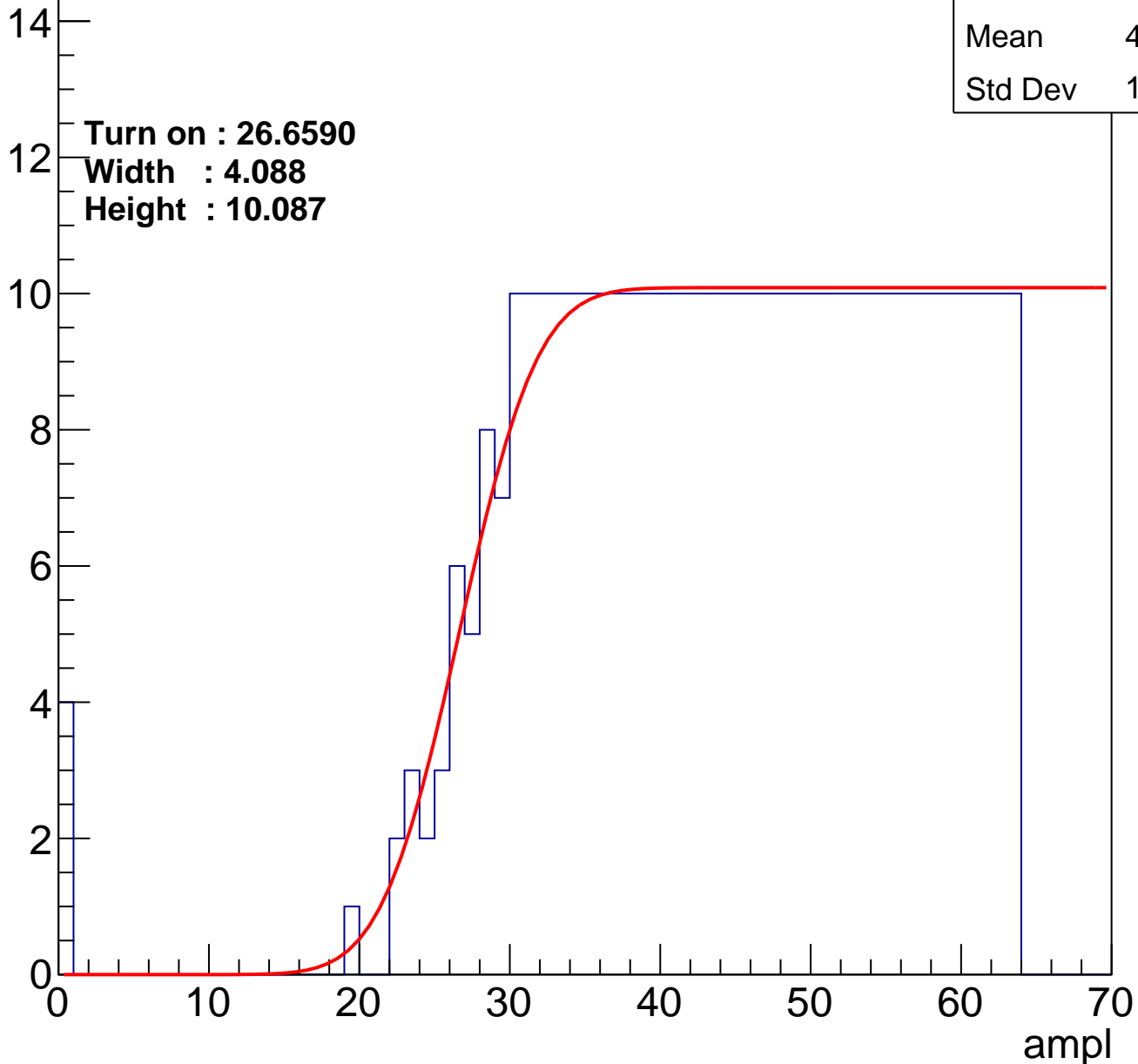
Entries	381
Mean	44.05
Std Dev	11.95

Turn on : 26.6590

Width : 4.088

Height : 10.087

Entry





# B1L003S, U22-ch79

calib\_packv5\_042523\_0143.root, FC#13, port D2

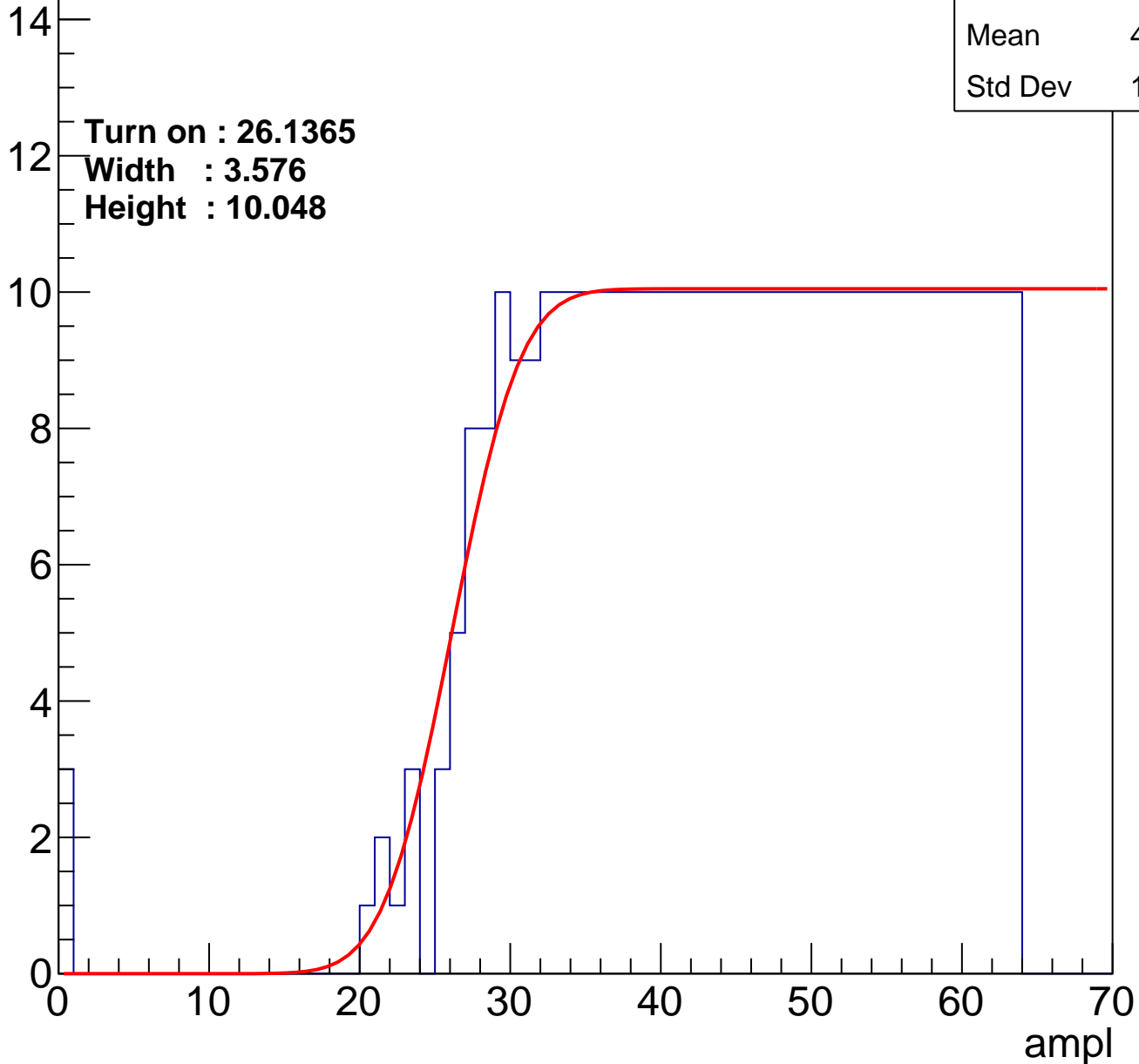
Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.1365

Width : 3.576

Height : 10.048

Entry



# B1L003S, U22-ch80

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	366
Mean	44.99
Std Dev	11.02

Turn on : 27.9985

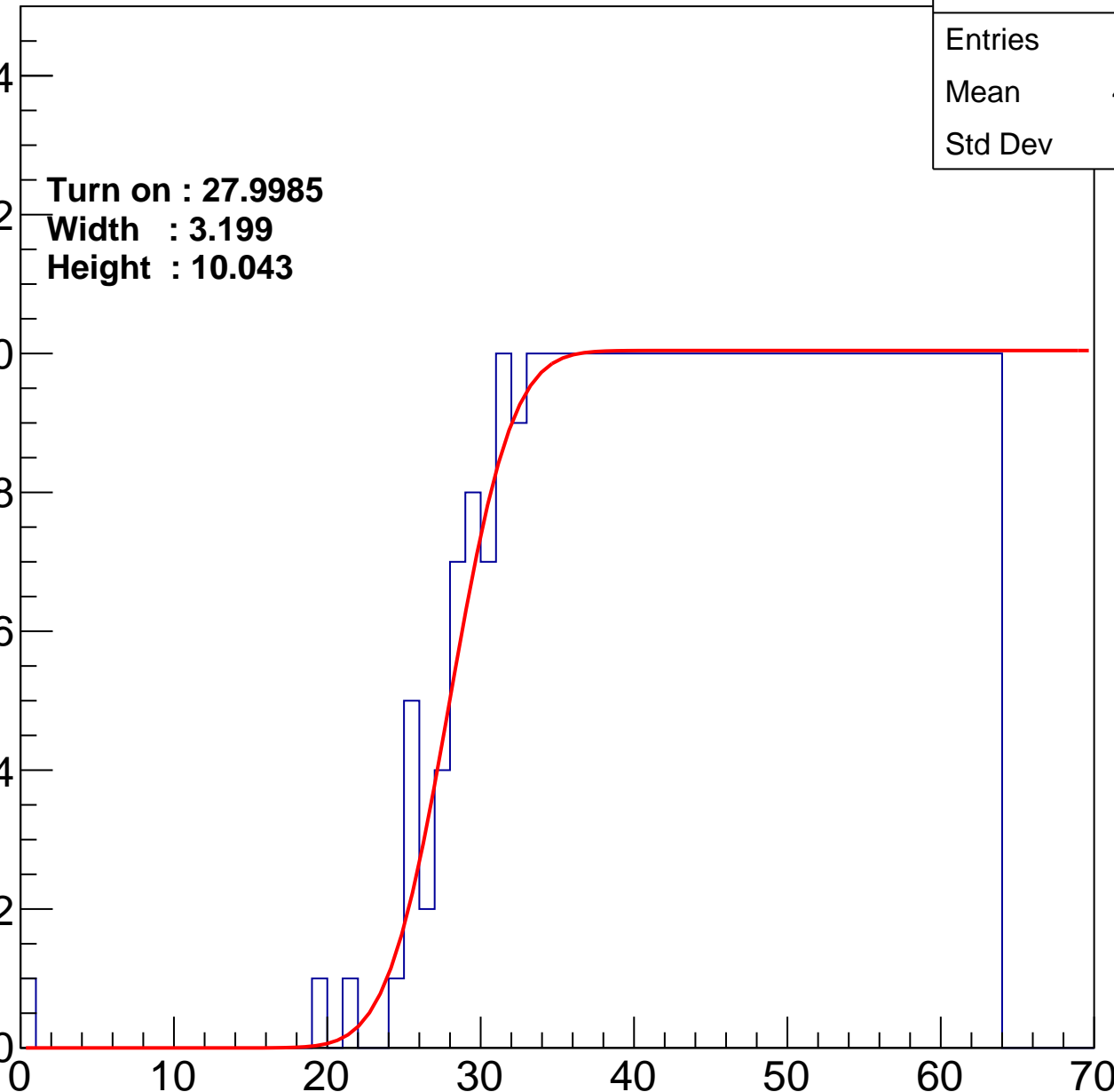
Width : 3.199

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch81

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	377
Mean	44.34
Std Dev	11.62

Turn on : 26.5122

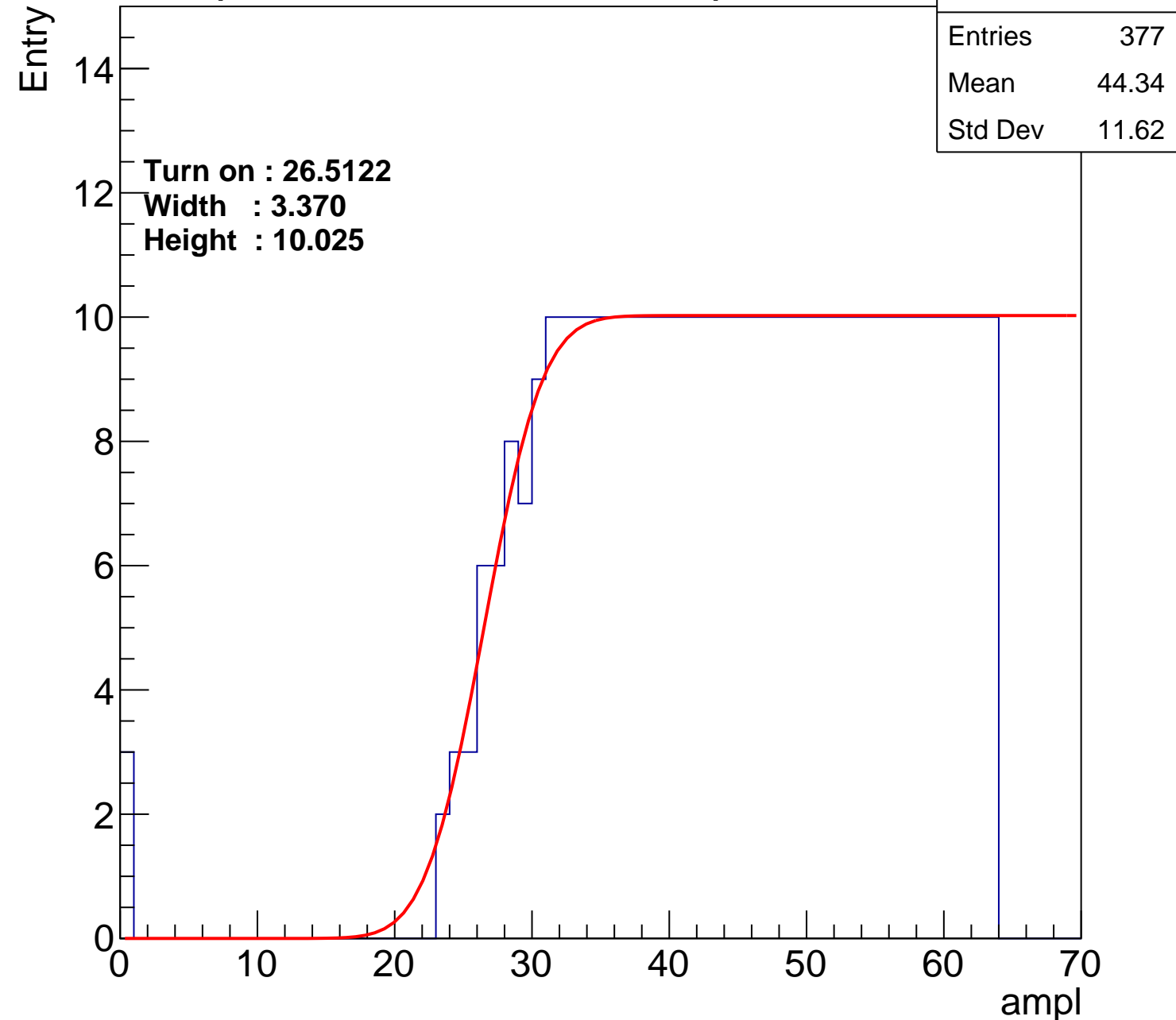
Width : 3.370

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch82

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	382
Mean	43.96
Std Dev	12.09

Turn on : 26.1678

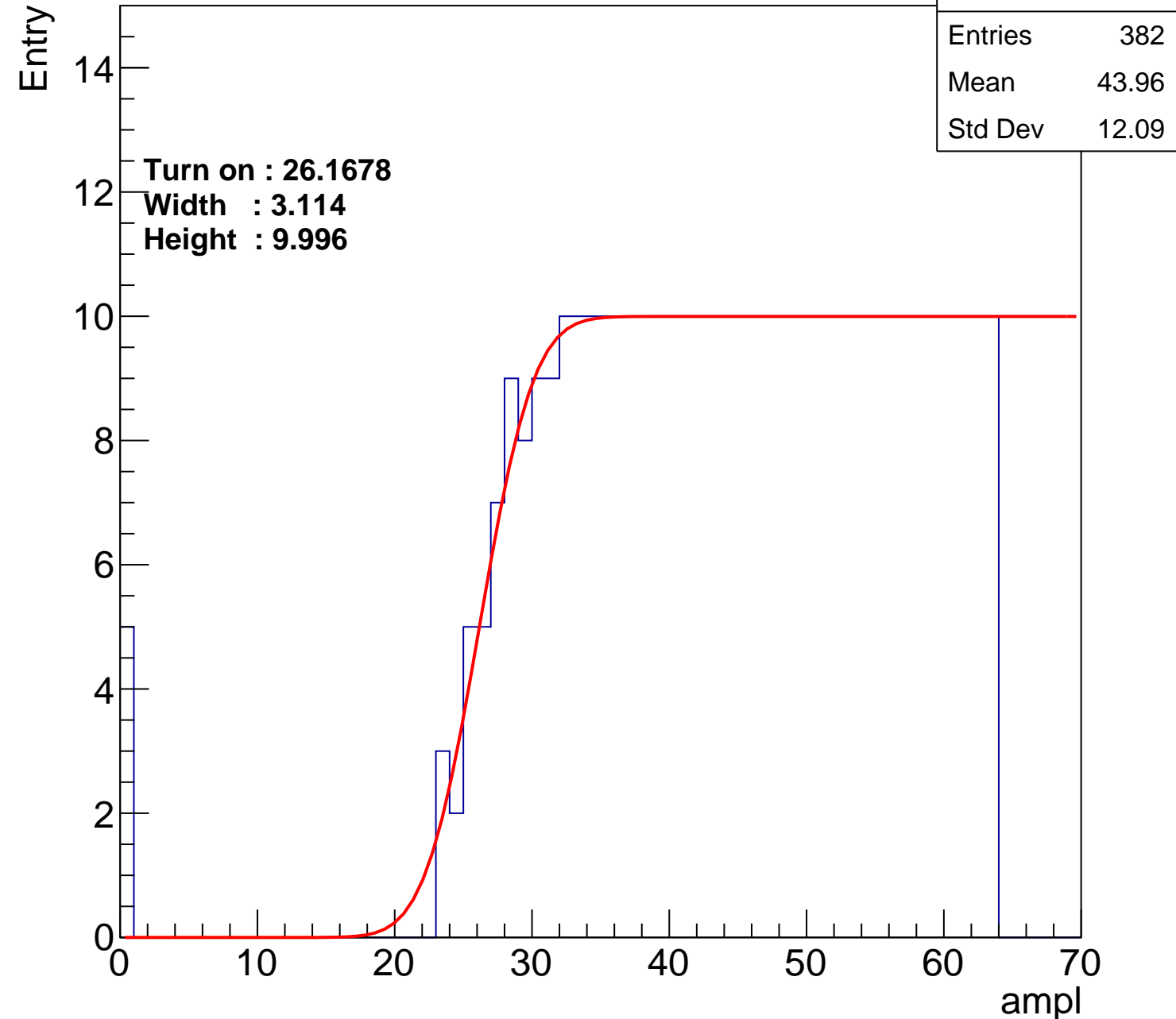
Width : 3.114

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch83

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	369
Mean	44.68
Std Dev	11.6

Turn on : 27.5705

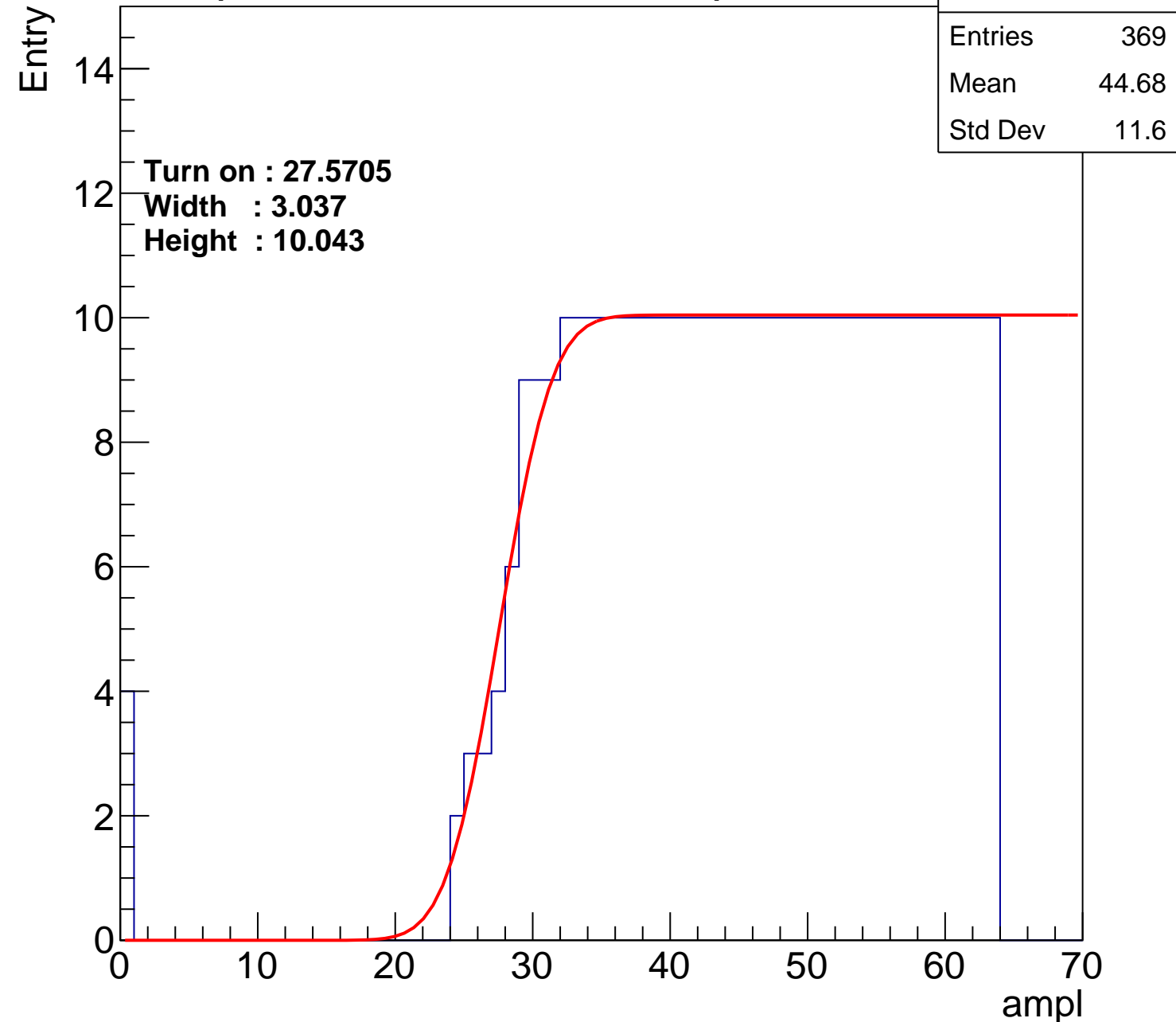
Width : 3.037

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch84

calib\_packv5\_042523\_0143.root, FC#13, port D2

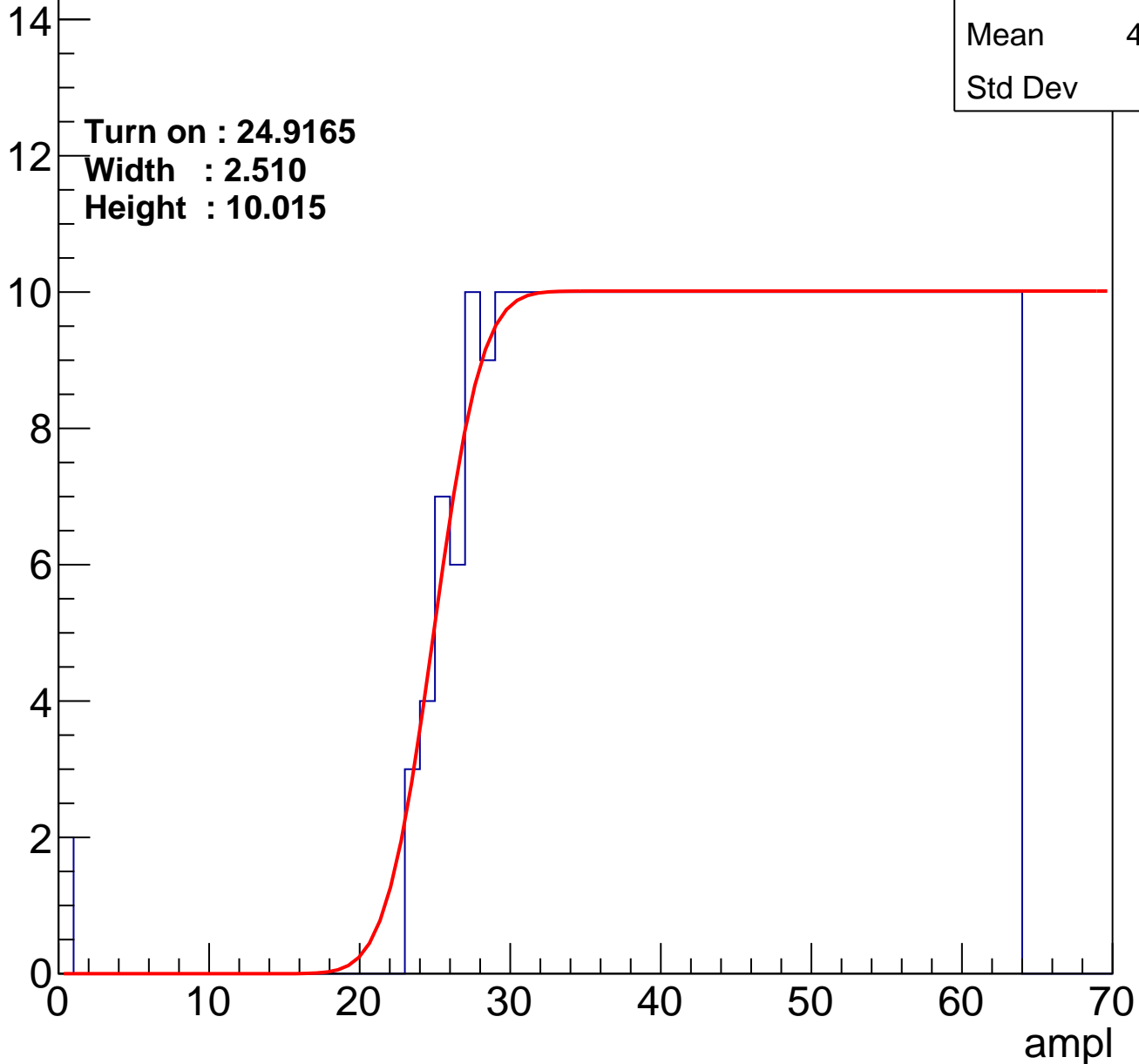
Entries	391
Mean	43.78
Std Dev	11.7

Turn on : 24.9165

Width : 2.510

Height : 10.015

Entry



# B1L003S, U22-ch85

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	358
Mean	45.32
Std Dev	10.99

Turn on : 28.8843

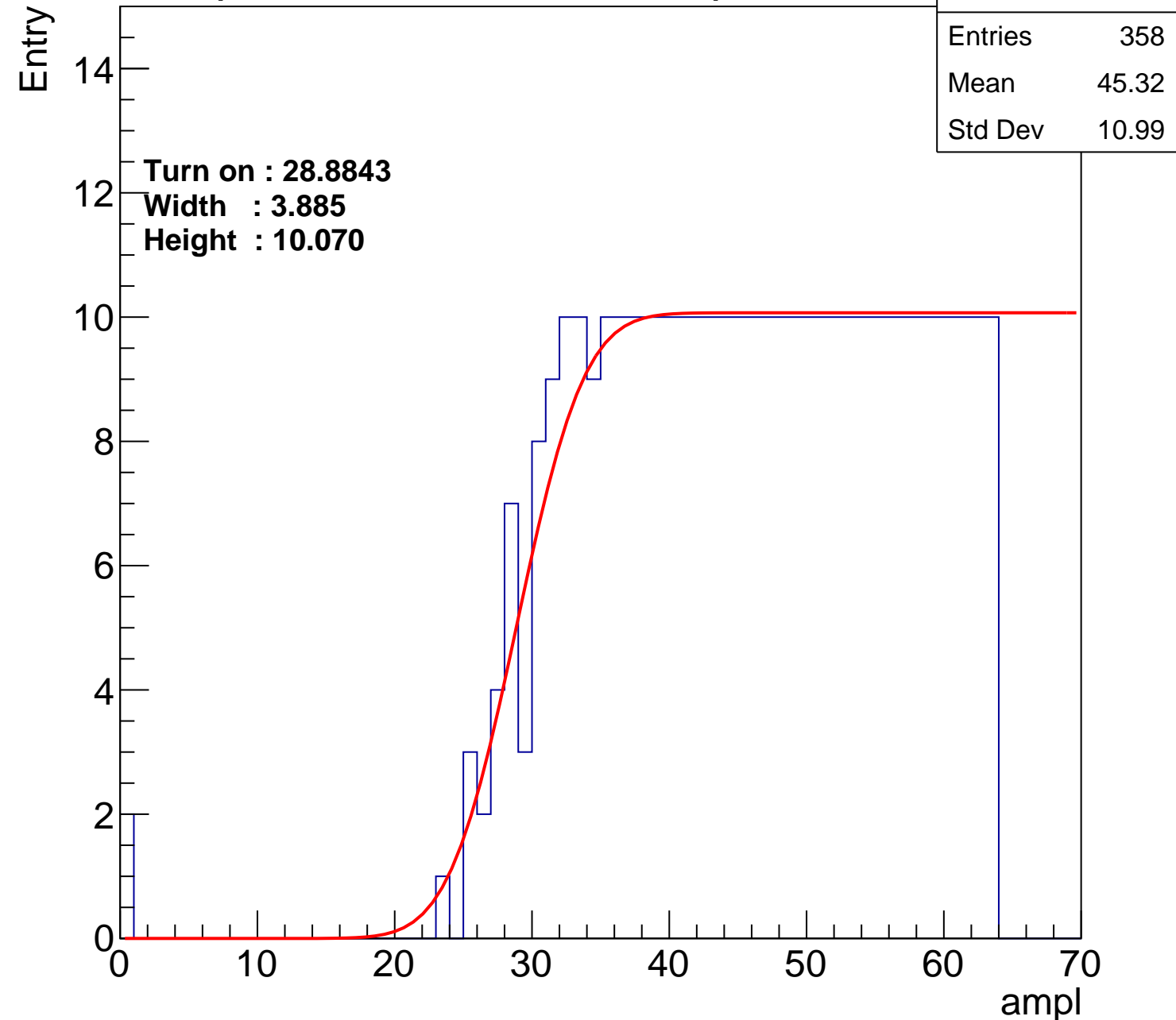
Width : 3.885

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch86

calib\_packv5\_042523\_0143.root, FC#13, port D2

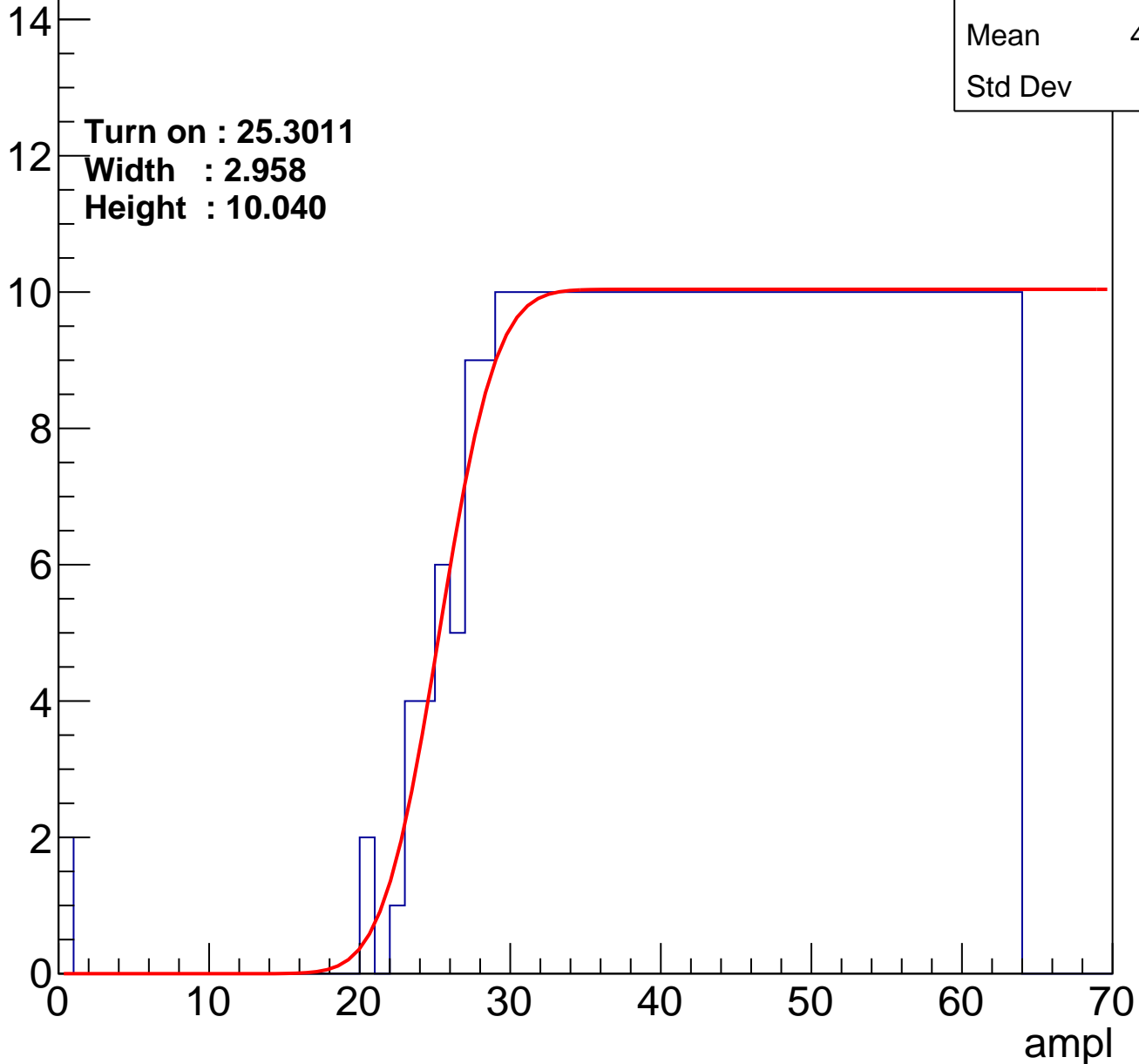
Entries	392
Mean	43.69
Std Dev	11.8

Turn on : 25.3011

Width : 2.958

Height : 10.040

Entry





# B1L003S, U22-ch87

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.8590

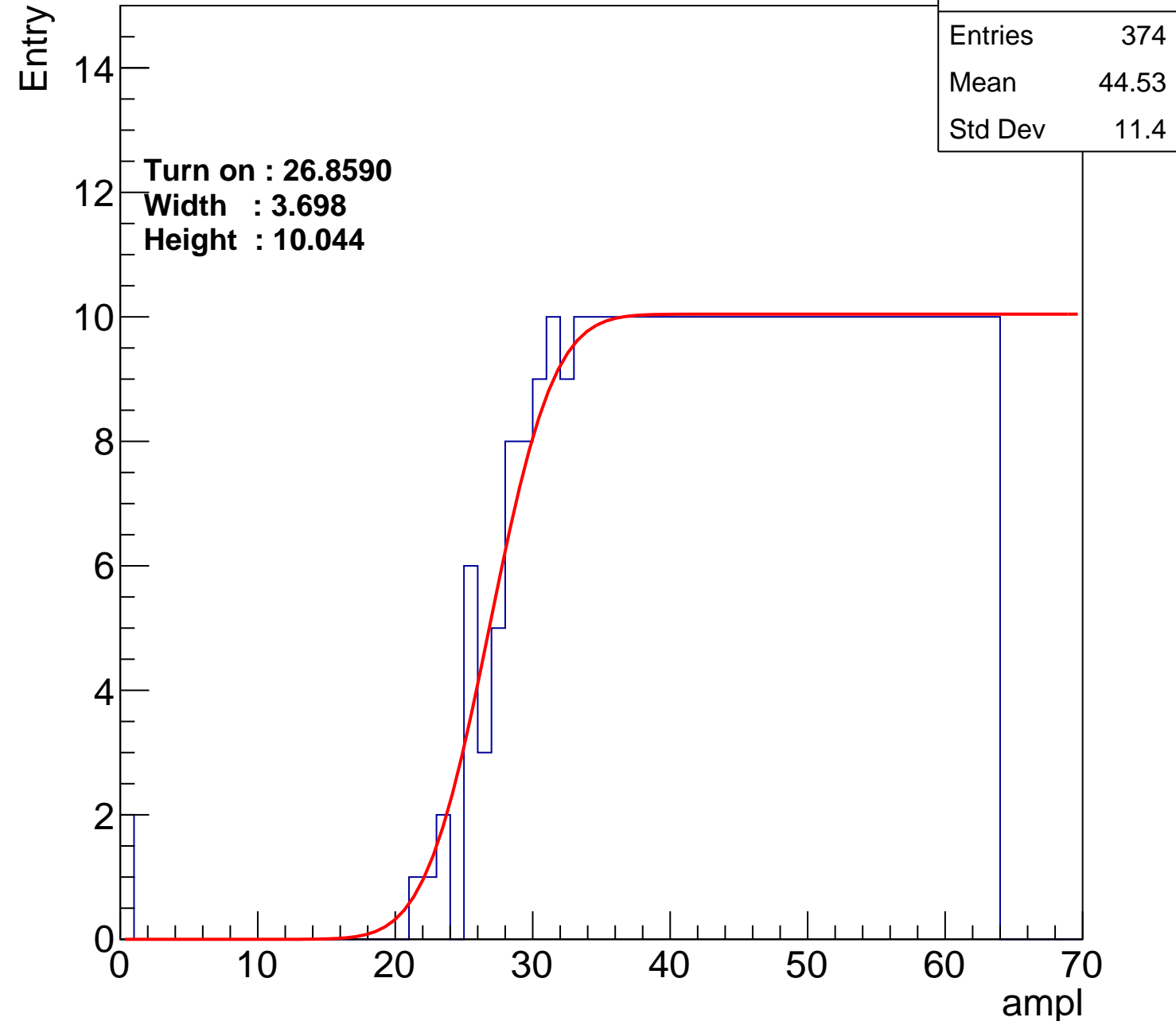
Width : 3.698

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch88

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 26.5308

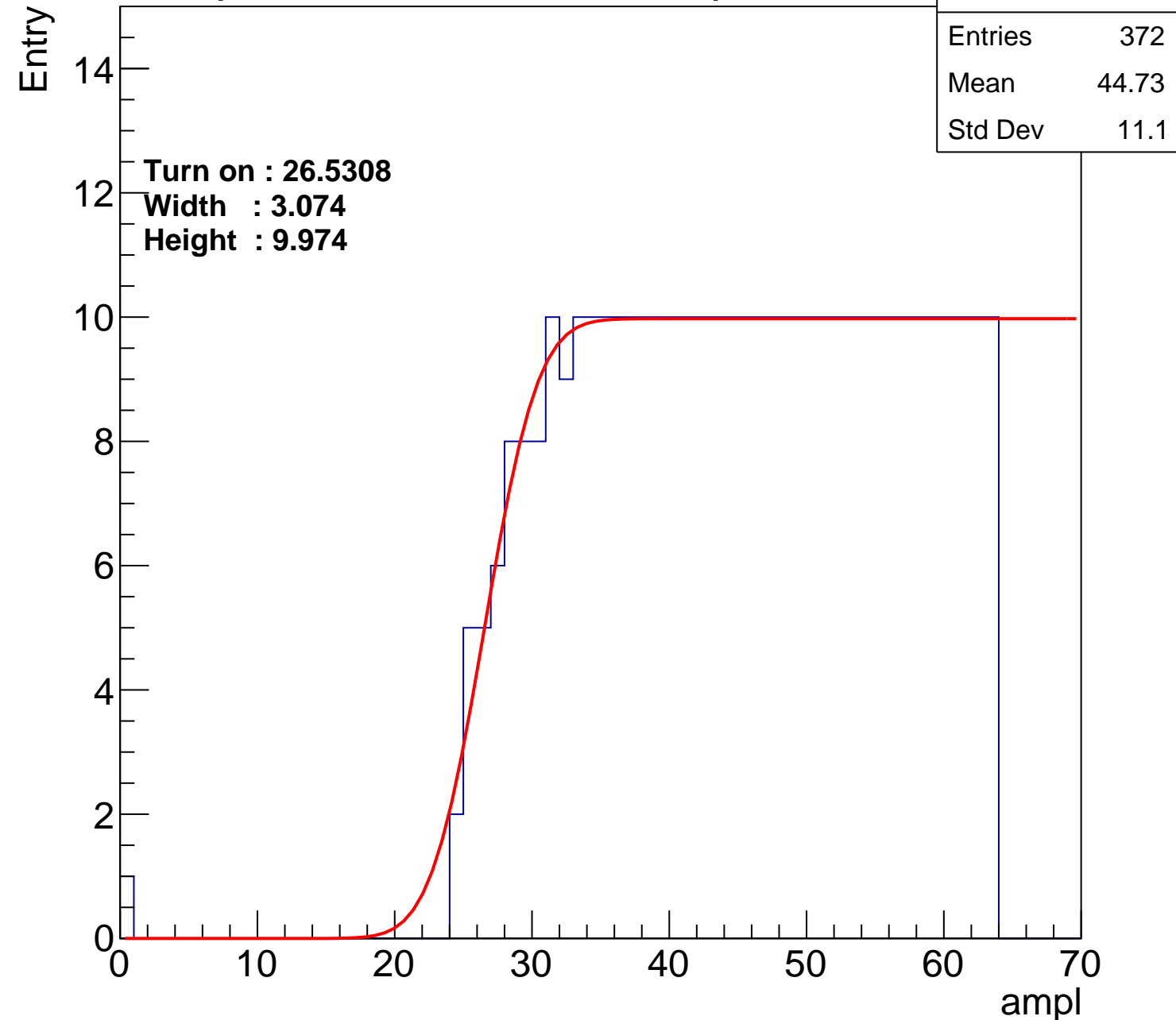
Width : 3.074

Height : 9.974

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch89

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	377
Mean	44.32
Std Dev	11.66

Turn on : 26.5553

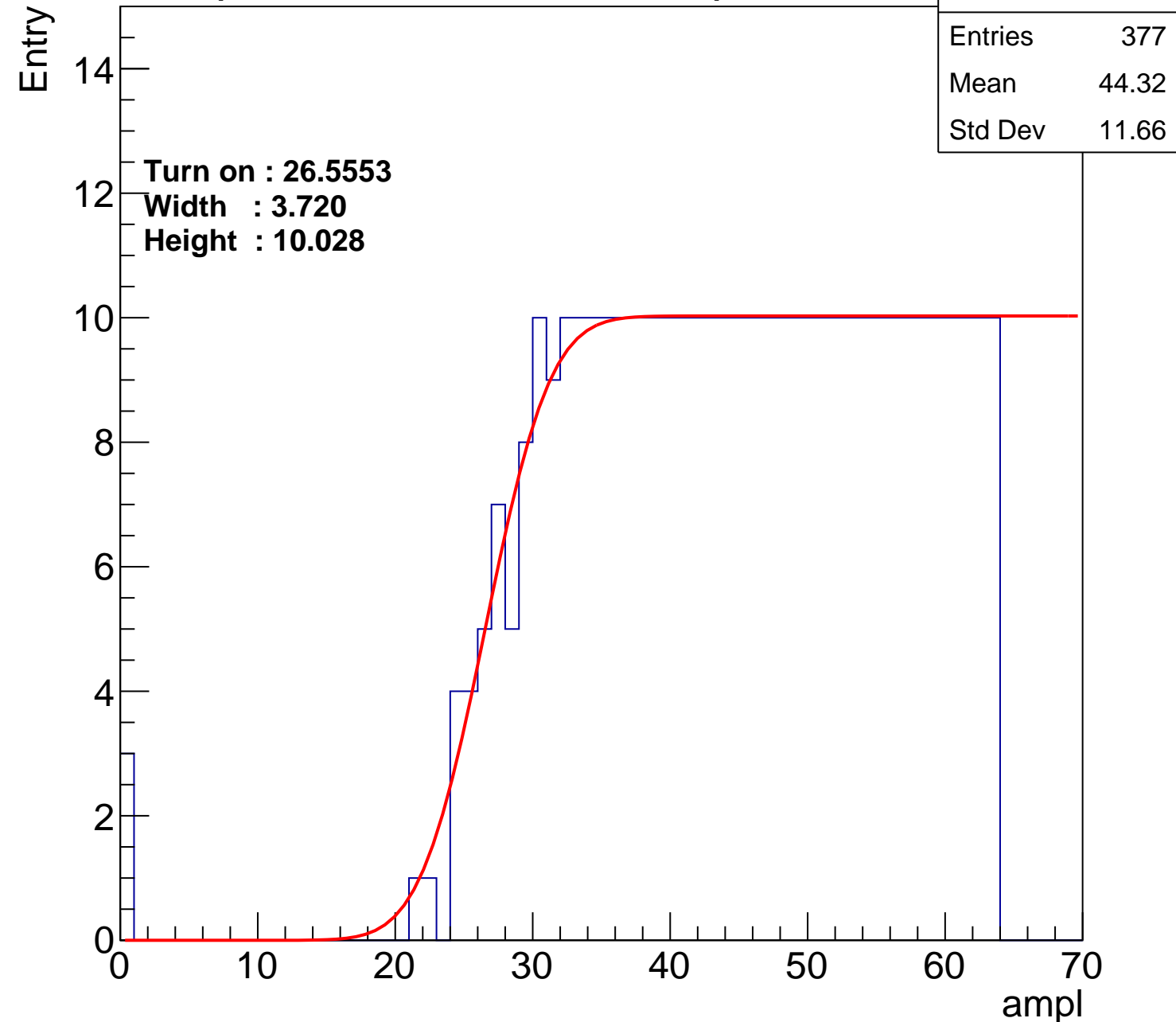
Width : 3.720

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch90

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	366
Mean	44.7
Std Dev	11.81

Turn on : 28.0292

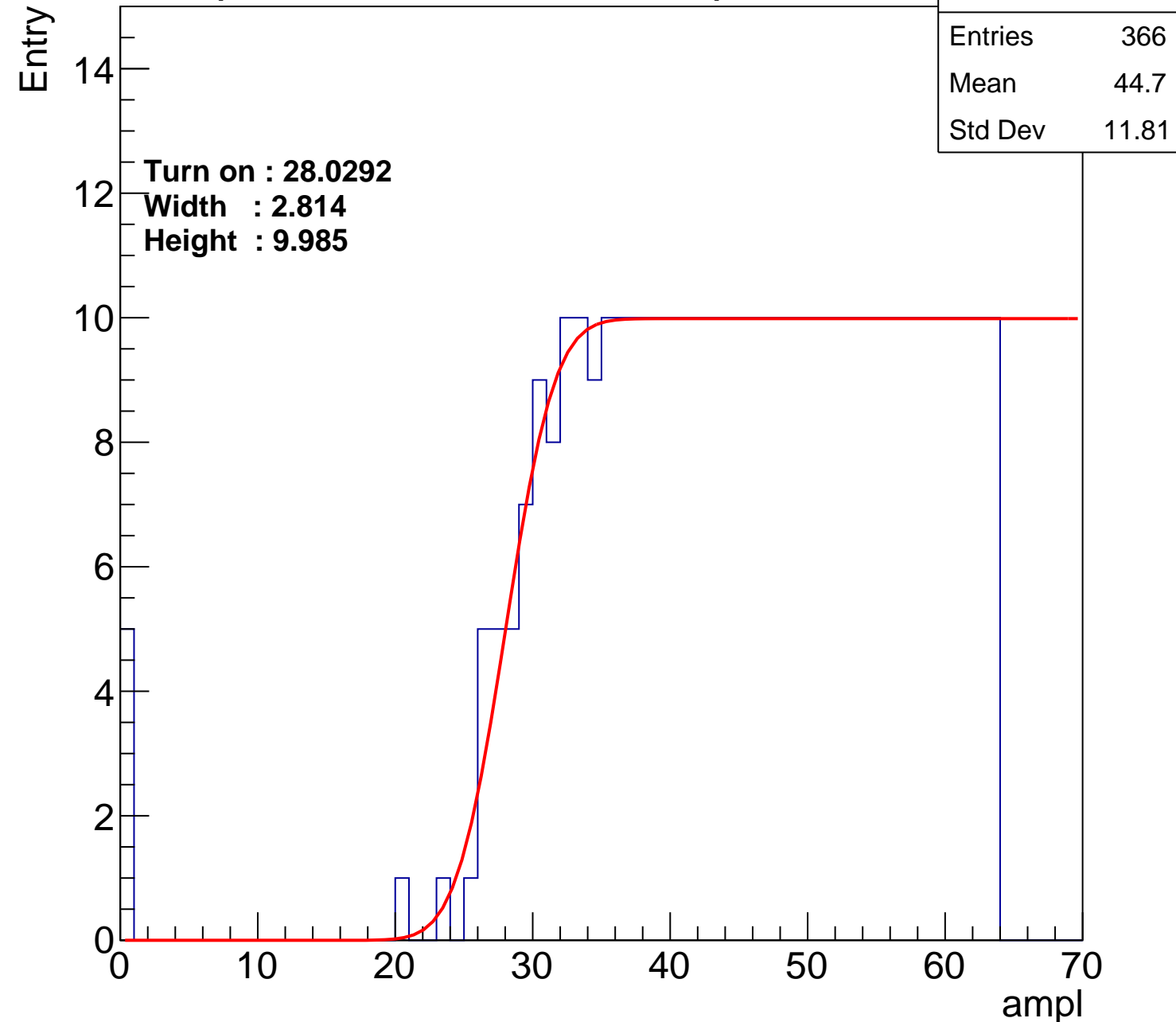
Width : 2.814

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch91

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	359
Mean	45.28
Std Dev	11.01

Turn on : 29.0827

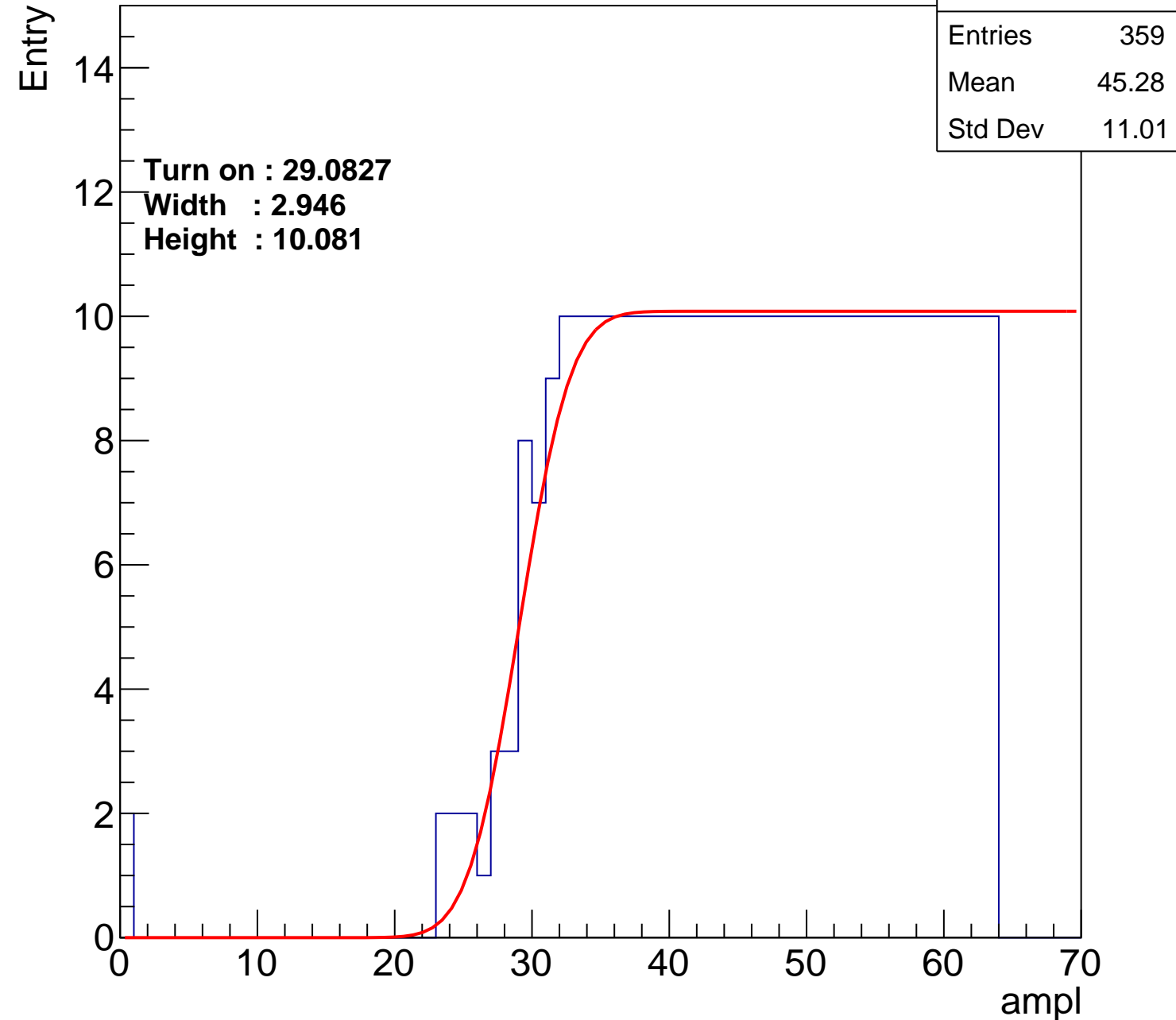
Width : 2.946

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch92

calib\_packv5\_042523\_0143.root, FC#13, port D2

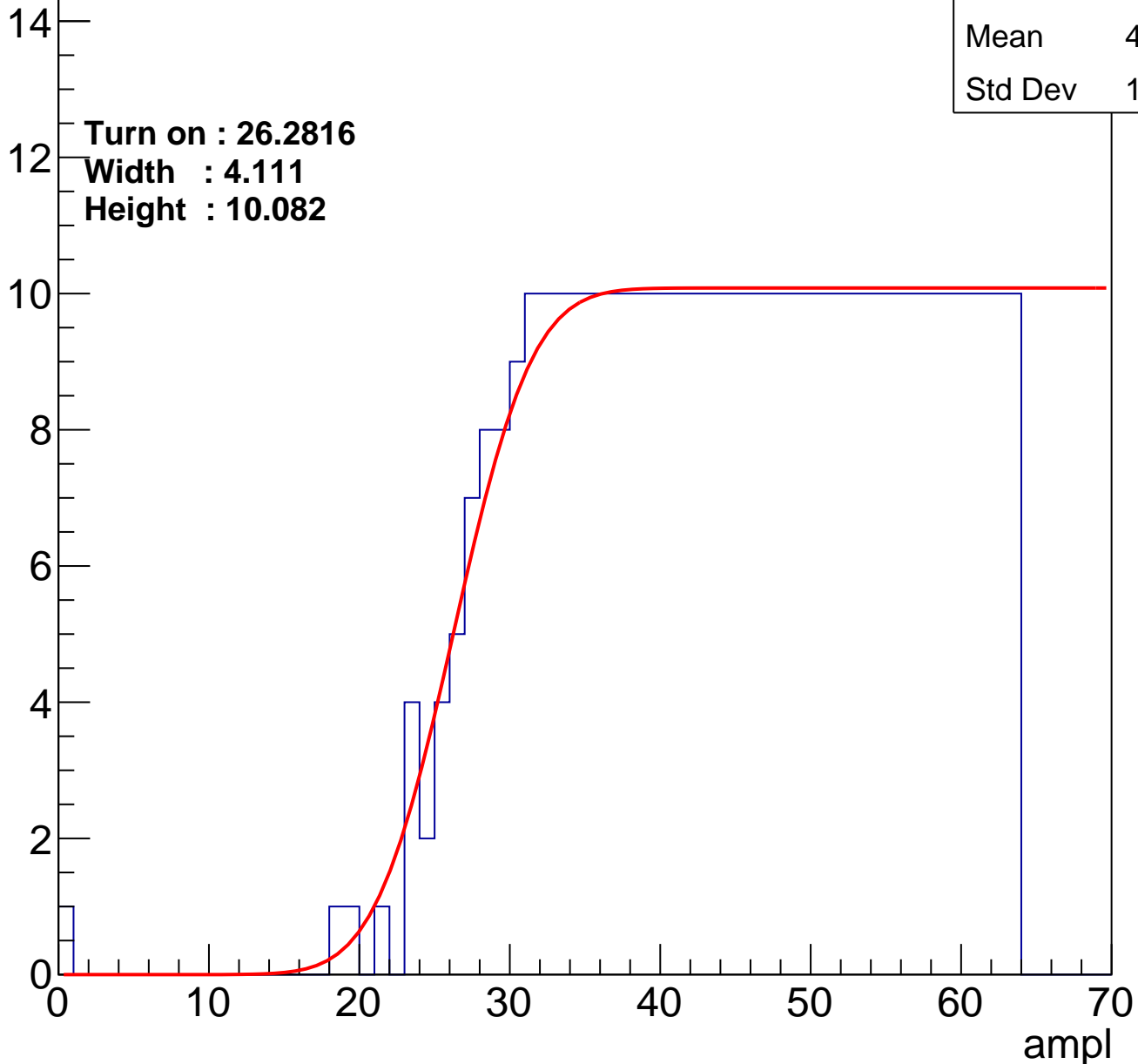
Entries	381
Mean	44.23
Std Dev	11.44

Turn on : 26.2816

Width : 4.111

Height : 10.082

Entry



# B1L003S, U22-ch93

calib\_packv5\_042523\_0143.root, FC#13, port D2

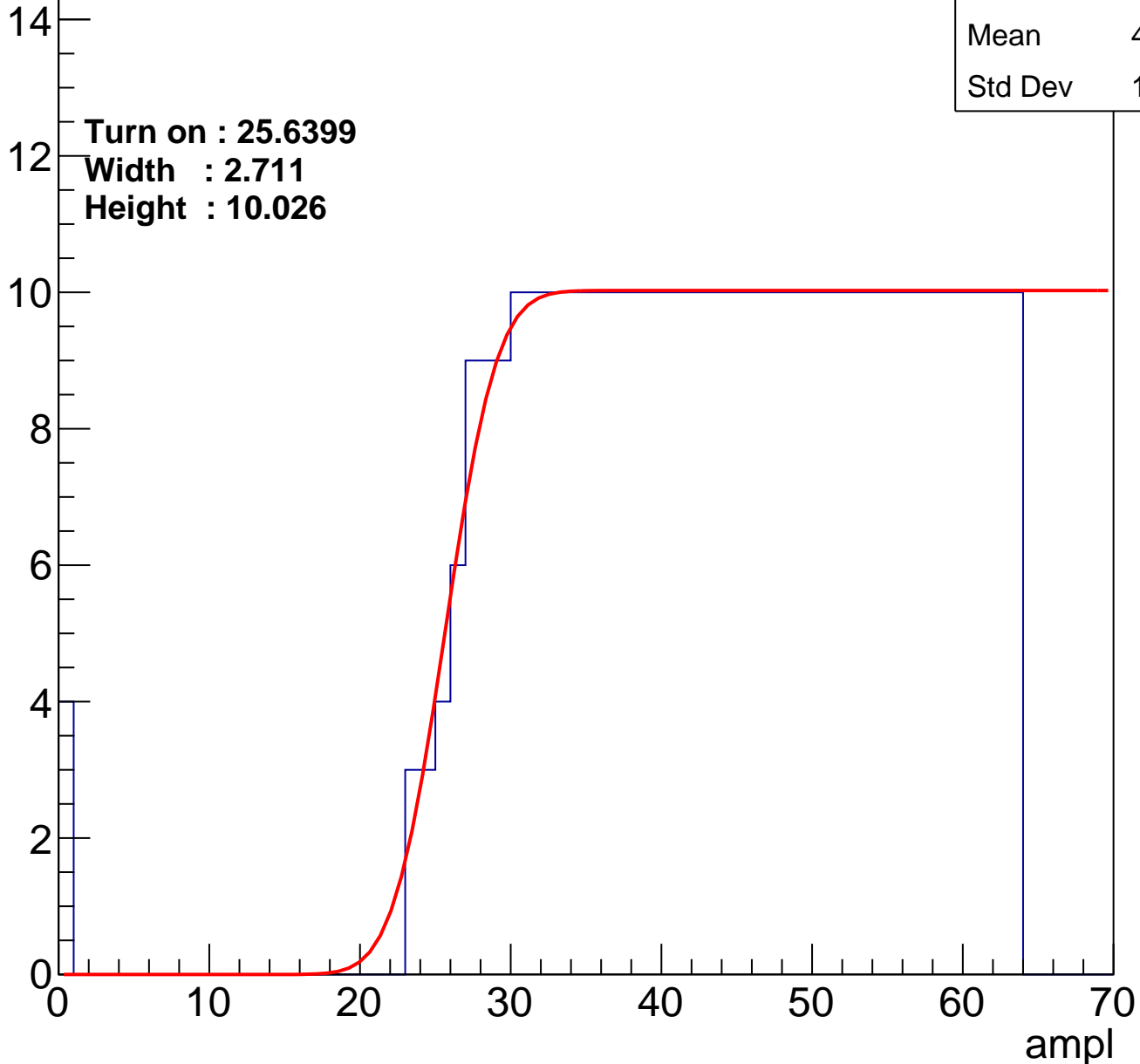
Entries	387
Mean	43.83
Std Dev	11.97

Turn on : 25.6399

Width : 2.711

Height : 10.026

Entry



# B1L003S, U22-ch94

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.0708

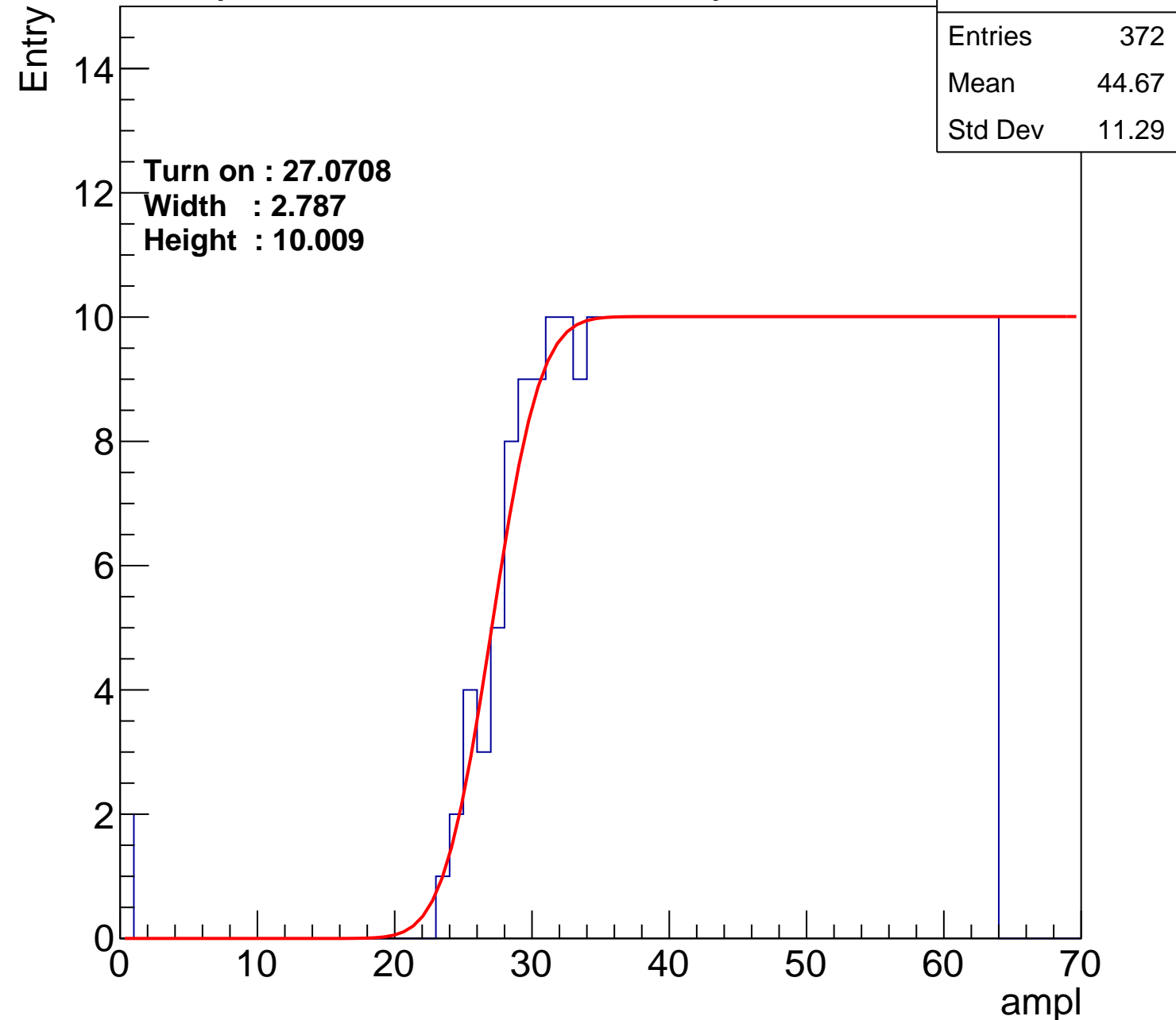
Width : 2.787

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch95

calib\_packv5\_042523\_0143.root, FC#13, port D2

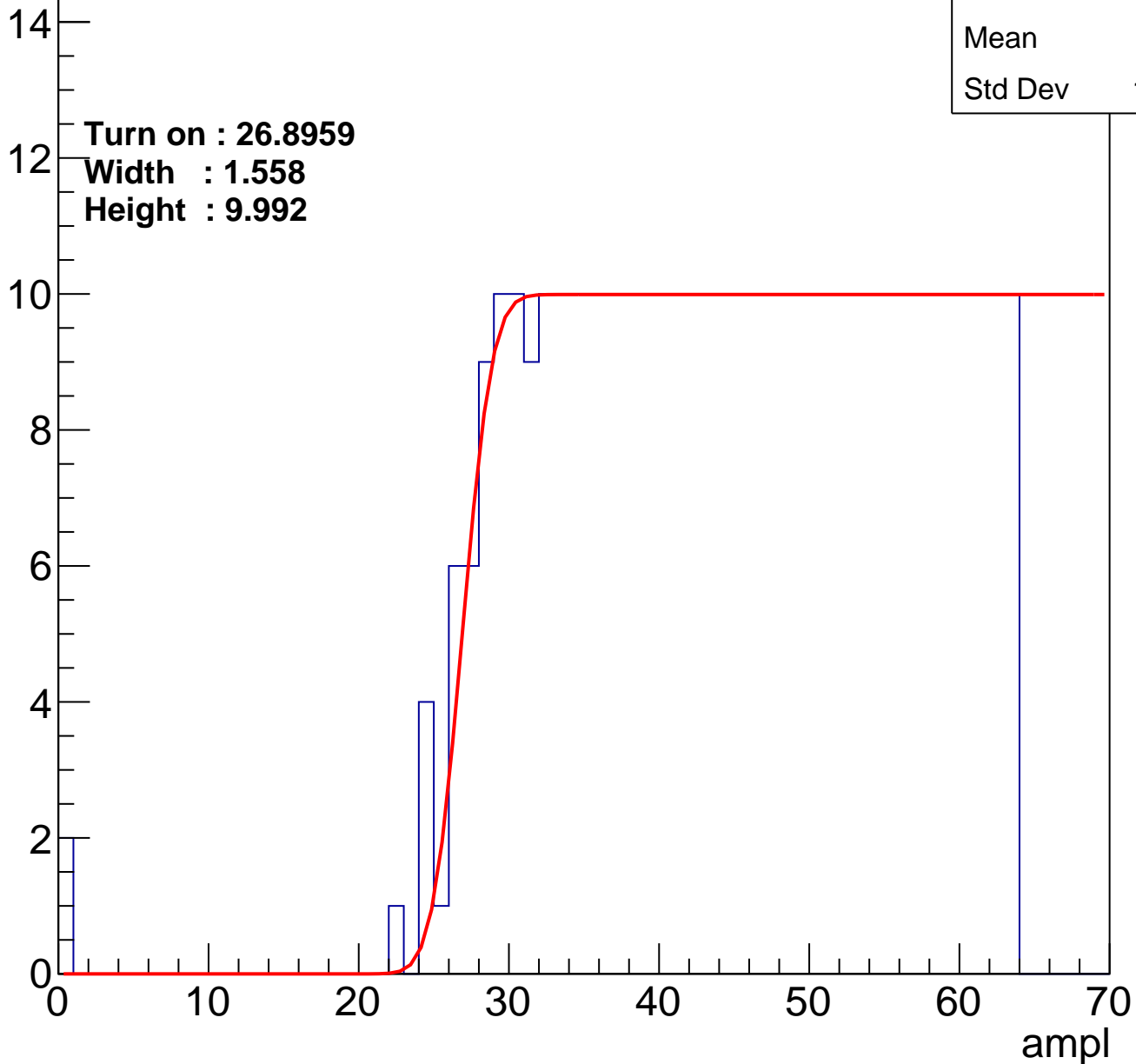
Entries	378
Mean	44.4
Std Dev	11.41

Turn on : 26.8959

Width : 1.558

Height : 9.992

Entry



# B1L003S, U22-ch96

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	378
Mean	44.41
Std Dev	11.38

Turn on : 25.9150

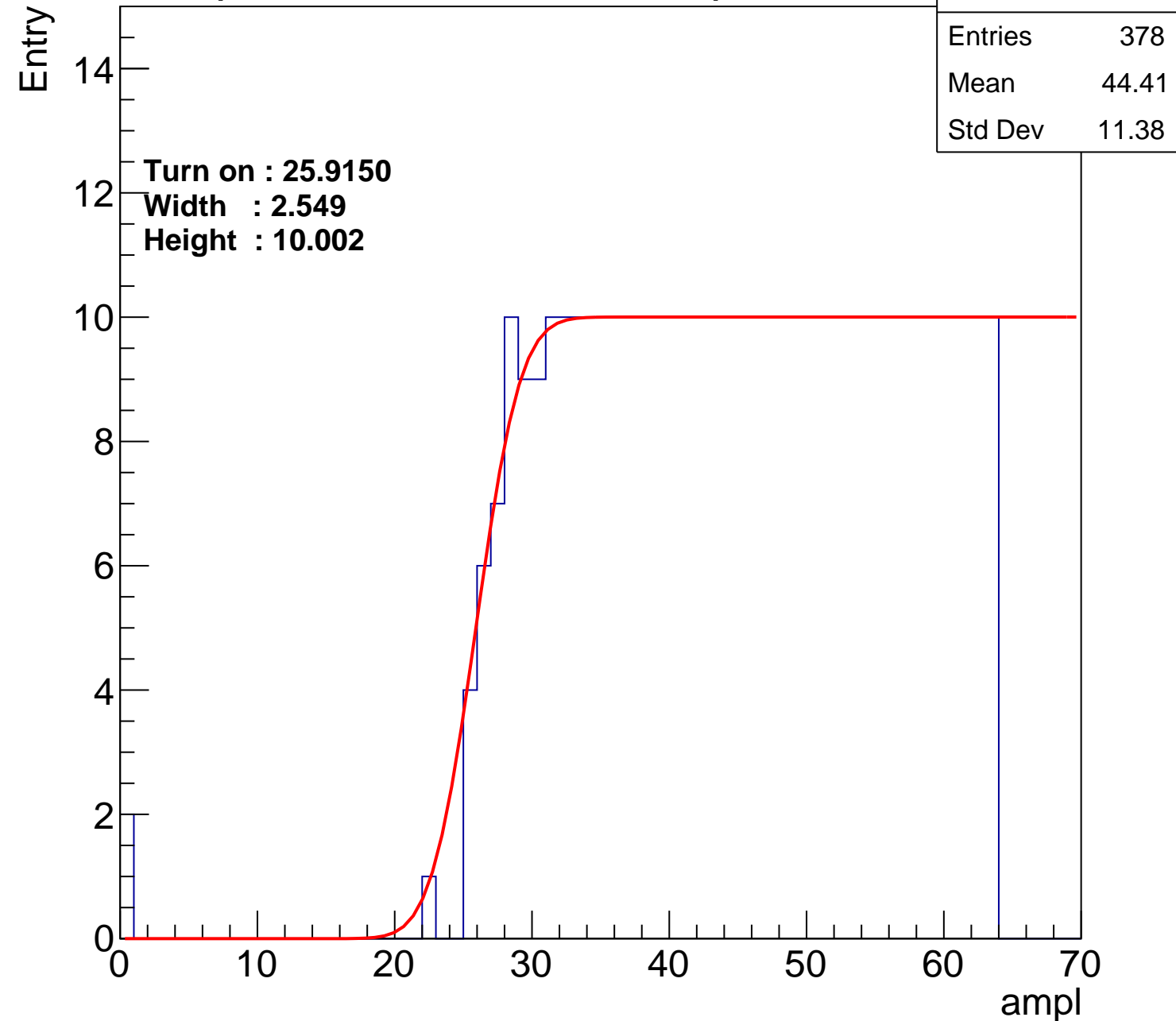
Width : 2.549

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch97

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 26.1506

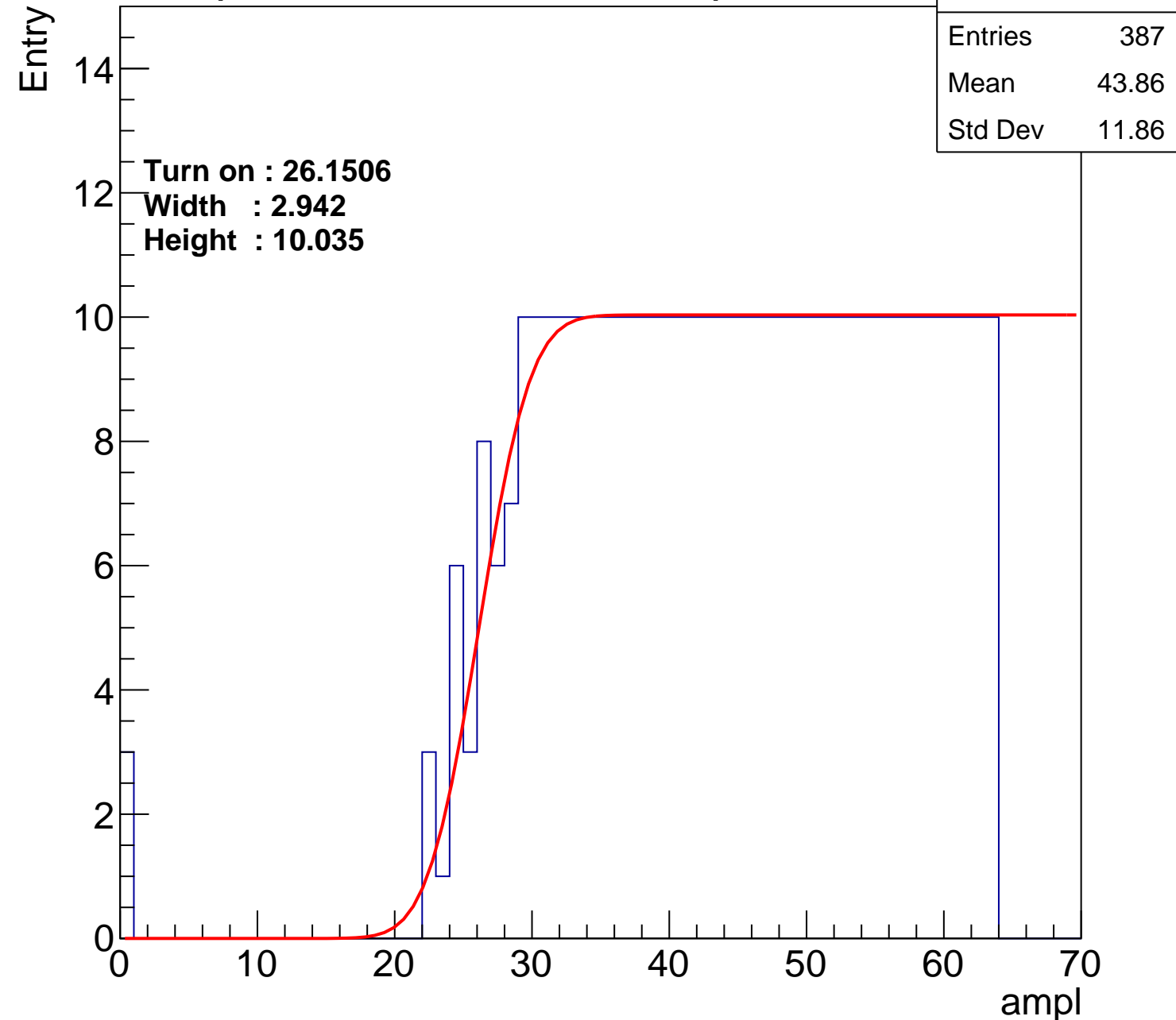
Width : 2.942

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch98

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	373
Mean	44.53
Std Dev	11.55

Turn on : 27.5115

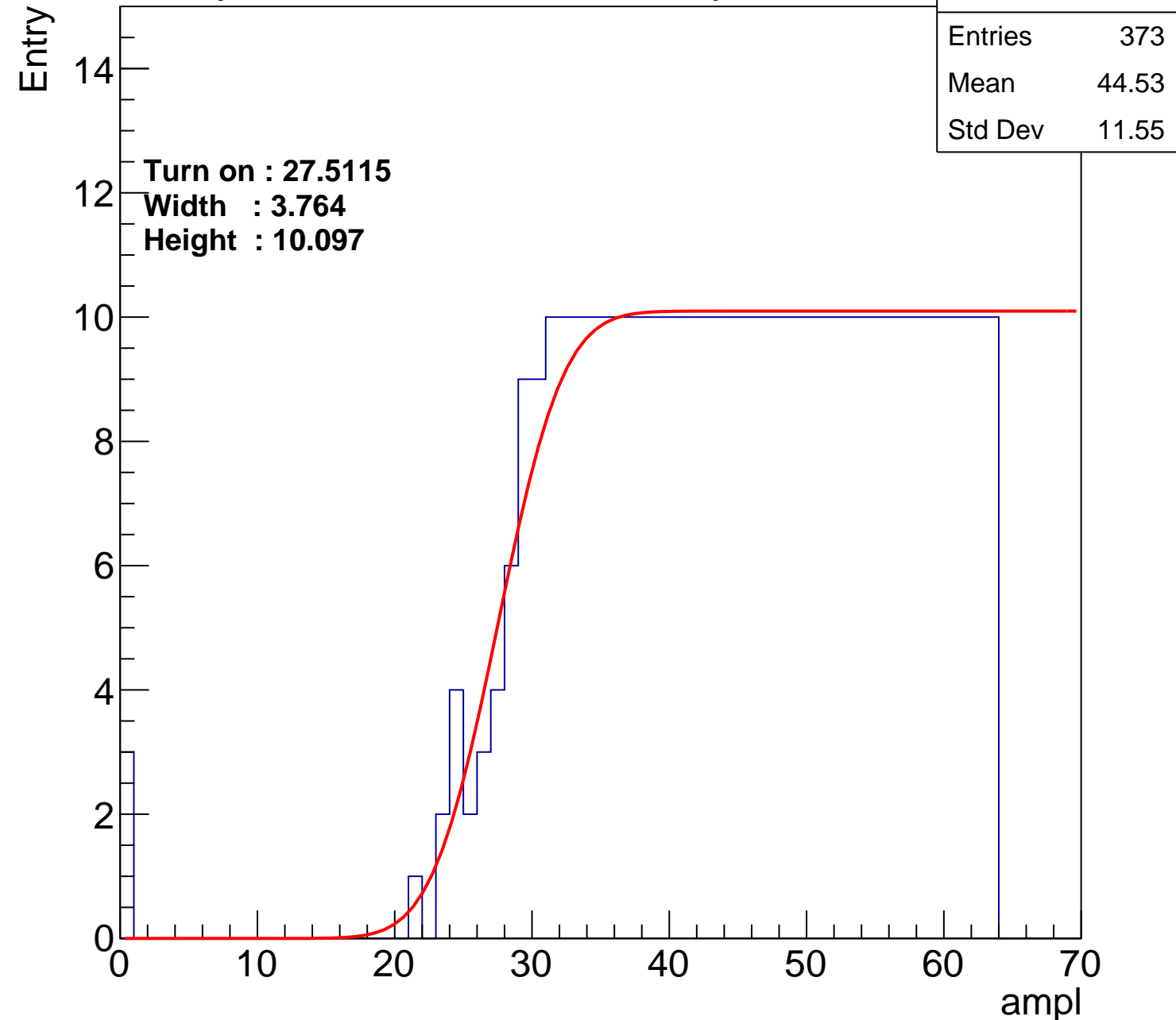
Width : 3.764

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch99

calib\_packv5\_042523\_0143.root, FC#13, port D2

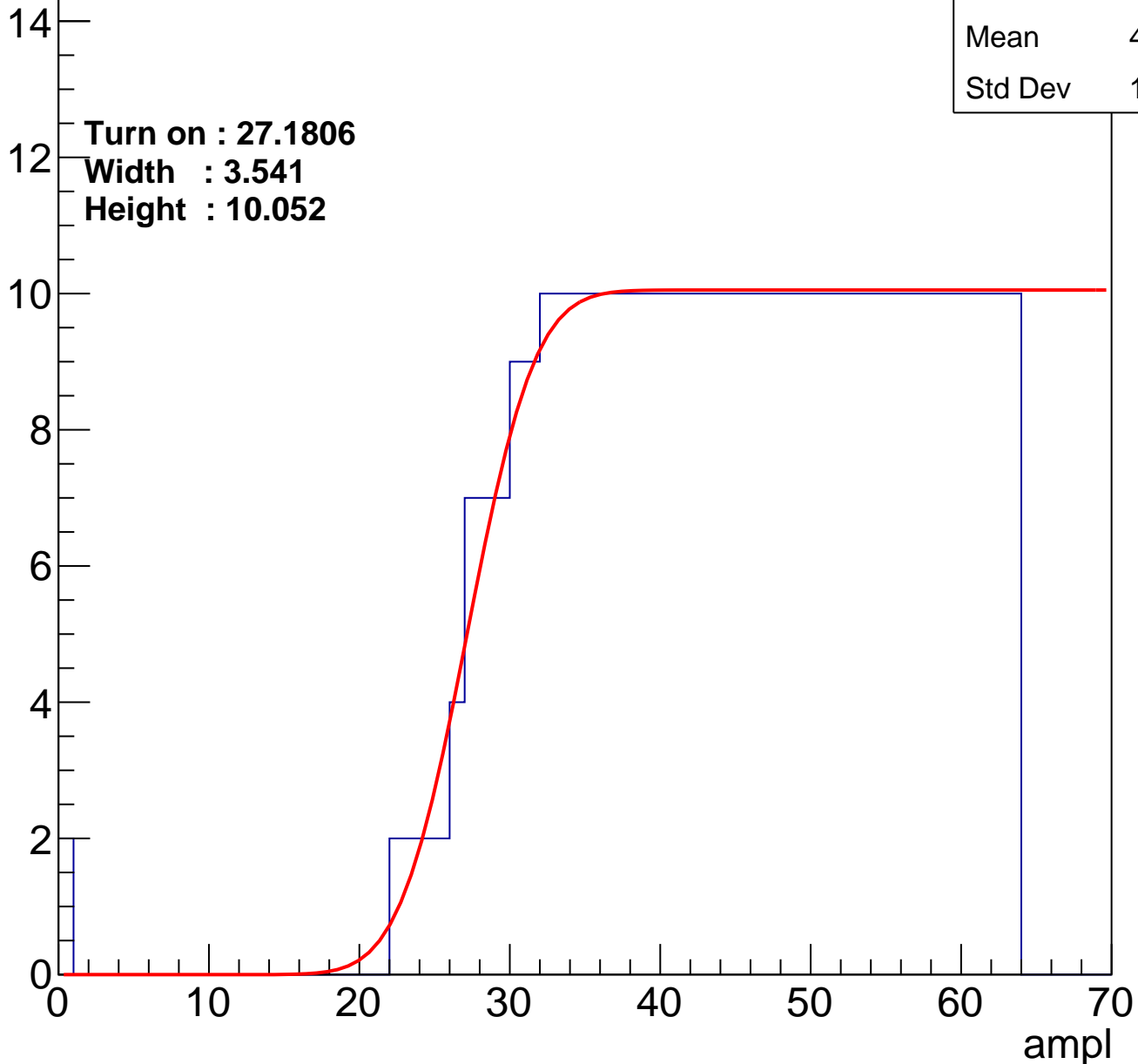
Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 27.1806

Width : 3.541

Height : 10.052

Entry



# B1L003S, U22-ch100

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.8106

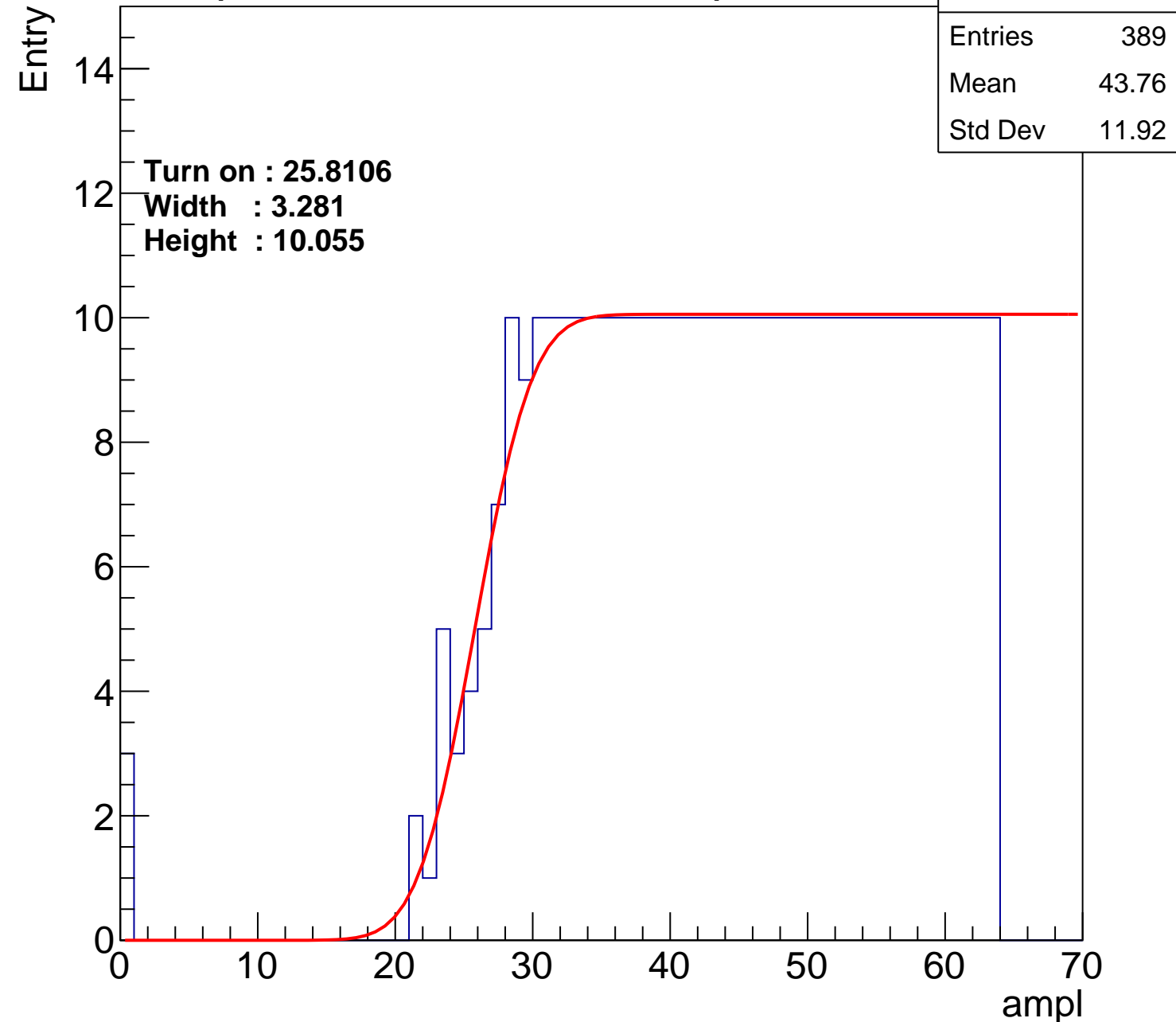
Width : 3.281

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch101

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	386
Mean	43.71
Std Dev	12.34

Turn on : 26.0246

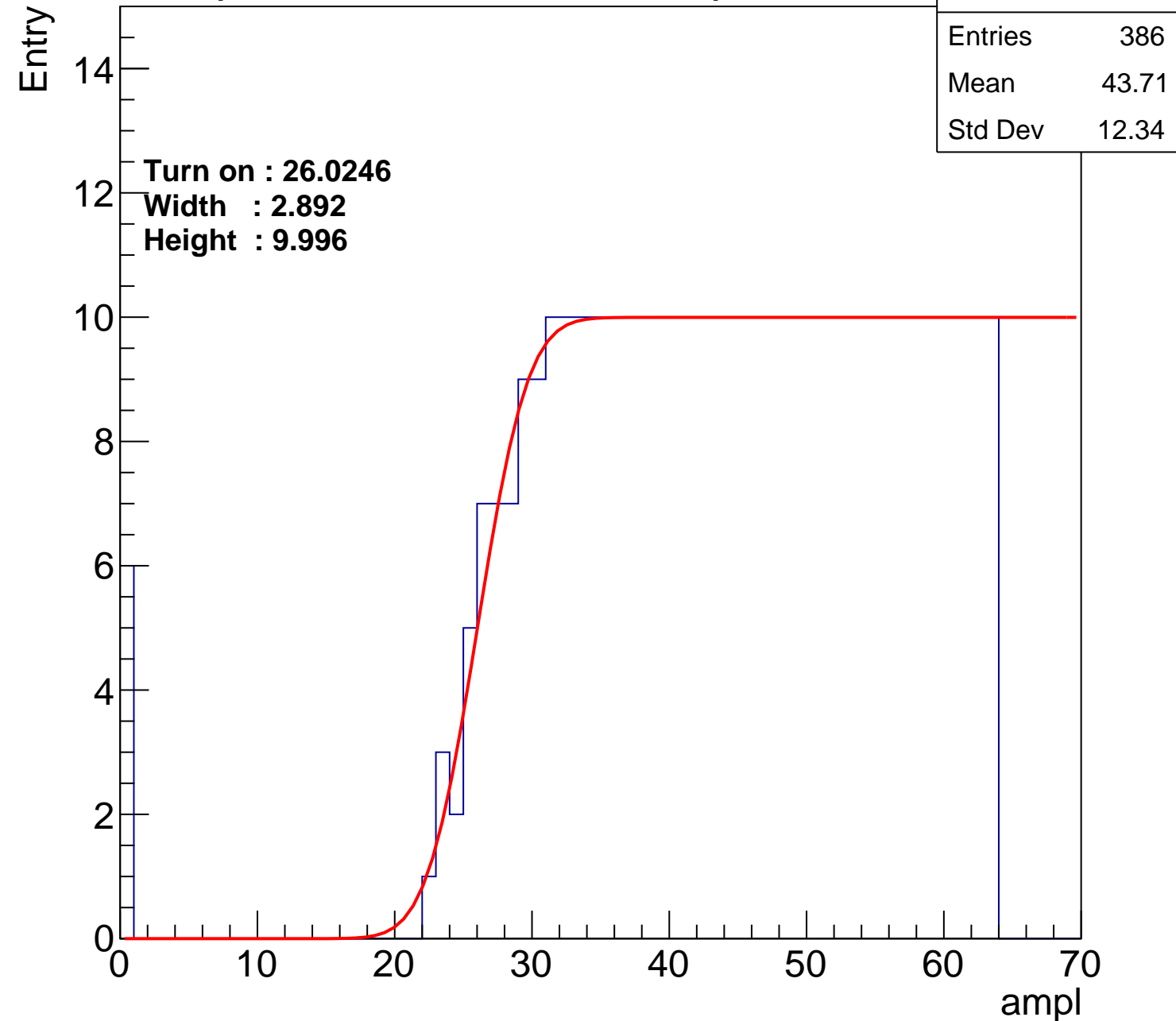
Width : 2.892

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch102

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	43.73
Std Dev	12.19

Turn on : 26.0007

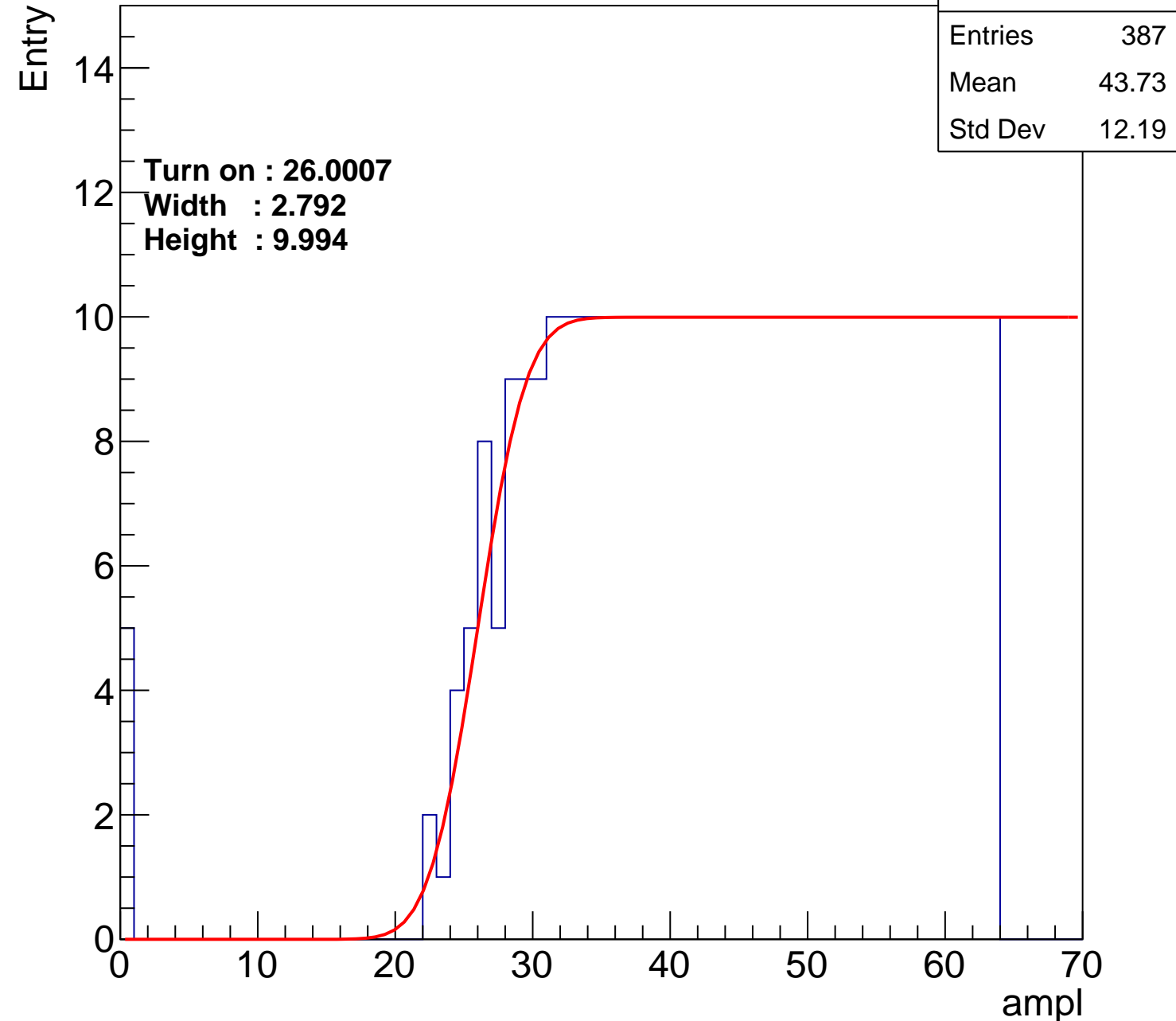
Width : 2.792

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch103

calib\_packv5\_042523\_0143.root, FC#13, port D2

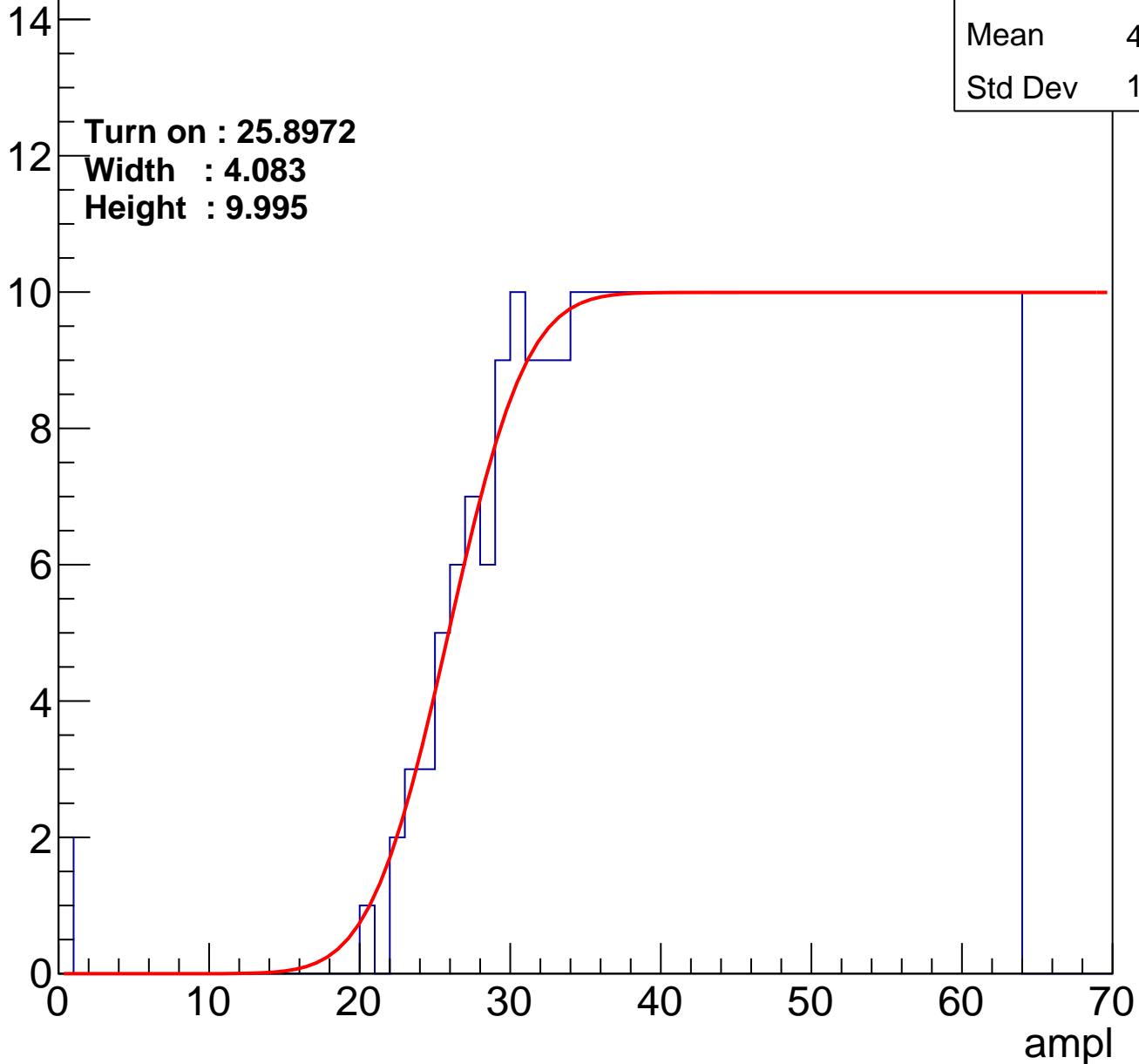
Entries	381
Mean	44.14
Std Dev	11.65

Turn on : 25.8972

Width : 4.083

Height : 9.995

Entry



# B1L003S, U22-ch104

calib\_packv5\_042523\_0143.root, FC#13, port D2

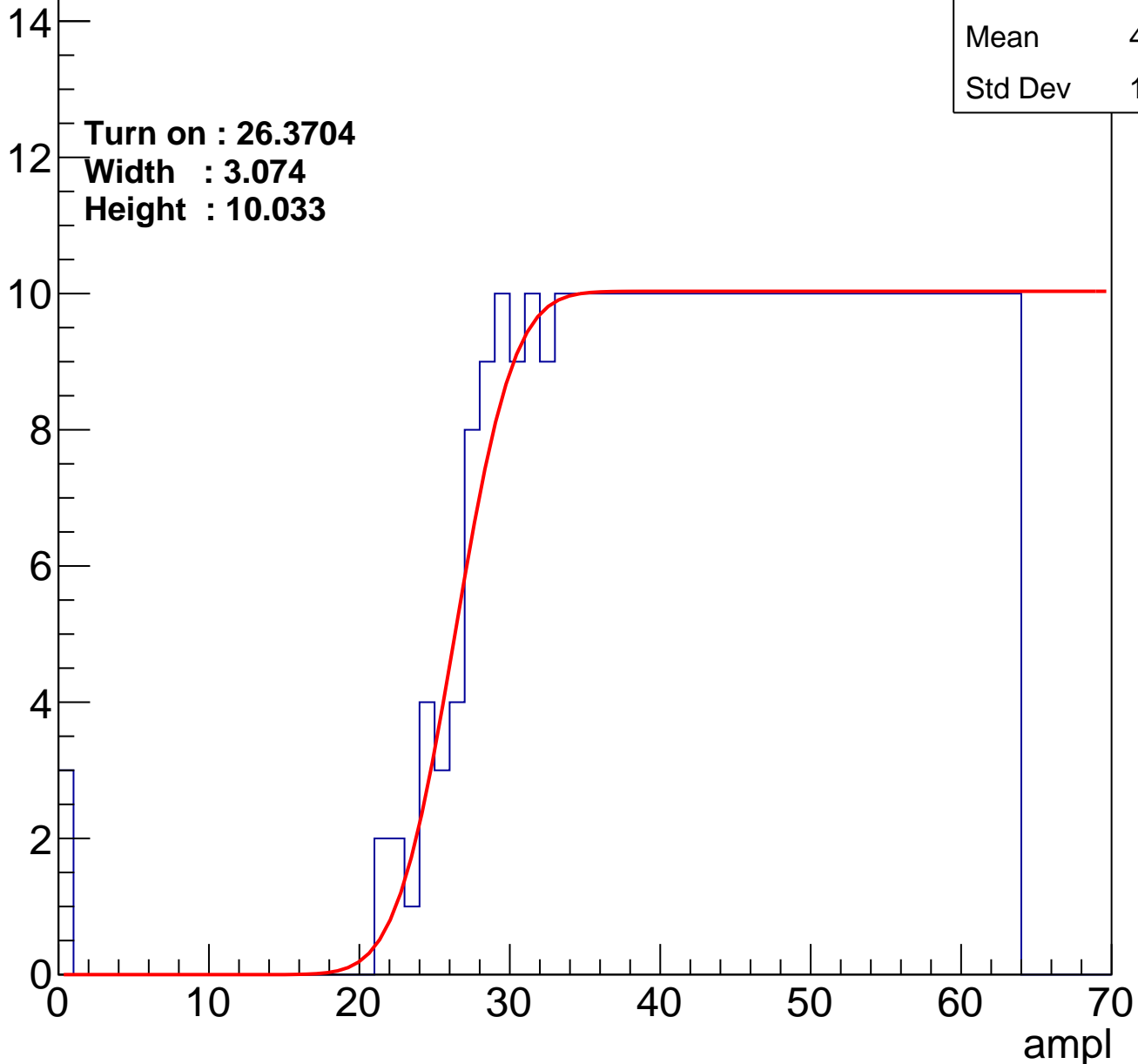
Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 26.3704

Width : 3.074

Height : 10.033

Entry



# B1L003S, U22-ch105

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	370
Mean	44.64
Std Dev	11.61

Turn on : 27.3365

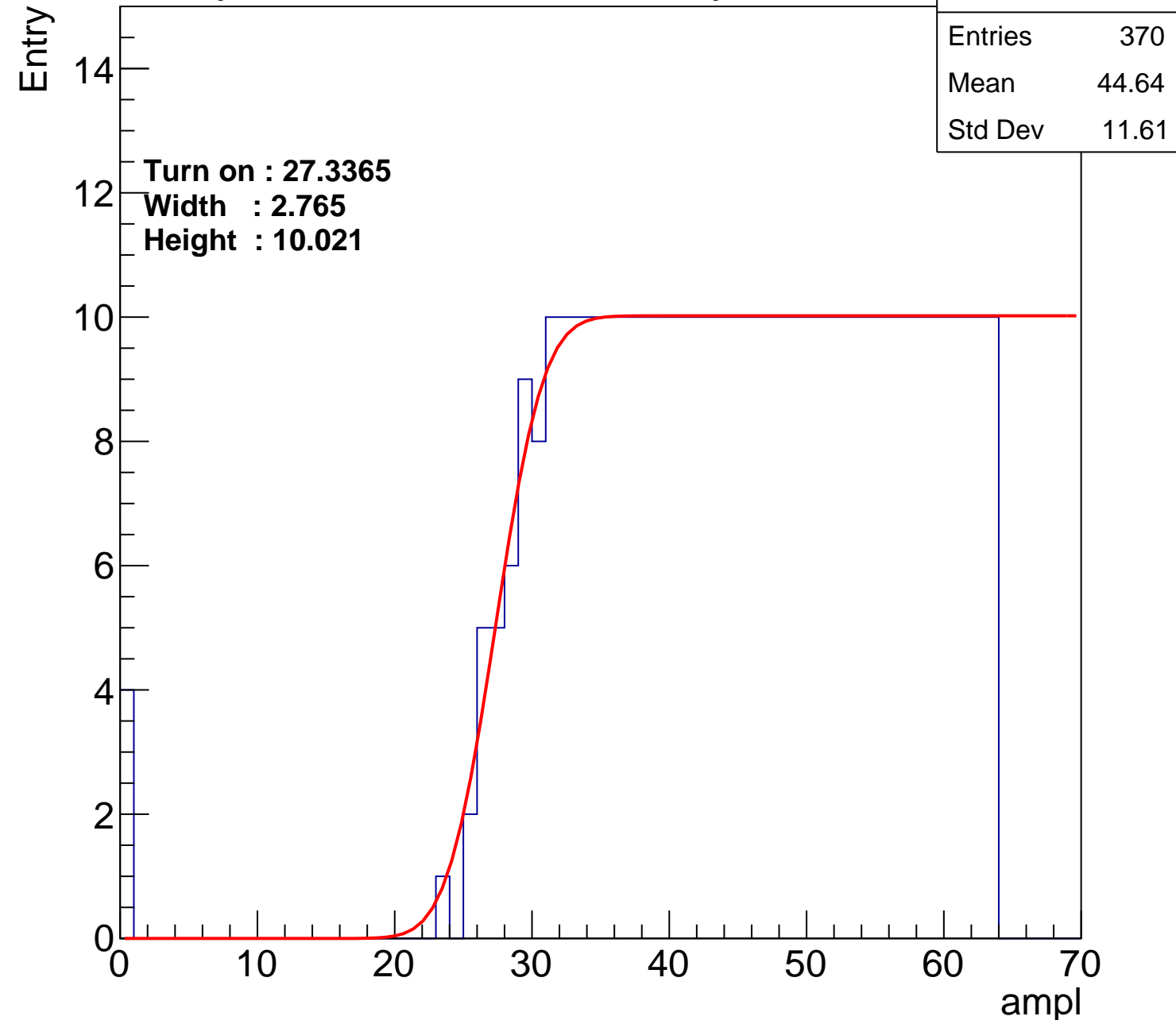
Width : 2.765

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch106

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	365
Mean	44.91
Std Dev	11.36

Turn on : 28.1033

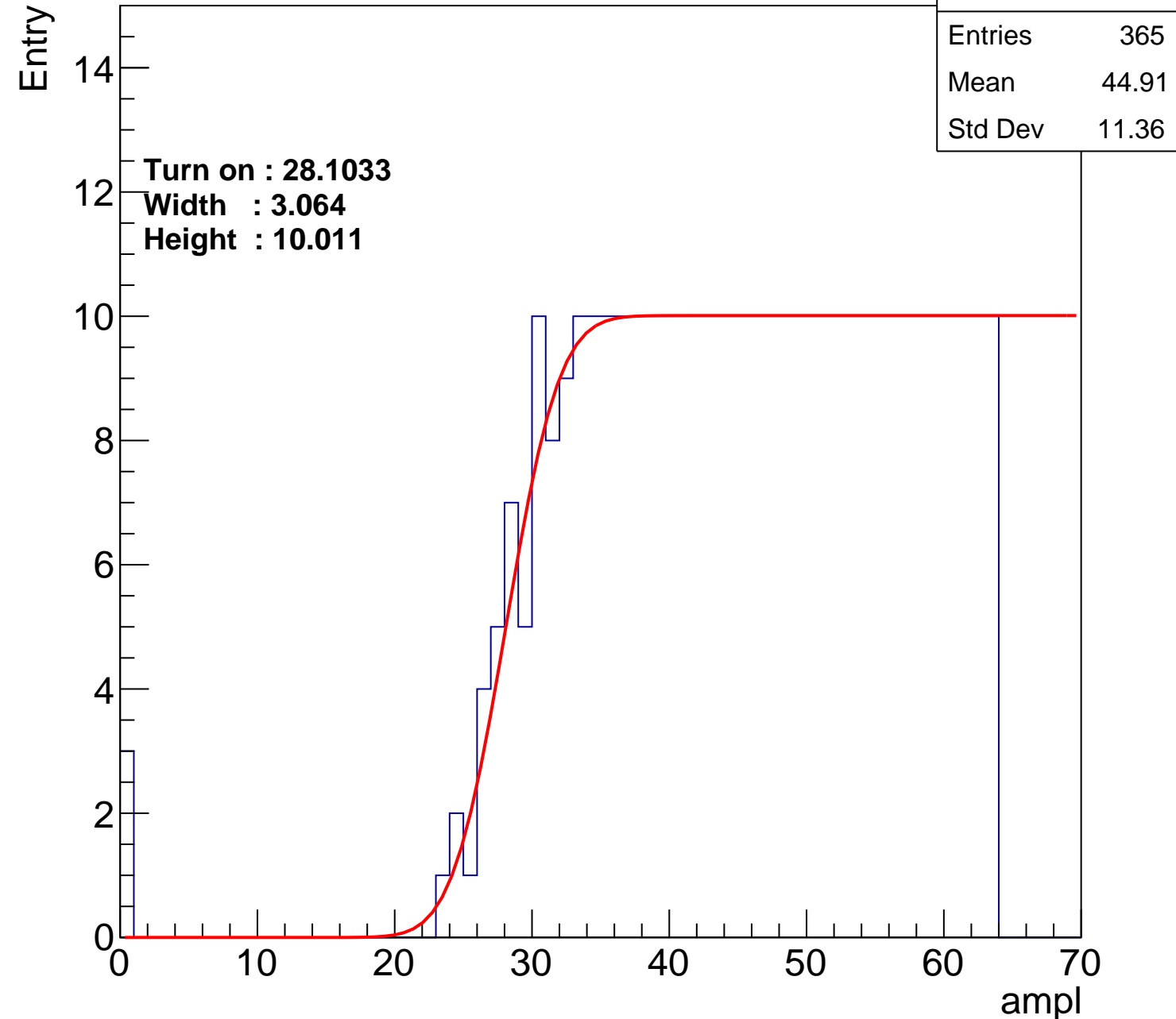
Width : 3.064

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch107

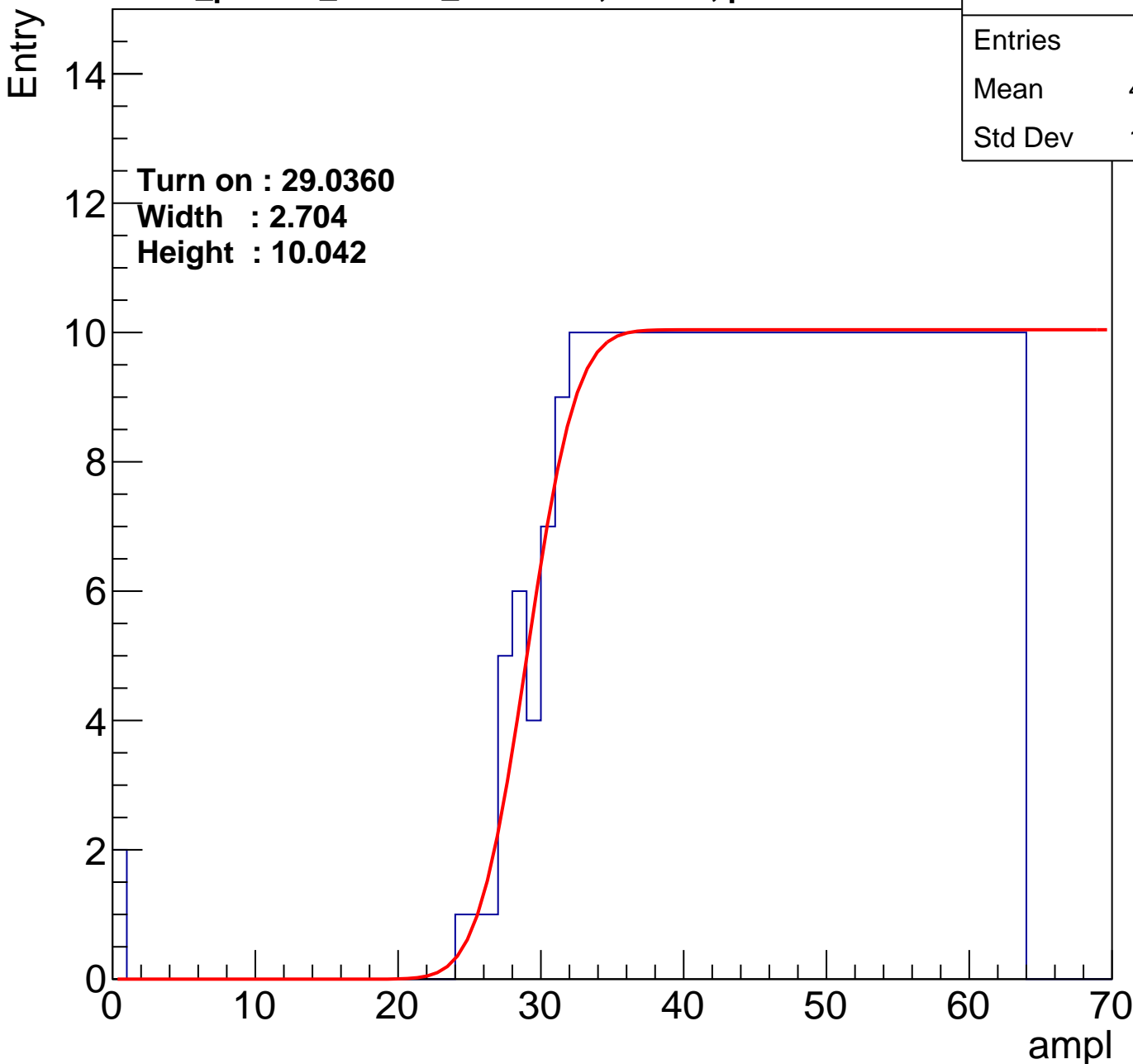
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Entries	356
Mean	45.46
Std Dev	10.88

**Turn on : 29.0360**

**Width : 2.704**

**Height : 10.042**



# B1L003S, U22-ch108

calib\_packv5\_042523\_0143.root, FC#13, port D2

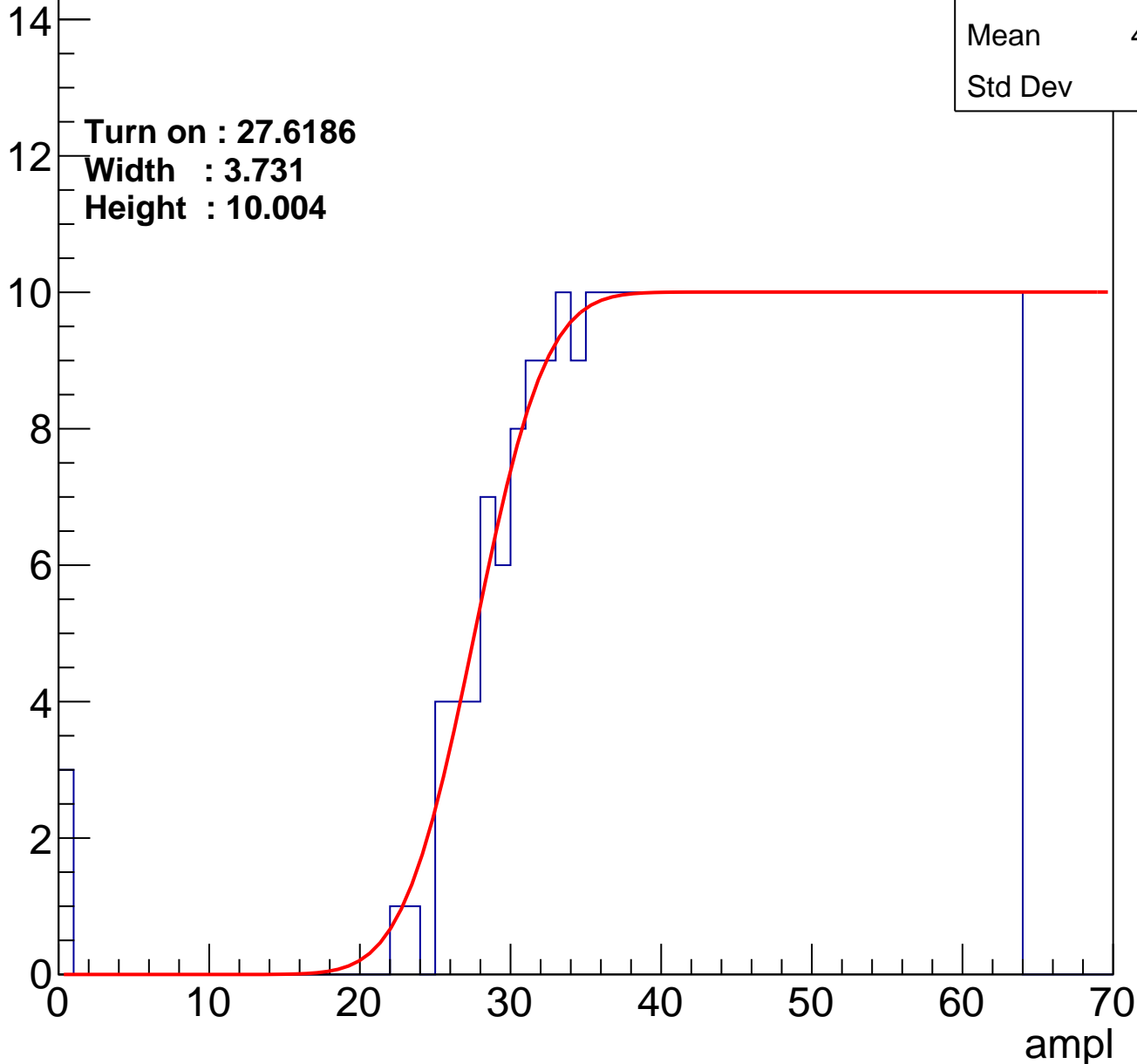
Entries	365
Mean	44.88
Std Dev	11.41

Turn on : 27.6186

Width : 3.731

Height : 10.004

Entry



# B1L003S, U22-ch109

calib\_packv5\_042523\_0143.root, FC#13, port D2

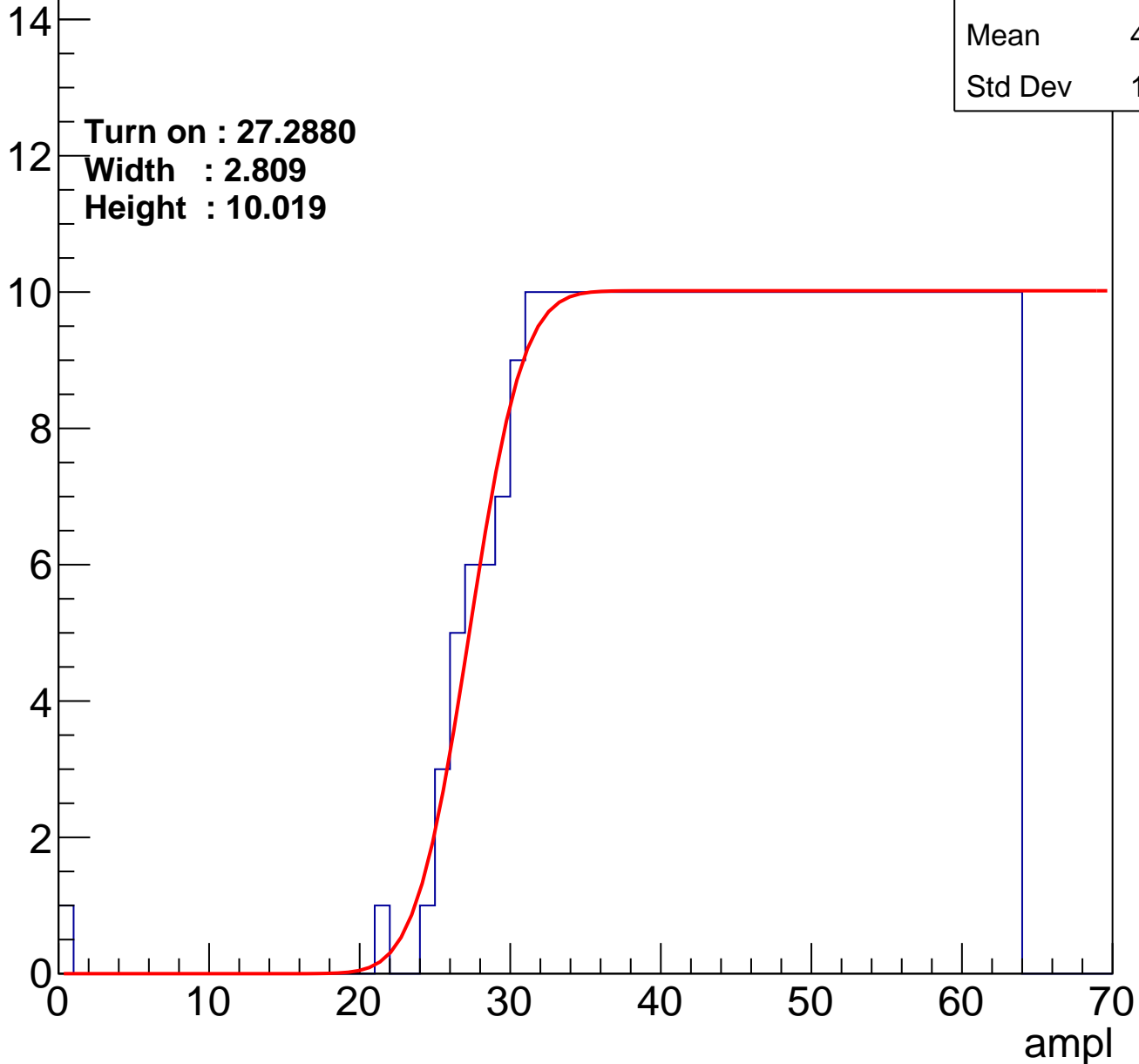
Entries	369
Mean	44.89
Std Dev	11.02

Turn on : 27.2880

Width : 2.809

Height : 10.019

Entry



# B1L003S, U22-ch110

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 25.9542

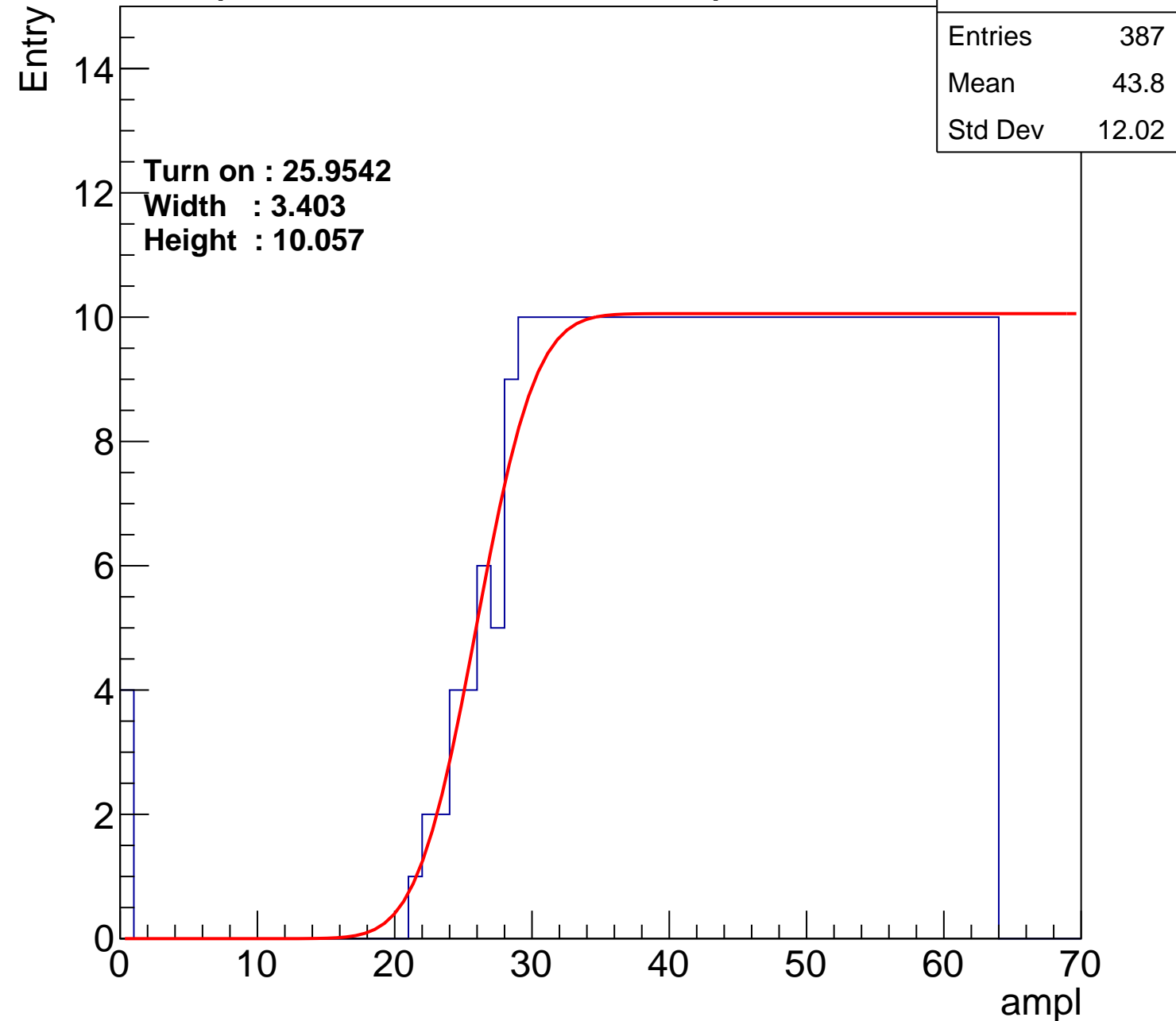
Width : 3.403

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch111

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	375
Mean	44.34
Std Dev	11.81

Turn on : 27.5523

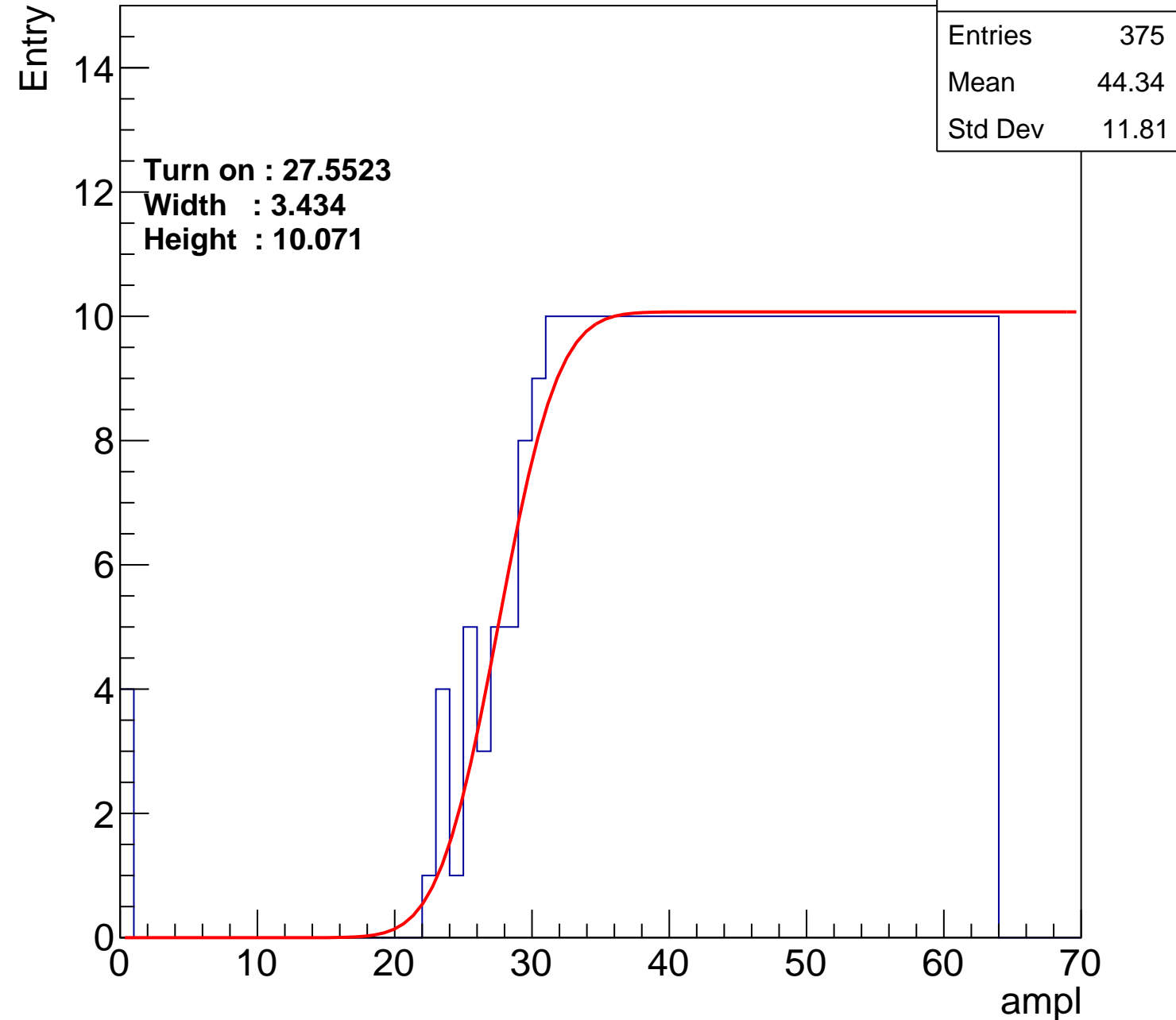
Width : 3.434

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch112

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.1205

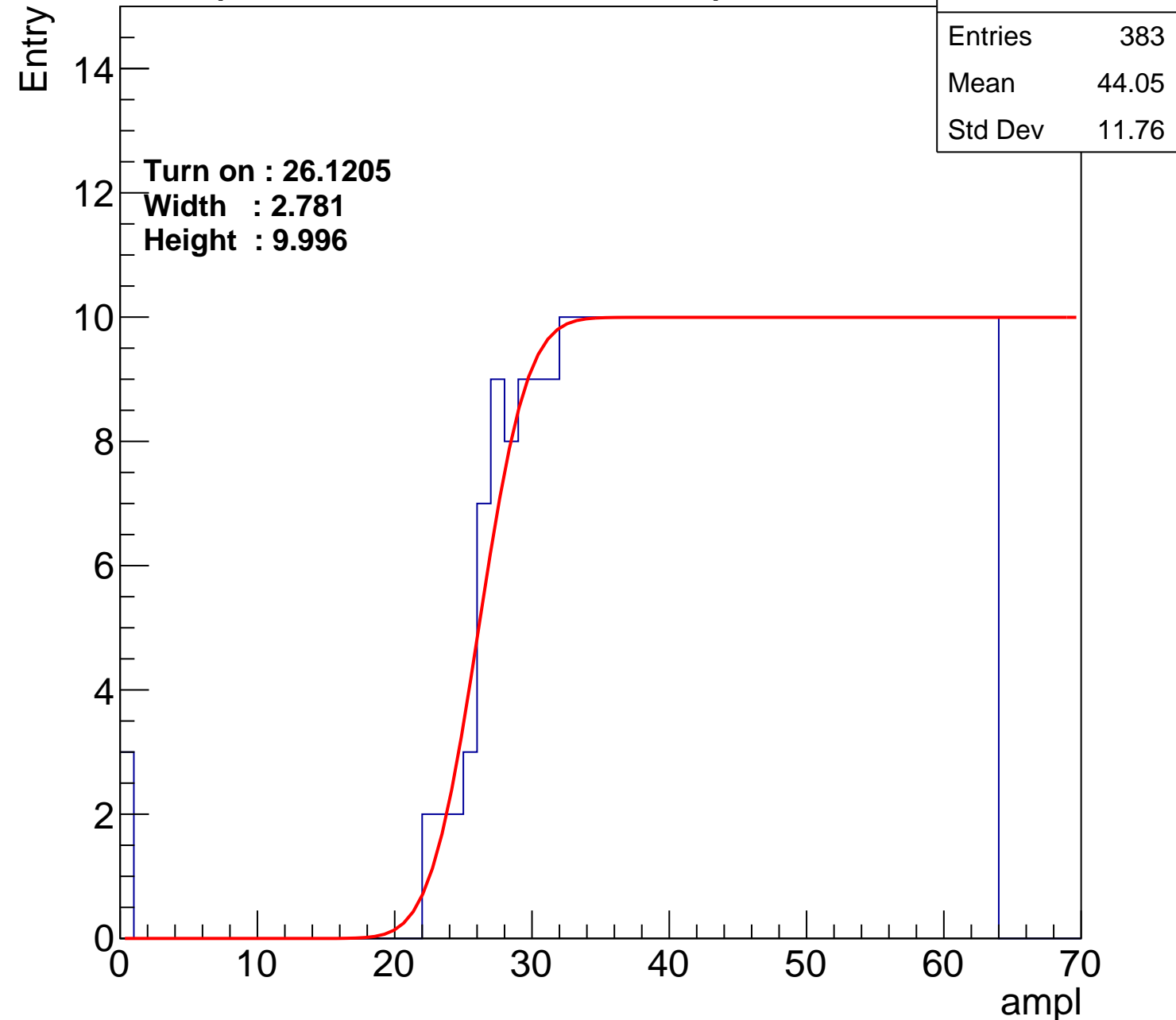
Width : 2.781

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch113

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	362
Mean	44.93
Std Dev	11.67

Turn on : 28.6054

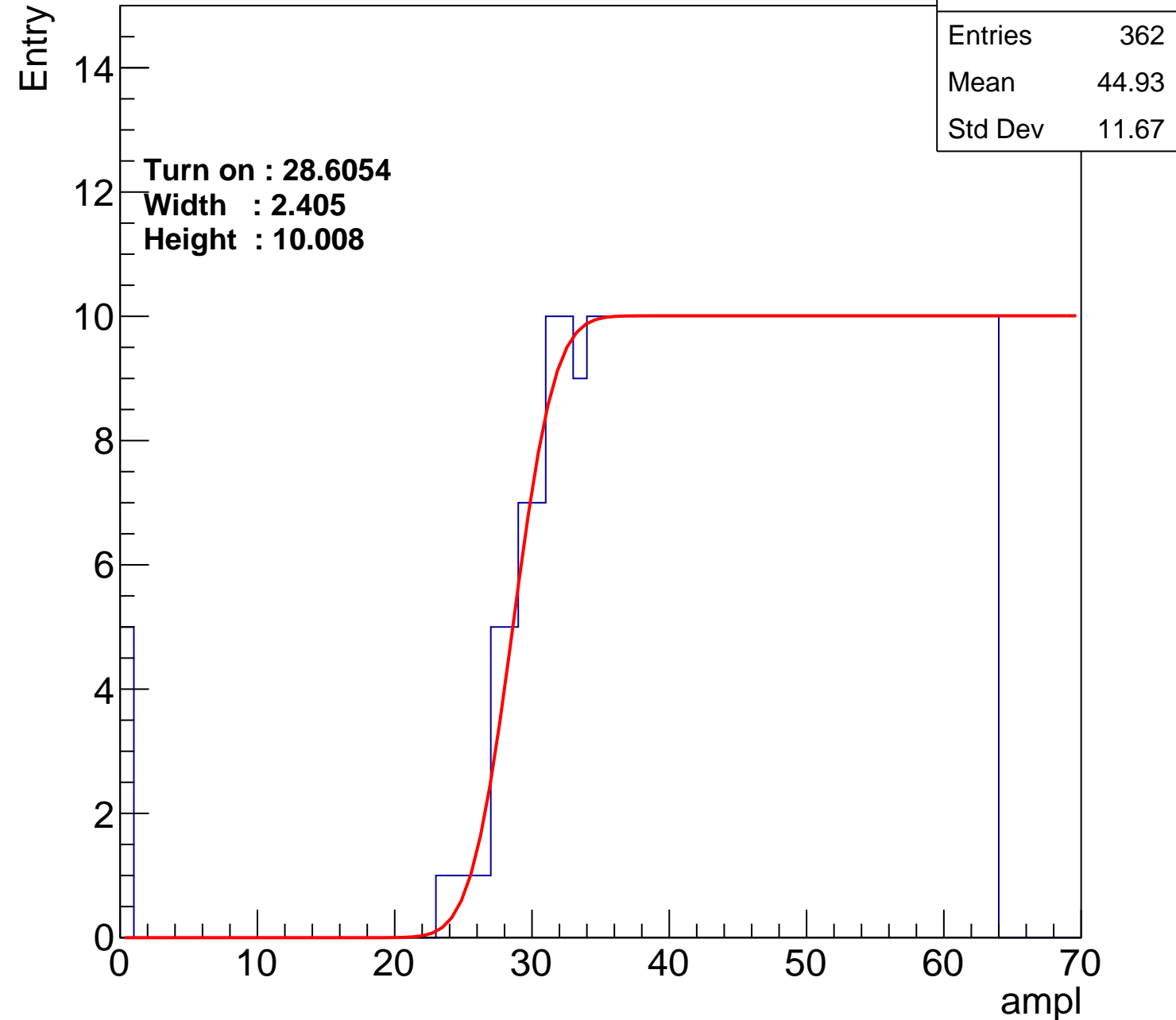
Width : 2.405

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch114

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 25.4602

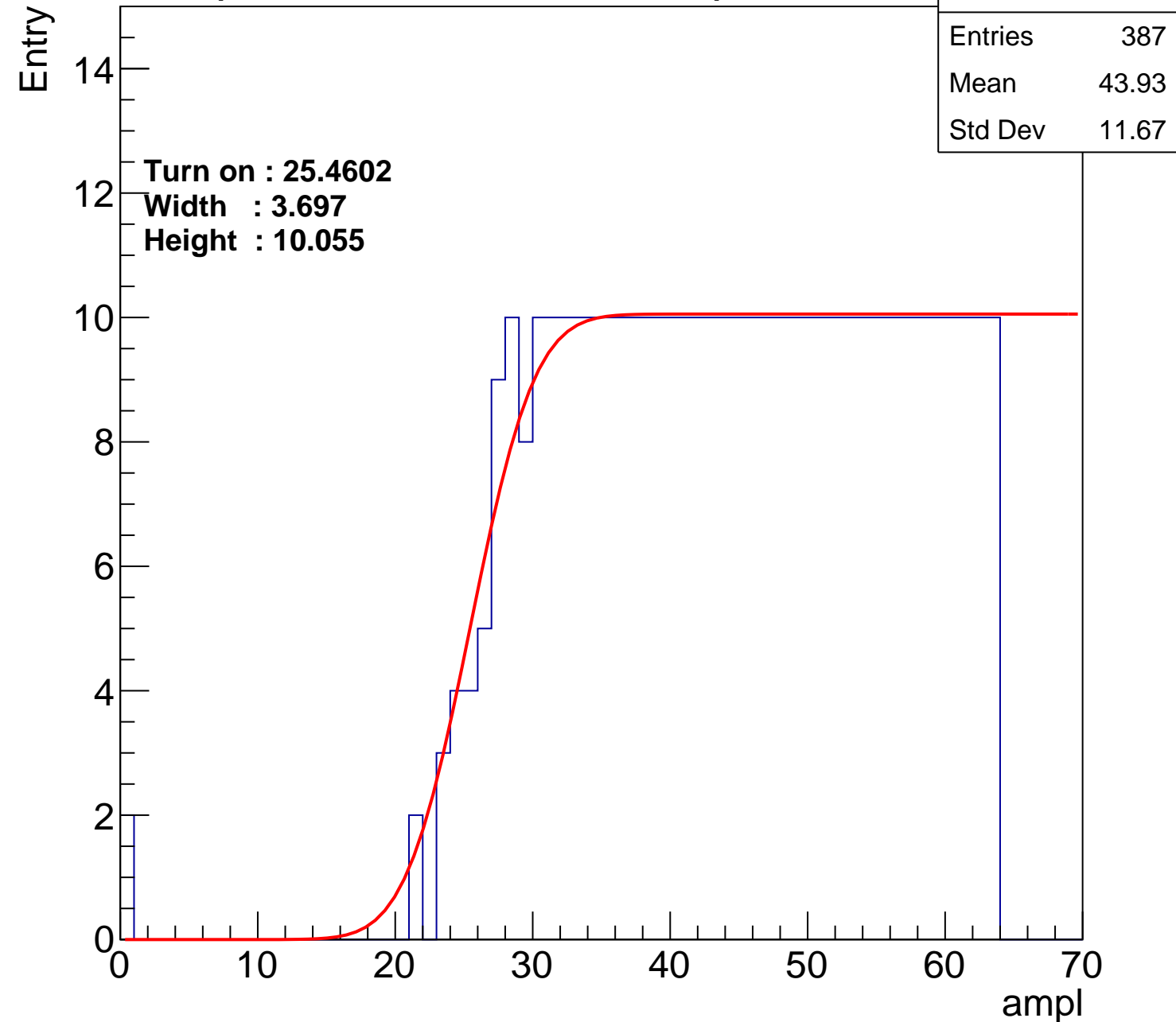
Width : 3.697

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch115

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	349
Mean	45.87
Std Dev	10.5

Turn on : 29.5919

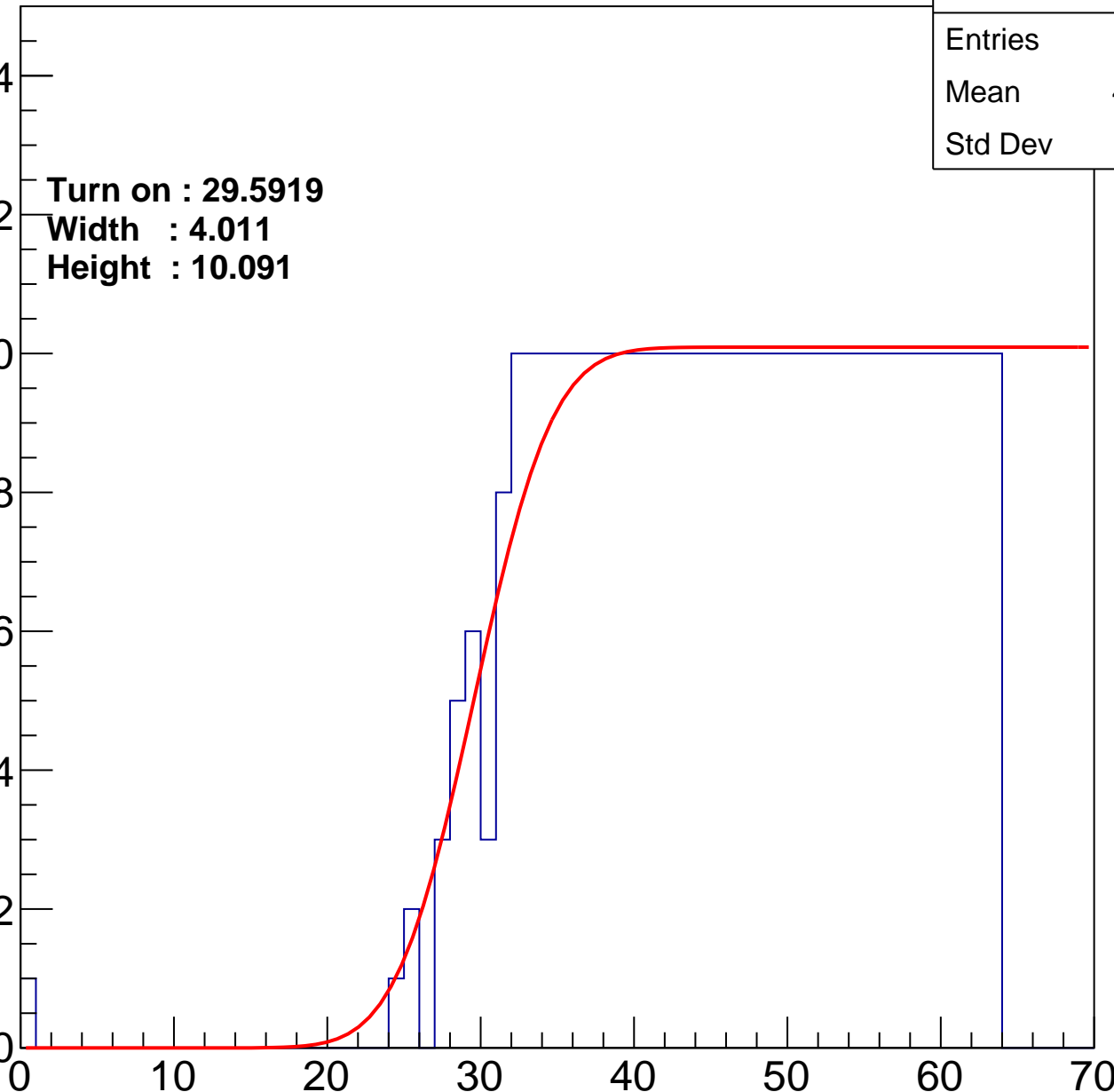
Width : 4.011

Height : 10.091

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch116

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	354
Mean	45.58
Std Dev	10.68

Turn on : 28.5348

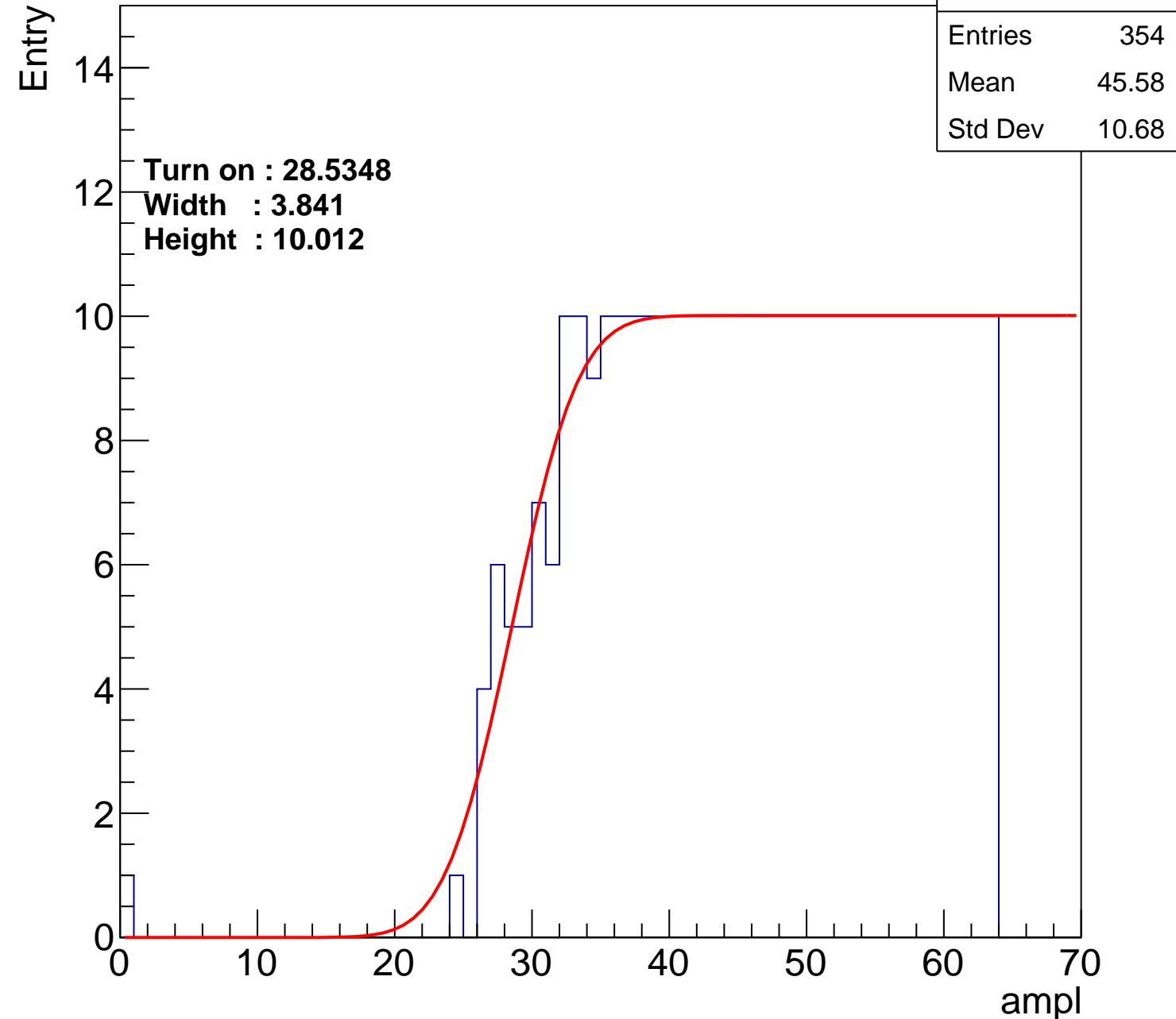
Width : 3.841

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch117

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	376
Mean	44.36
Std Dev	11.63

Turn on : 27.1438

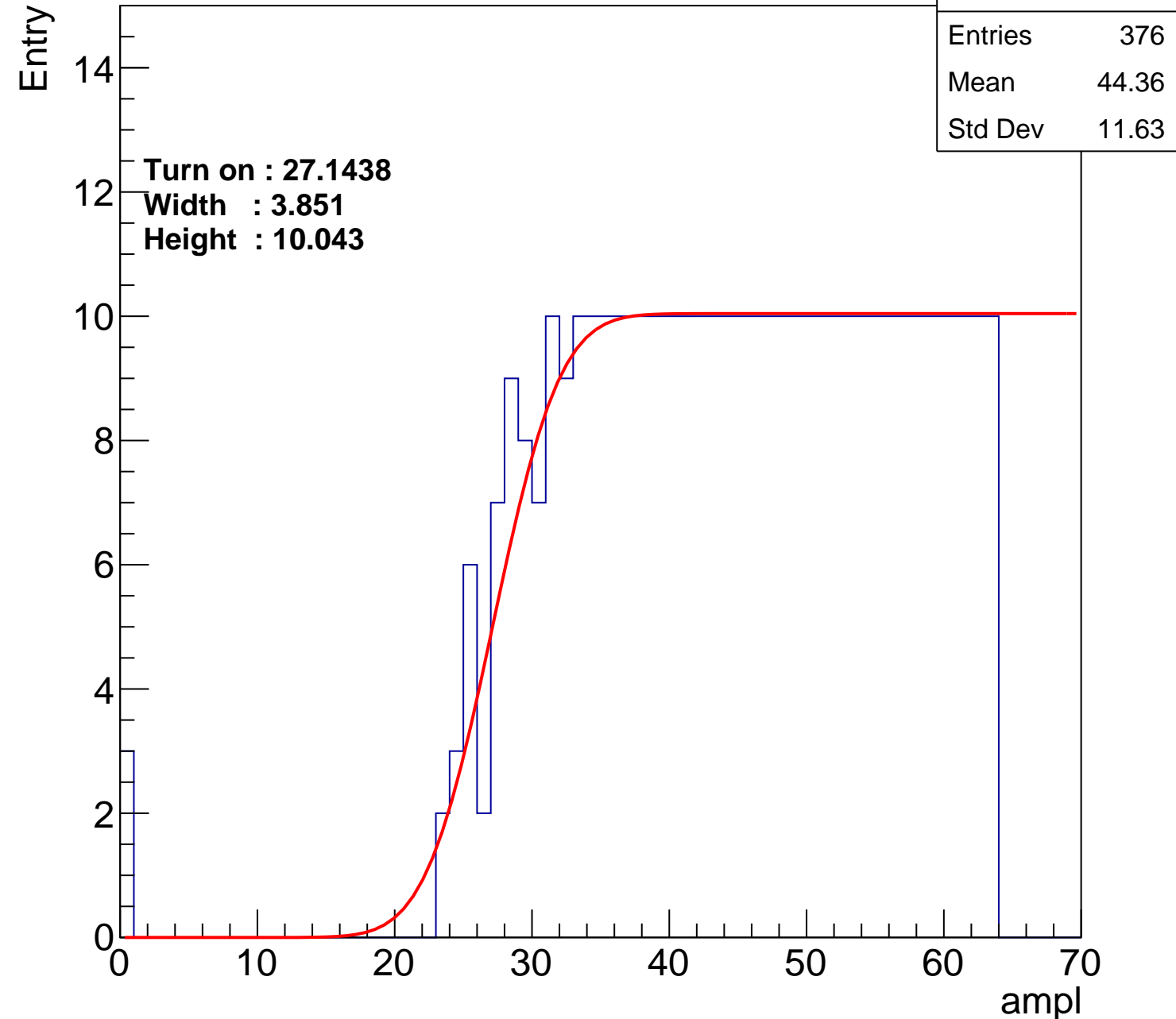
Width : 3.851

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch118

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Entries	362
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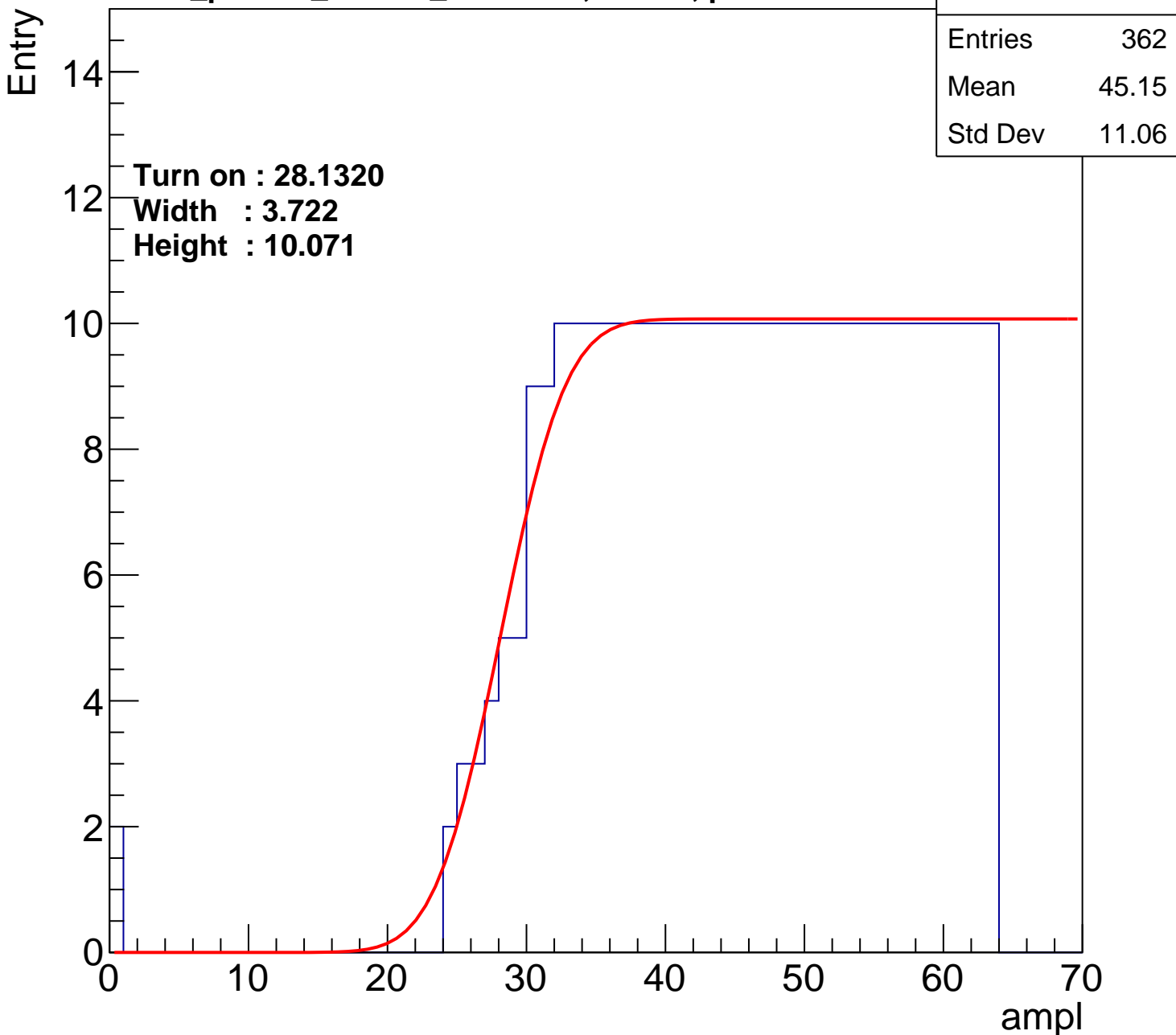
Mean	45.15
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Std Dev	11.06
---------	-------

**Turn on : 28.1320**

**Width : 3.722**

**Height : 10.071**





# B1L003S, U22-ch119

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.1215

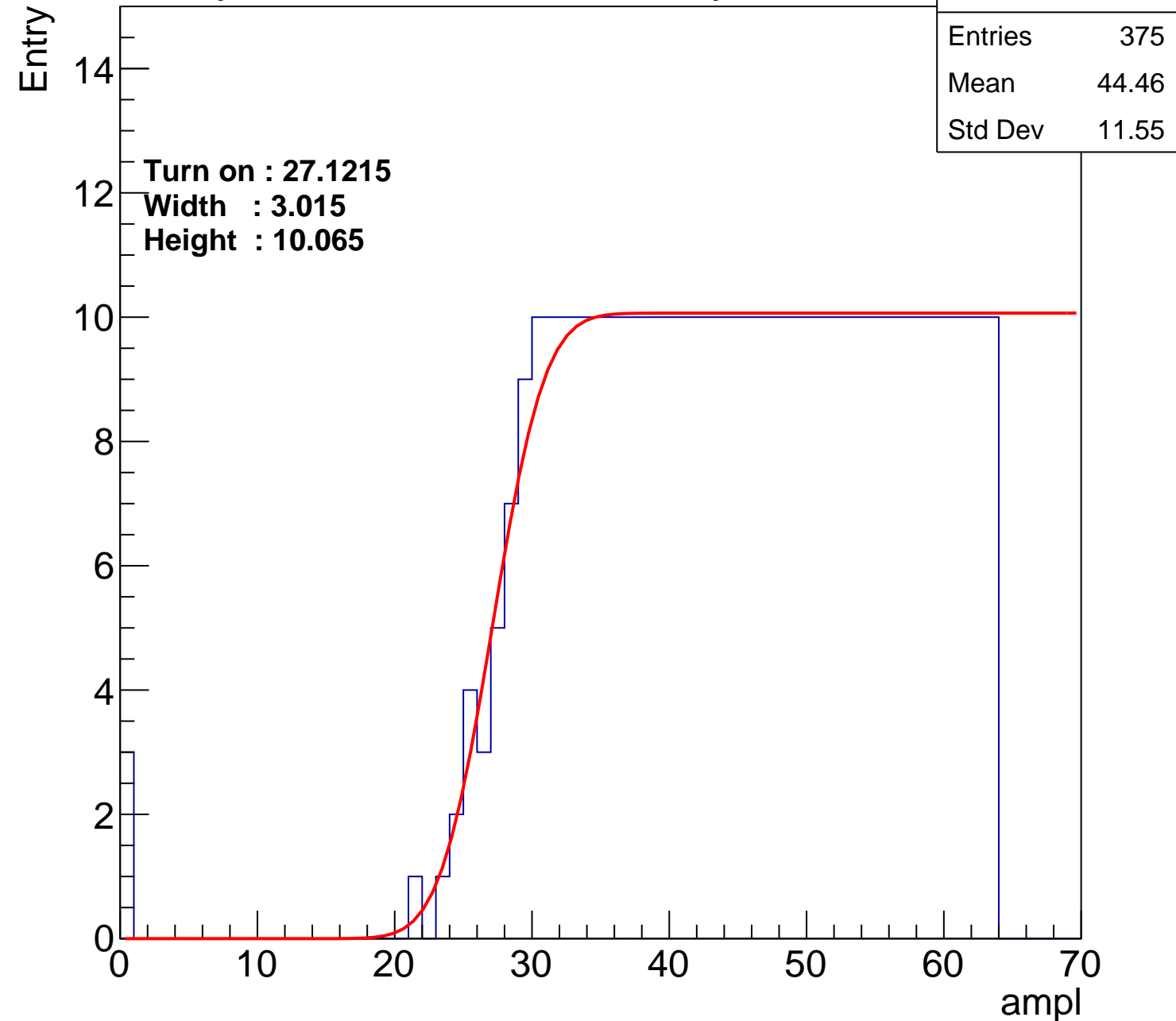
Width : 3.015

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch120

calib\_packv5\_042523\_0143.root, FC#13, port D2

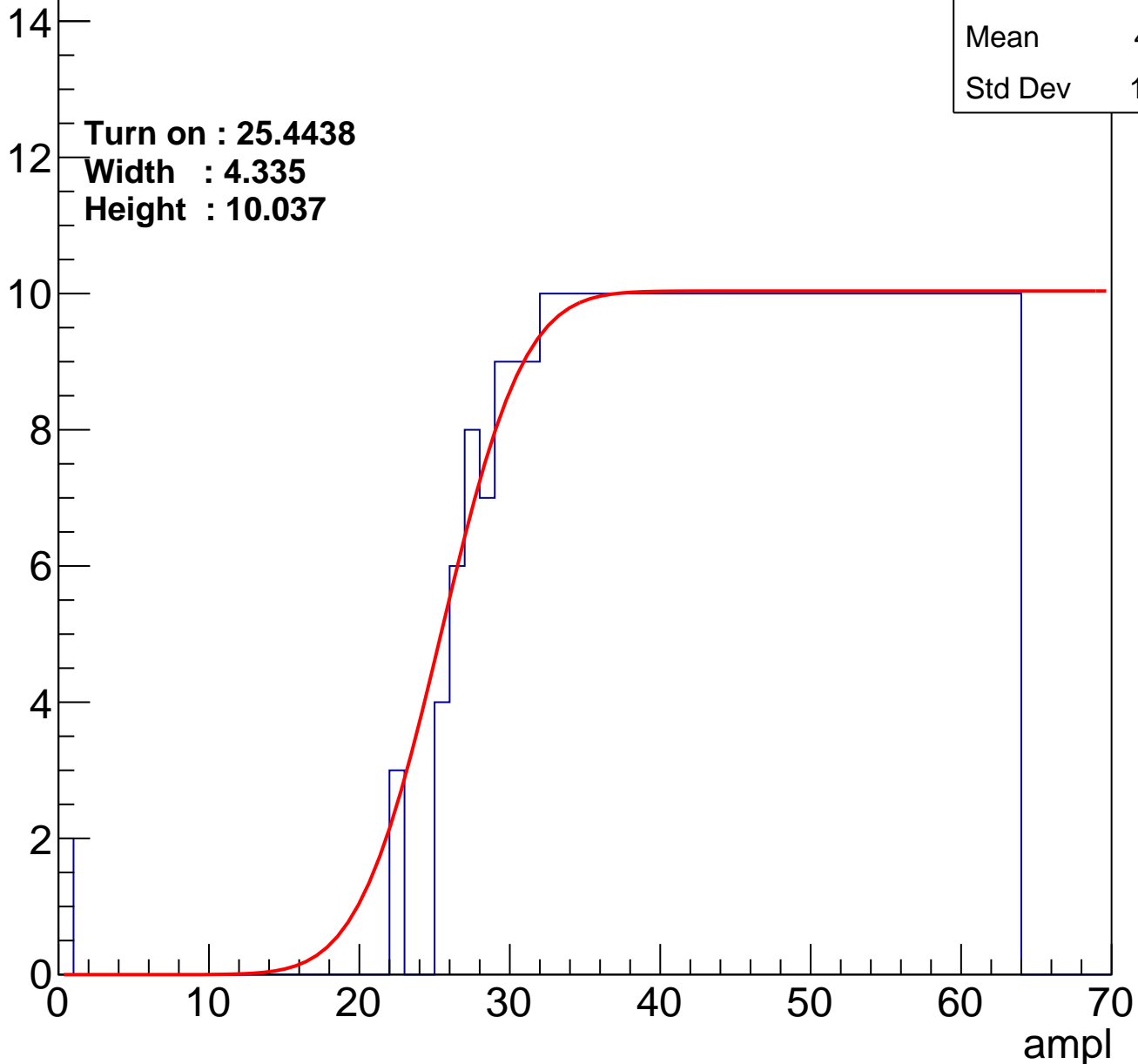
Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 25.4438

Width : 4.335

Height : 10.037

Entry



# B1L003S, U22-ch121

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	395
Mean	43.54
Std Dev	11.87

Turn on : 25.1688

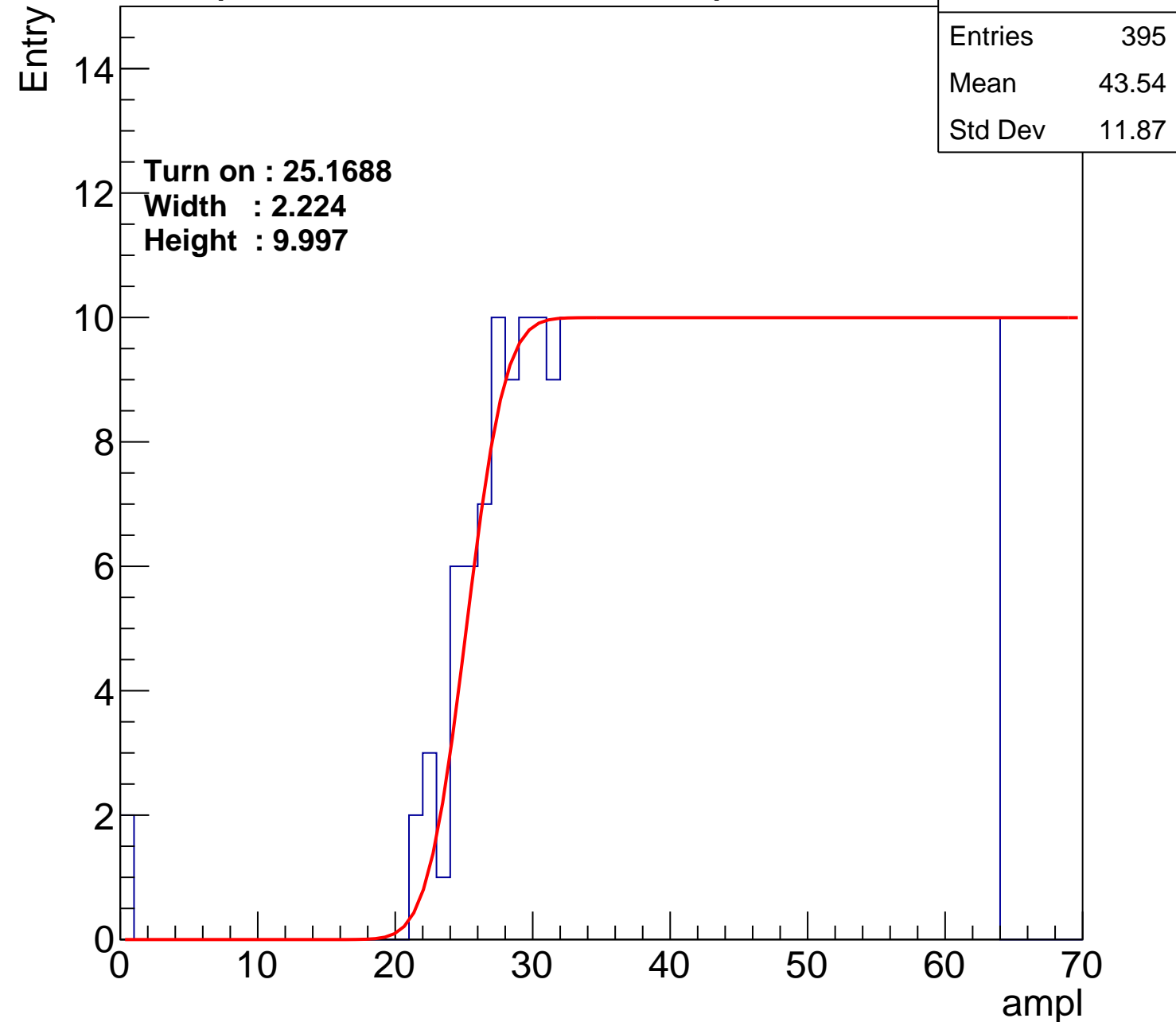
Width : 2.224

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch122

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	370
Mean	44.64
Std Dev	11.61

Turn on : 27.7017

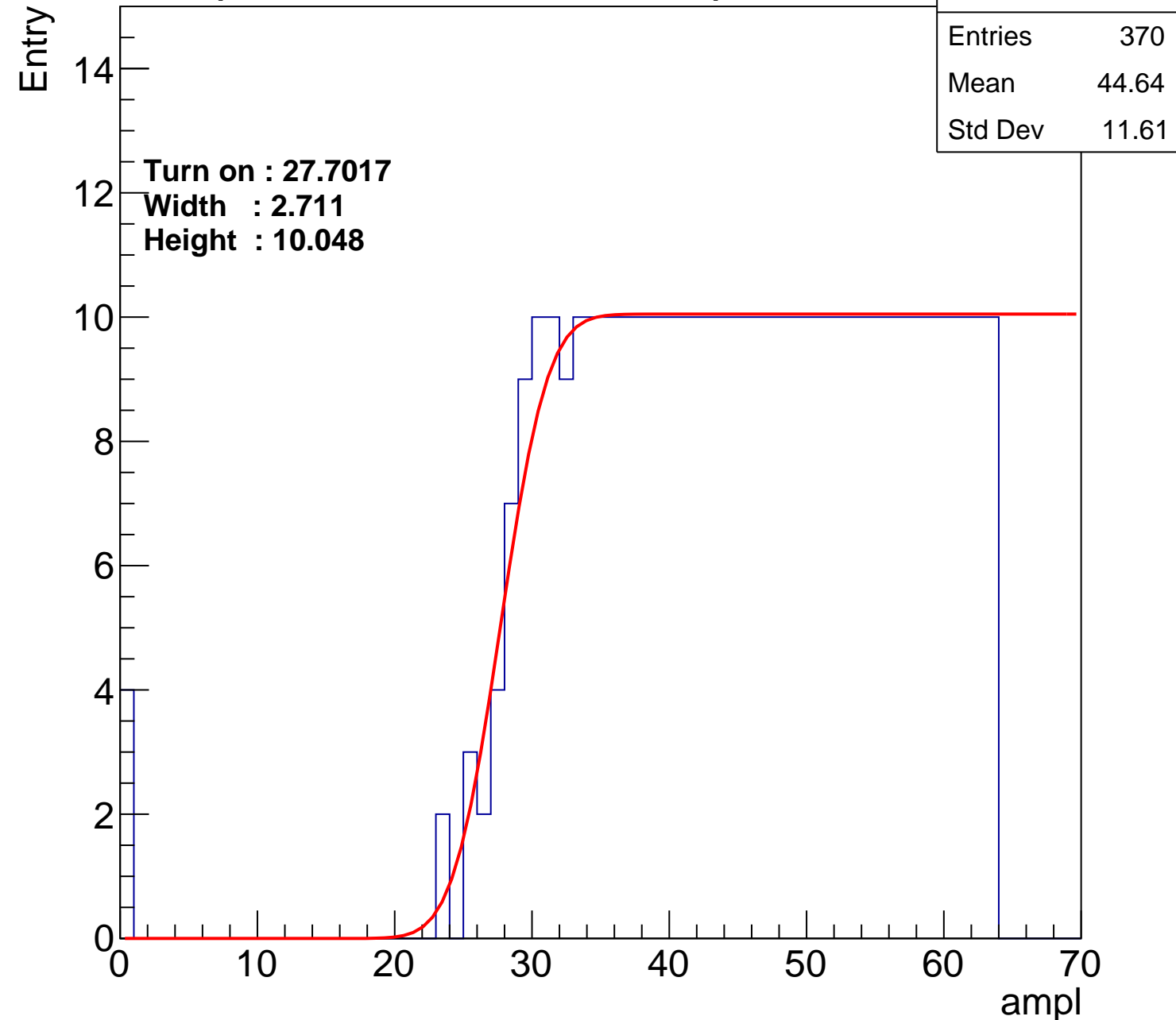
Width : 2.711

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch123

calib\_packv5\_042523\_0143.root, FC#13, port D2

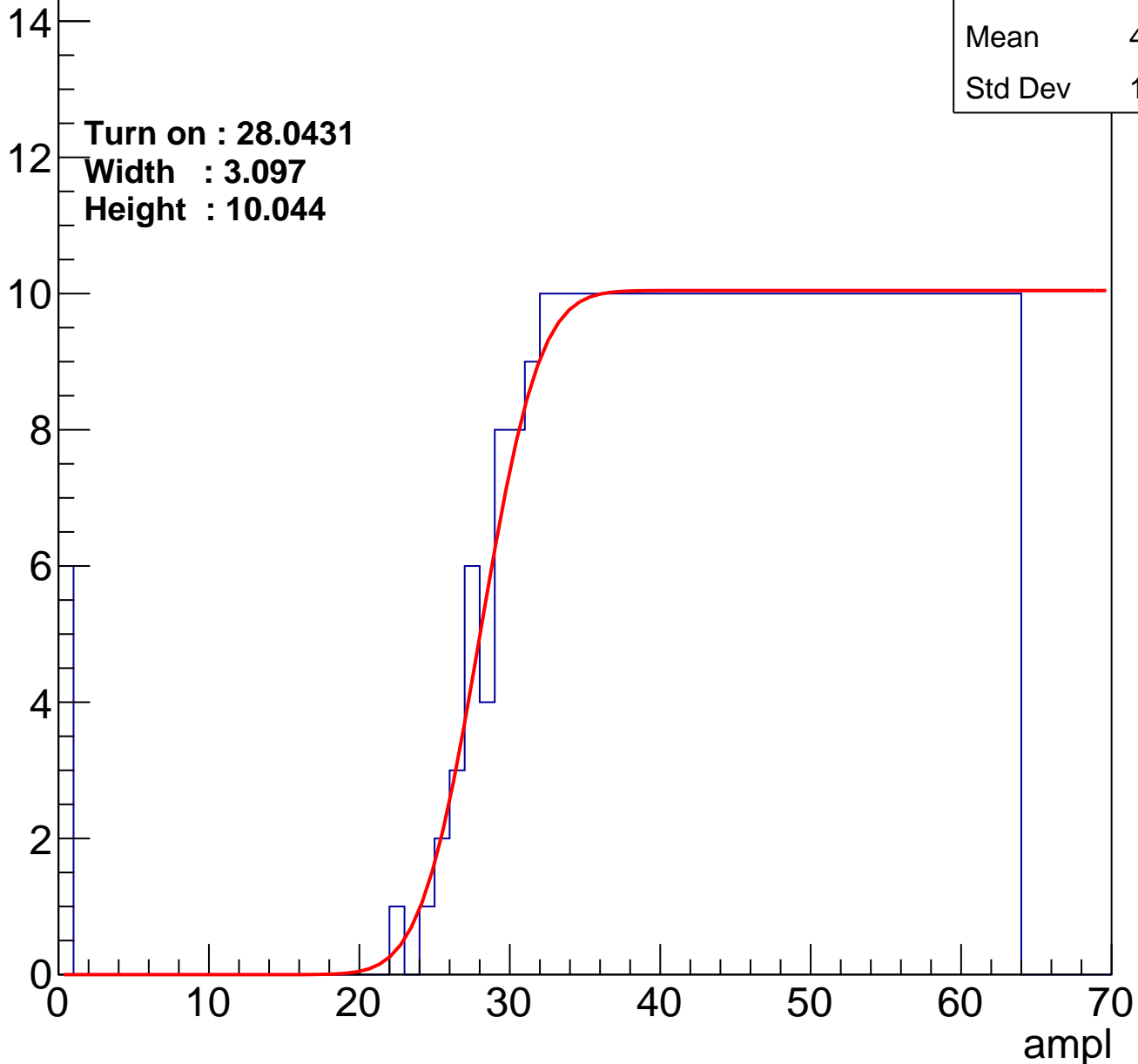
Entries	368
Mean	44.56
Std Dev	11.99

Turn on : 28.0431

Width : 3.097

Height : 10.044

Entry



# B1L003S, U22-ch124

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 25.6879

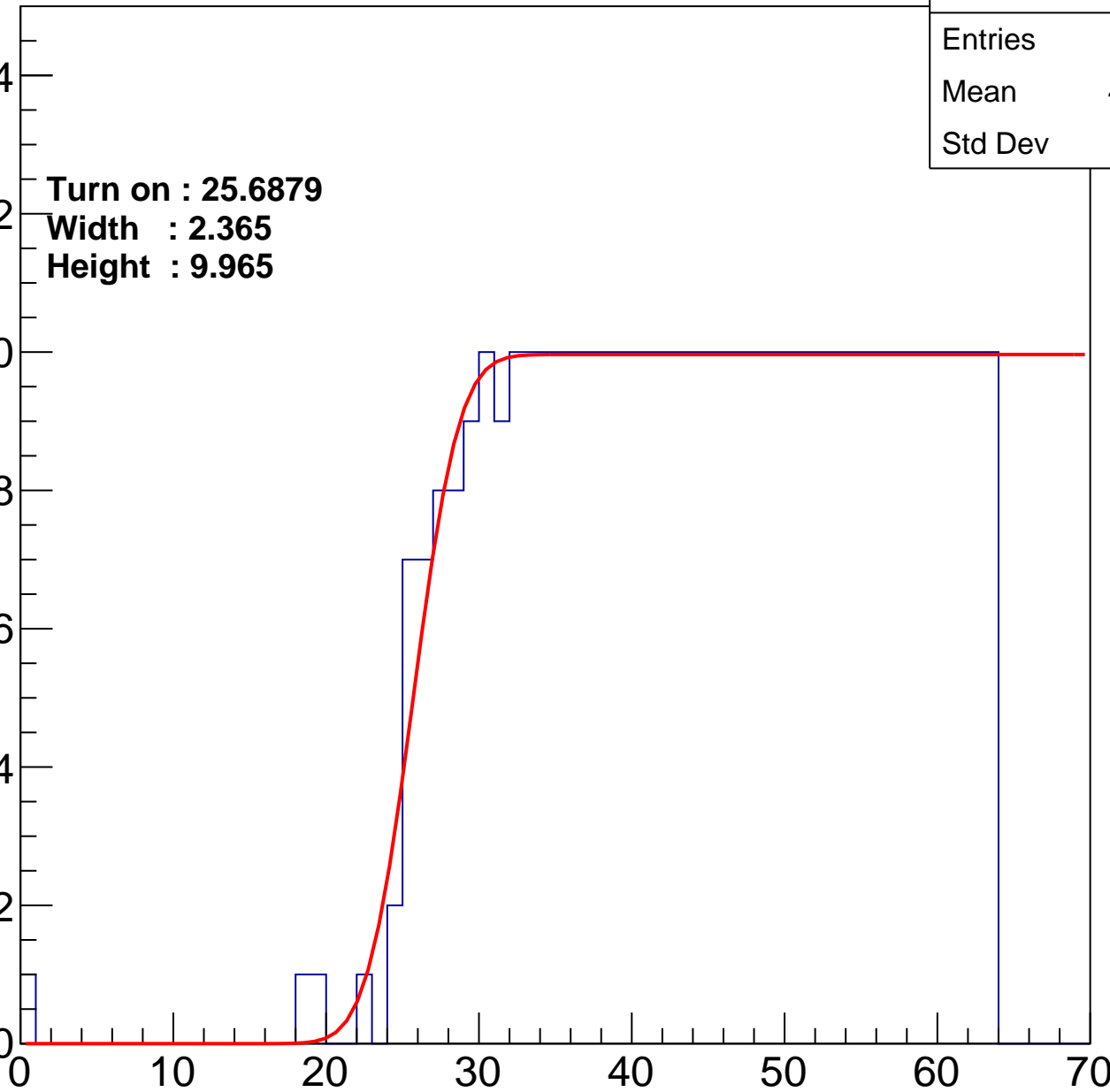
Width : 2.365

Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch125

calib\_packv5\_042523\_0143.root, FC#13, port D2

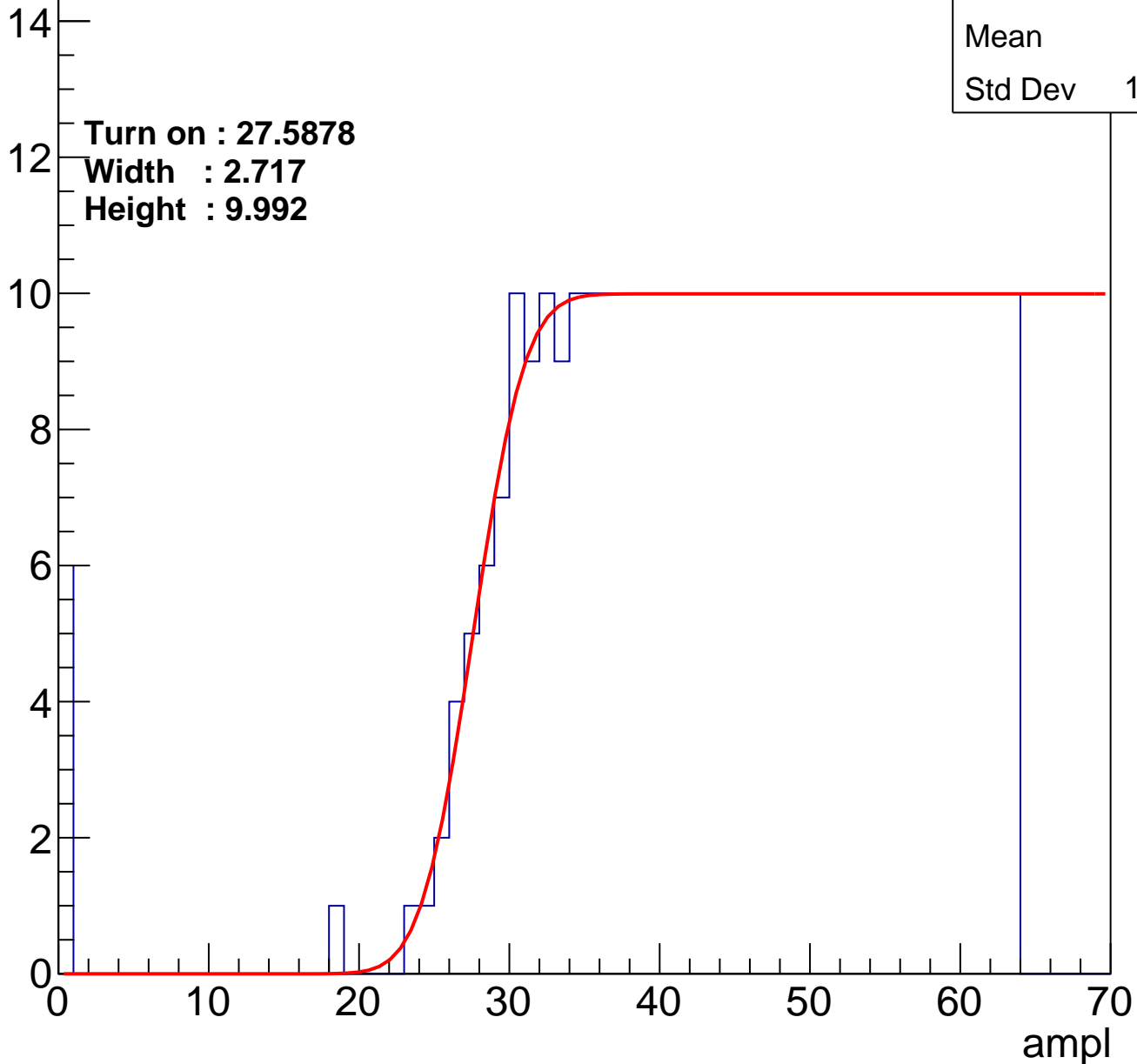
Entries	371
Mean	44.4
Std Dev	12.09

Turn on : 27.5878

Width : 2.717

Height : 9.992

Entry



# B1L003S, U22-ch126

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	364
Mean	44.88
Std Dev	11.47

**Turn on : 28.0155**

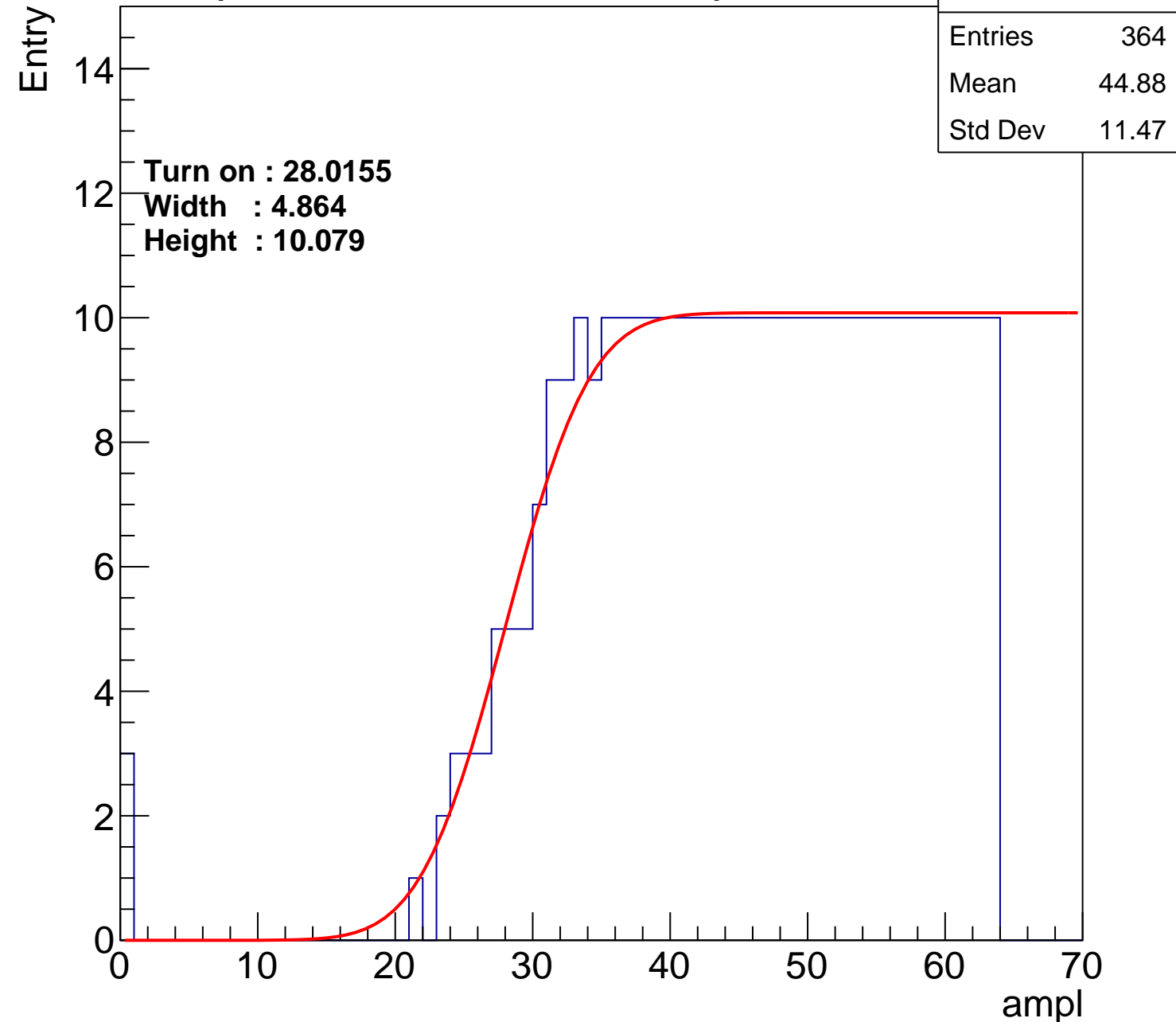
**Width : 4.864**

**Height : 10.079**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U22-ch127

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.4772

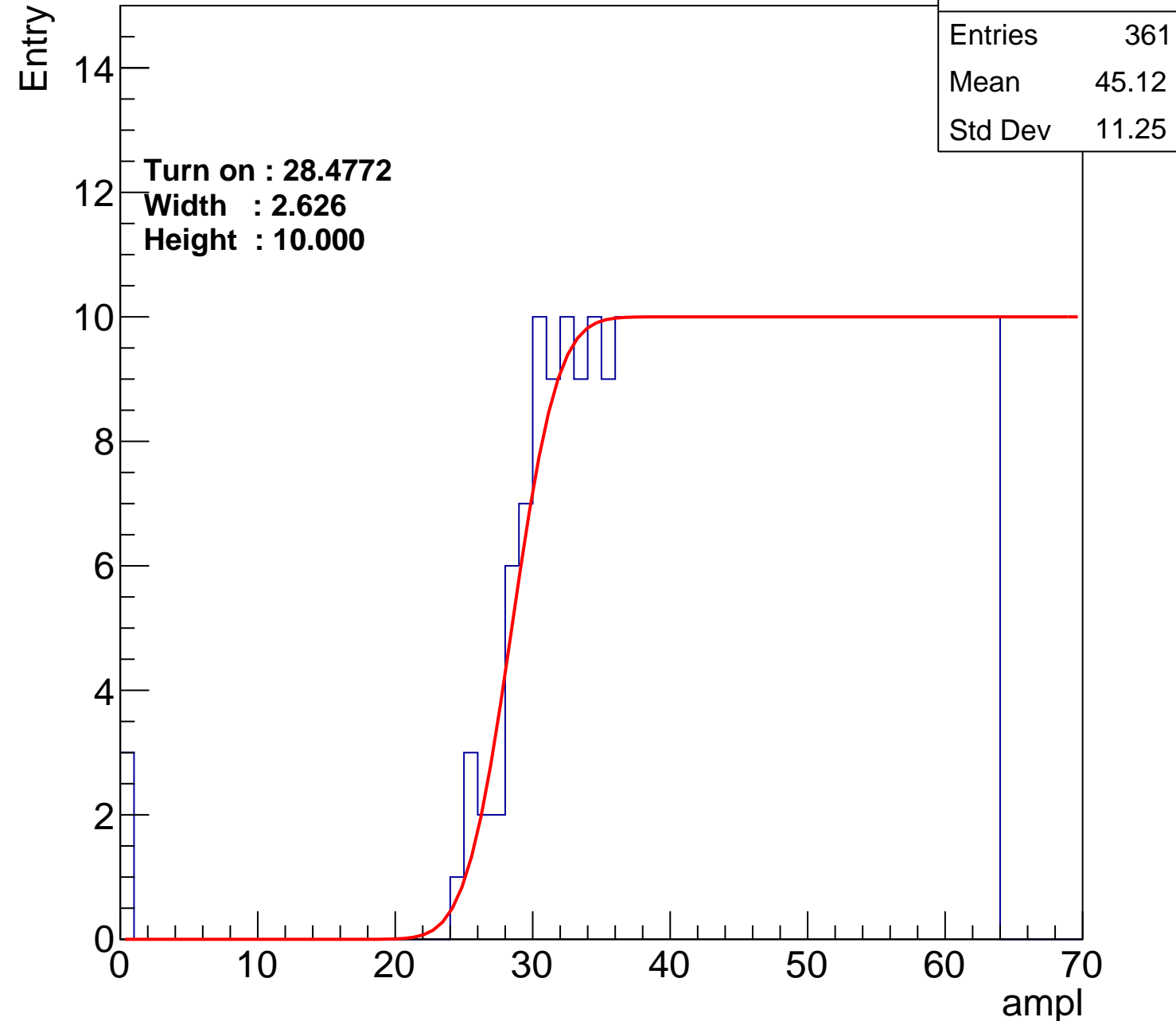
Width : 2.626

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U22-ch127

calib\_packv5\_042523\_0143.root, FC#13, port D2

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.4772

Width : 2.626

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

