B0L100S, channel entry distribution (ampl > 54)

calib_packv5_041523_1627.root, FC#6, port A1
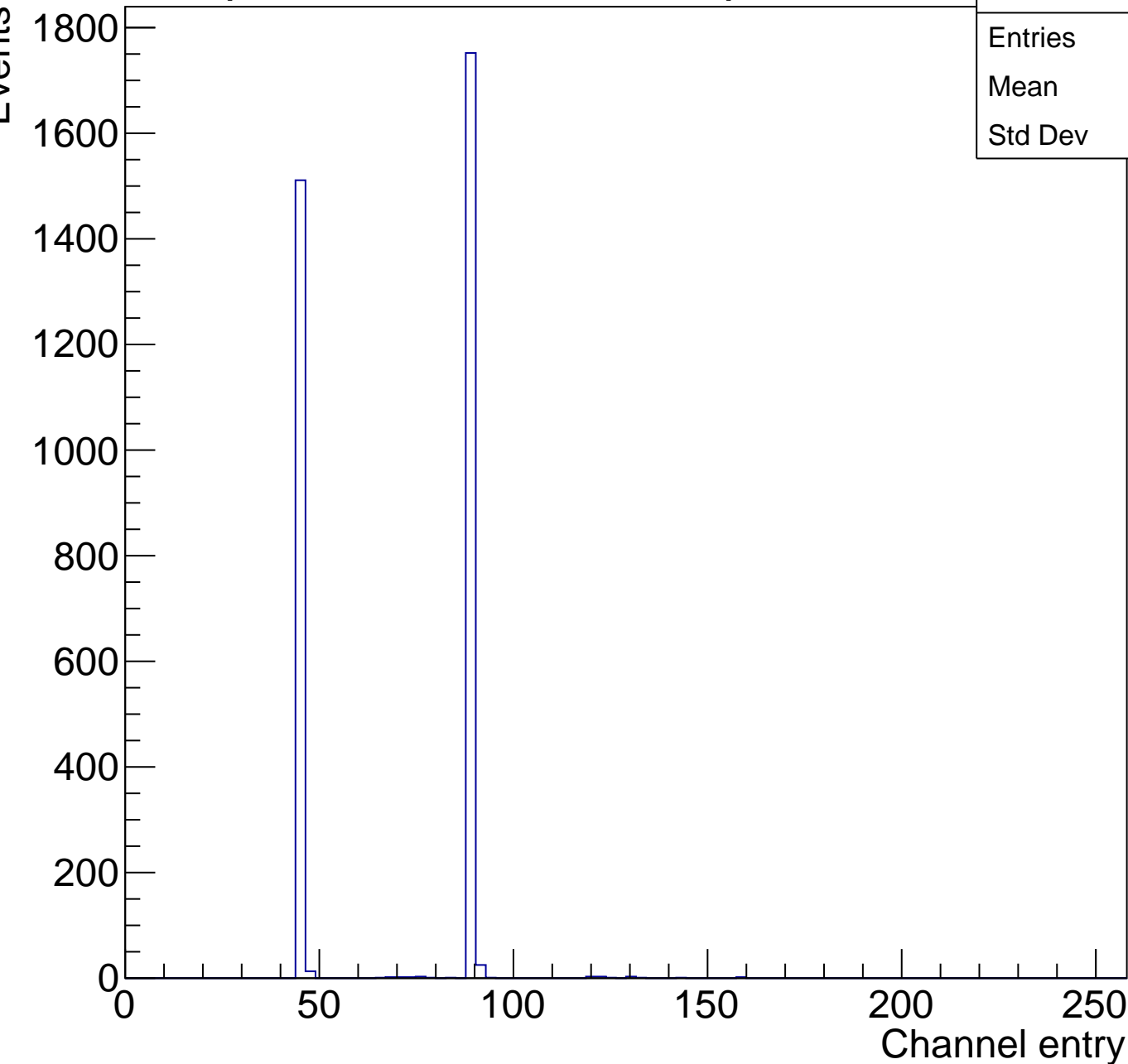
Entries	3328
Mean	69.53
Std Dev	22.71

Channel entry