

B0L001S, U22-ch0, adc0

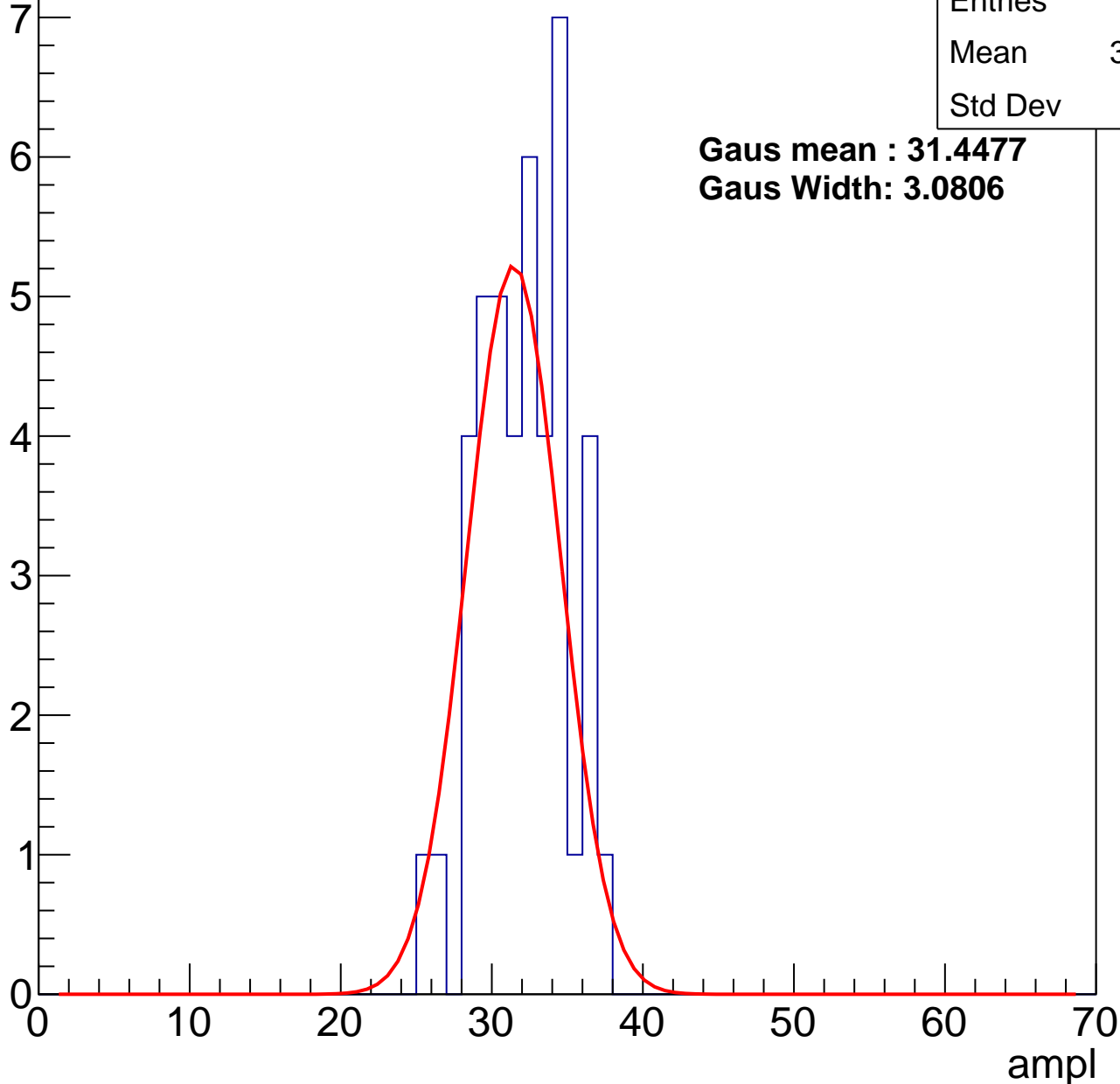
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	31.63
Std Dev	2.82

Gaus mean : 31.4477

Gaus Width: 3.0806



B0L001S, U22-ch0, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	37.58
Std Dev	3.593

Gaus mean : 39.0806

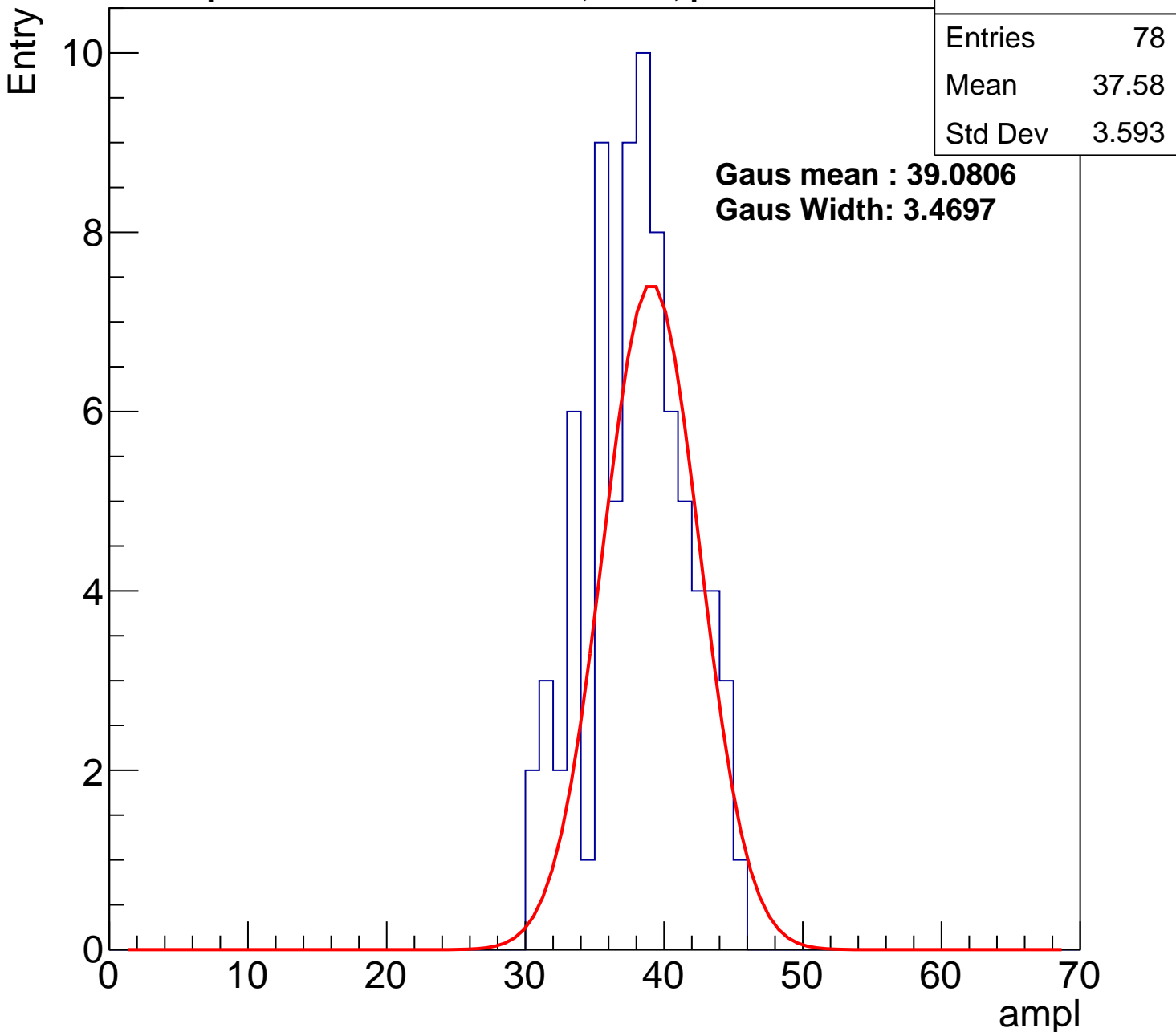
Gaus Width: 3.4697

Entry

10
8
6
4
2
0

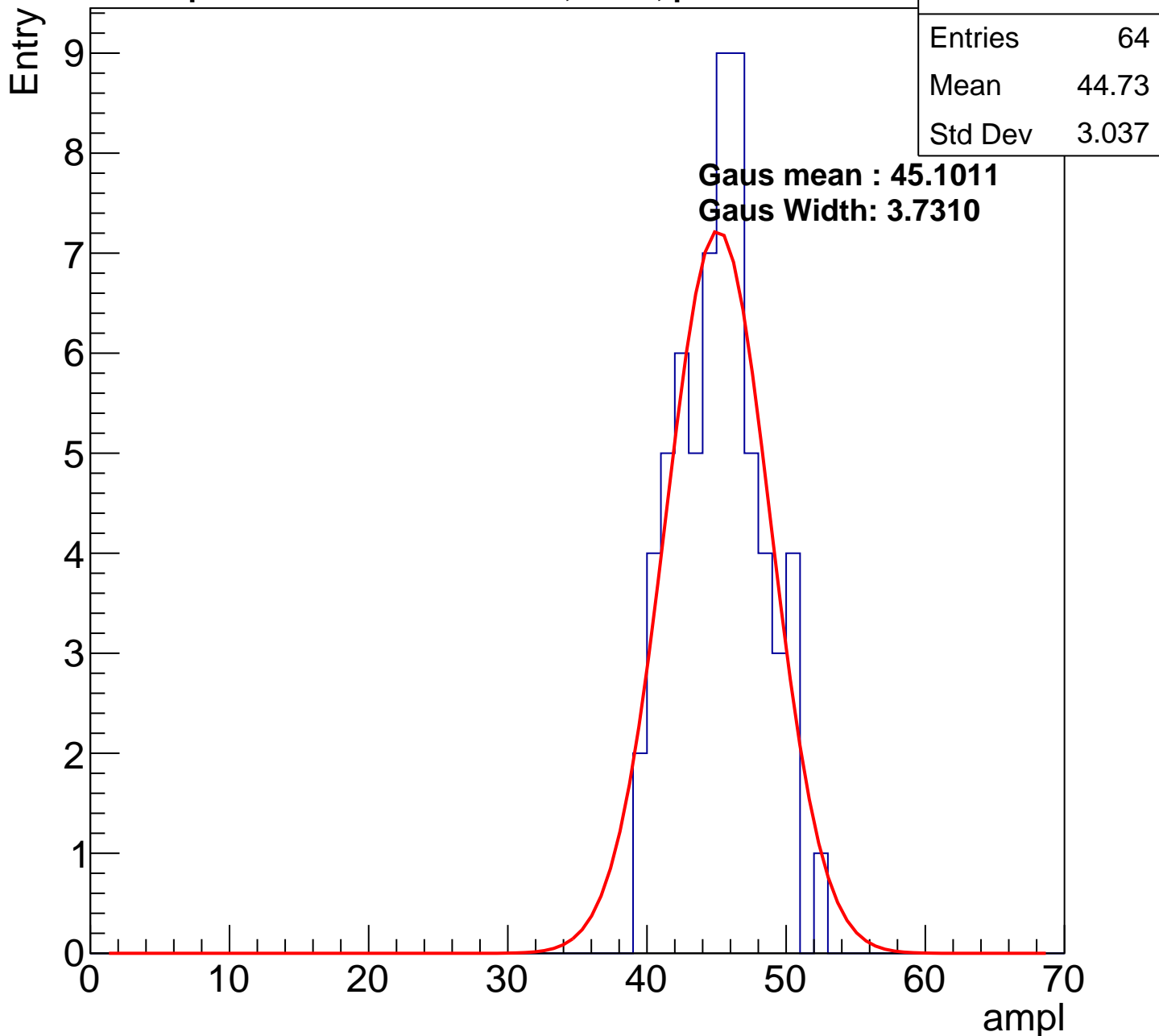
0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch0, adc2

calib_packv5_042523_0143.root, FC#9, port A1

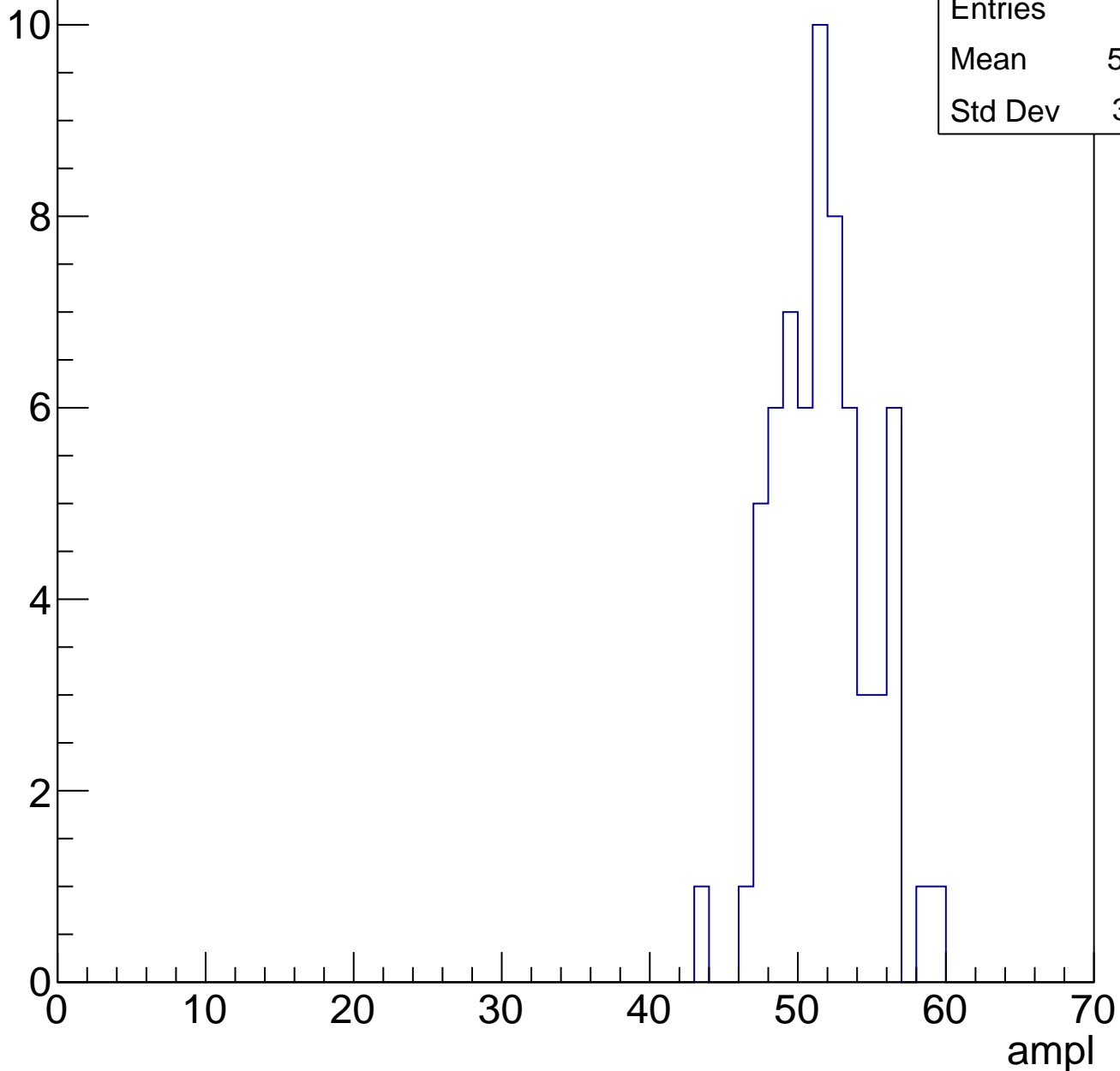


B0L001S, U22-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	51.23
Std Dev	3.121

Entry

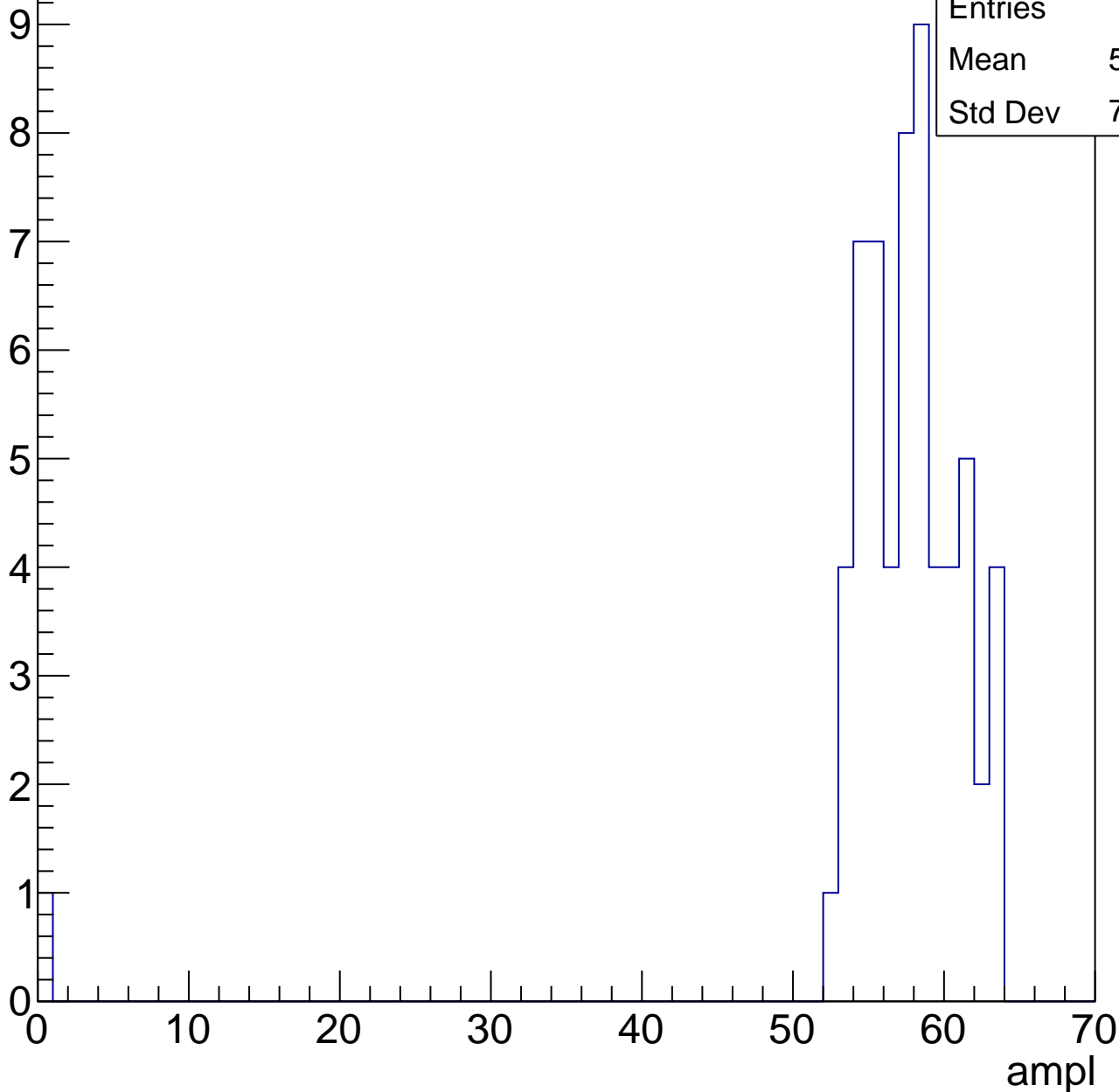


B0L001S, U22-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.43
Std Dev	7.904

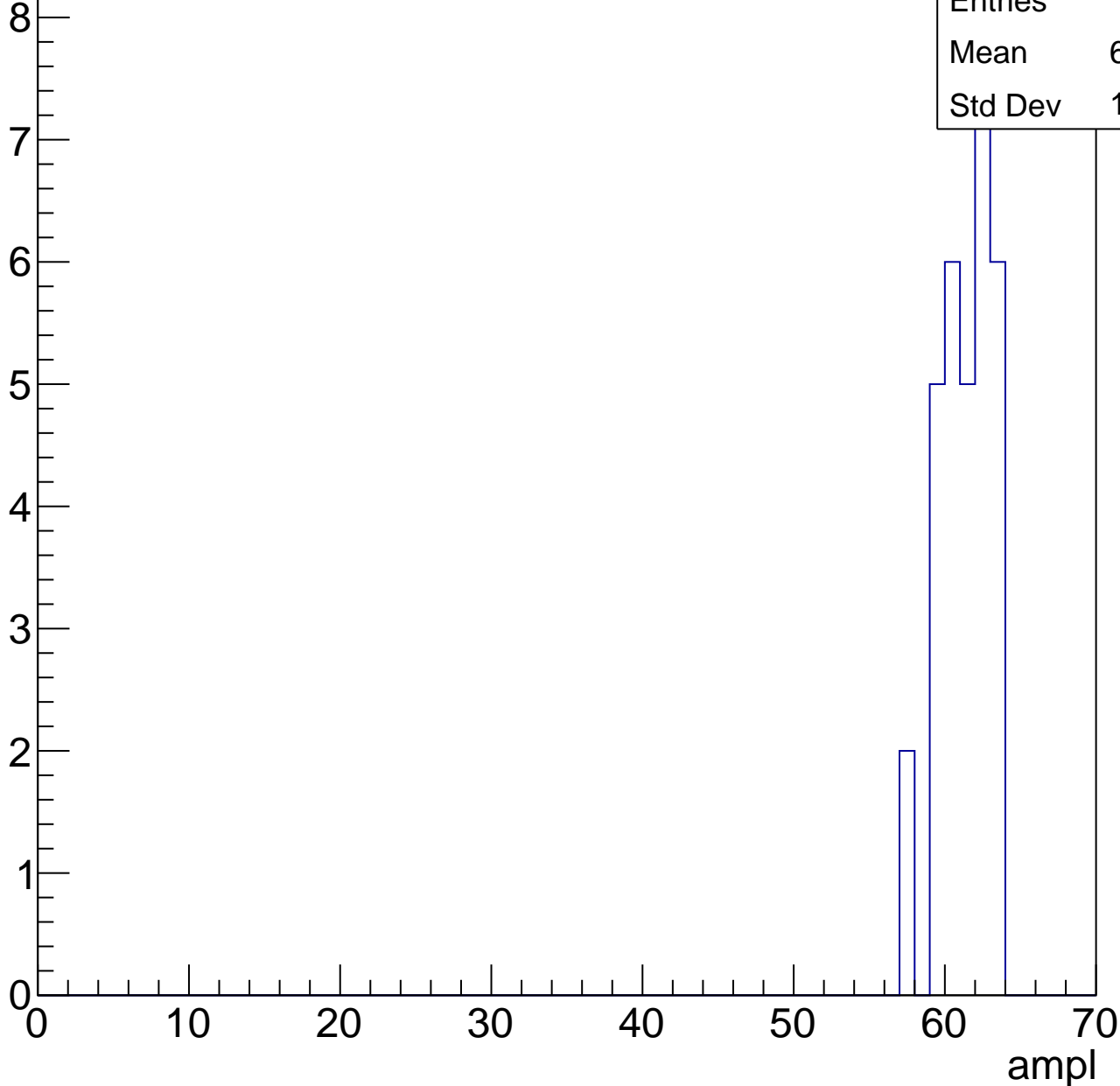


B0L001S, U22-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	60.88
Std Dev	1.672



B0L001S, U22-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch1, adc0

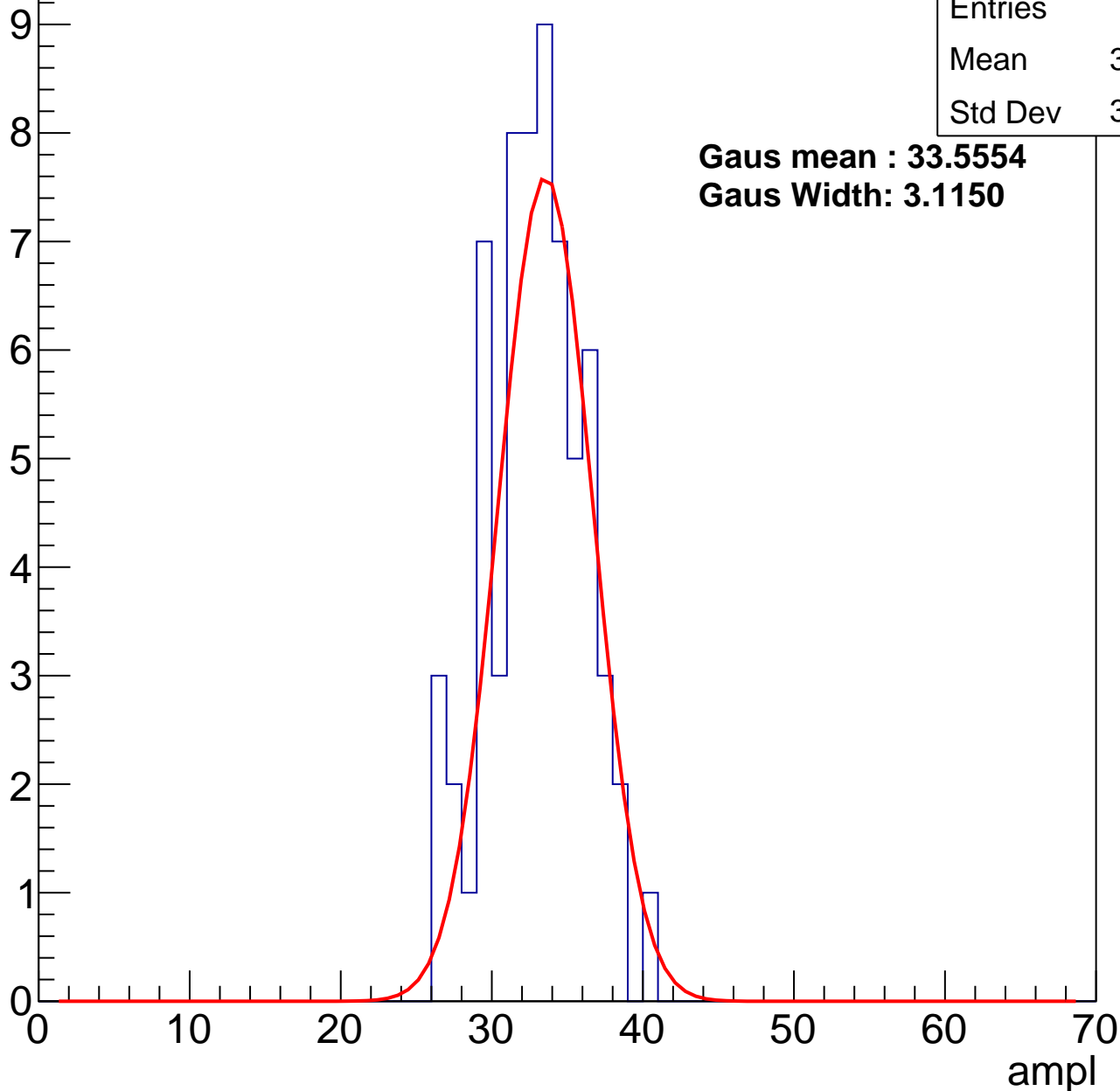
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	32.46
Std Dev	3.119

Gaus mean : 33.5554

Gaus Width: 3.1150



B0L001S, U22-ch1, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	39.78
Std Dev	3.303

Gaus mean : 40.2707

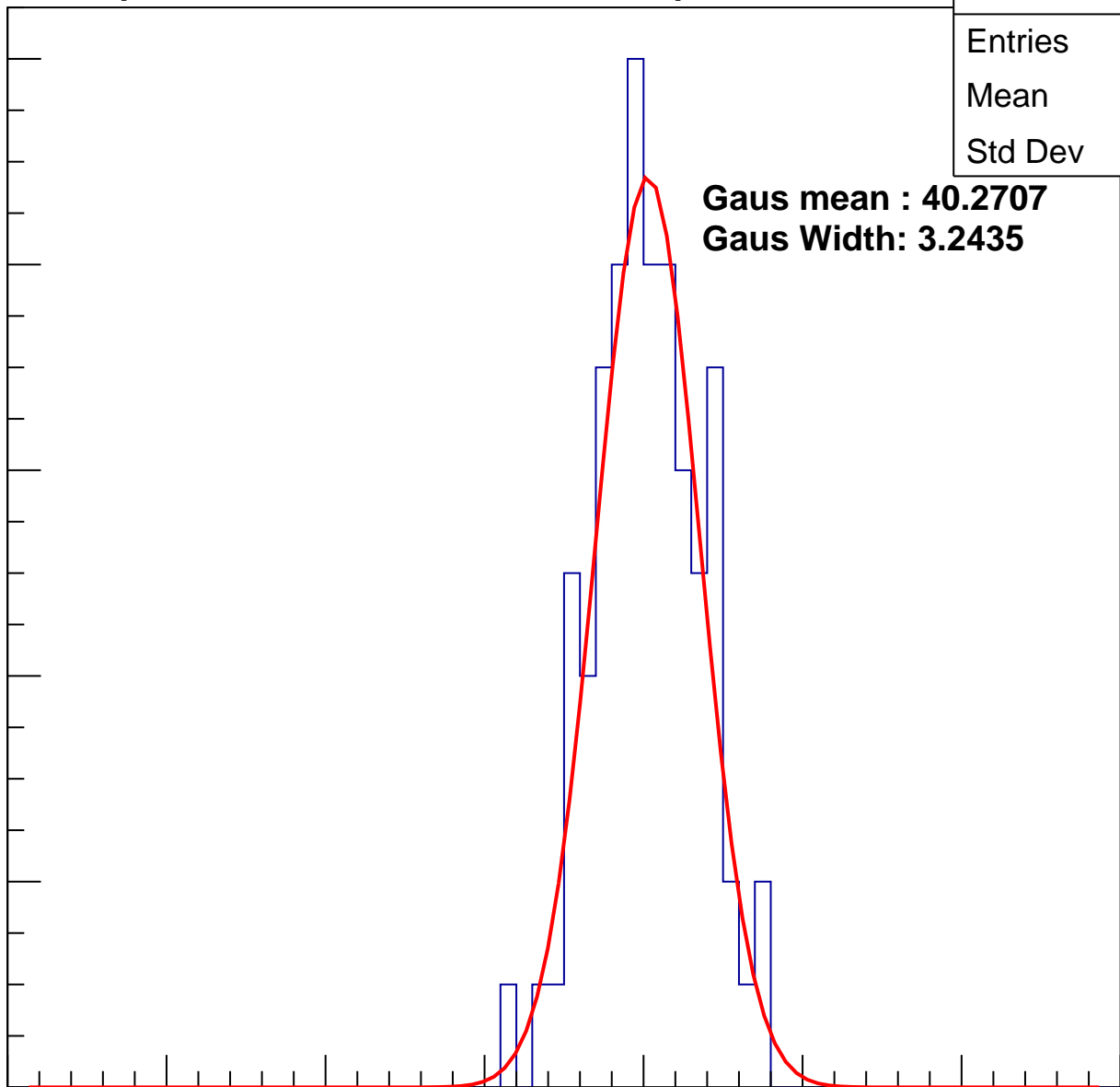
Gaus Width: 3.2435

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch1, adc2

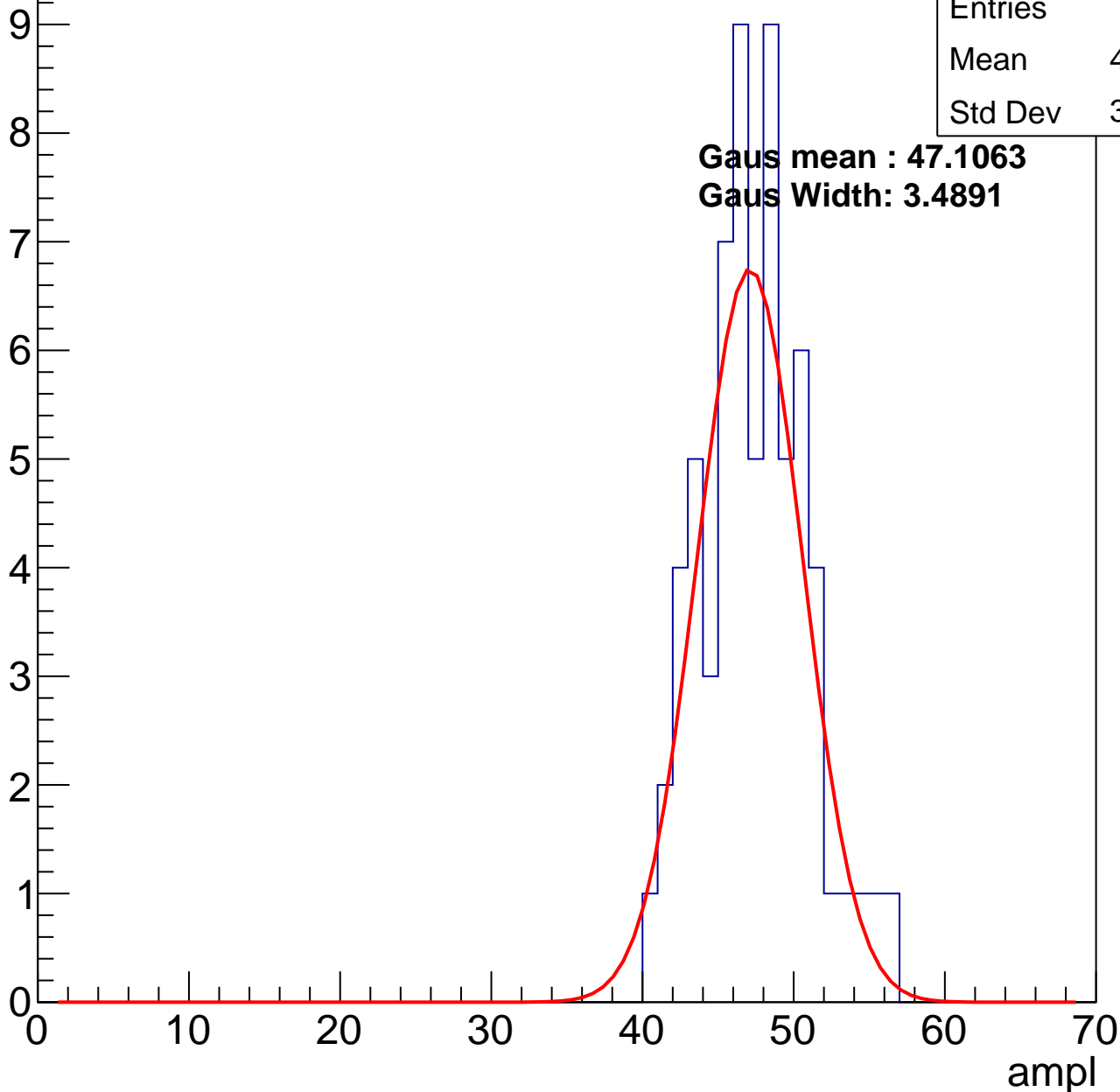
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	46.95
Std Dev	3.435

Gaus mean : 47.1063

Gaus Width: 3.4891

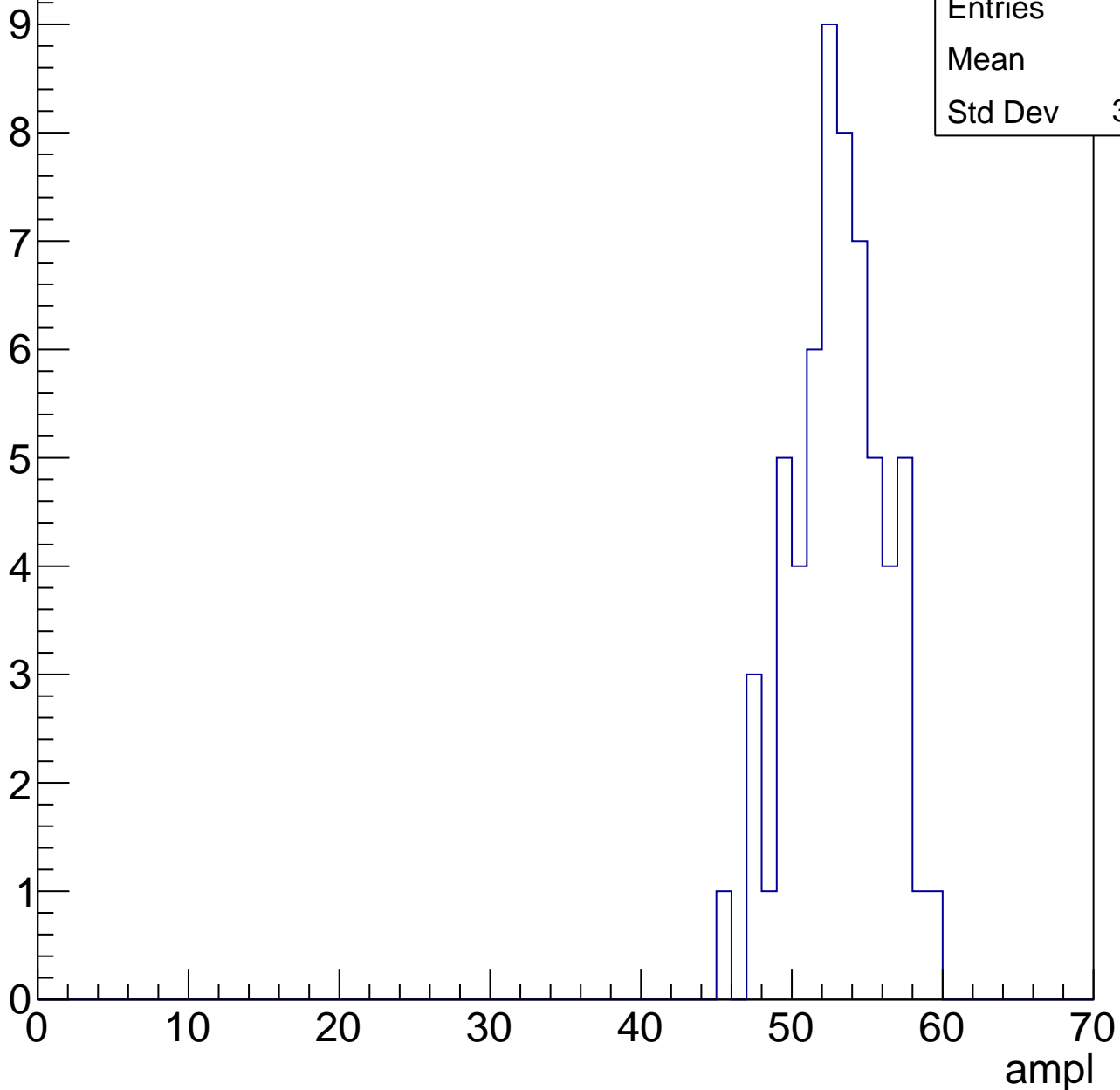


B0L001S, U22-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	52.6
Std Dev	3.001



B0L001S, U22-ch1, adc4

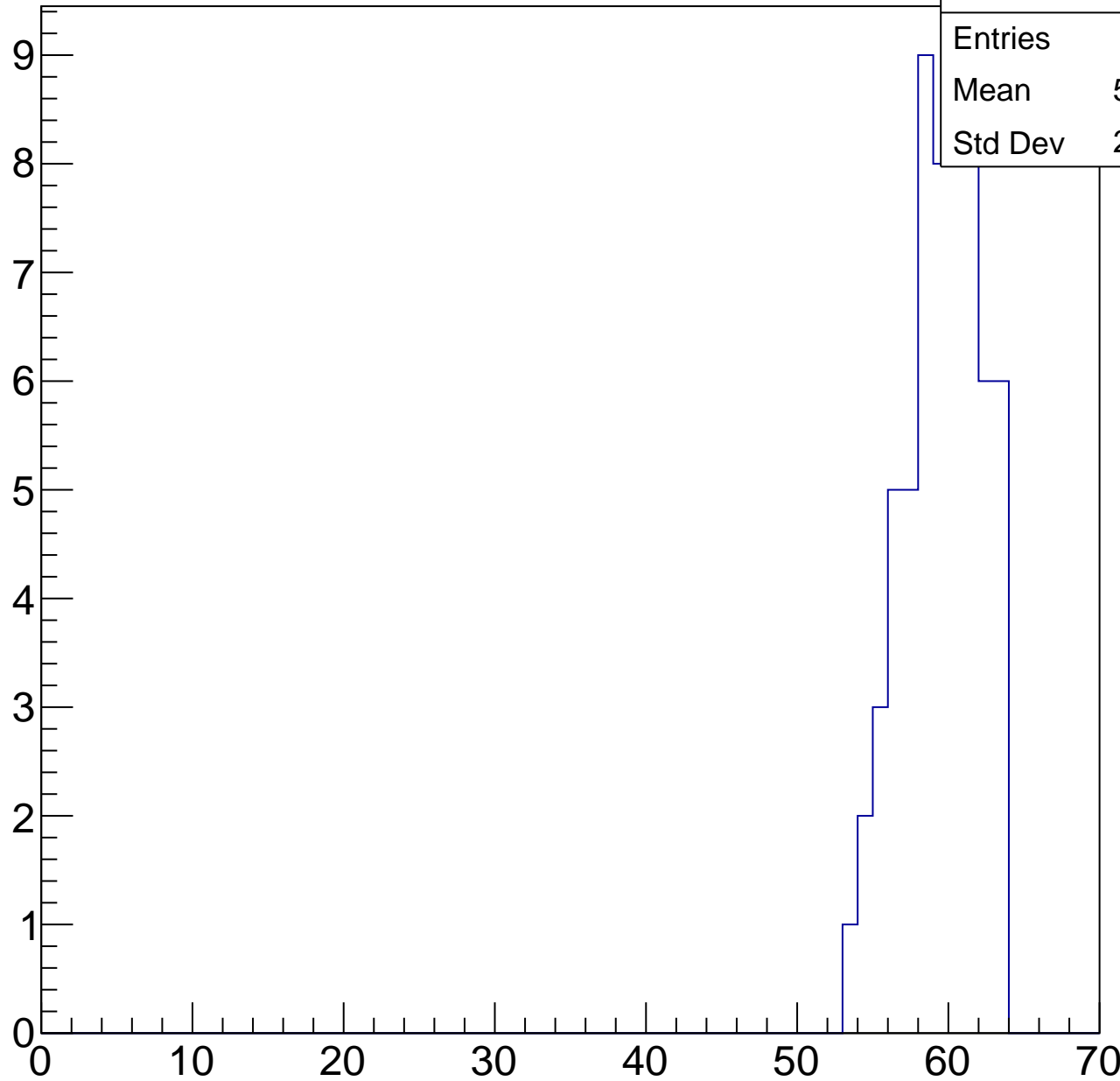
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	59.07
Std Dev	2.553

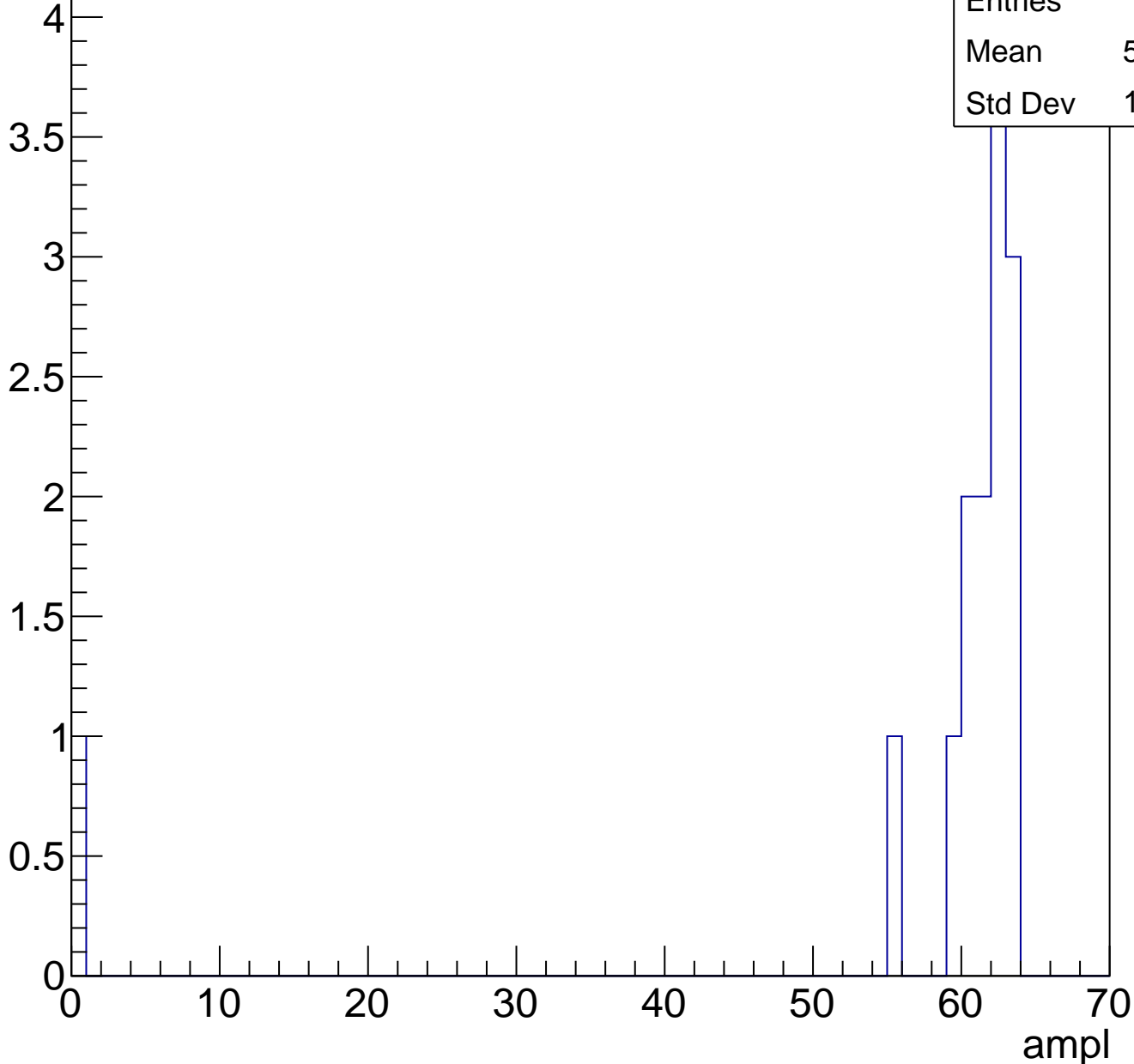
ampl



B0L001S, U22-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch2, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.92
Std Dev	4.871

Gaus mean : 31.6588

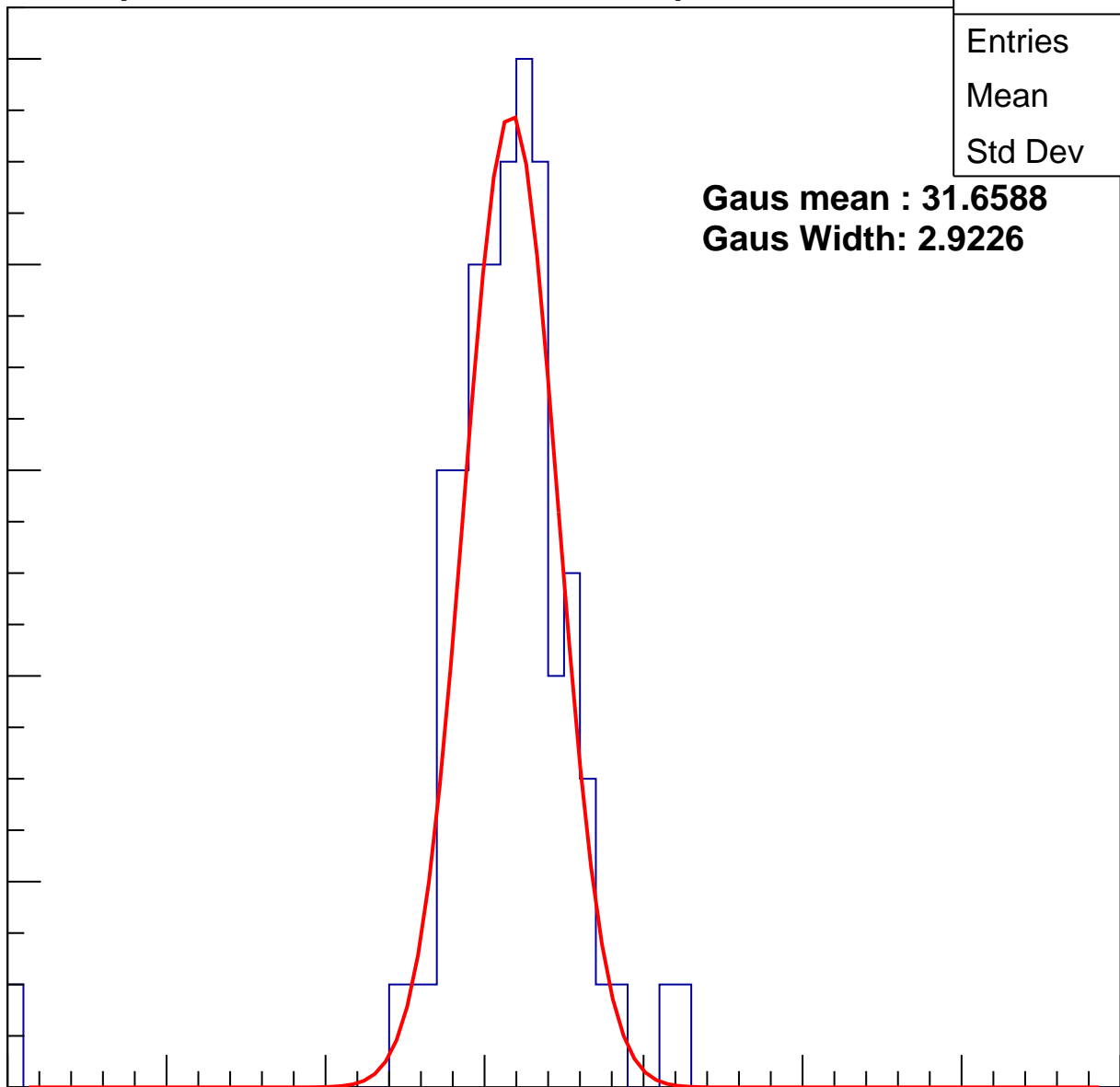
Gaus Width: 2.9226

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch2, adc1

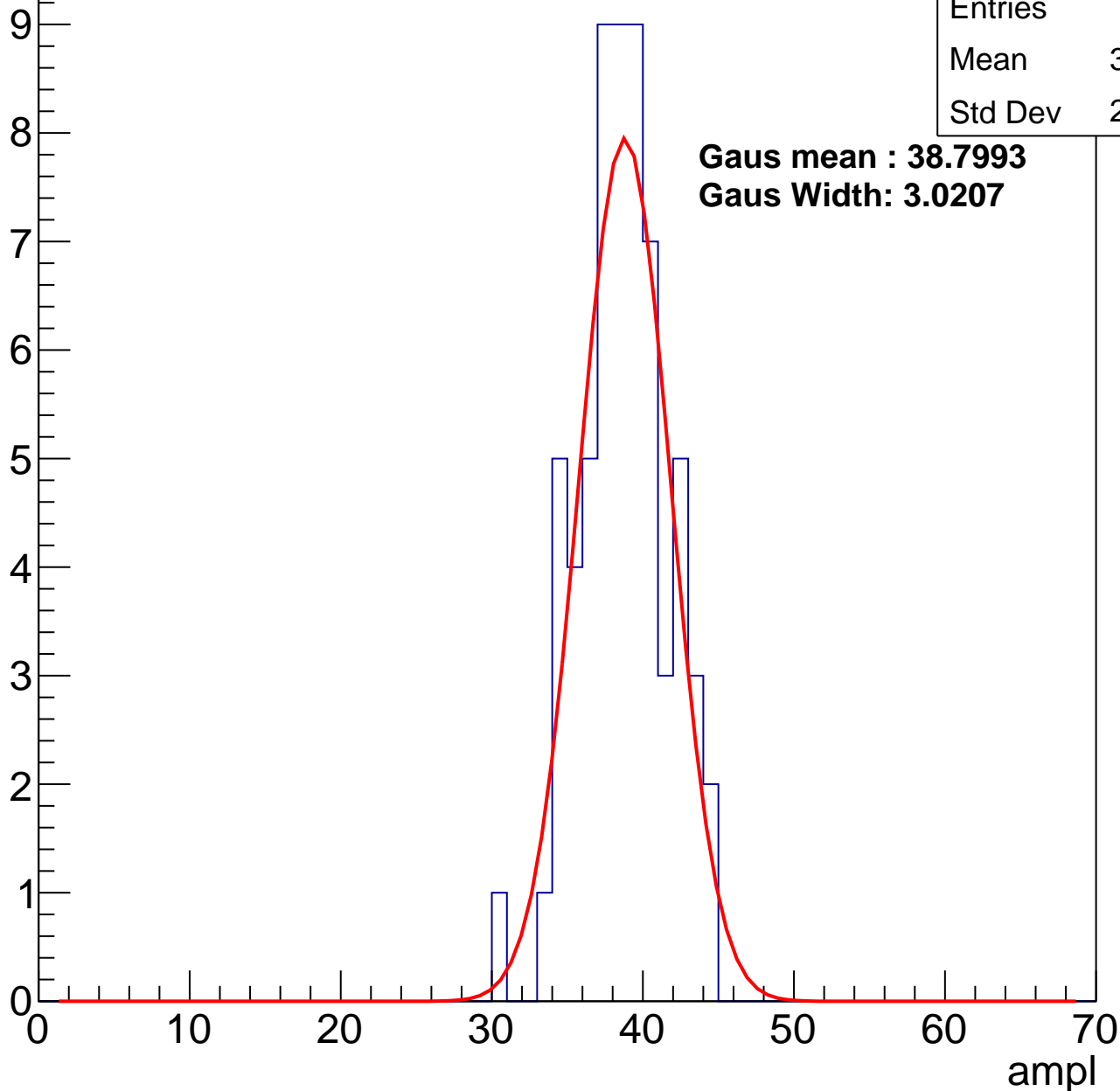
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.24
Std Dev	2.877

Gaus mean : 38.7993

Gaus Width: 3.0207



B0L001S, U22-ch2, adc2

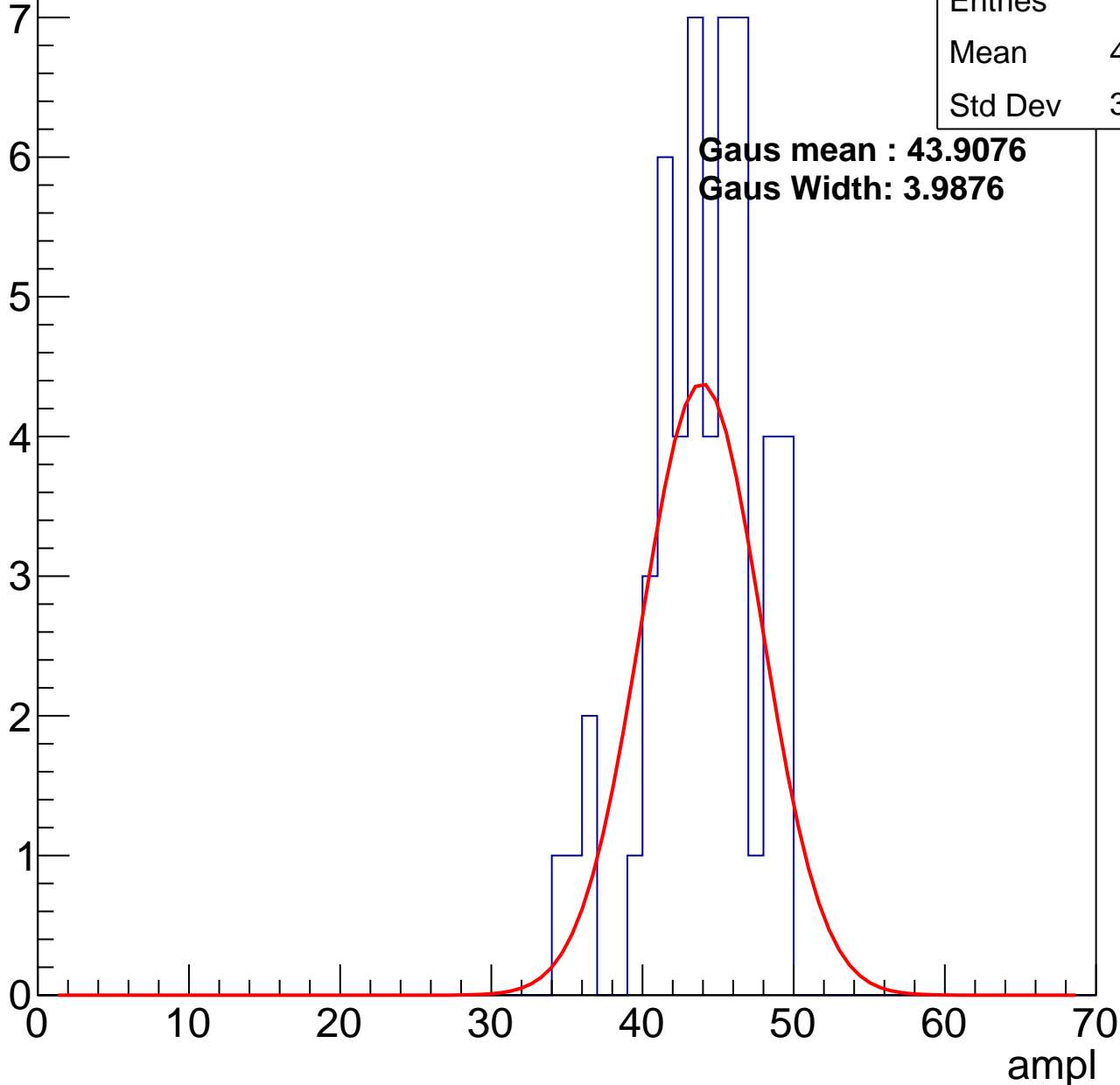
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.52
Std Dev	3.549

Gaus mean : 43.9076

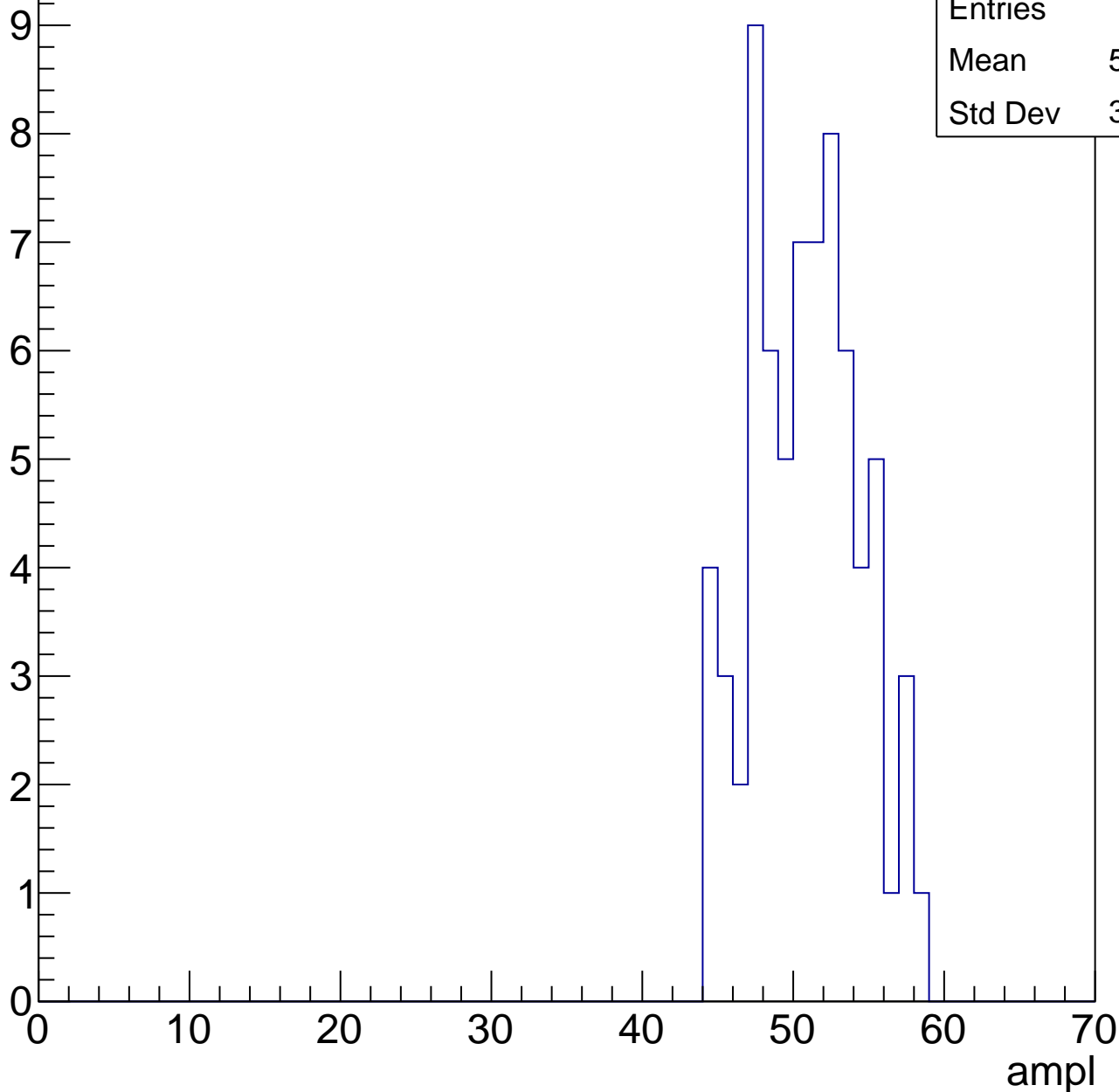
Gaus Width: 3.9876



B0L001S, U22-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



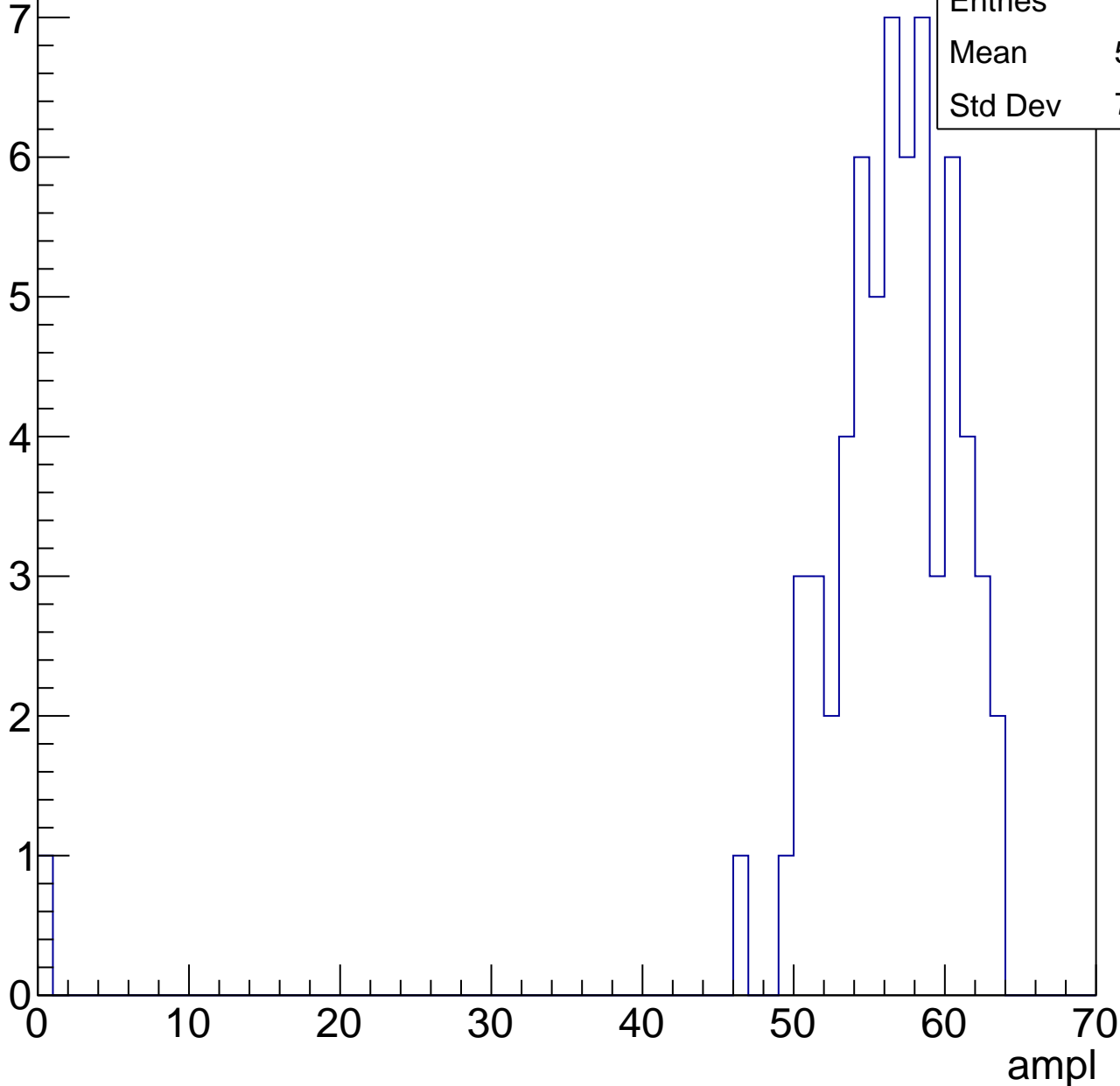
Entries	71
Mean	50.37
Std Dev	3.522

B0L001S, U22-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.41
Std Dev	7.911



B0L001S, U22-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.56
Std Dev	2.02

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

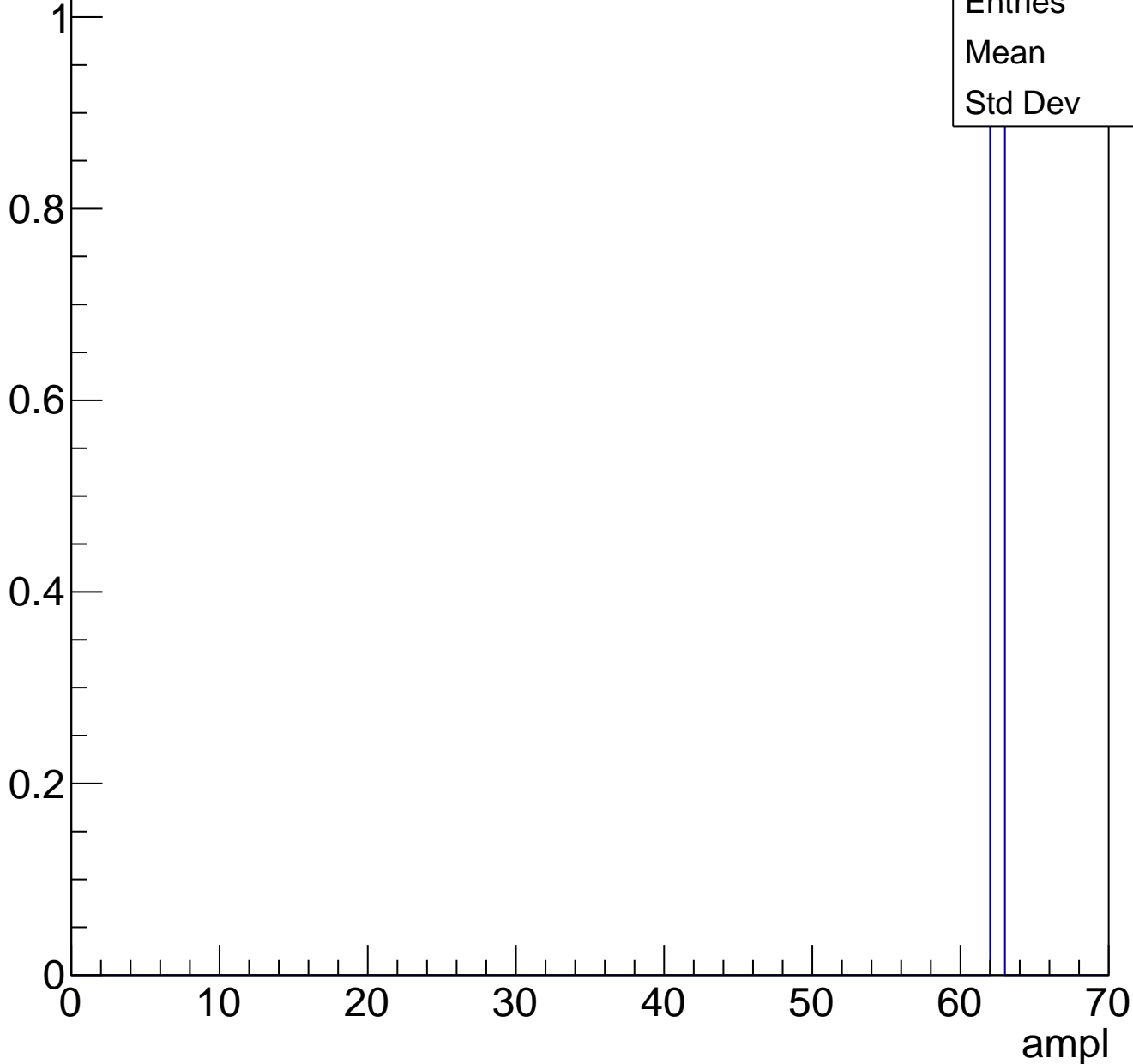
7

8

B0L001S, U22-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch3, adc0

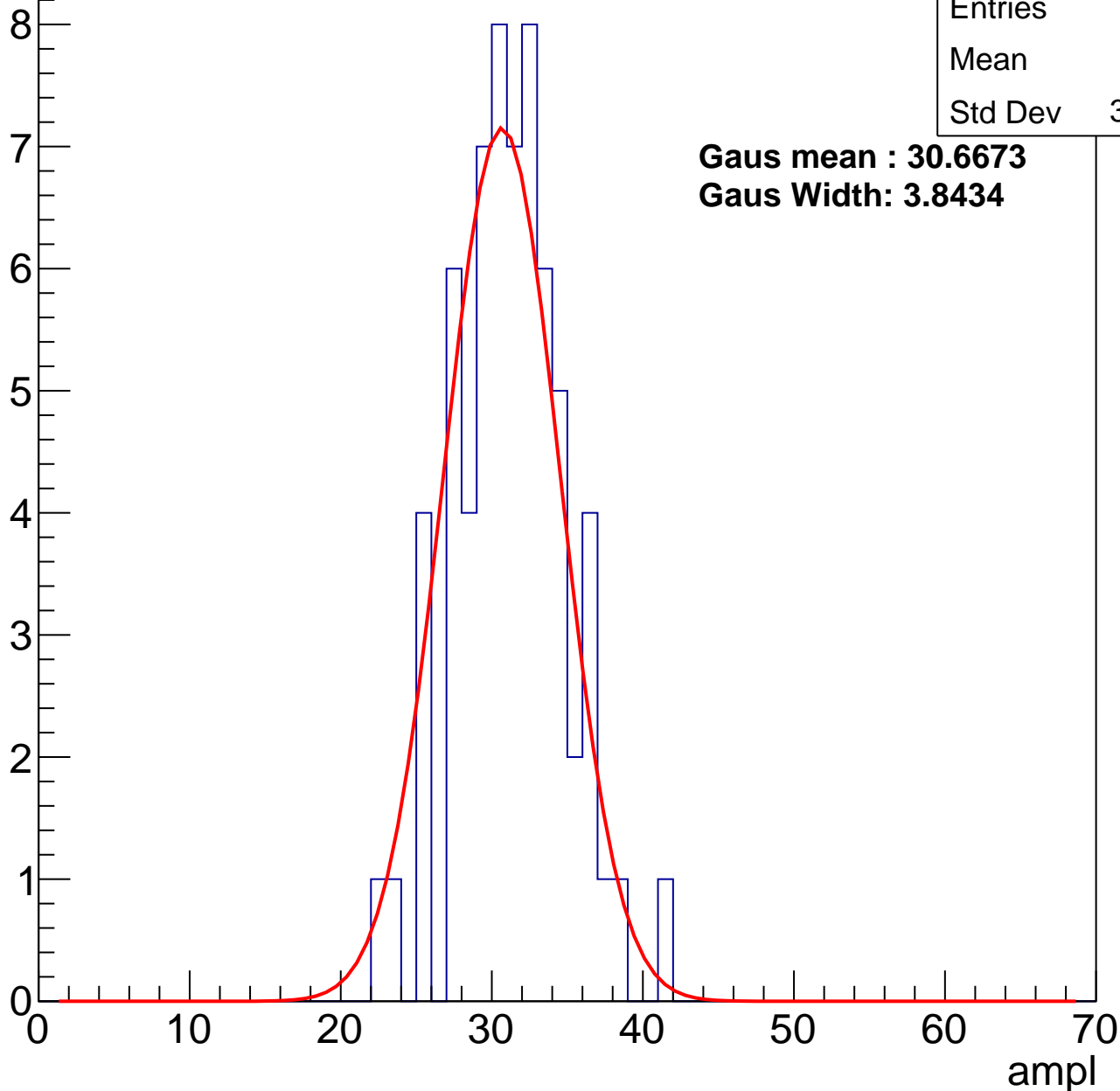
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.8
Std Dev	3.585

Gaus mean : 30.6673

Gaus Width: 3.8434



B0L001S, U22-ch3, adc1

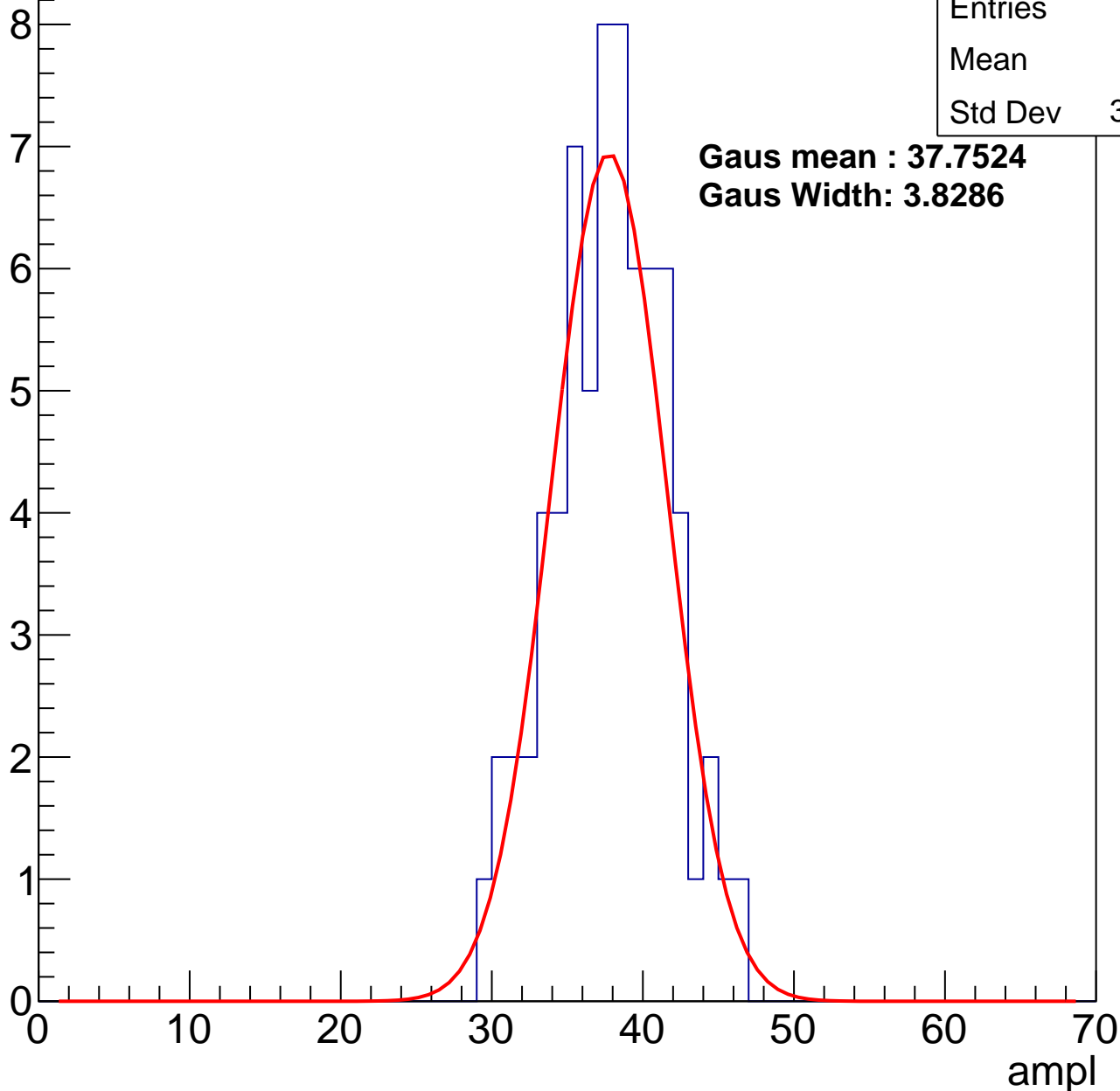
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.4
Std Dev	3.716

Gaus mean : 37.7524

Gaus Width: 3.8286



B0L001S, U22-ch3, adc2

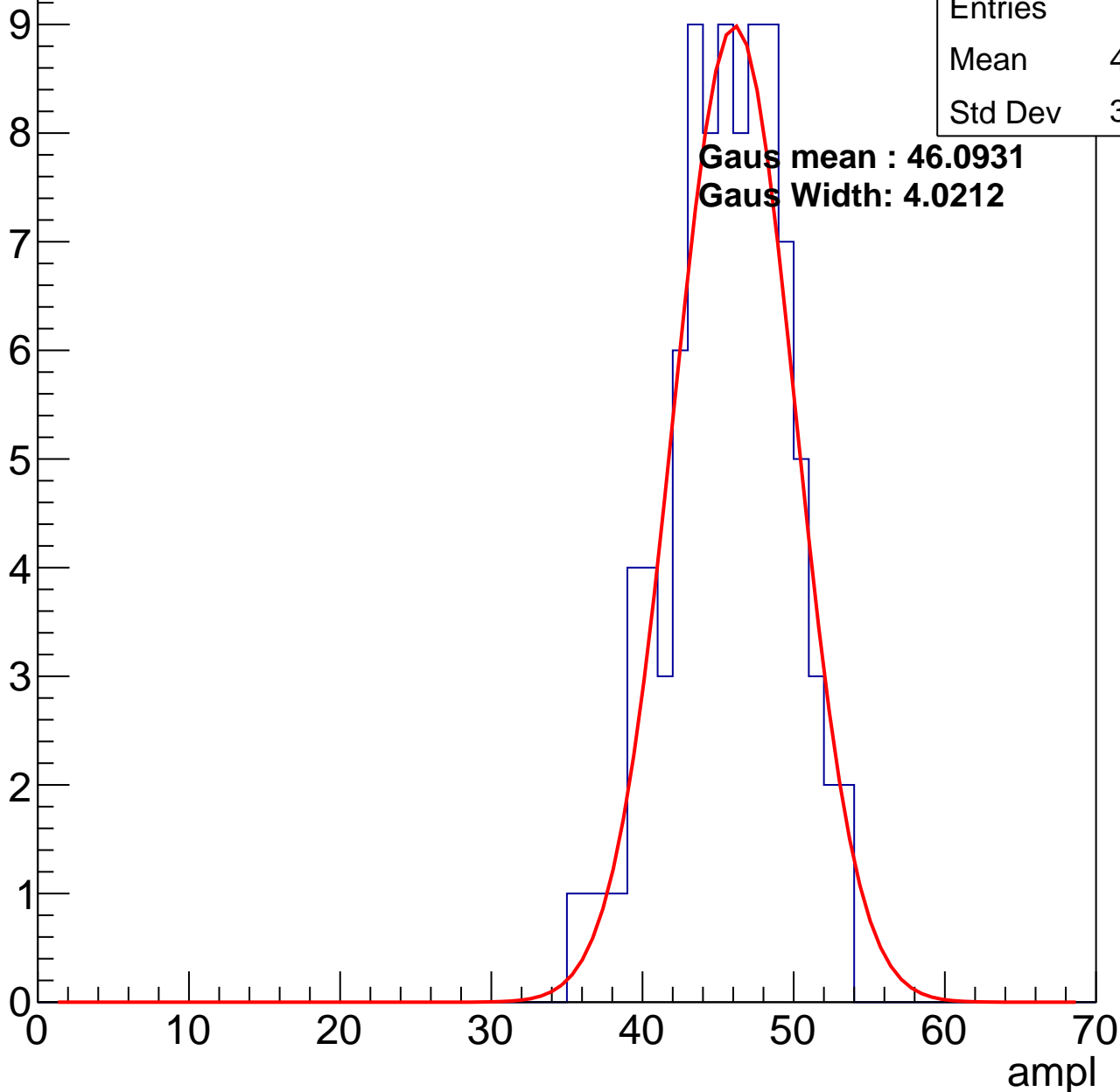
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	92
Mean	45.22
Std Dev	3.867

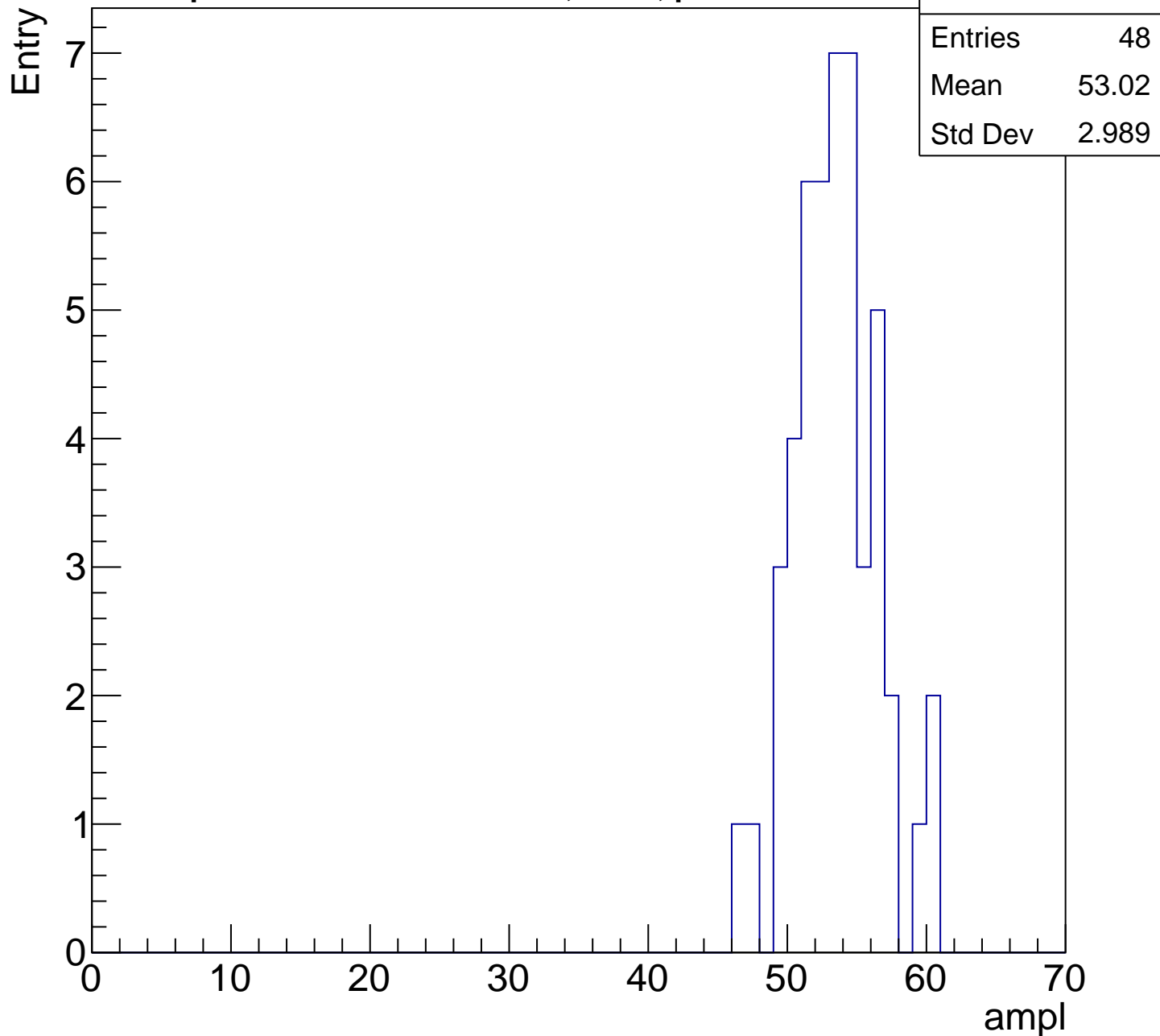
Gaus mean : 46.0931

Gaus Width: 4.0212



B0L001S, U22-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

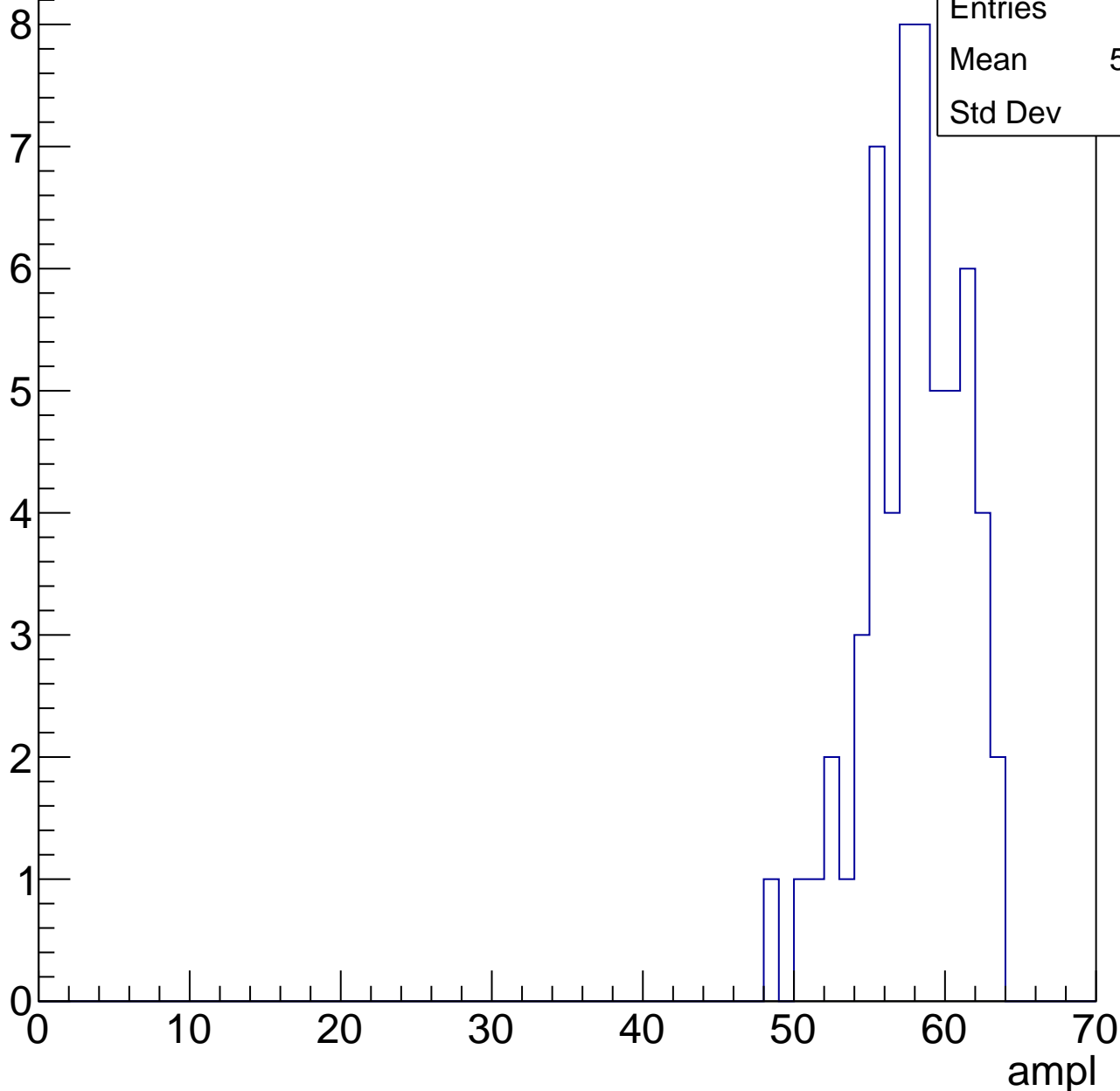


B0L001S, U22-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.45
Std Dev	3.27

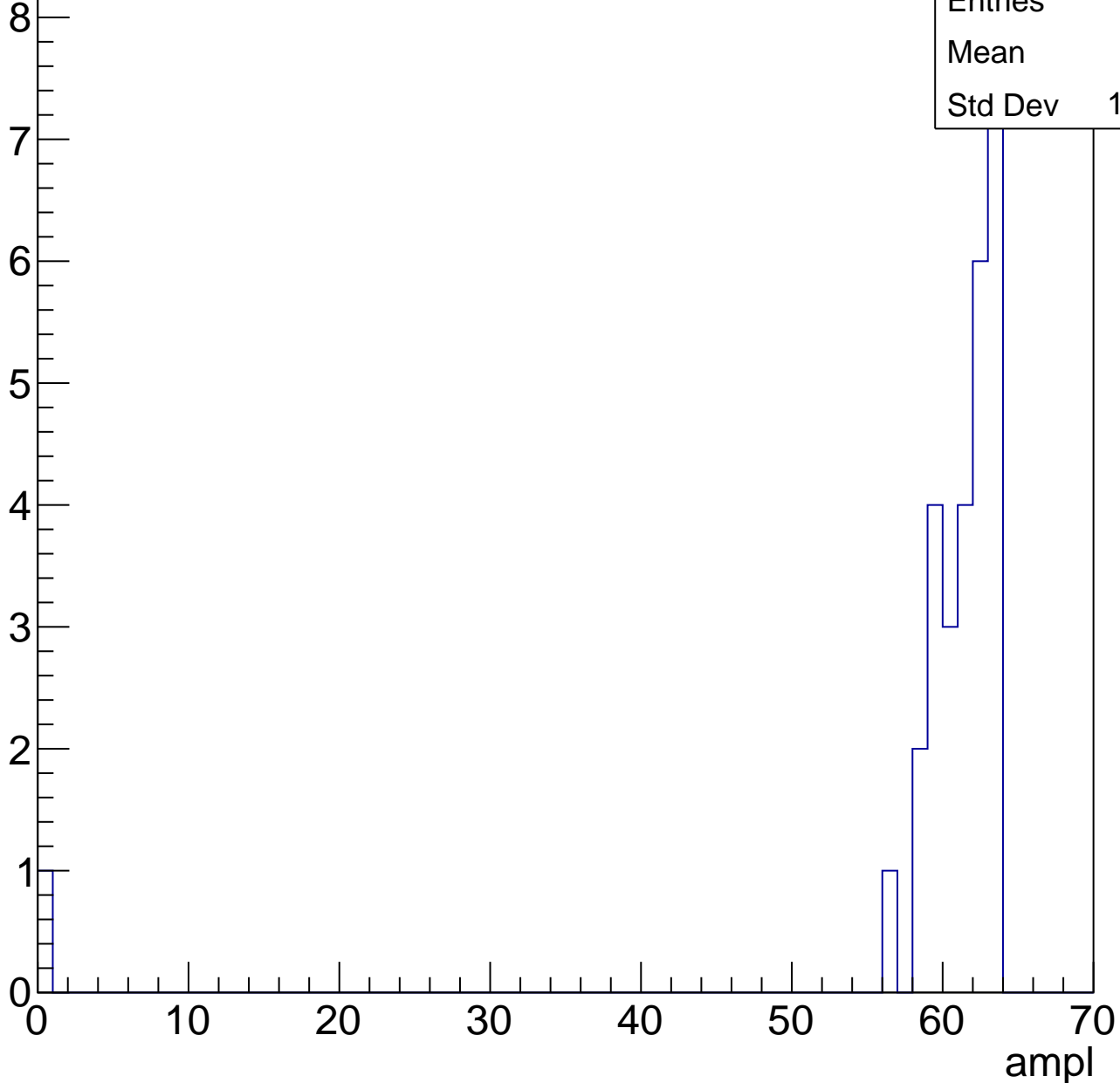


B0L001S, U22-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.9
Std Dev	11.28



B0L001S, U22-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch4, adc0

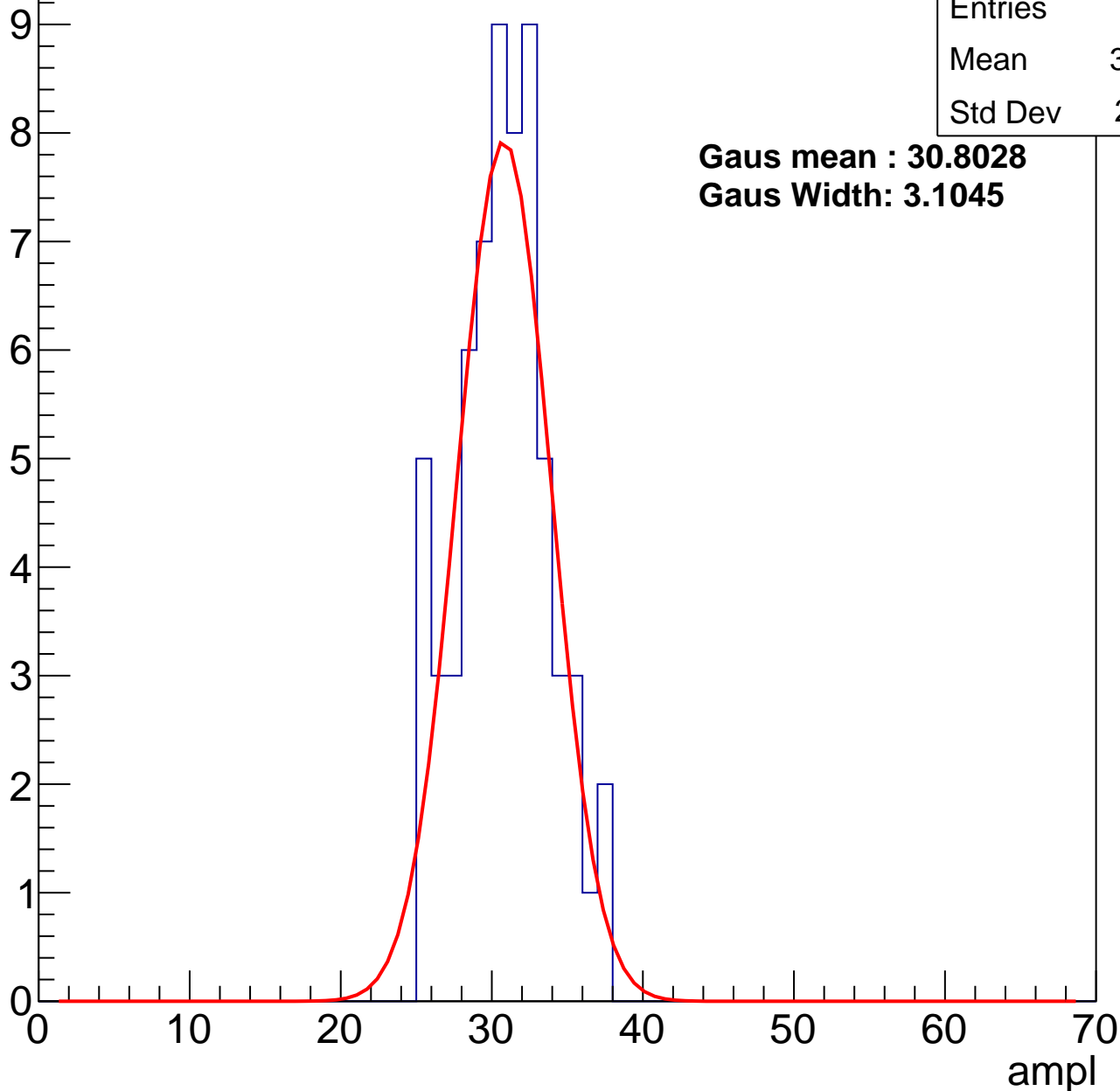
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.36
Std Dev	2.981

Gaus mean : 30.8028

Gaus Width: 3.1045



B0L001S, U22-ch4, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	37.46
Std Dev	3.758

Gaus mean : 37.9910

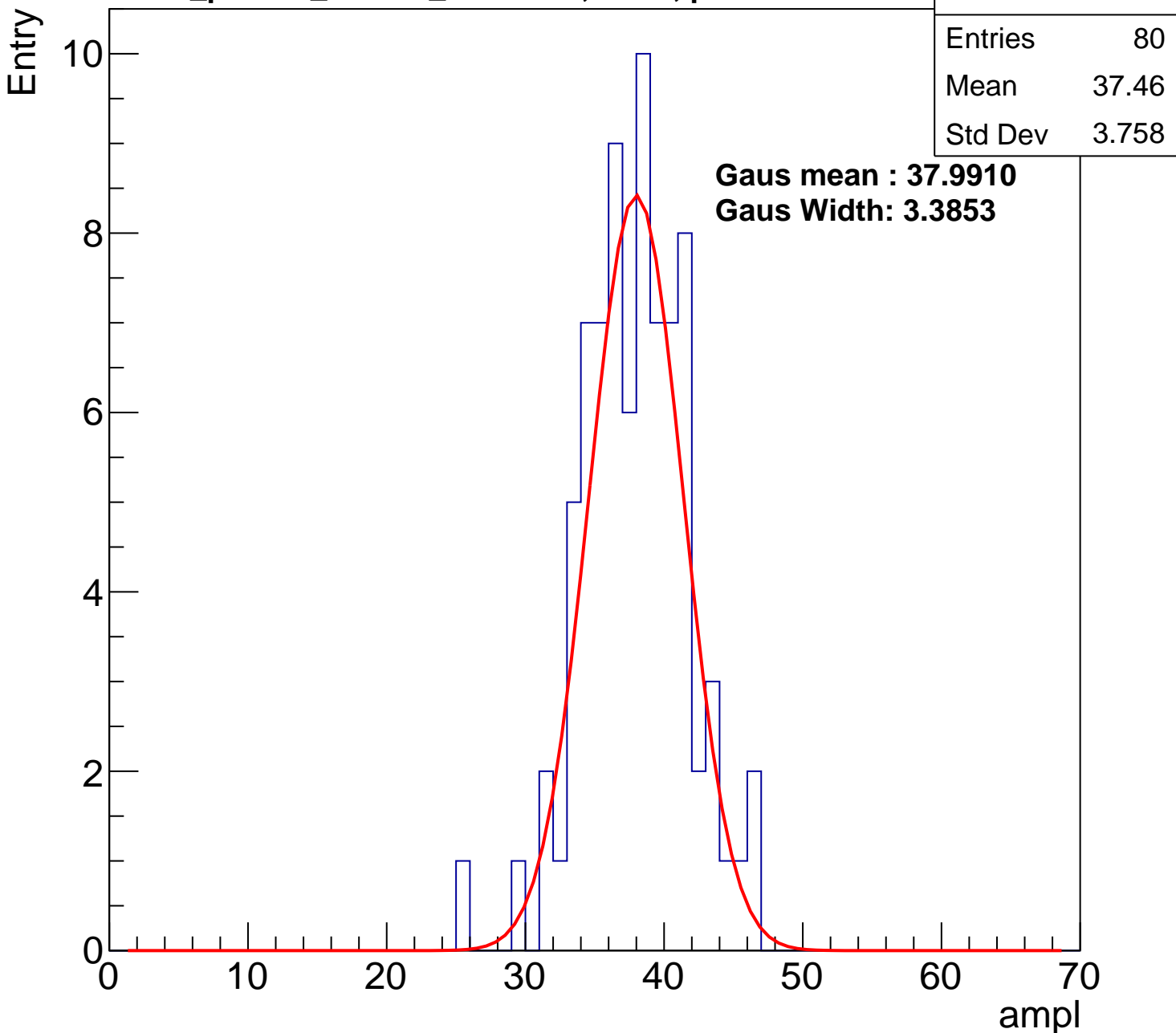
Gaus Width: 3.3853

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch4, adc2

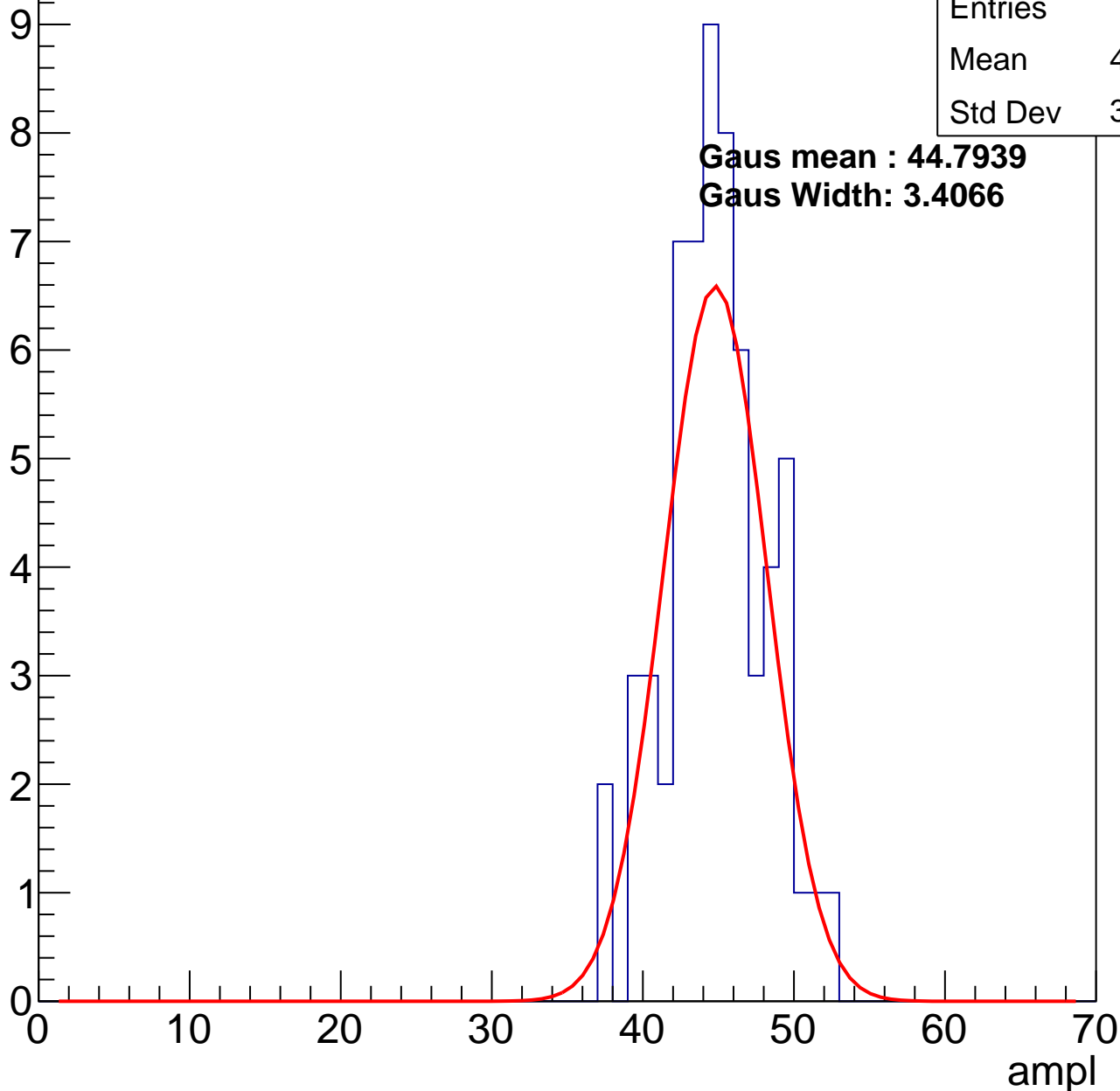
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.37
Std Dev	3.269

Gaus mean : 44.7939

Gaus Width: 3.4066

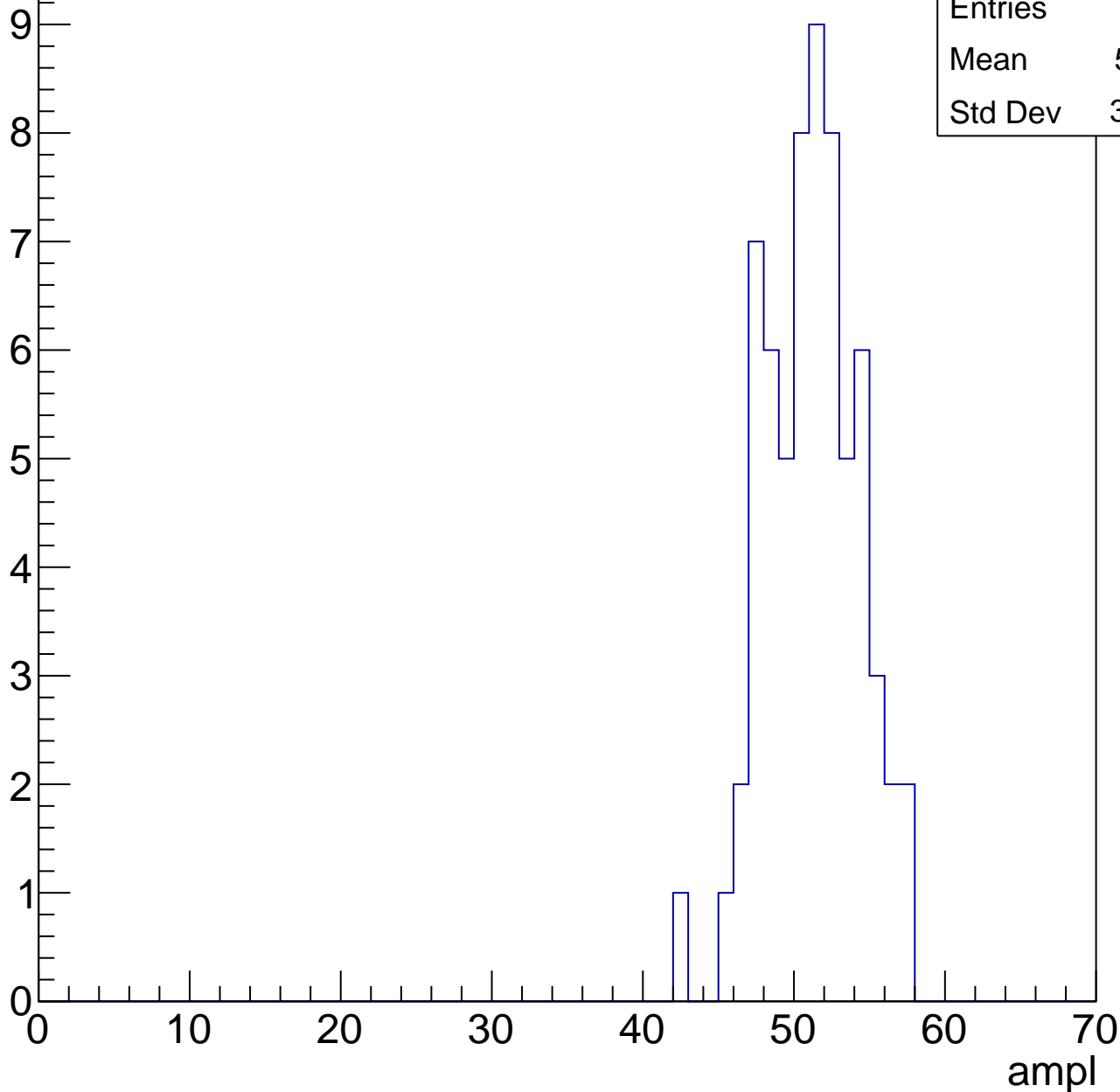


B0L001S, U22-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.71
Std Dev	3.057

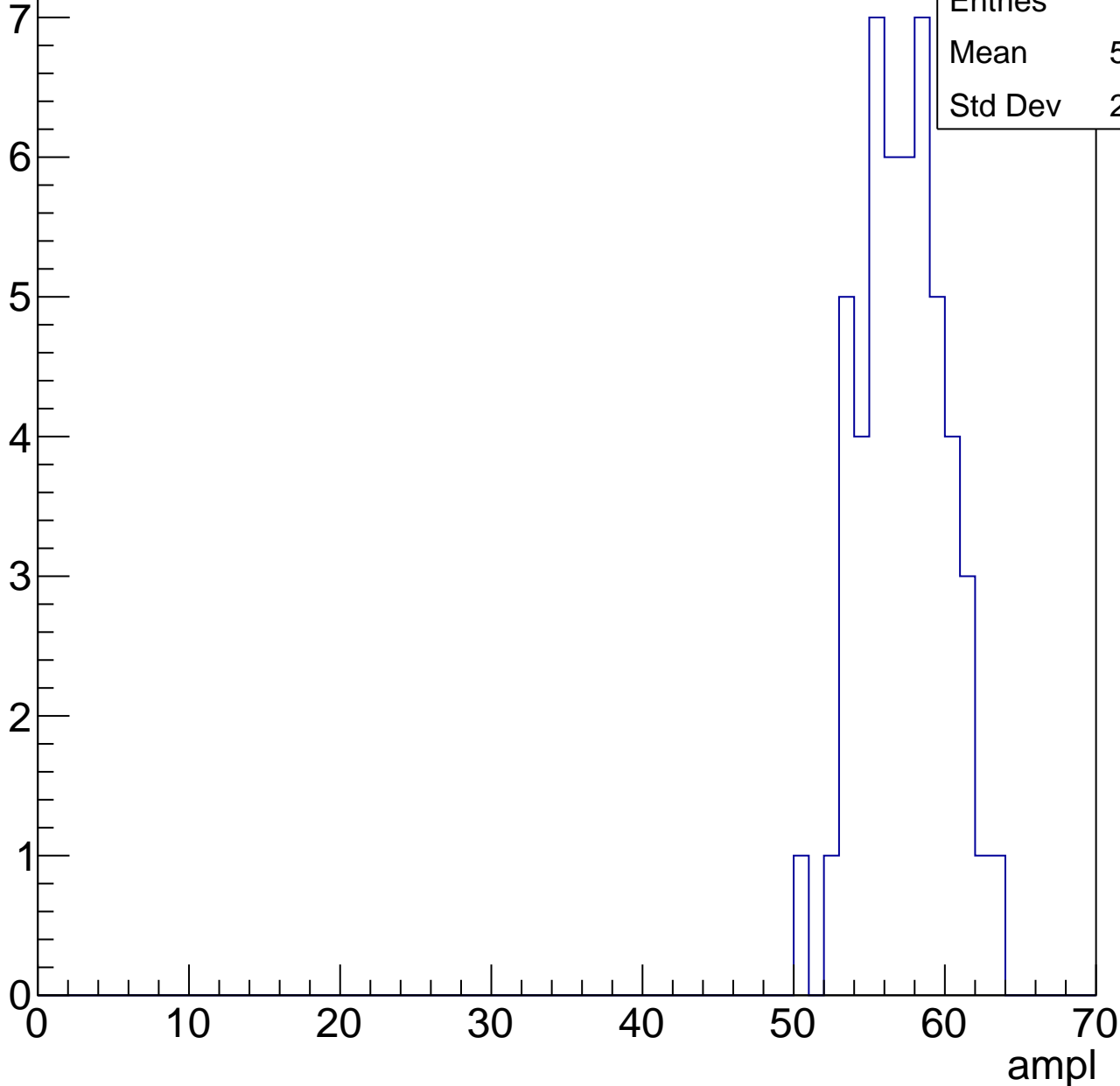


B0L001S, U22-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.76
Std Dev	2.777

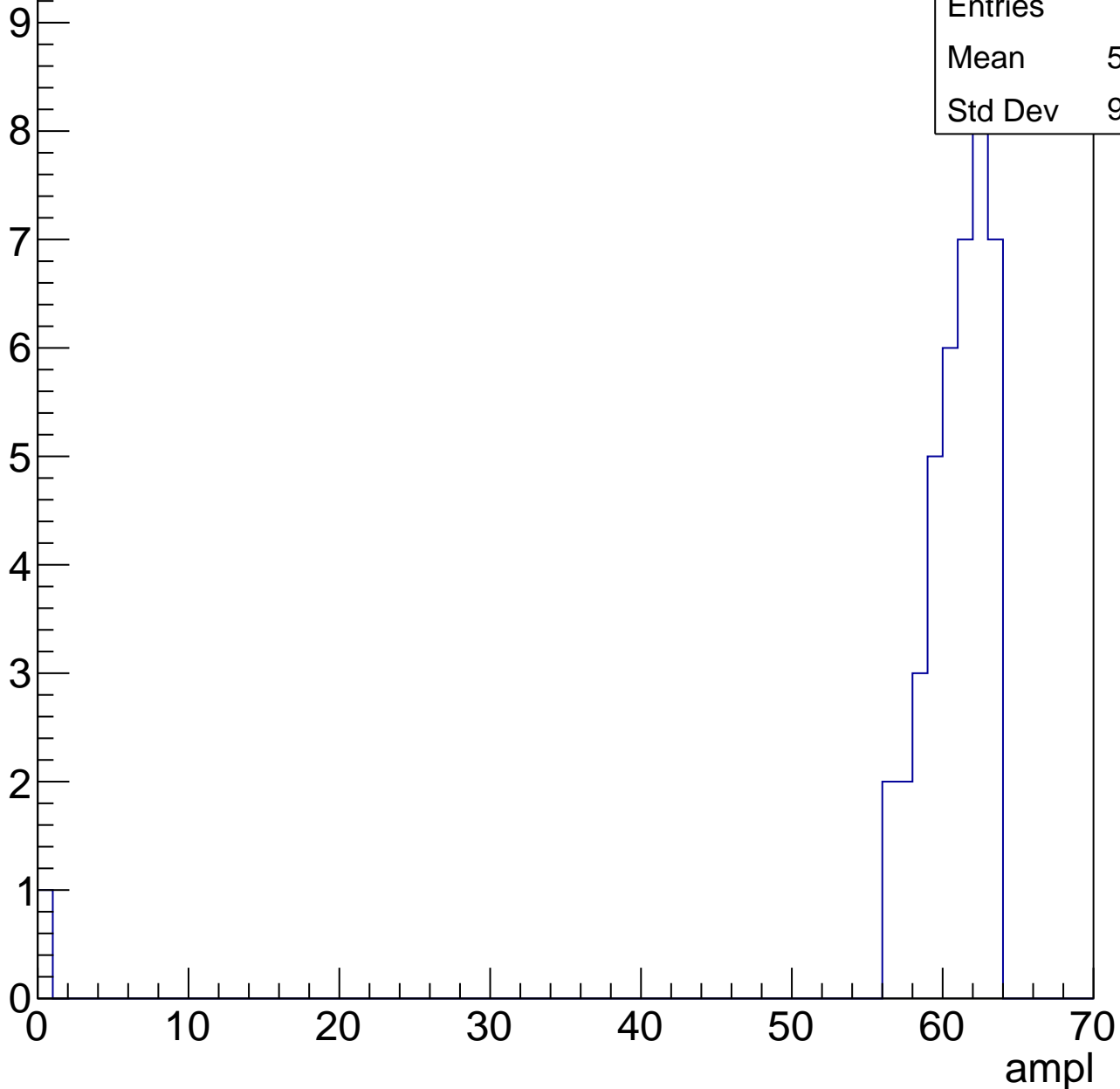


B0L001S, U22-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.07
Std Dev	9.432



B0L001S, U22-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch5, adc0

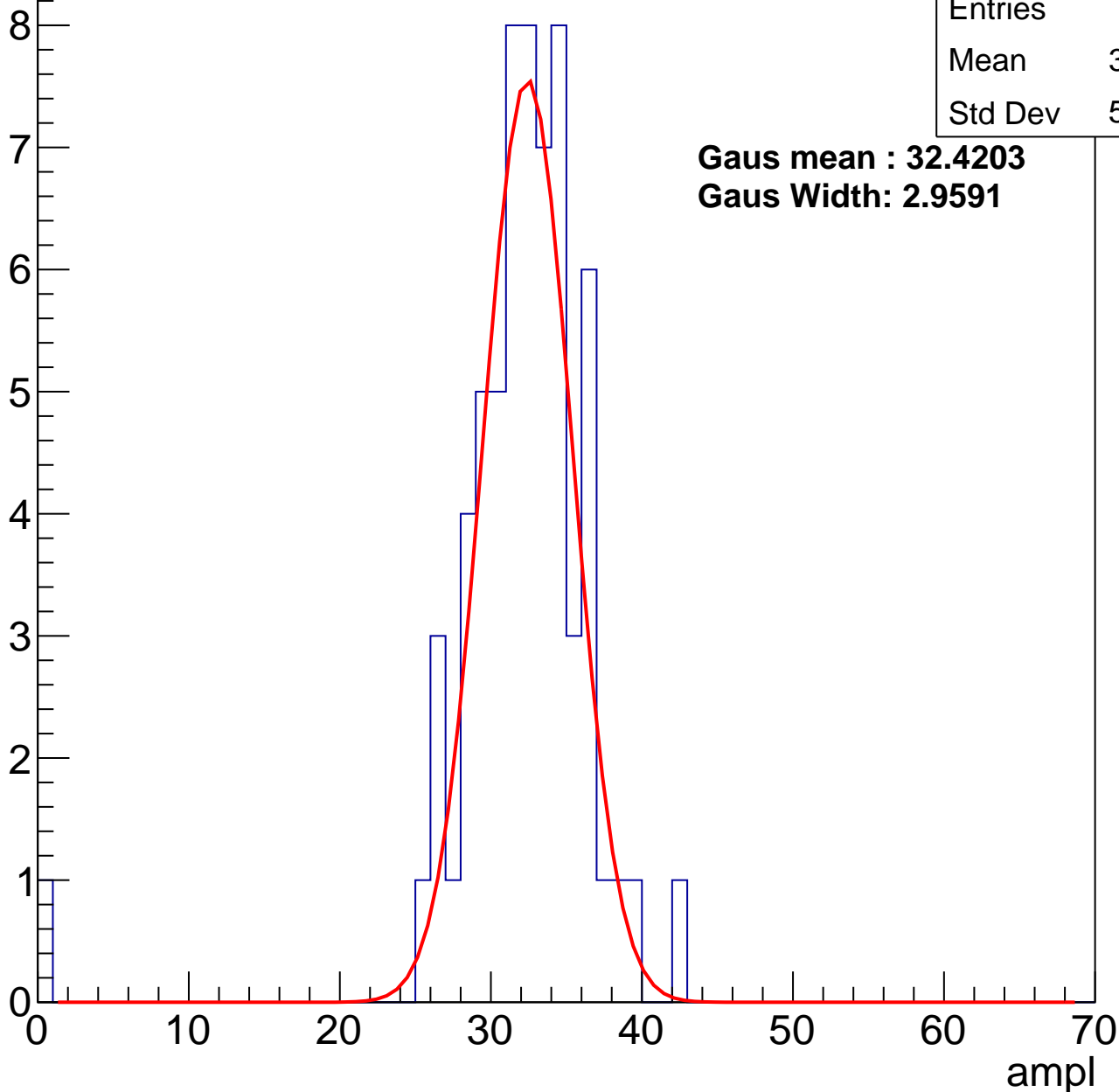
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	31.58
Std Dev	5.159

Gaus mean : 32.4203

Gaus Width: 2.9591



B0L001S, U22-ch5, adc1

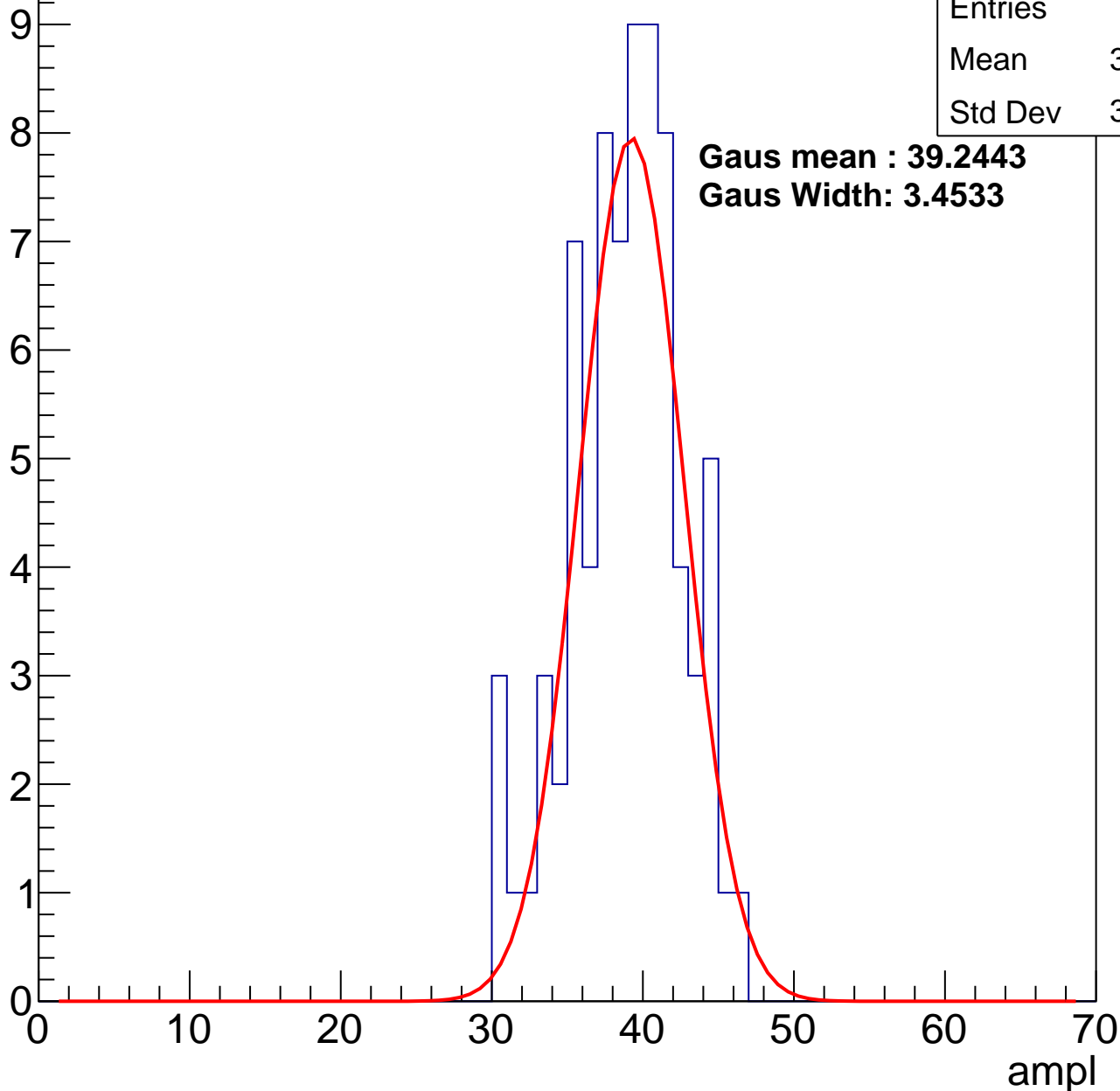
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	38.39
Std Dev	3.642

Gaus mean : 39.2443

Gaus Width: 3.4533



B0L001S, U22-ch5, adc2

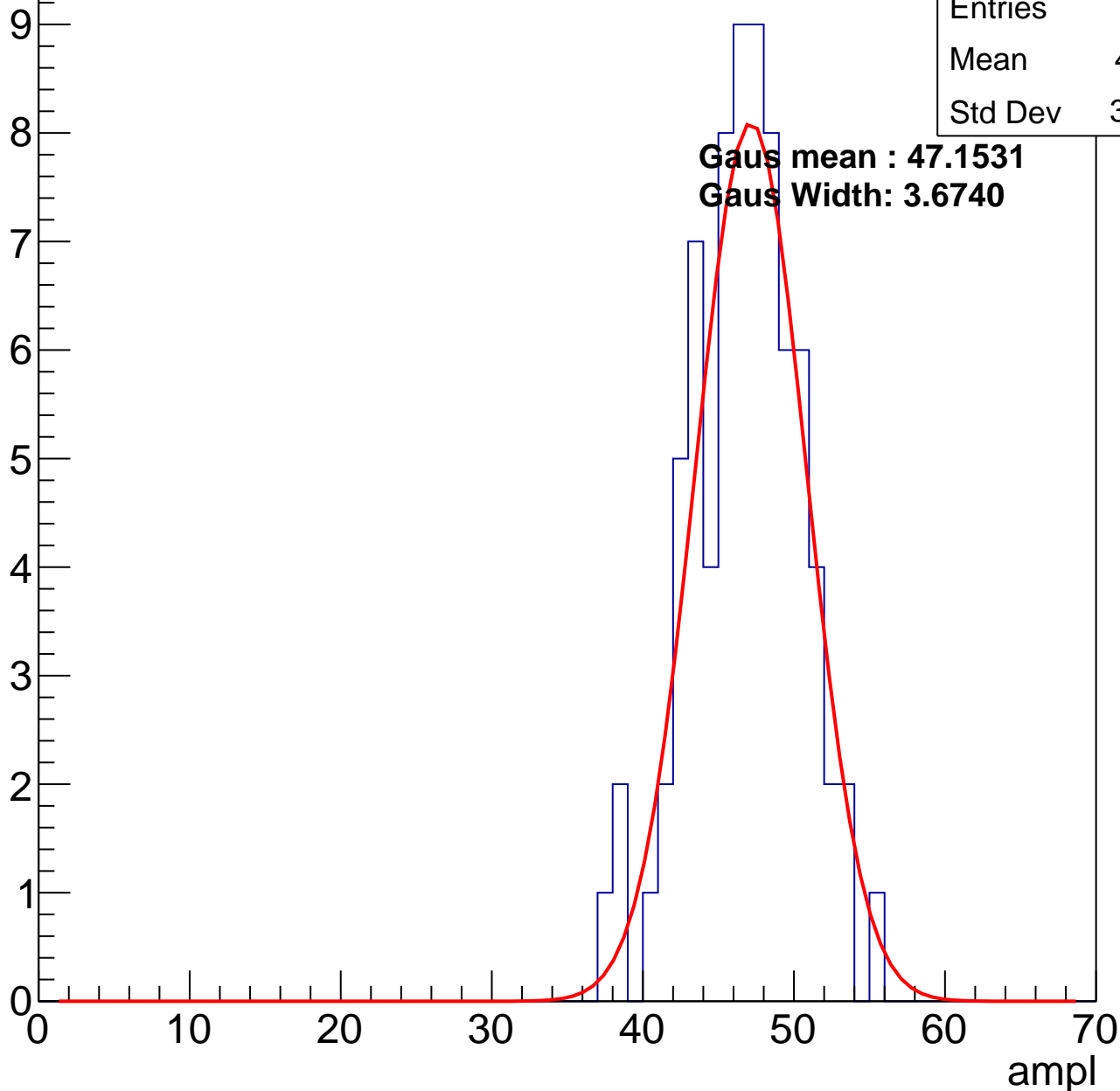
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	46.31
Std Dev	3.597

Gaus mean : 47.1531

Gaus Width: 3.6740

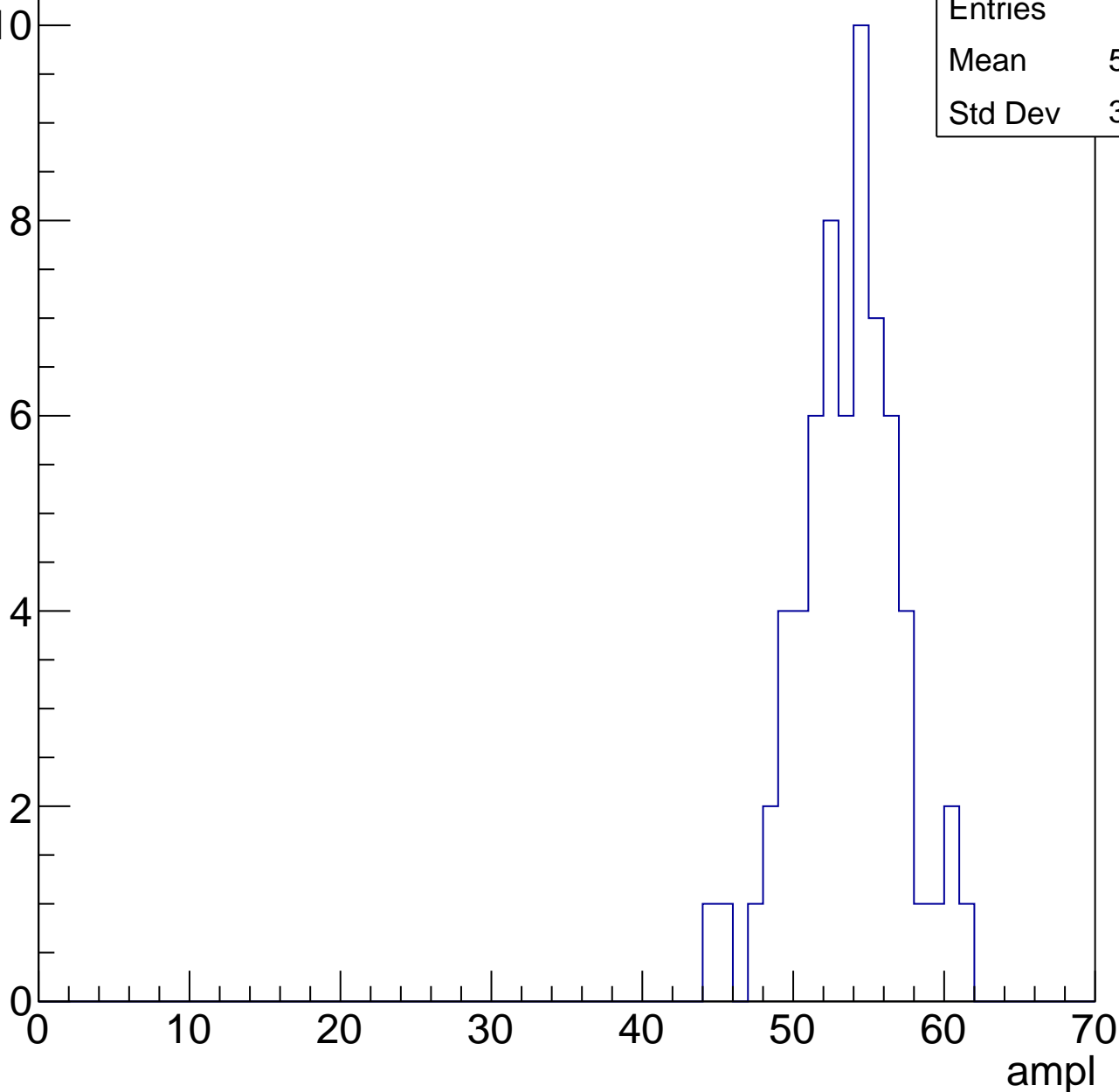


B0L001S, U22-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

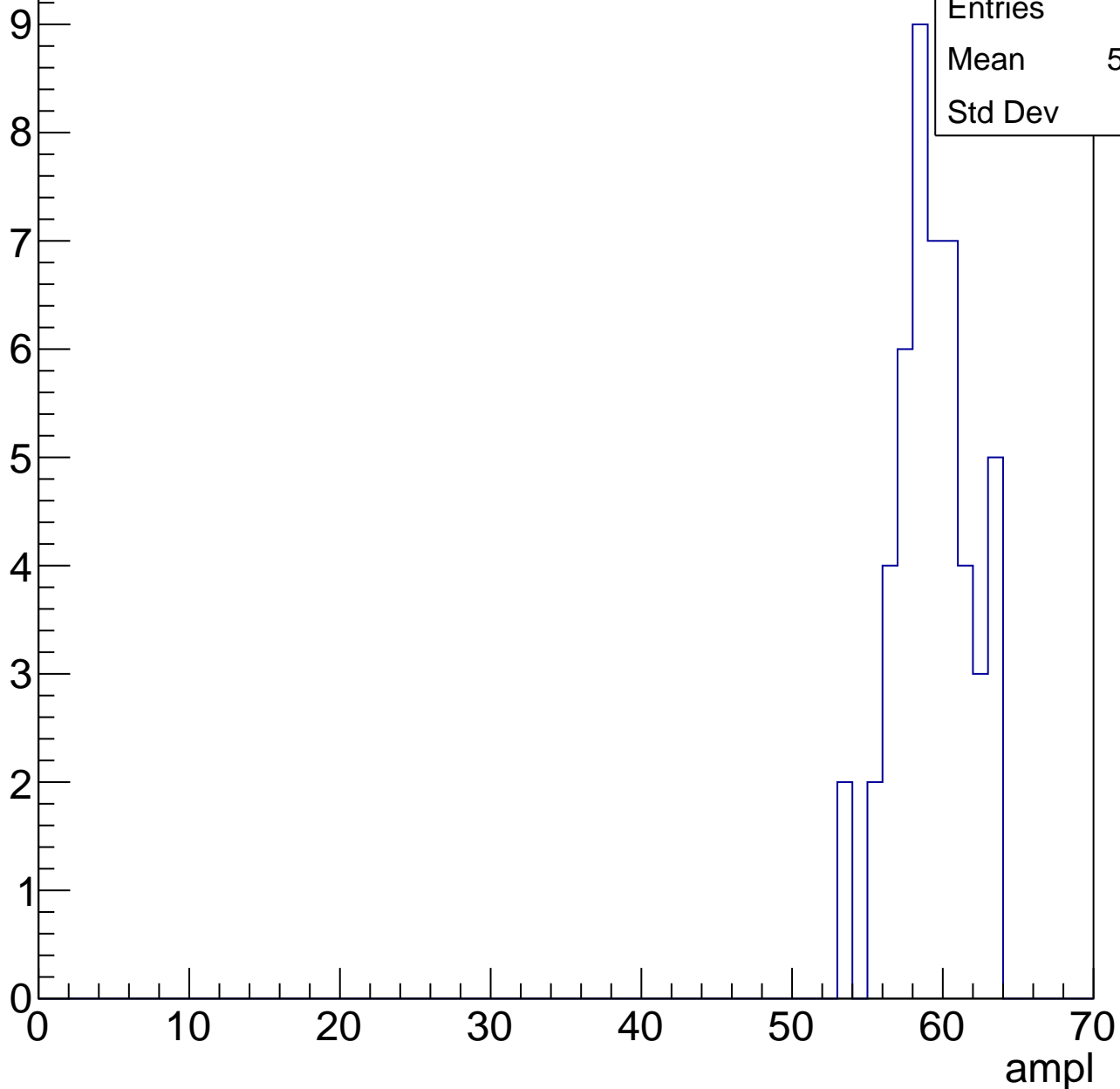
Entries	65
Mean	53.15
Std Dev	3.375



B0L001S, U22-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

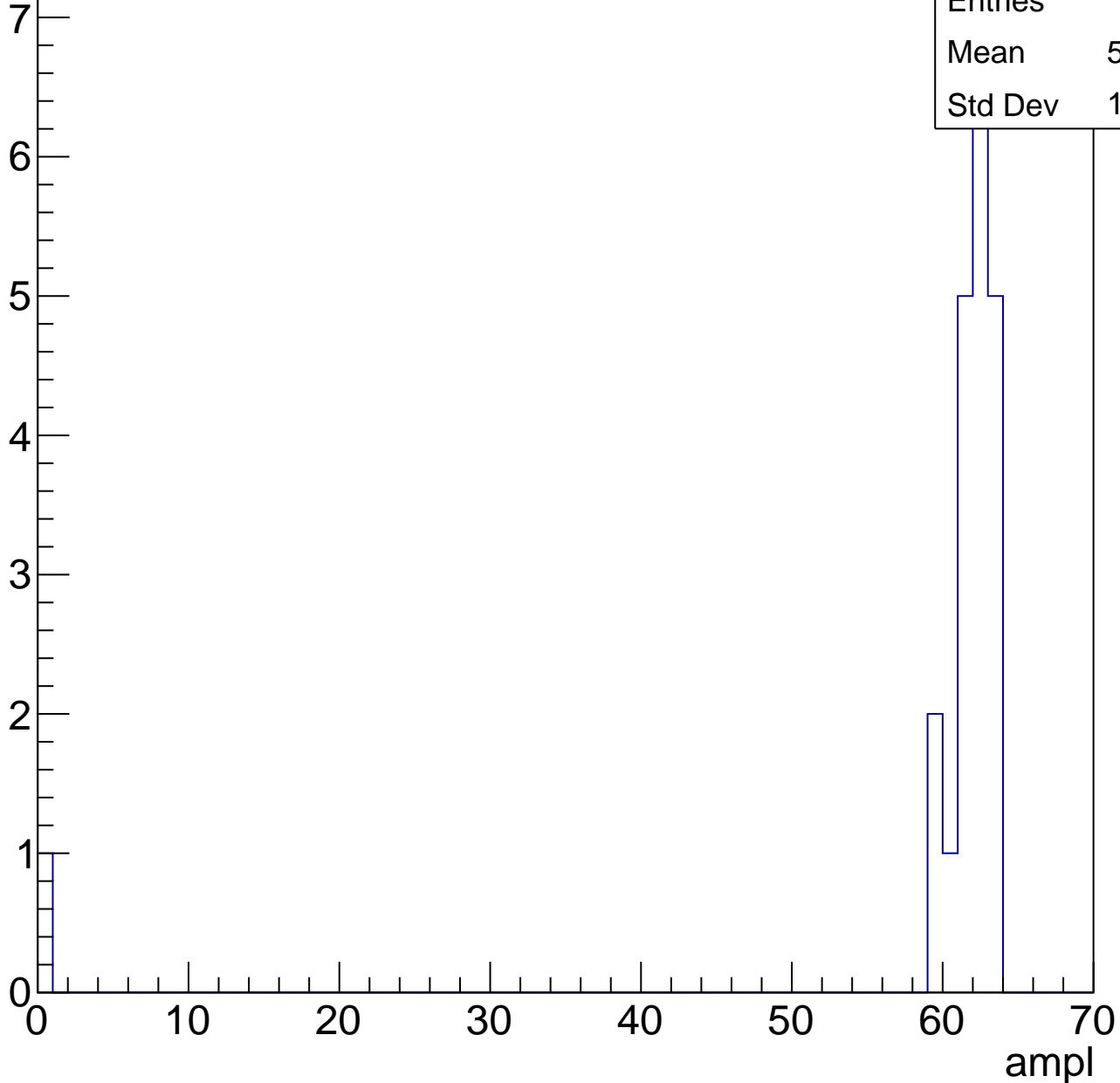


B0L001S, U22-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.67
Std Dev	13.17



B0L001S, U22-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch6, adc0

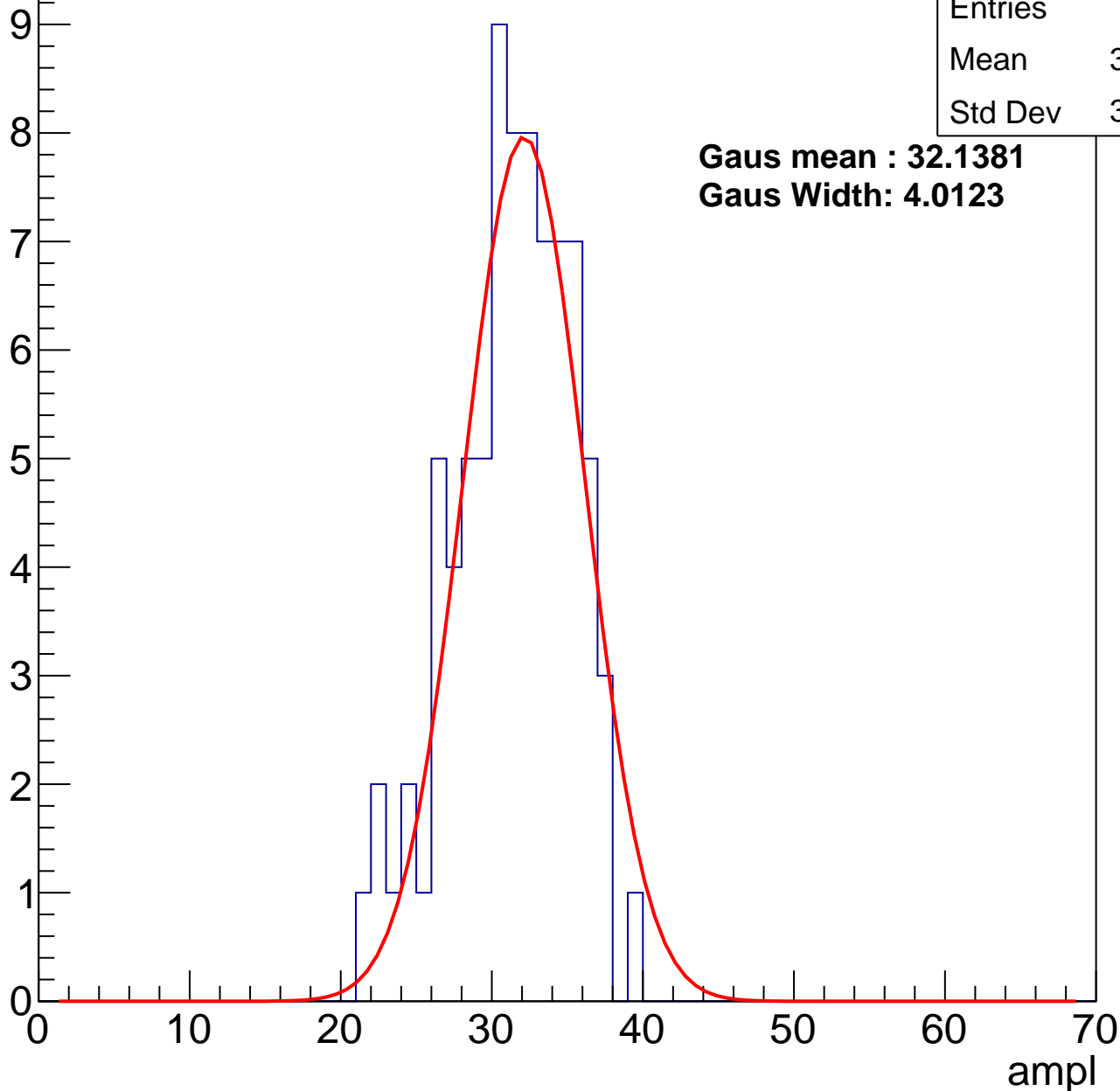
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	30.89
Std Dev	3.897

Gaus mean : 32.1381

Gaus Width: 4.0123



B0L001S, U22-ch6, adc1

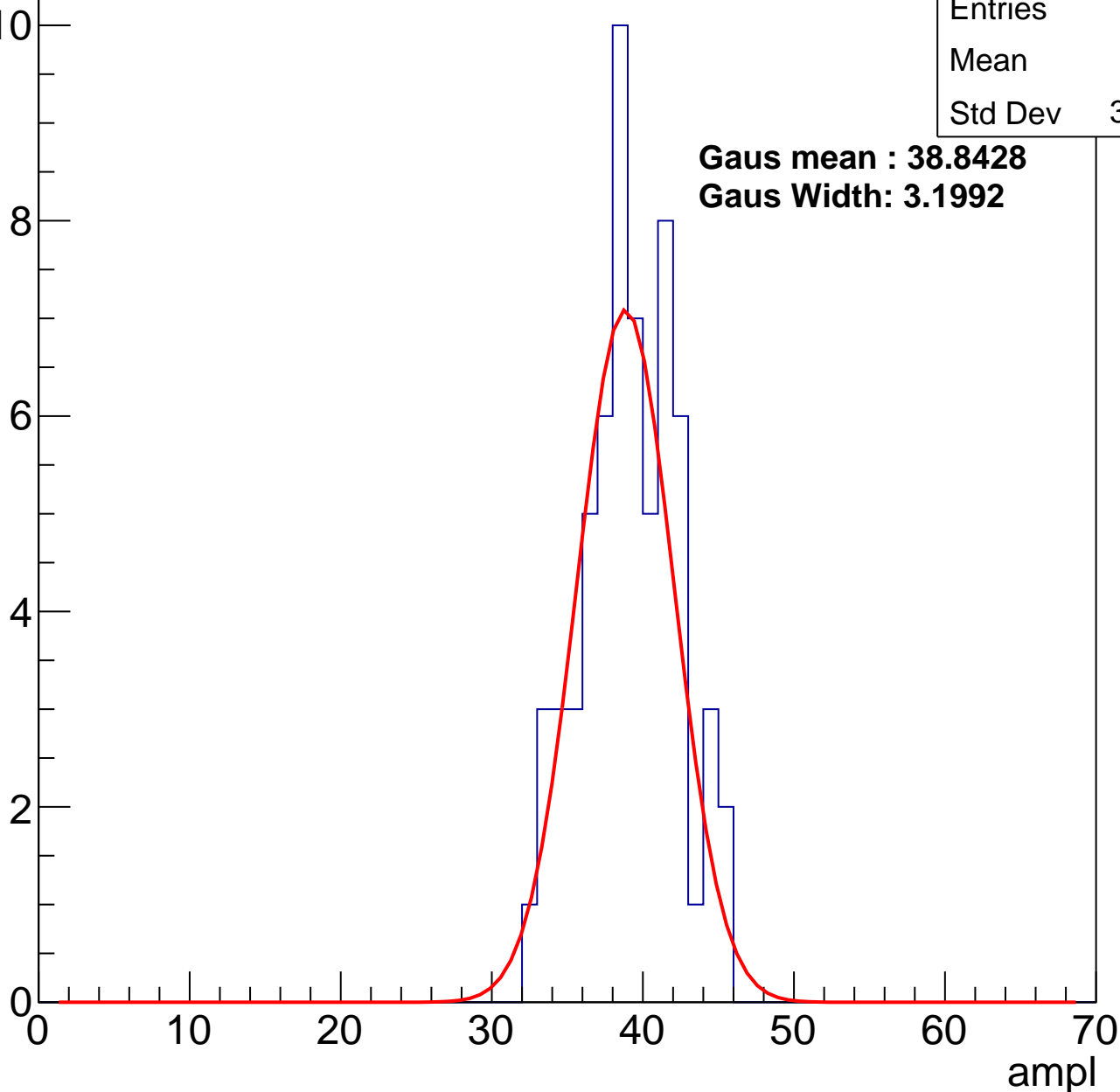
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.7
Std Dev	3.105

Gaus mean : 38.8428

Gaus Width: 3.1992



B0L001S, U22-ch6, adc2

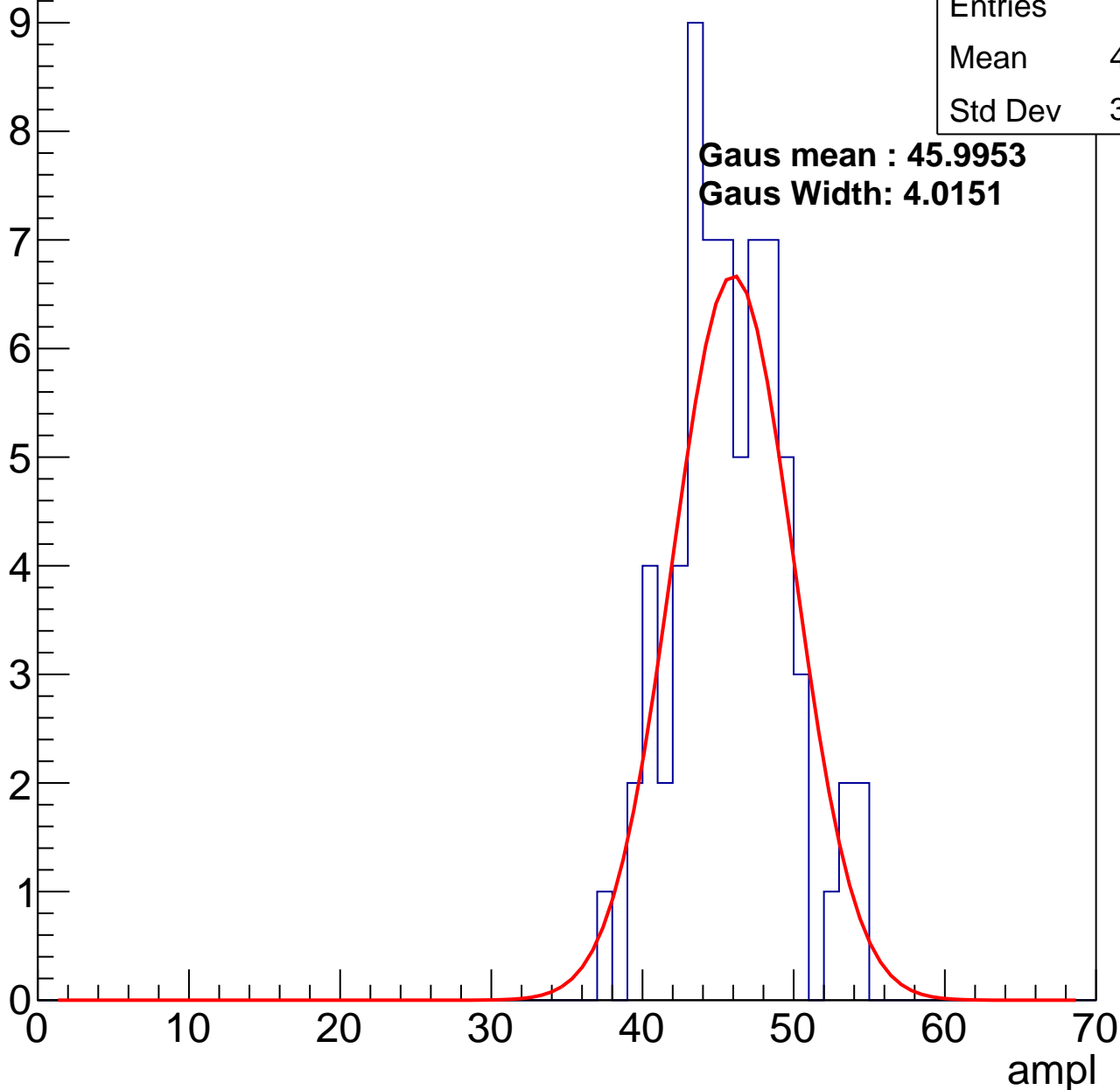
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45.46
Std Dev	3.684

Gaus mean : 45.9953

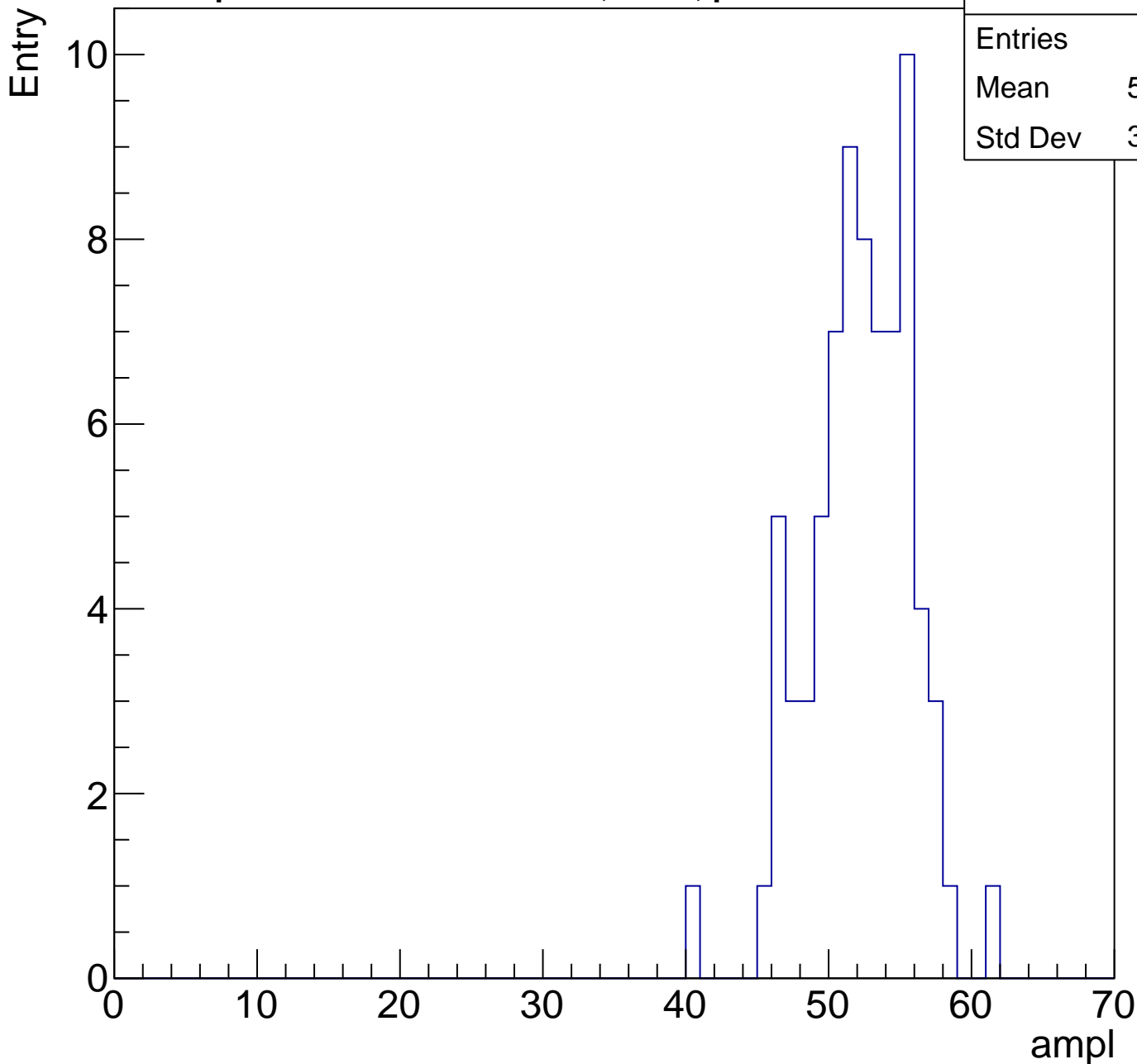
Gaus Width: 4.0151



B0L001S, U22-ch6, adc3

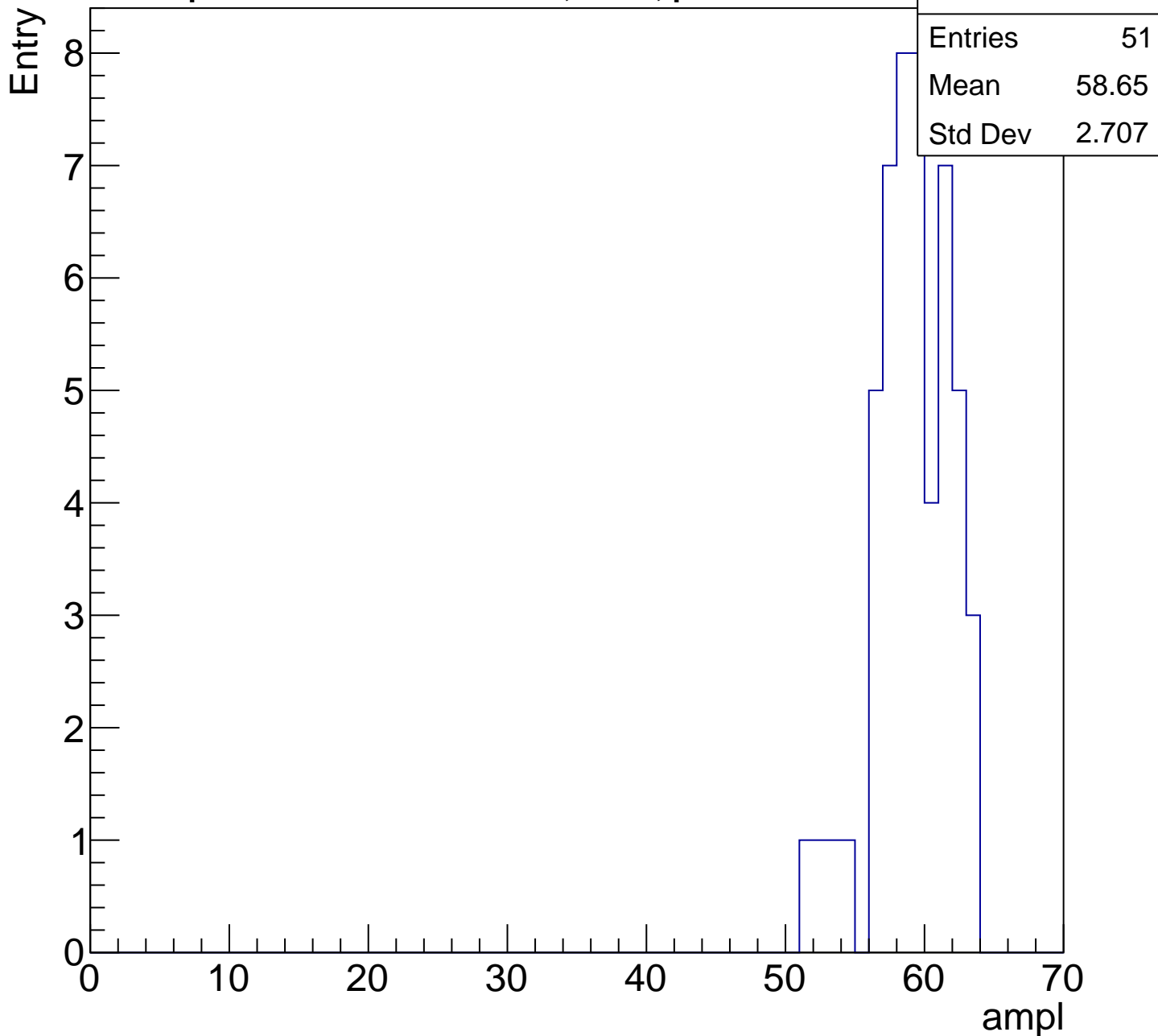
calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	51.77
Std Dev	3.569



B0L001S, U22-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

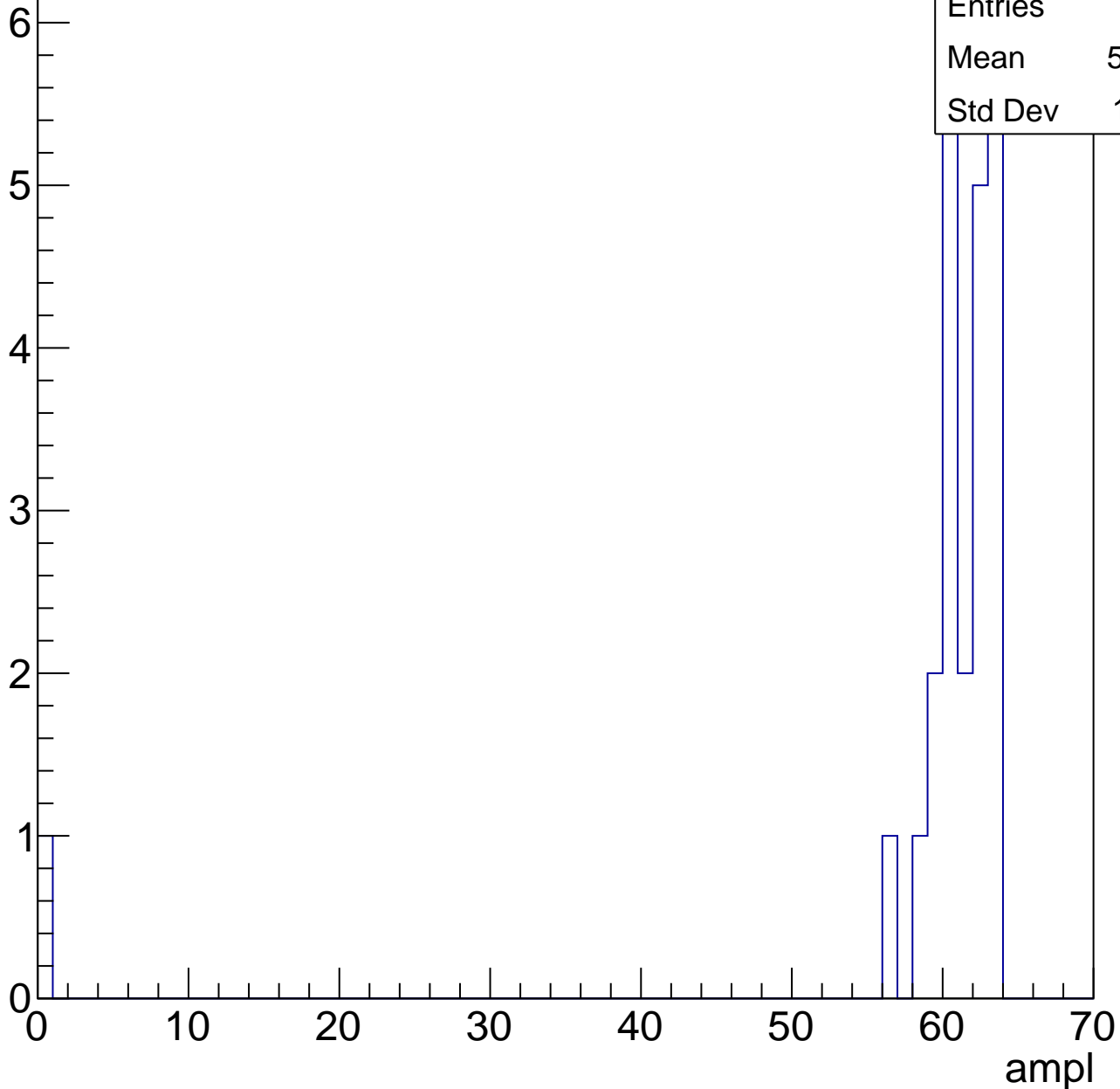


B0L001S, U22-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.42
Std Dev	12.31



B0L001S, U22-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch7, adc0

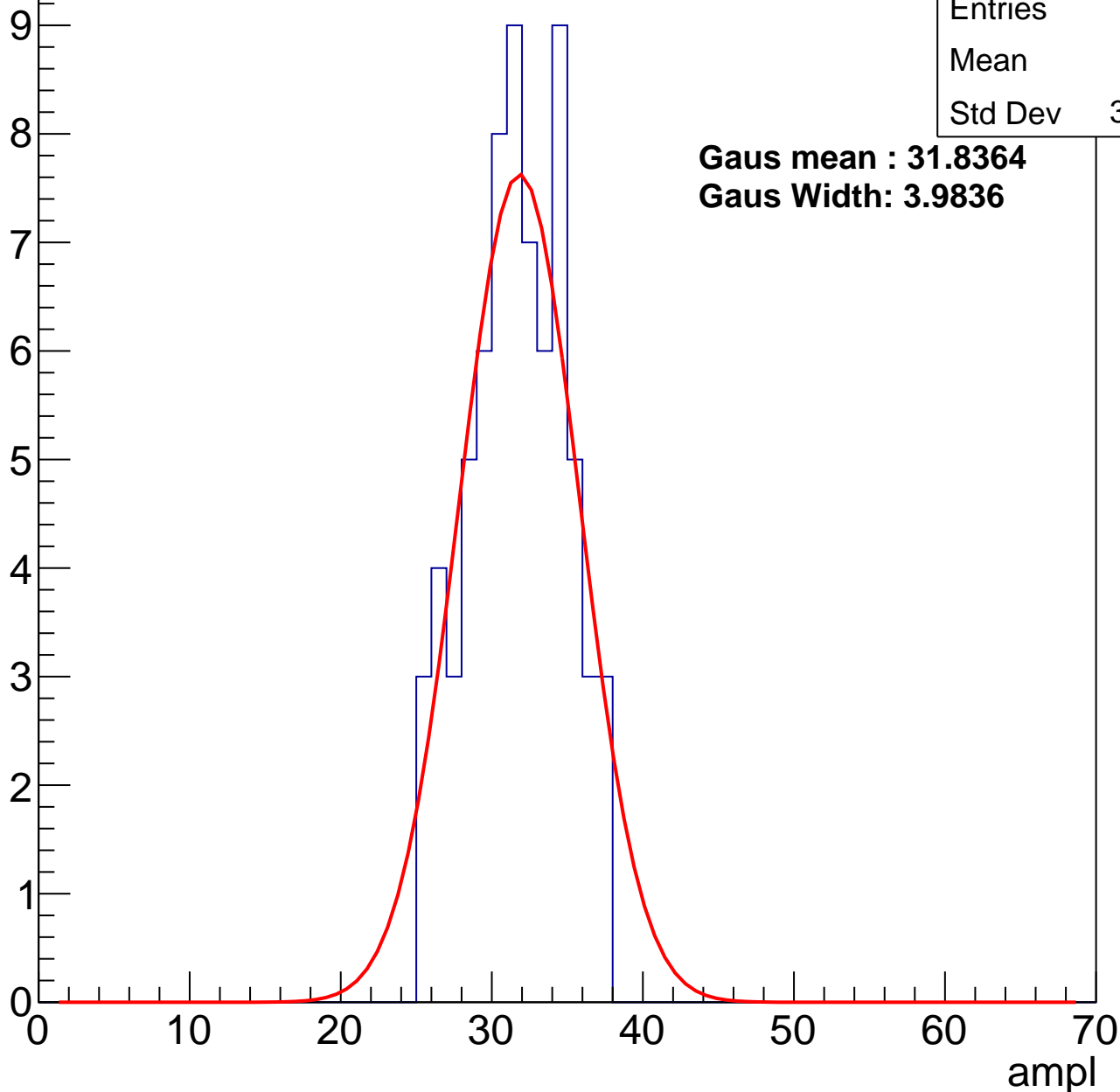
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	31.2
Std Dev	3.152

Gaus mean : 31.8364

Gaus Width: 3.9836



B0L001S, U22-ch7, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	38.72
Std Dev	3.423

Gaus mean : 39.5386

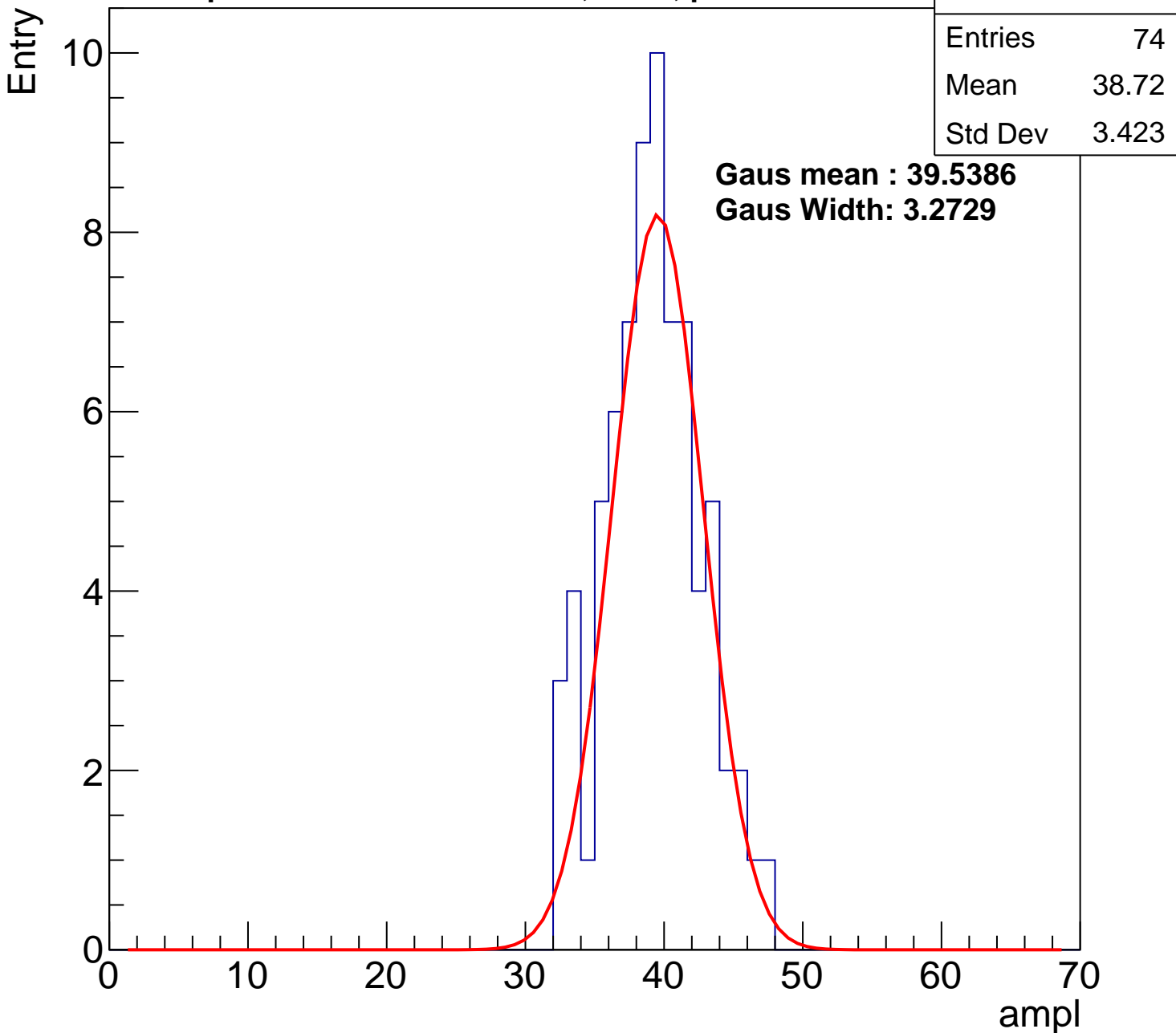
Gaus Width: 3.2729

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch7, adc2

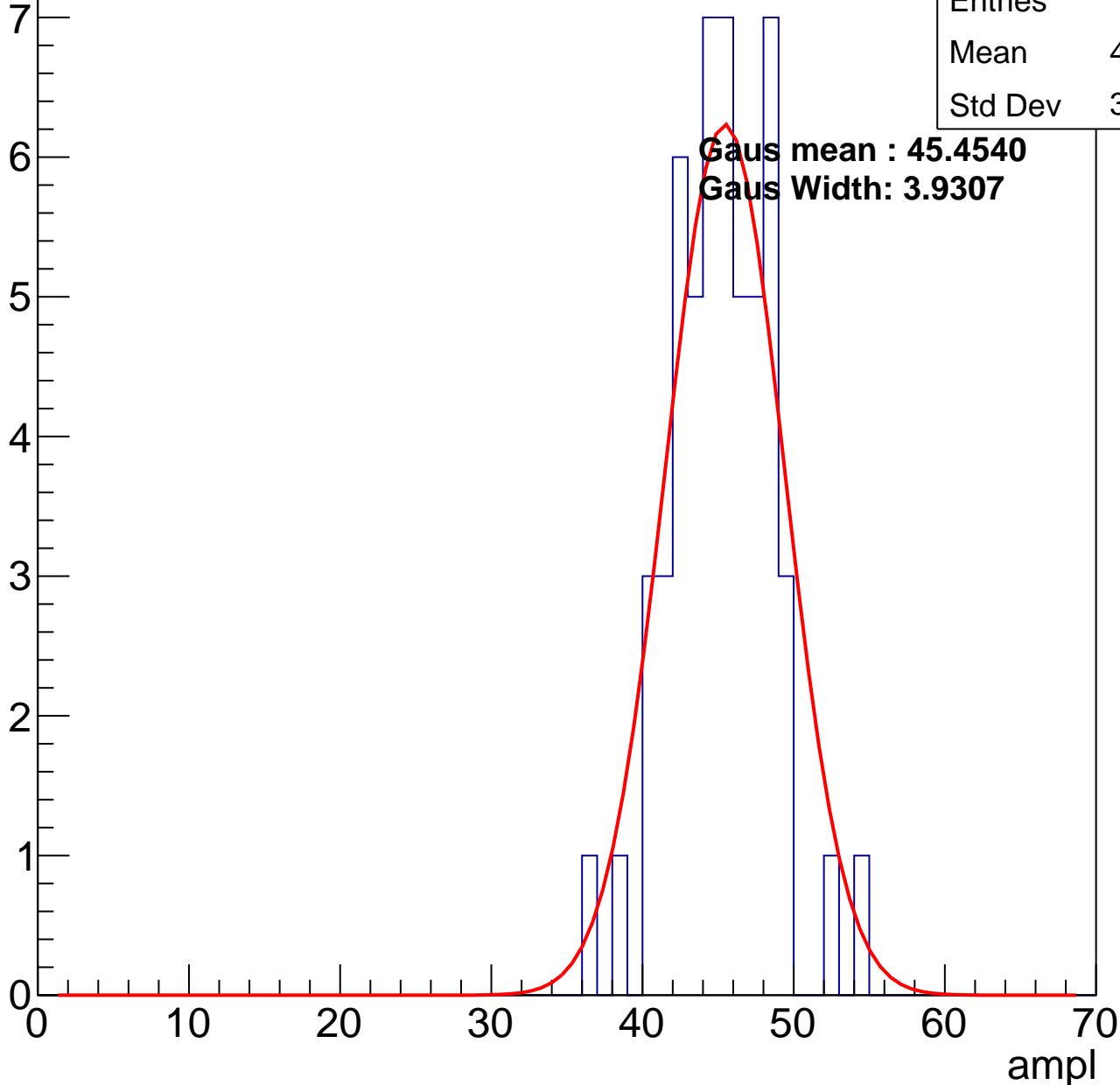
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.75
Std Dev	3.293

Gaus mean : 45.4540

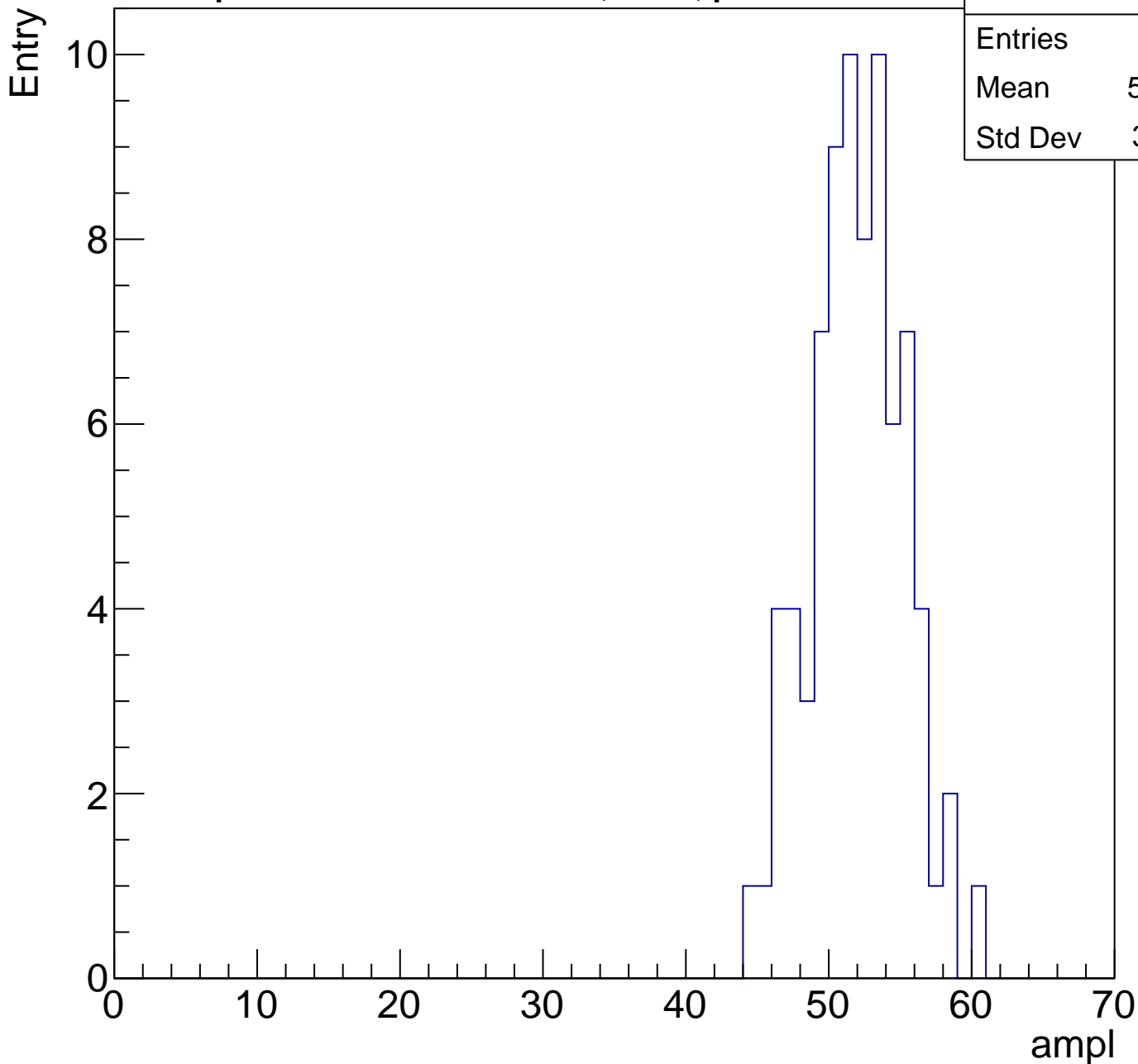
Gaus Width: 3.9307



B0L001S, U22-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	51.54
Std Dev	3.241

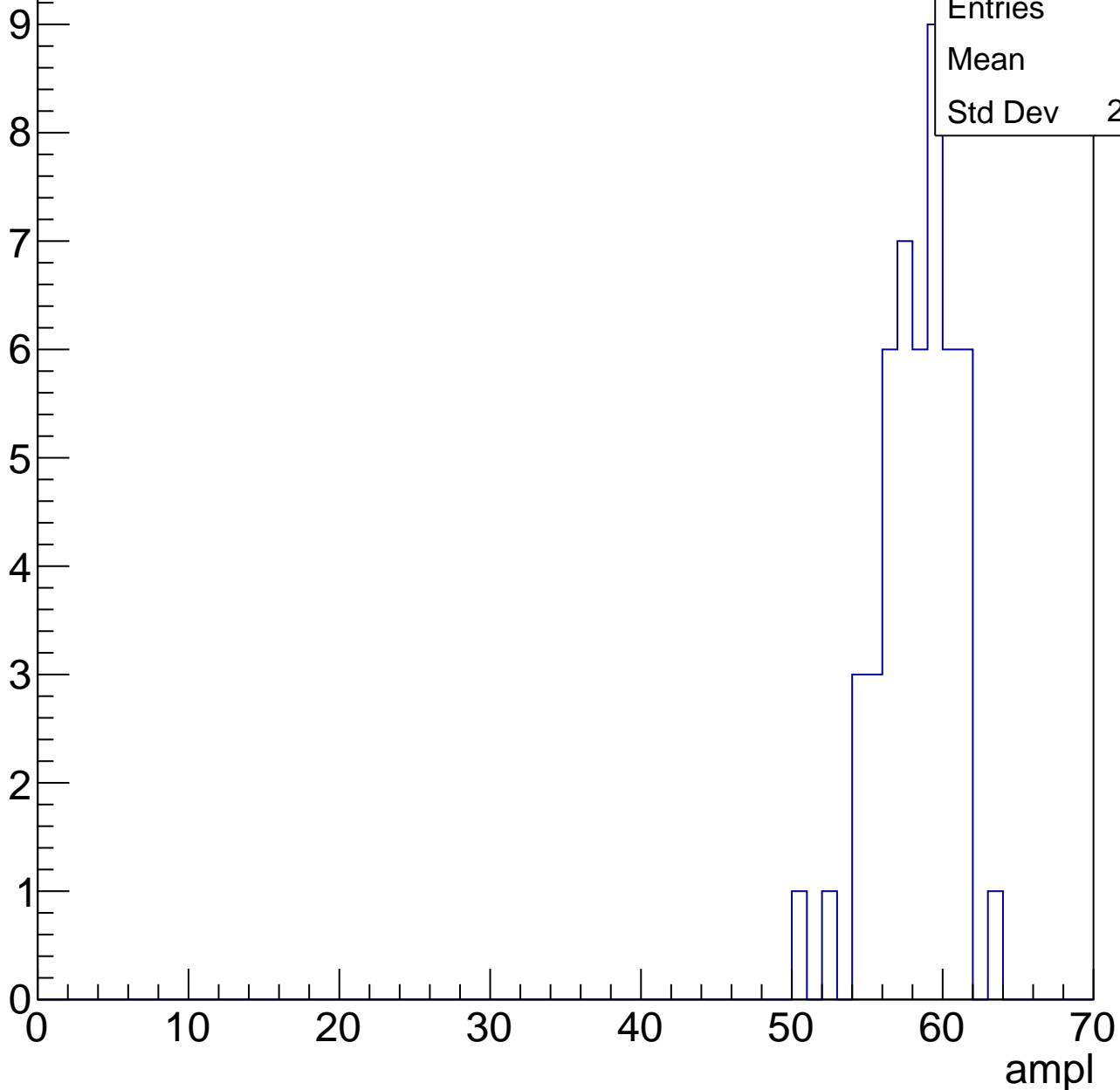


B0L001S, U22-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	57.8
Std Dev	2.539

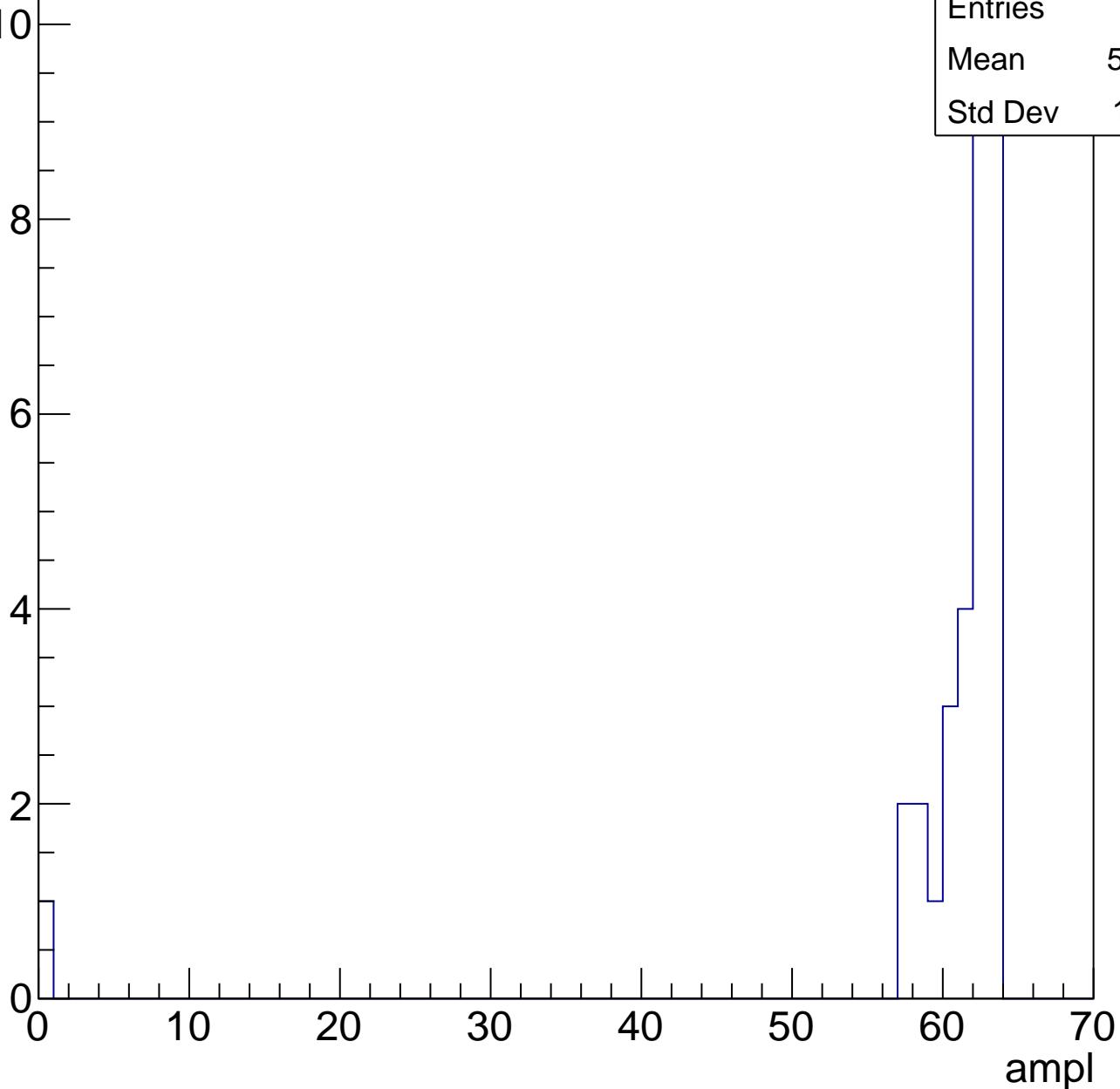


B0L001S, U22-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59.38
Std Dev	10.81



B0L001S, U22-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B0L001S, U22-ch8, adc0

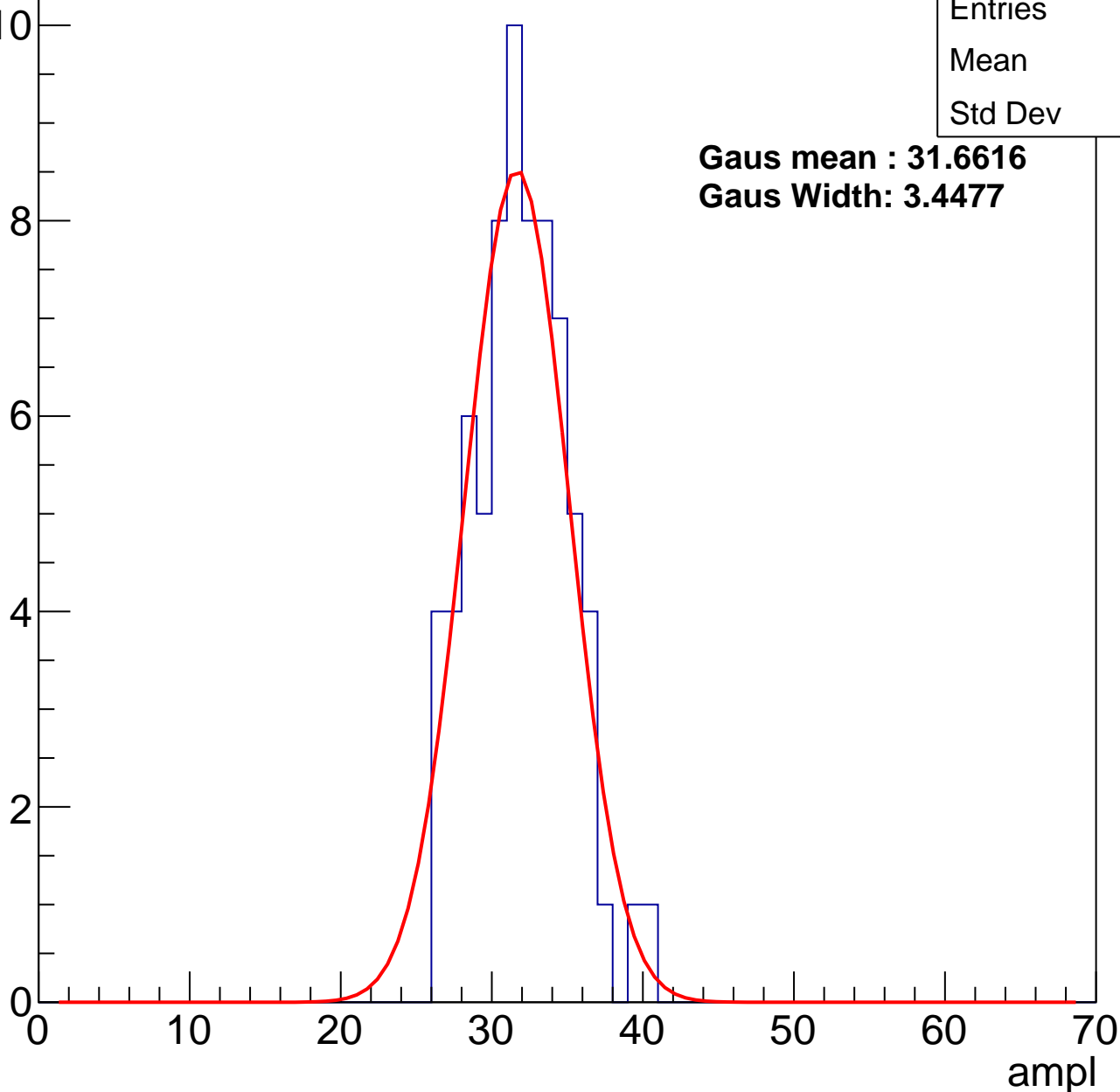
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	31.5
Std Dev	3.1

Gaus mean : 31.6616

Gaus Width: 3.4477



B0L001S, U22-ch8, adc1

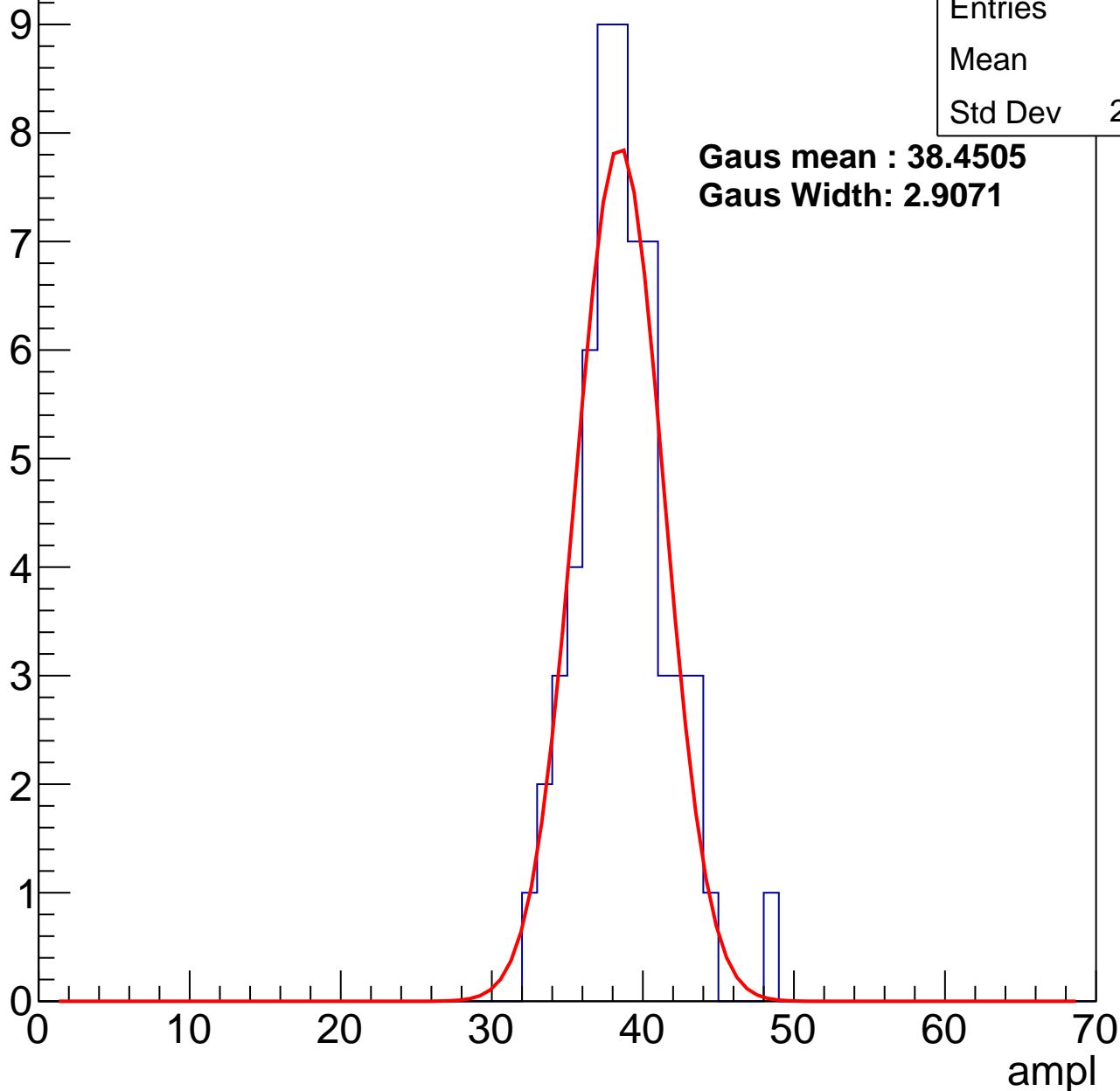
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.2
Std Dev	2.973

Gaus mean : 38.4505

Gaus Width: 2.9071



B0L001S, U22-ch8, adc2

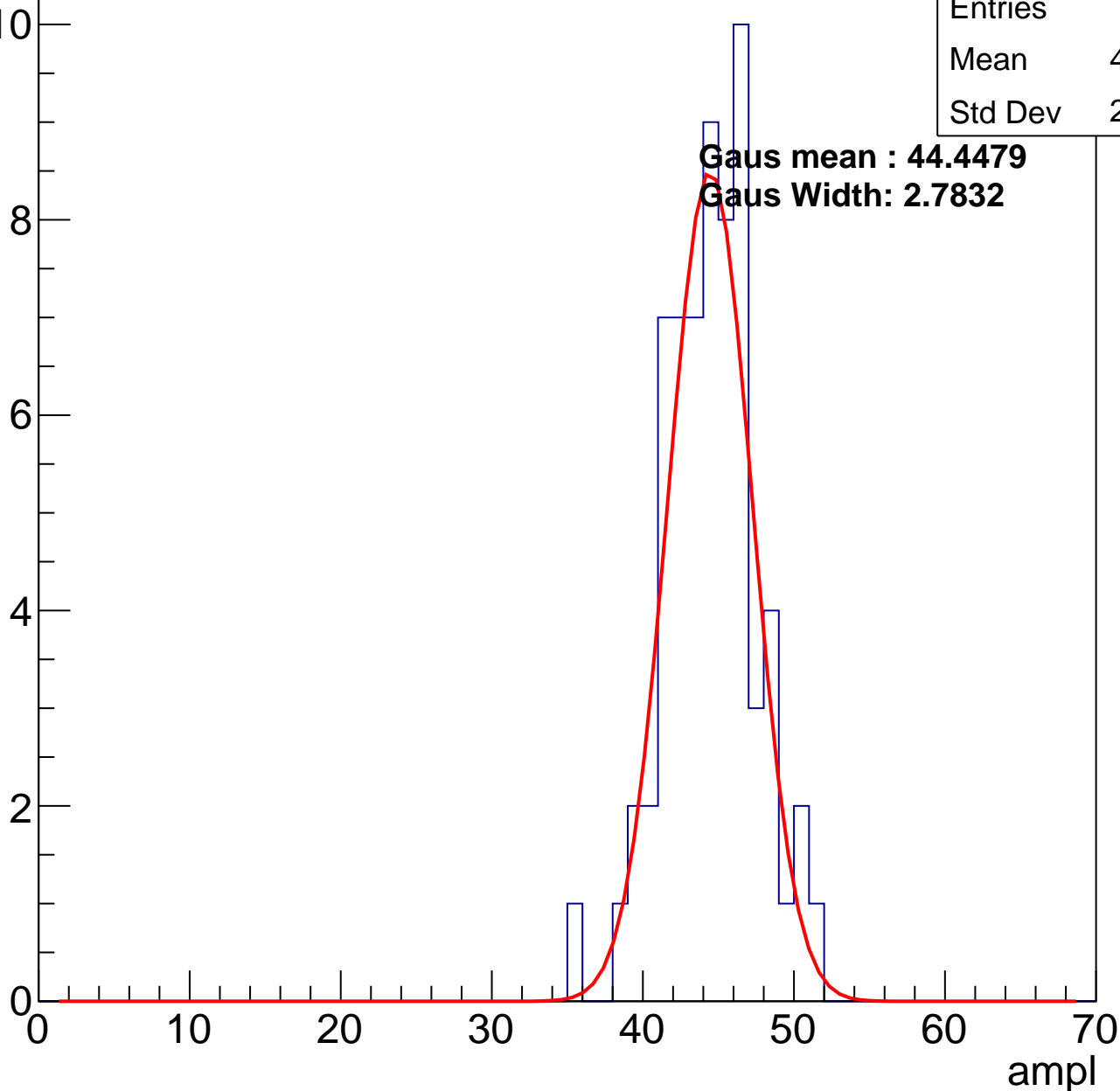
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.03
Std Dev	2.992

Gaus mean : 44.4479

Gaus Width: 2.7832



B0L001S, U22-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	51.7
Std Dev	3.348

Entry

10

8

6

4

2

0

0

10

20

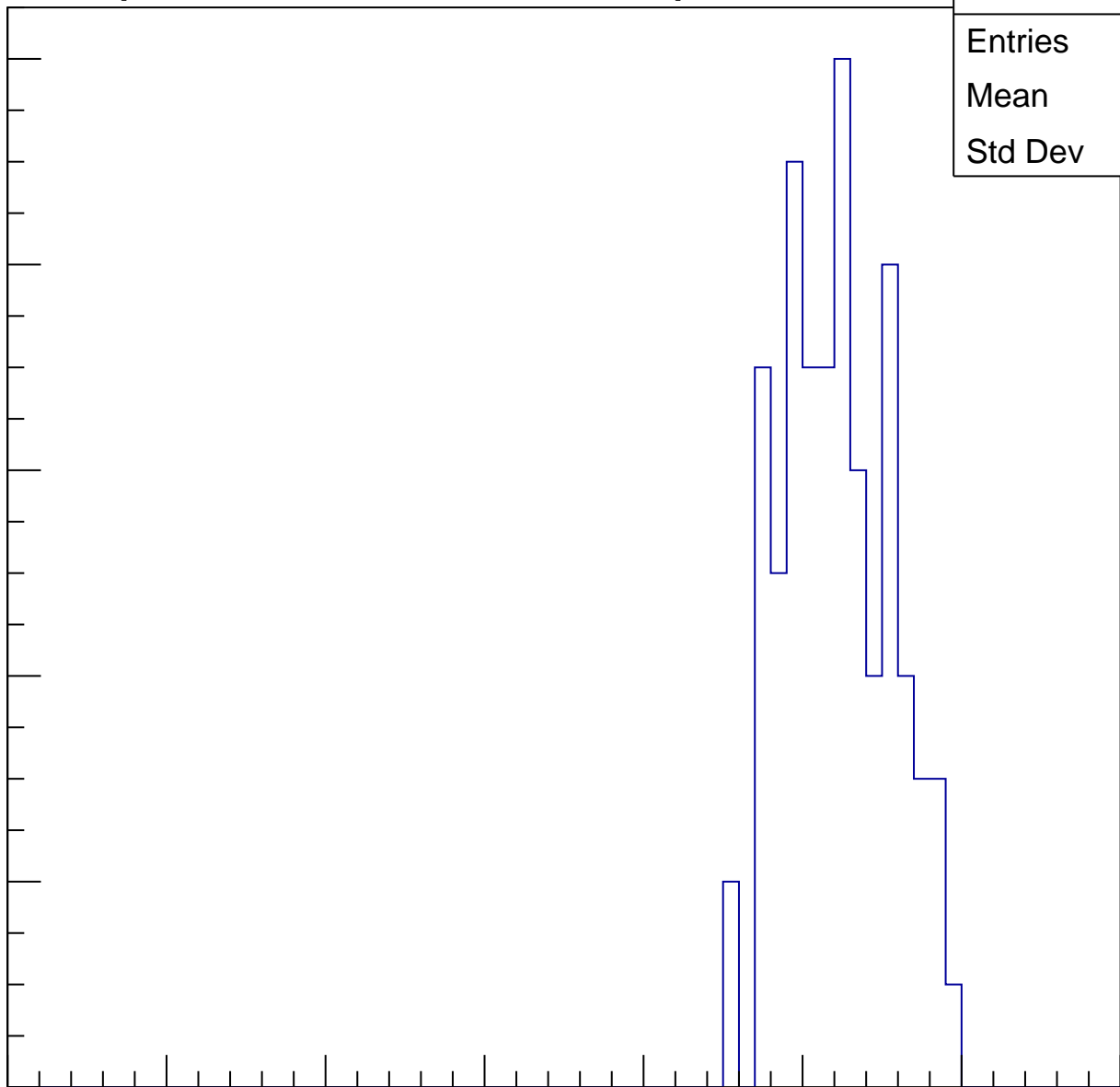
30

40

50

60

ampl

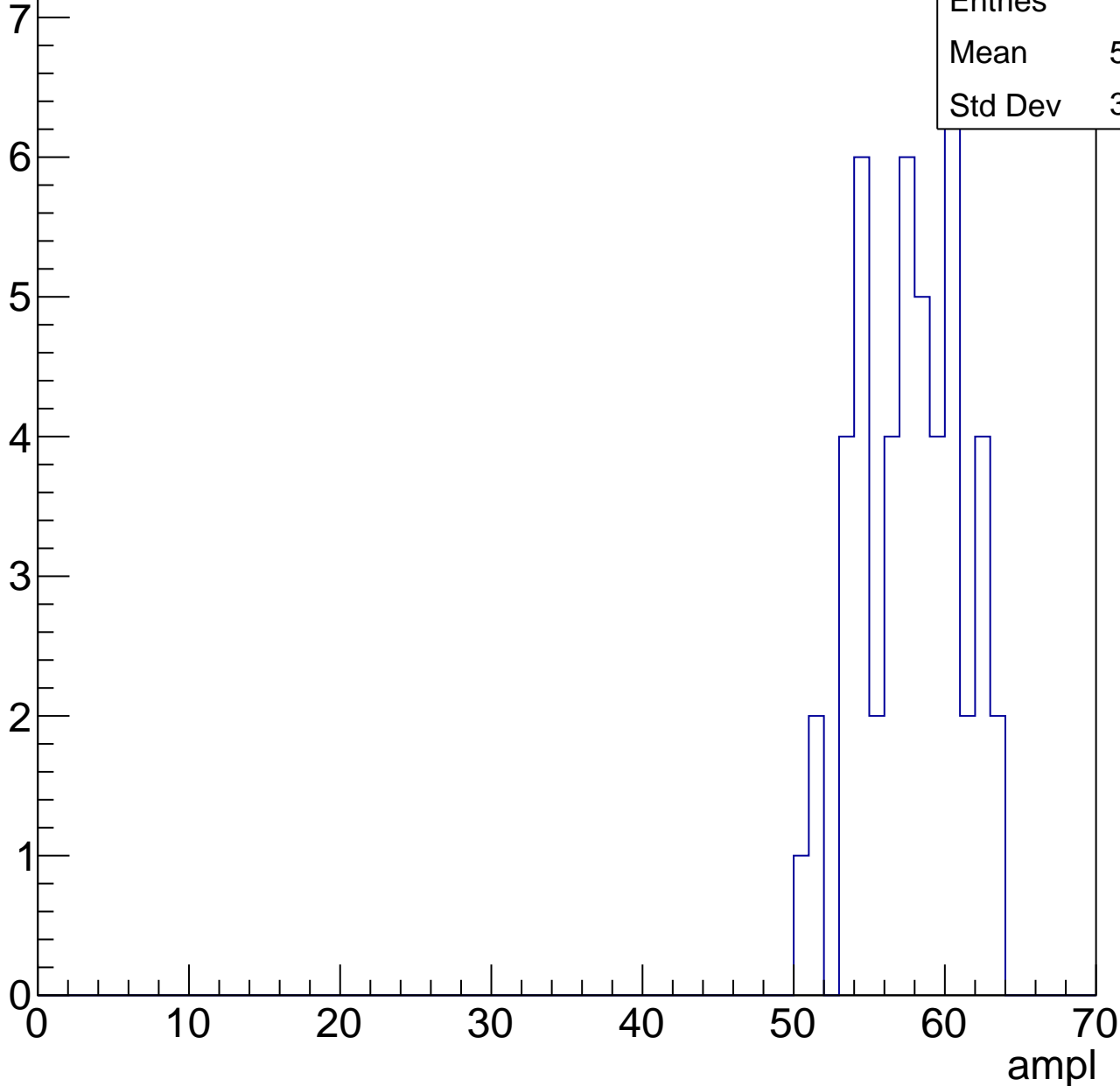


B0L001S, U22-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	57.27
Std Dev	3.306

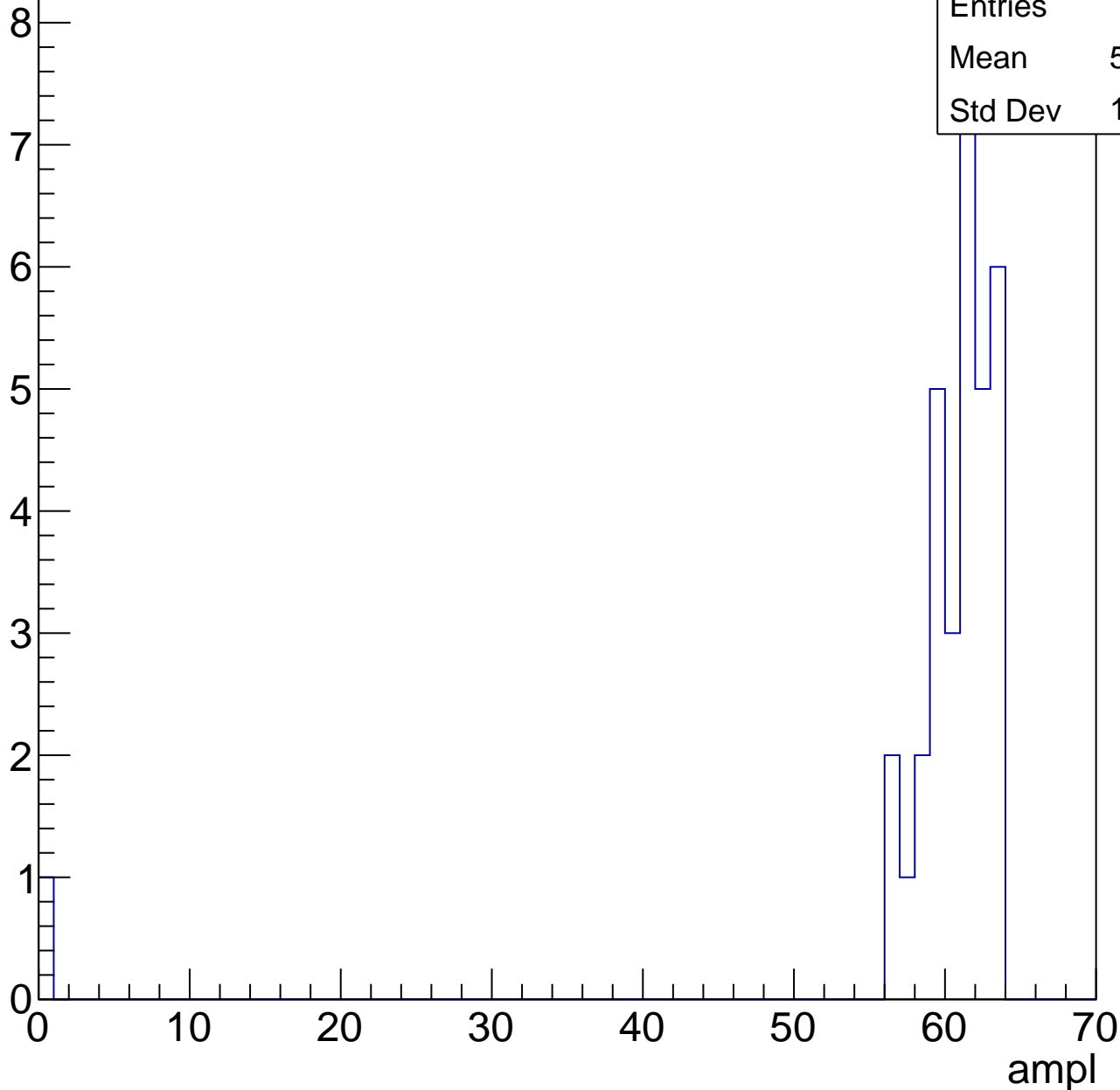


B0L001S, U22-ch8, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.67
Std Dev	10.56



B0L001S, U22-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch9, adc0

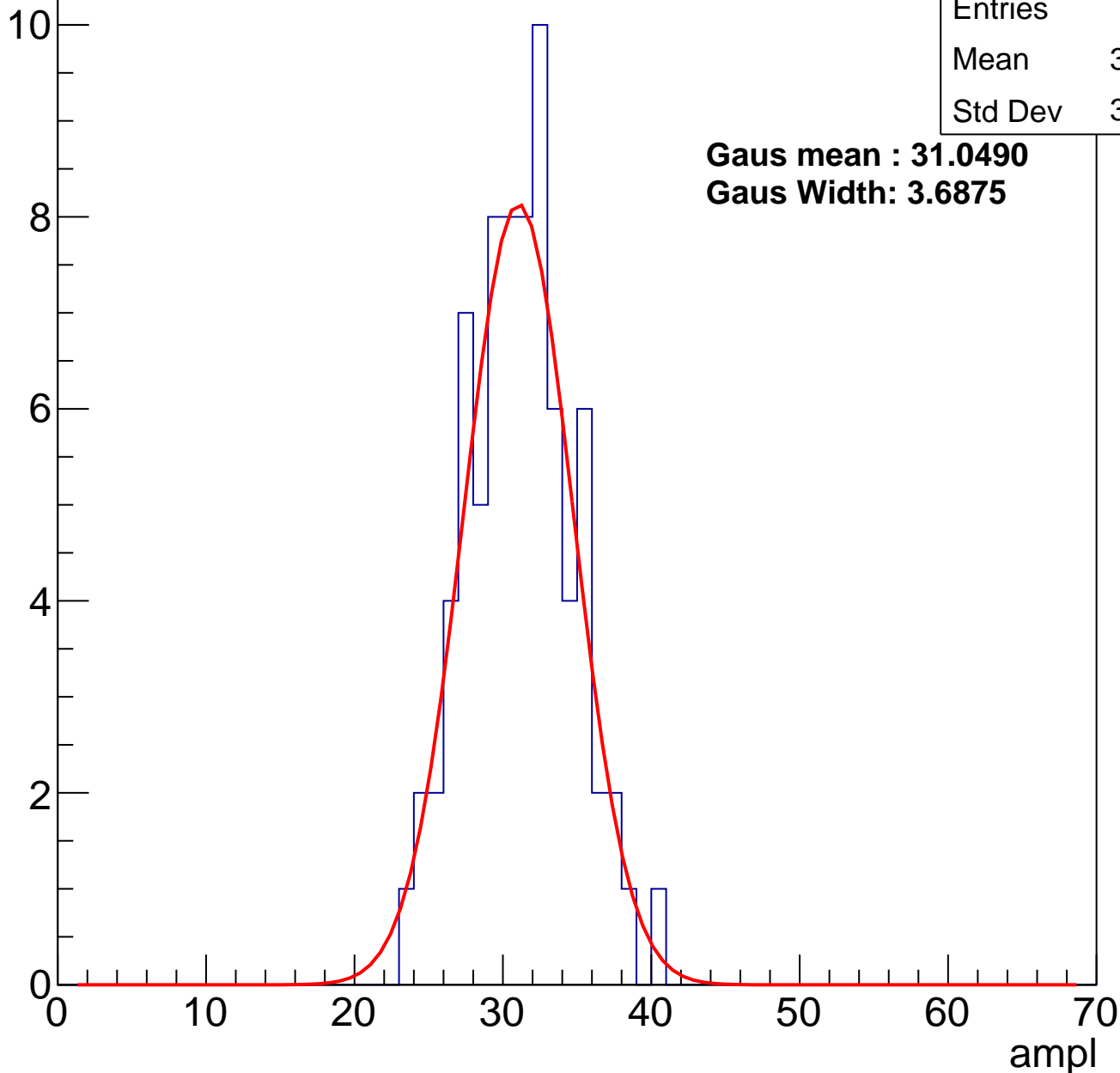
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	30.68
Std Dev	3.496

Gaus mean : 31.0490

Gaus Width: 3.6875

Entry



B0L001S, U22-ch9, adc1

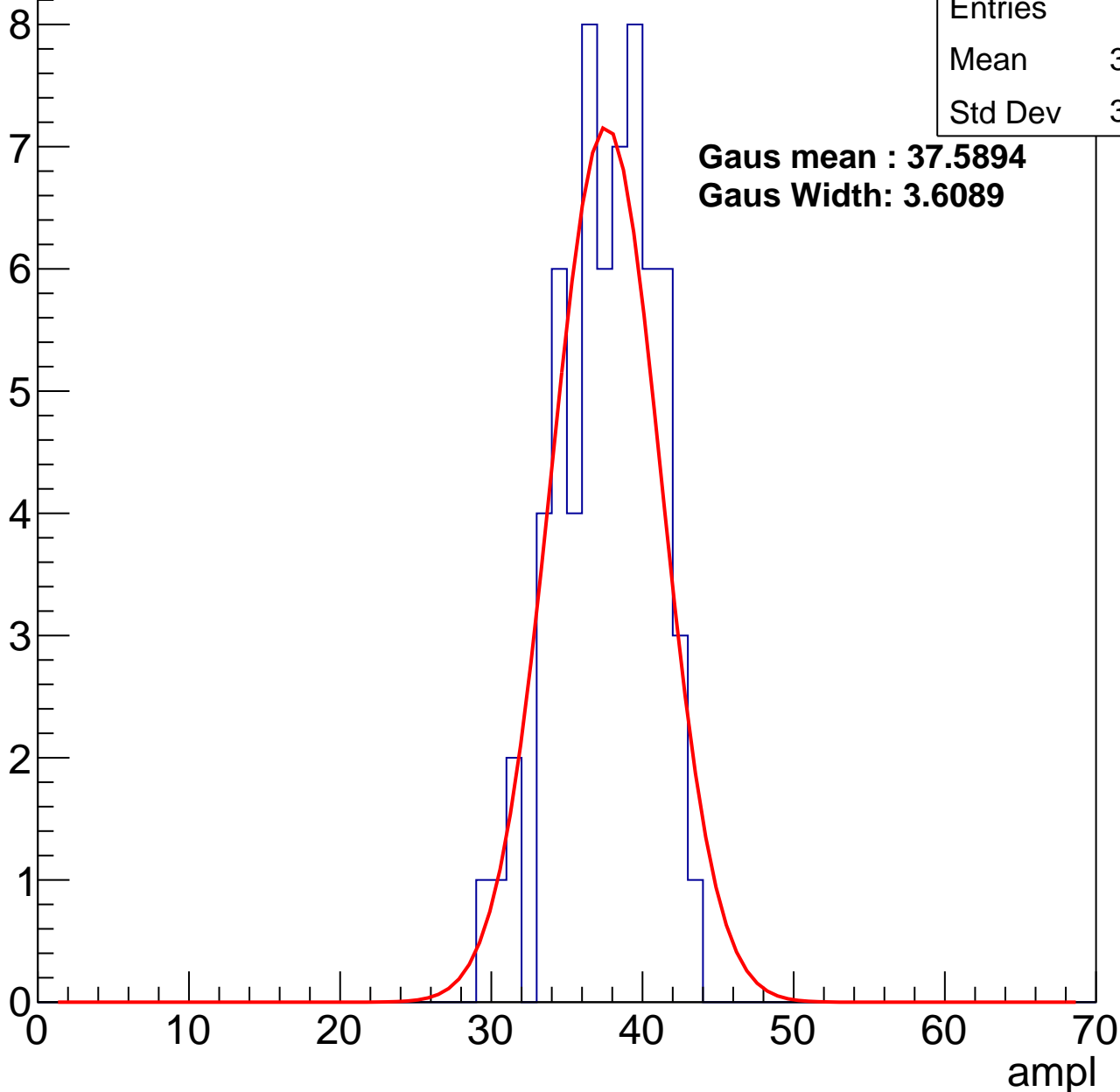
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.14
Std Dev	3.152

Gaus mean : 37.5894

Gaus Width: 3.6089



B0L001S, U22-ch9, adc2

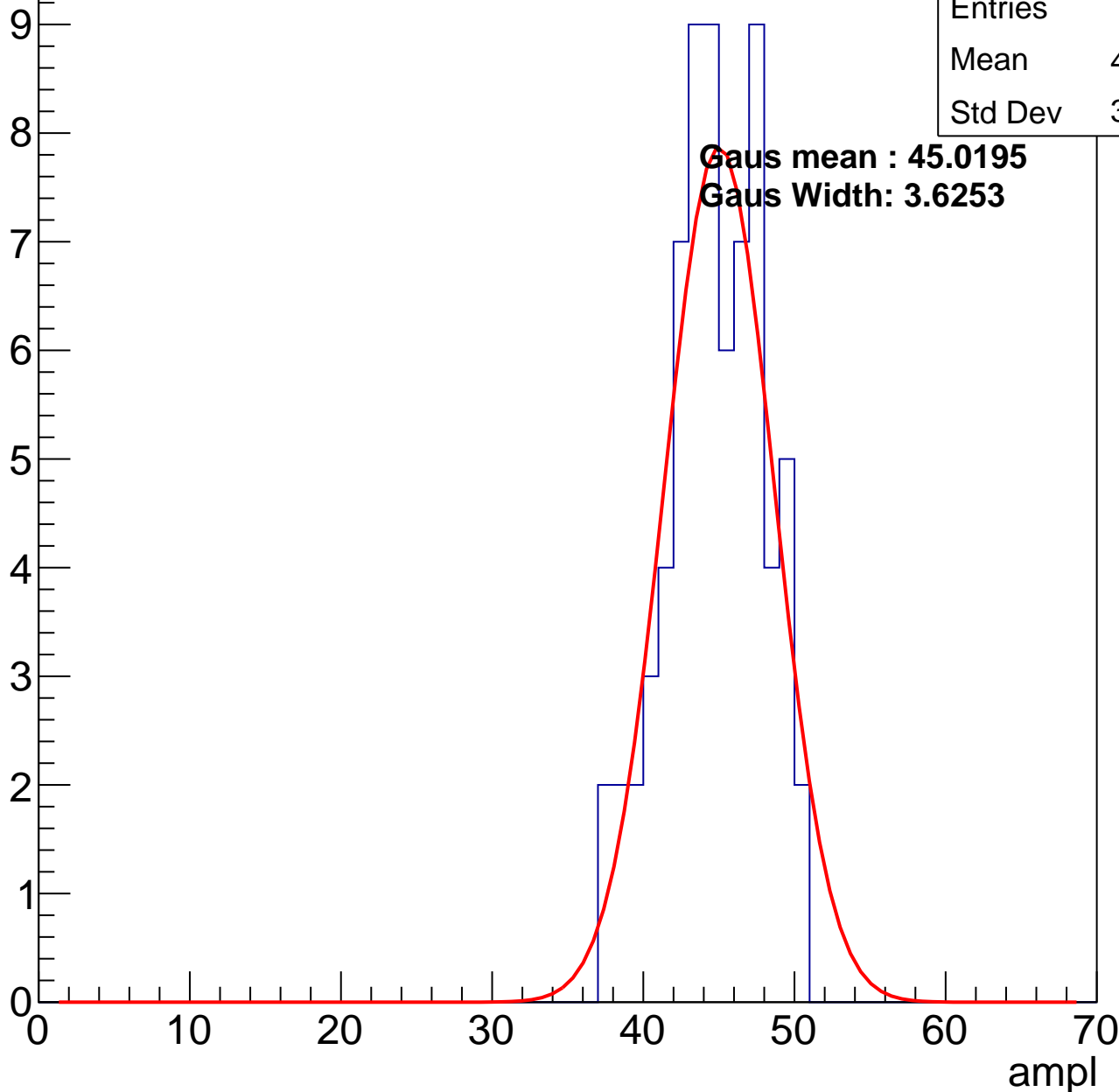
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.24
Std Dev	3.169

Gaus mean : 45.0195

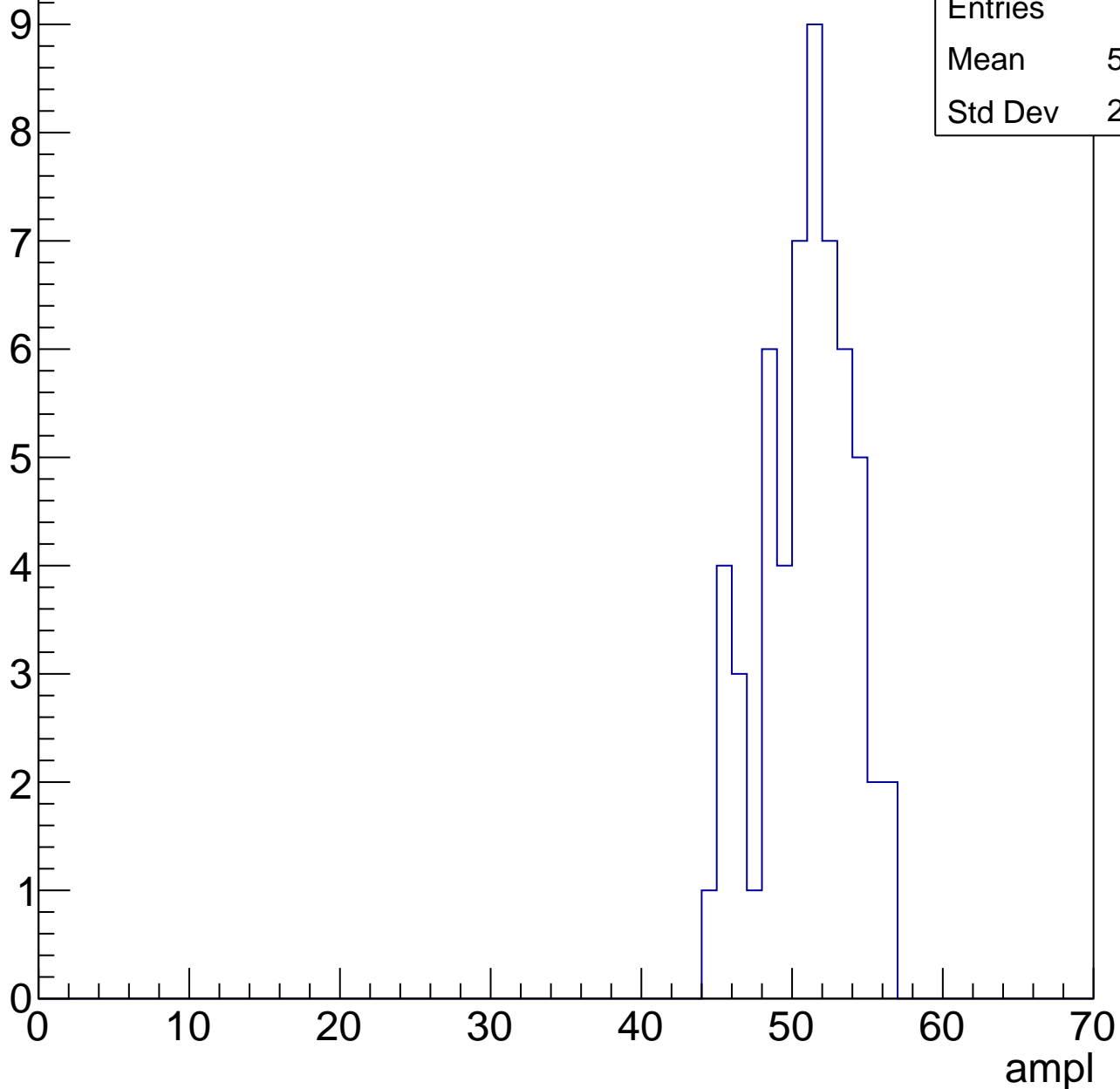
Gaus Width: 3.6253



B0L001S, U22-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	57
Mean	50.46
Std Dev	2.968

B0L001S, U22-ch9, adc4

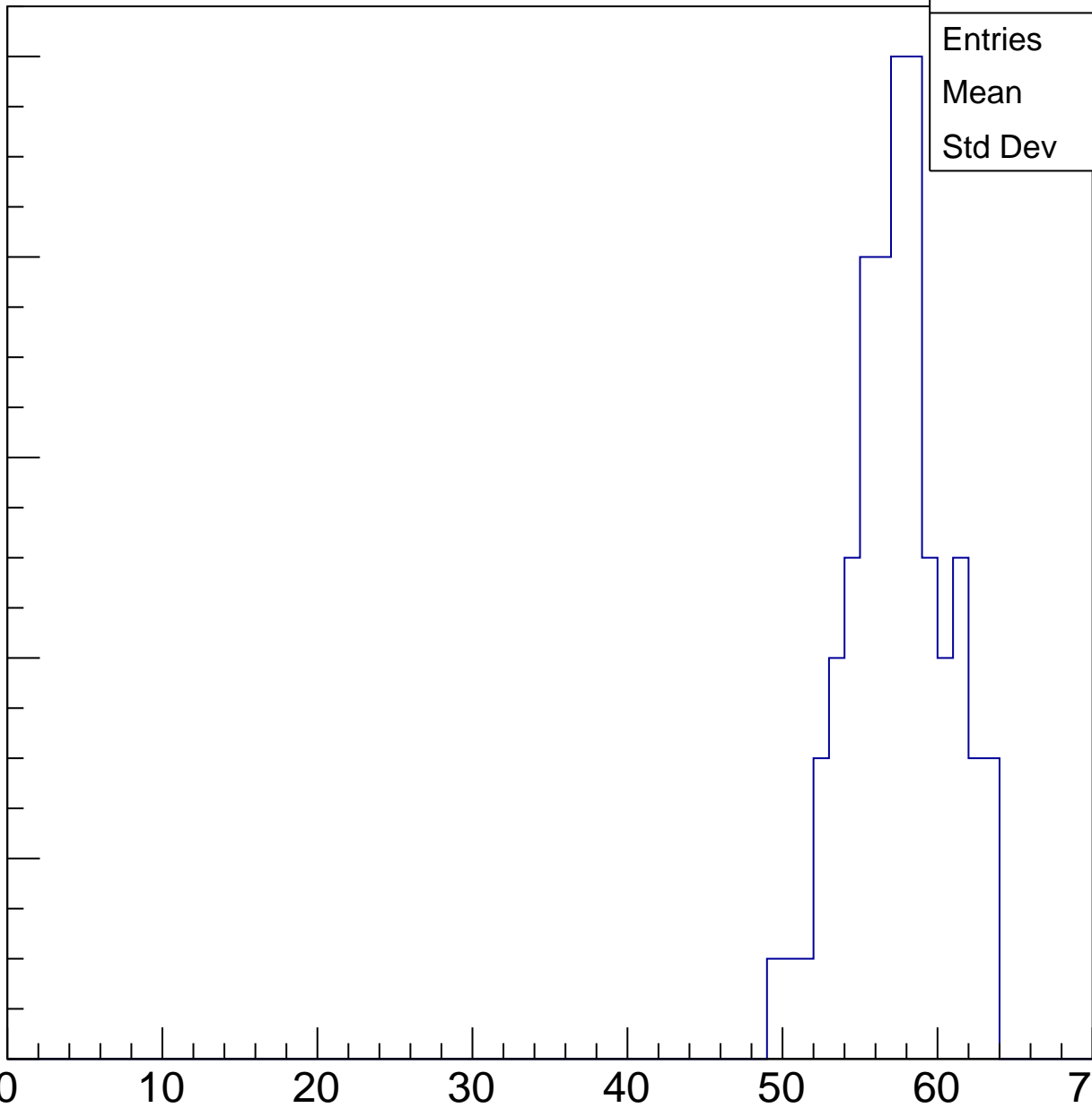
calib_packv5_042523_0143.root, FC#9, port A1

Entry

10
8
6
4
2
0

Entries	71
Mean	56.92
Std Dev	3.143

ampl

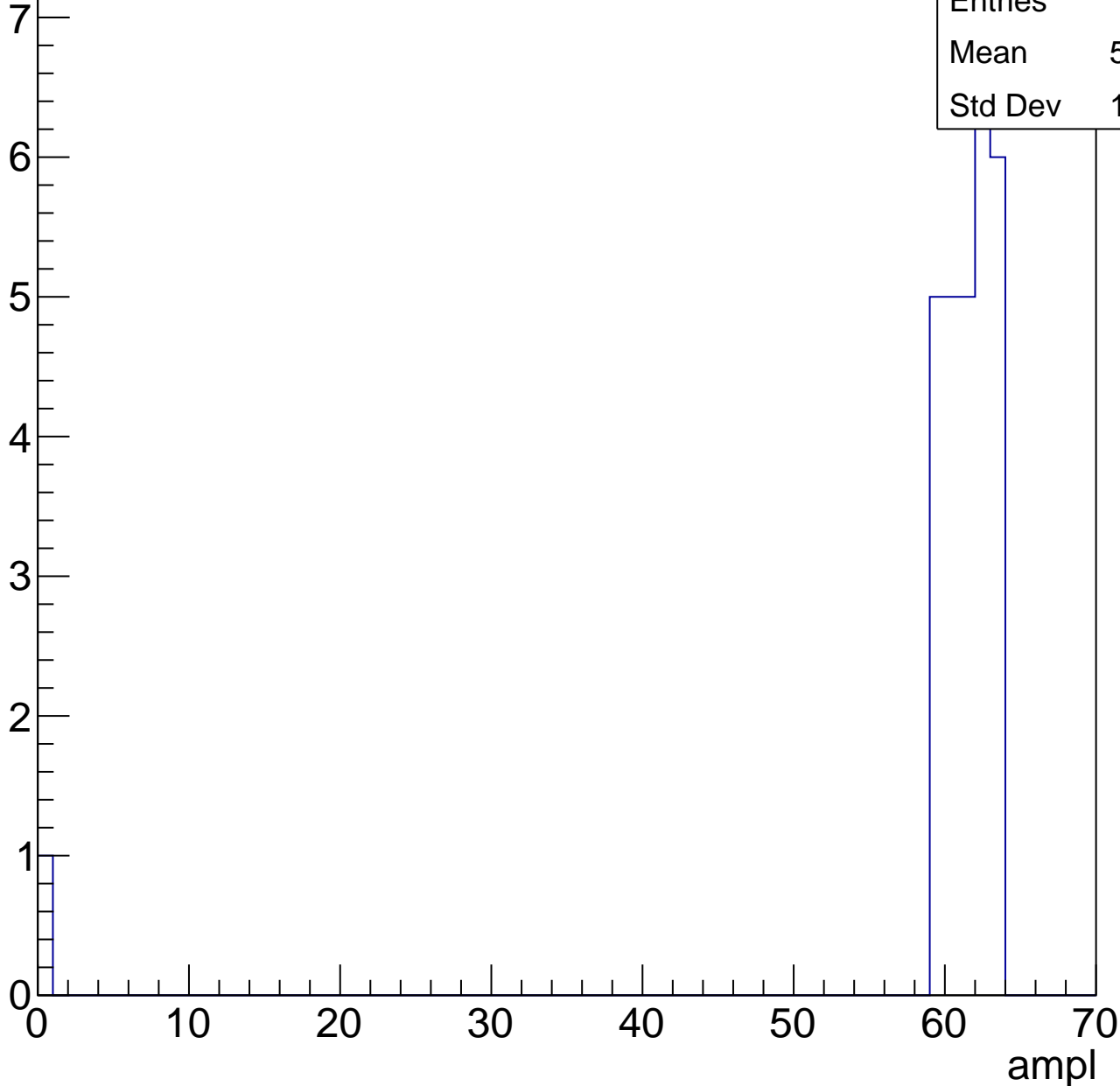


B0L001S, U22-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	59.03
Std Dev	11.24



B0L001S, U22-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch10, adc0

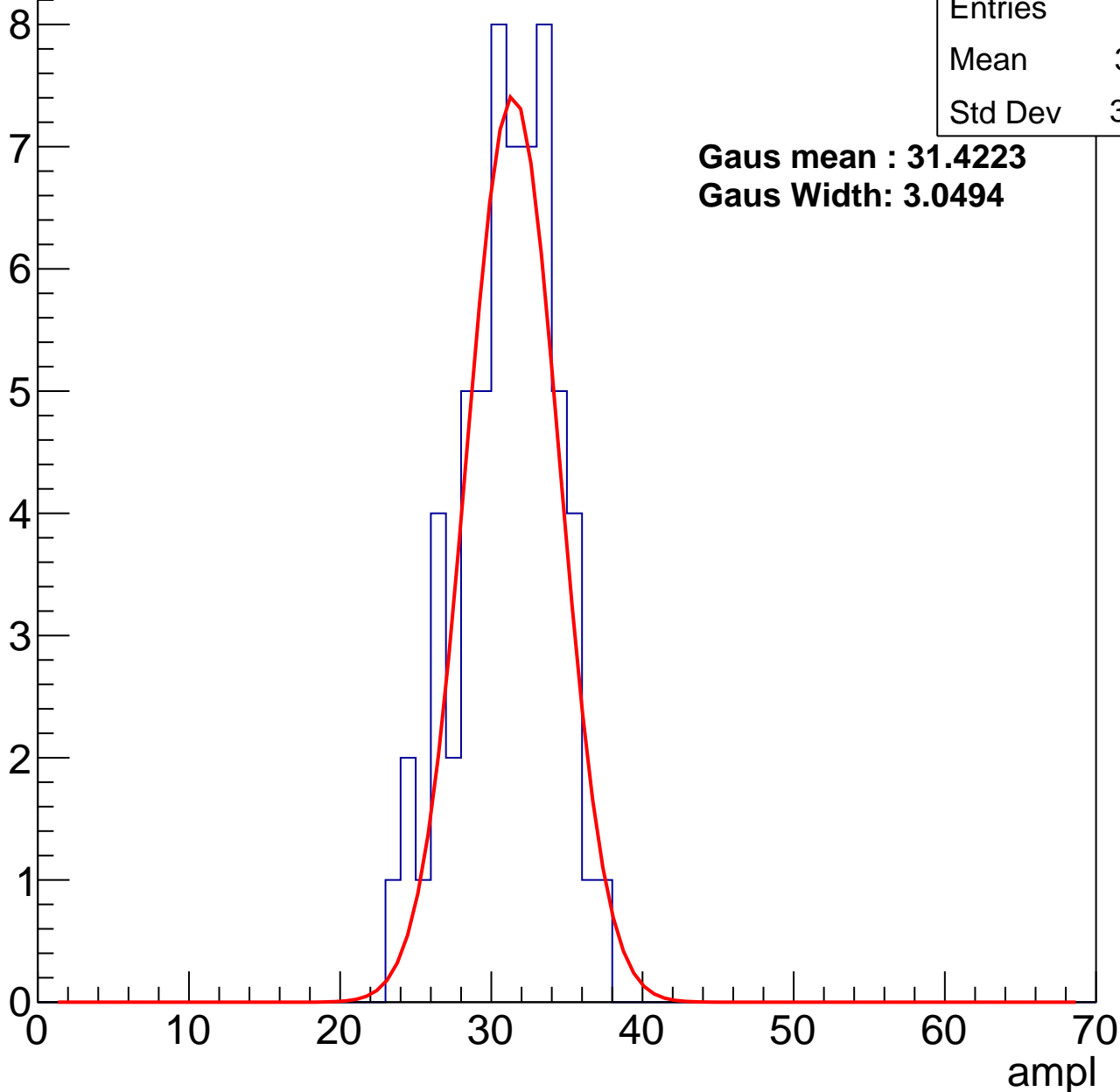
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.61
Std Dev	3.143

Gaus mean : 31.4223

Gaus Width: 3.0494



B0L001S, U22-ch10, adc1

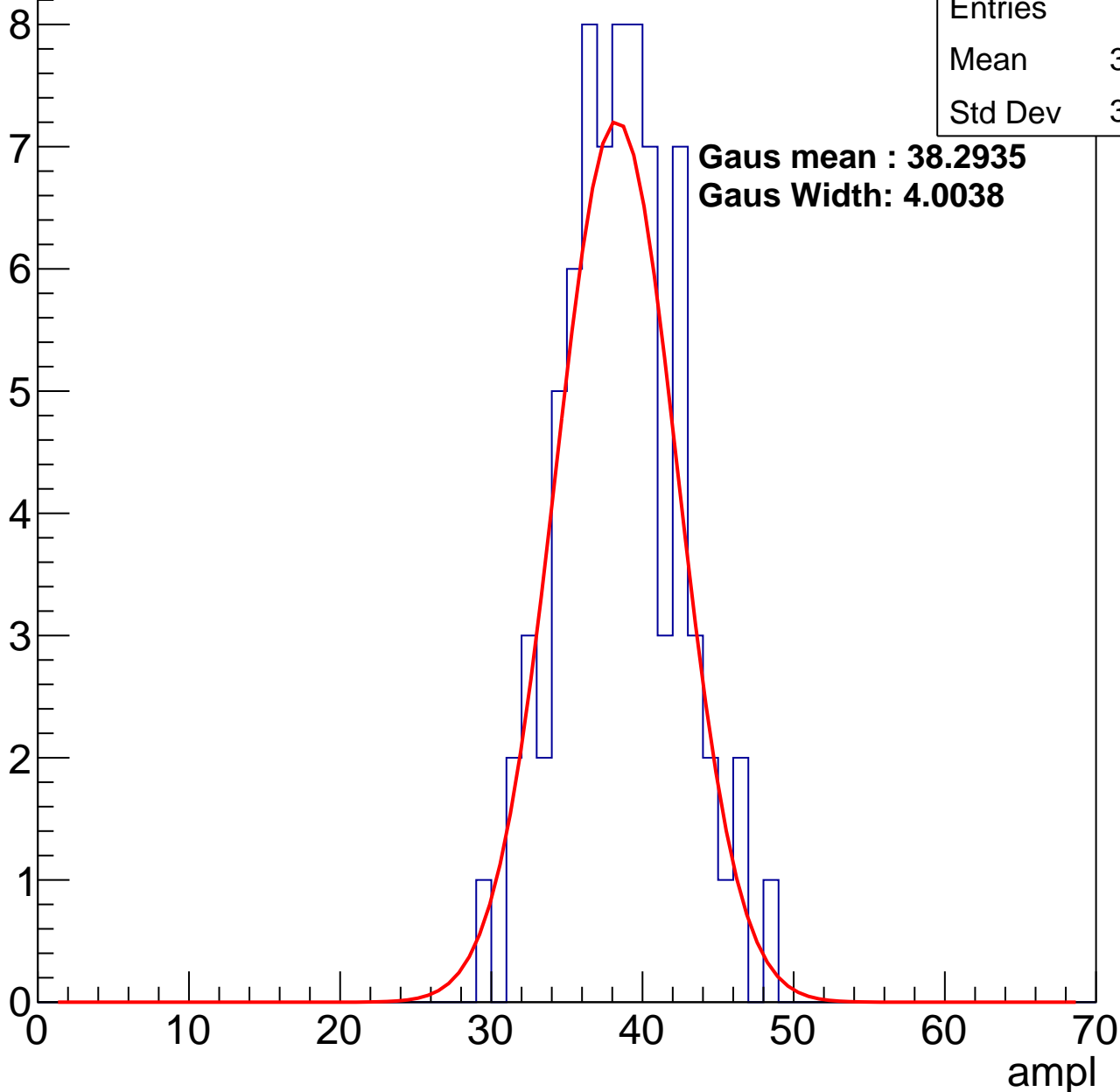
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	38.09
Std Dev	3.819

Gaus mean : 38.2935

Gaus Width: 4.0038



B0L001S, U22-ch10, adc2

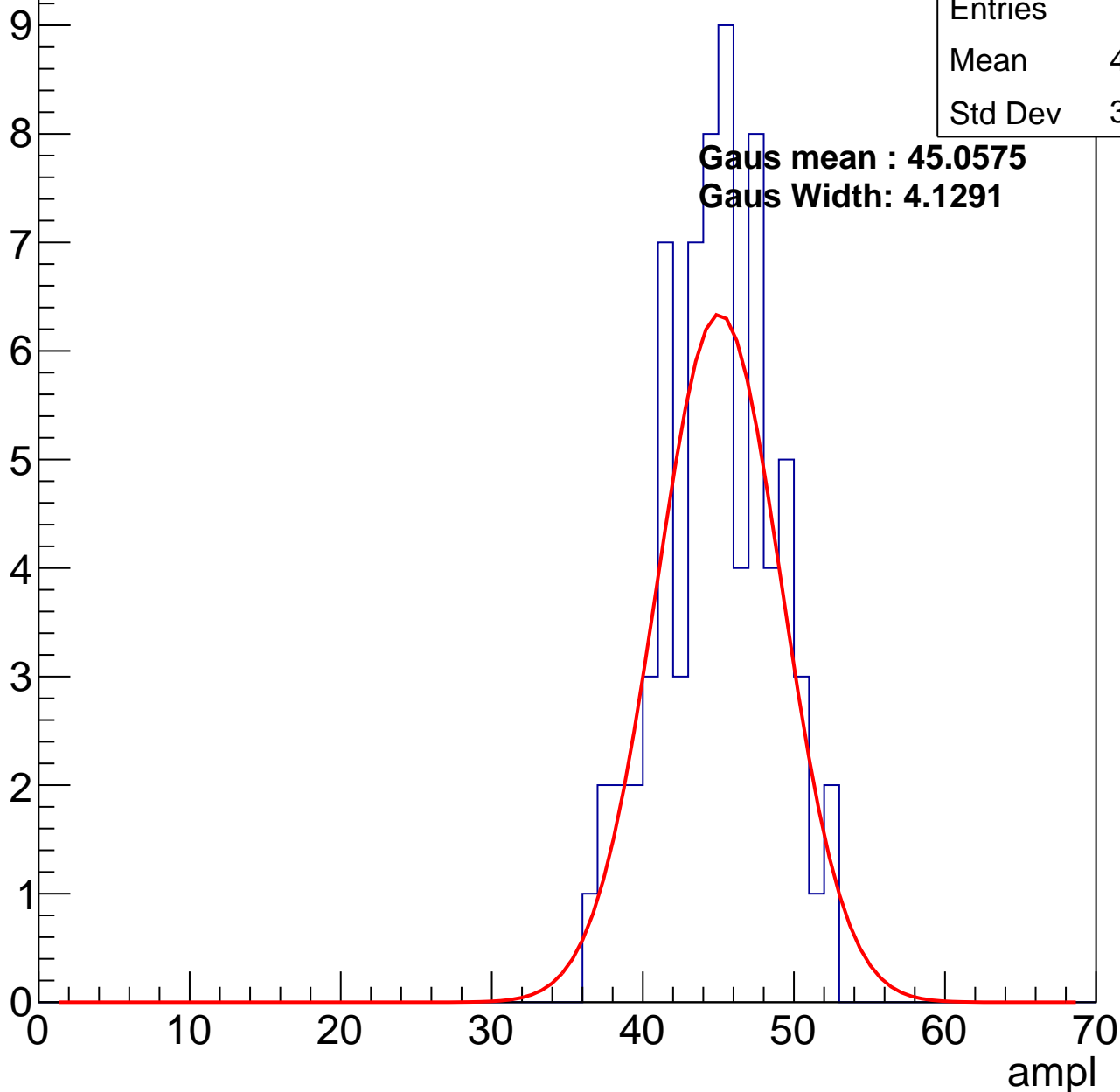
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.46
Std Dev	3.696

Gaus mean : 45.0575

Gaus Width: 4.1291

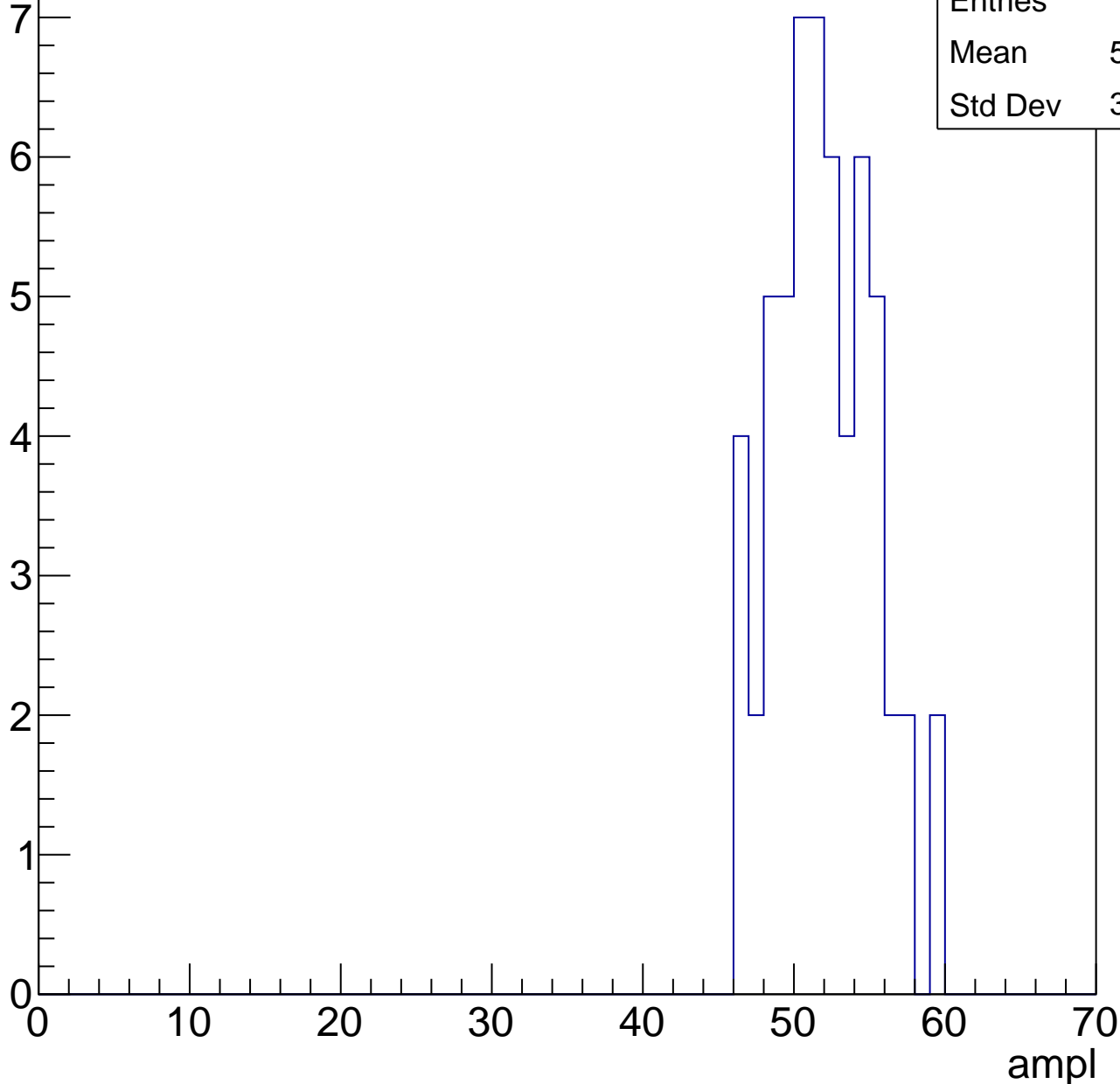


B0L001S, U22-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	51.53
Std Dev	3.229

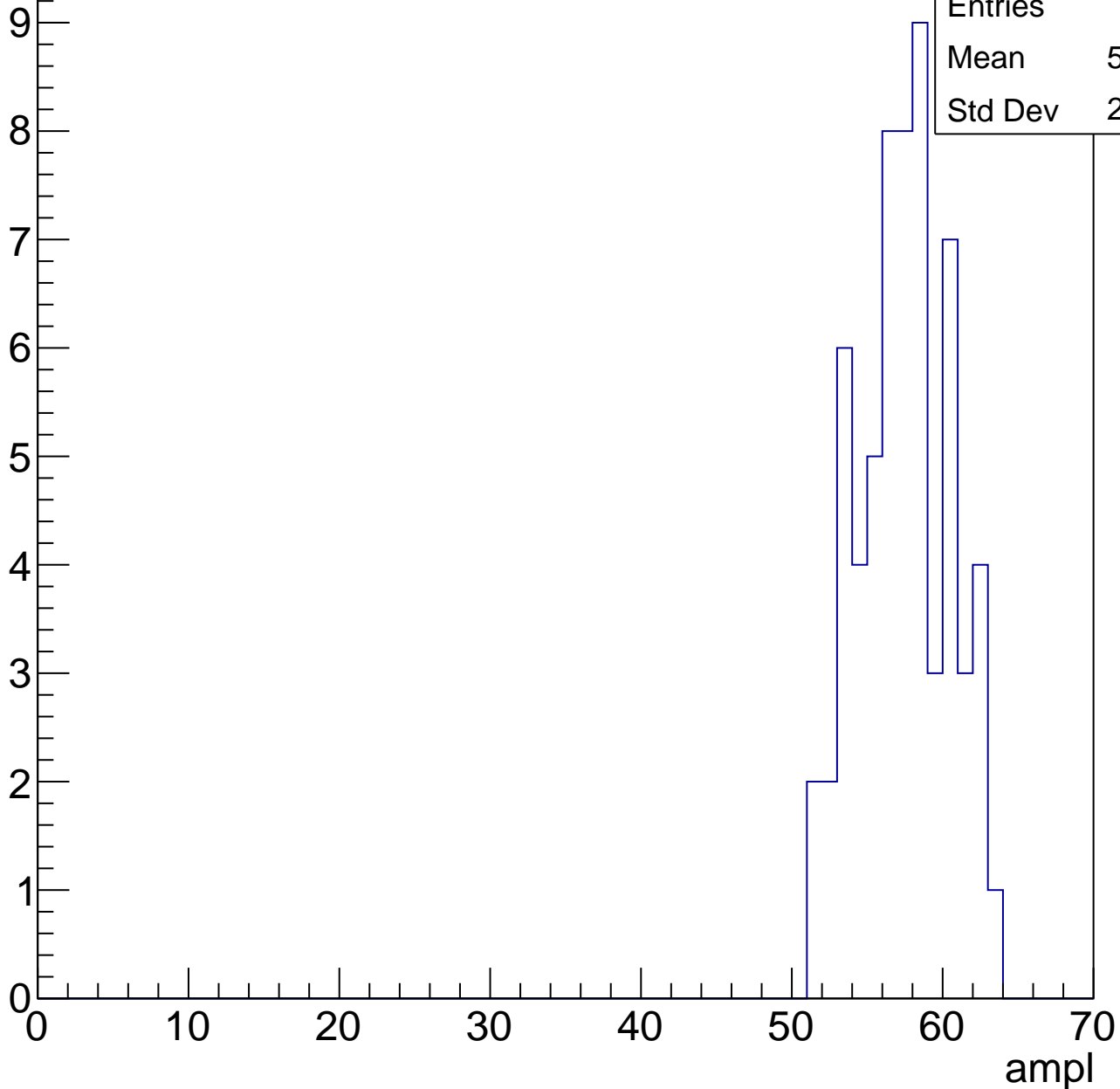


B0L001S, U22-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.97
Std Dev	2.978

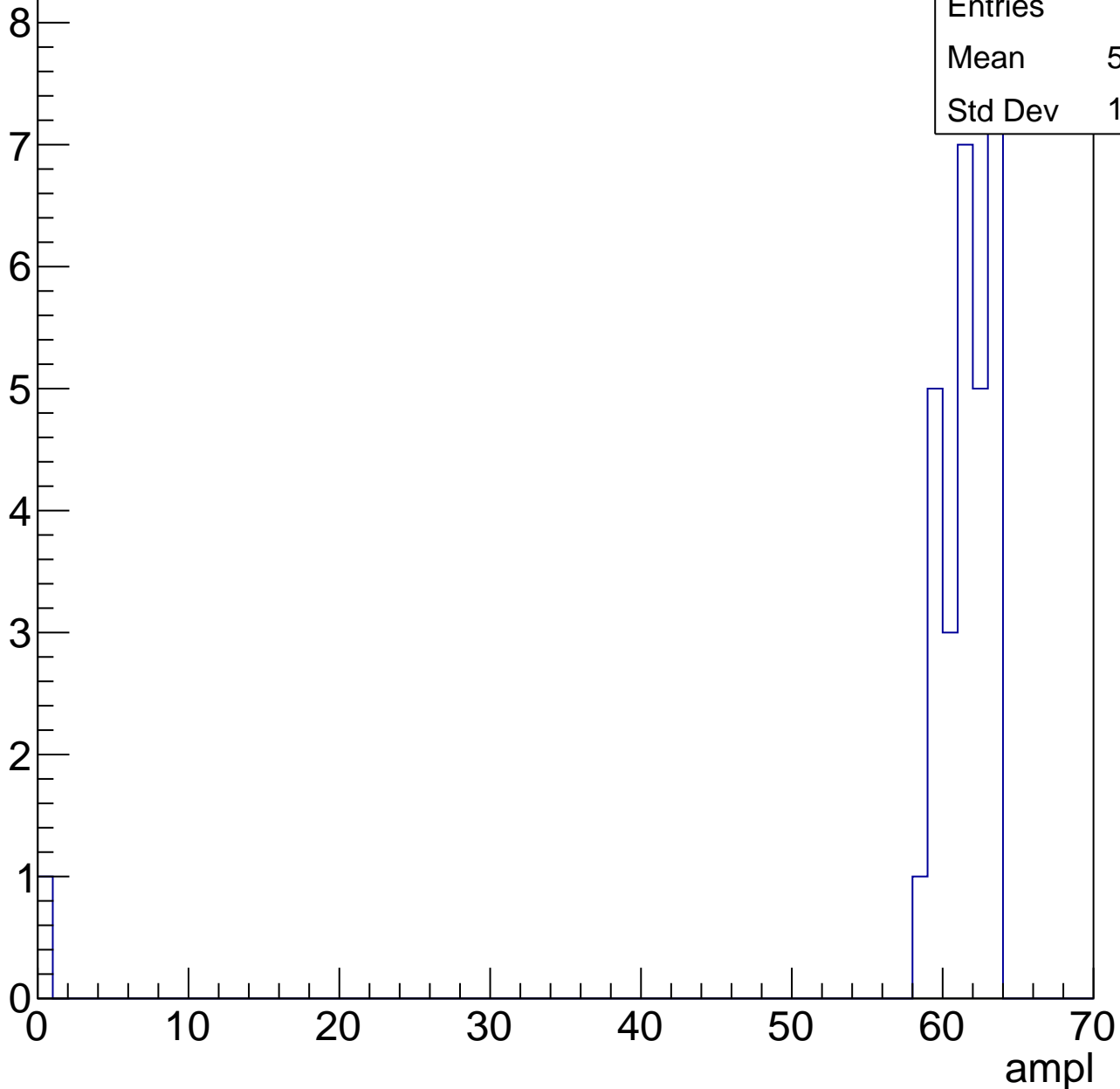


B0L001S, U22-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.13
Std Dev	11.08



B0L001S, U22-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch11, adc0

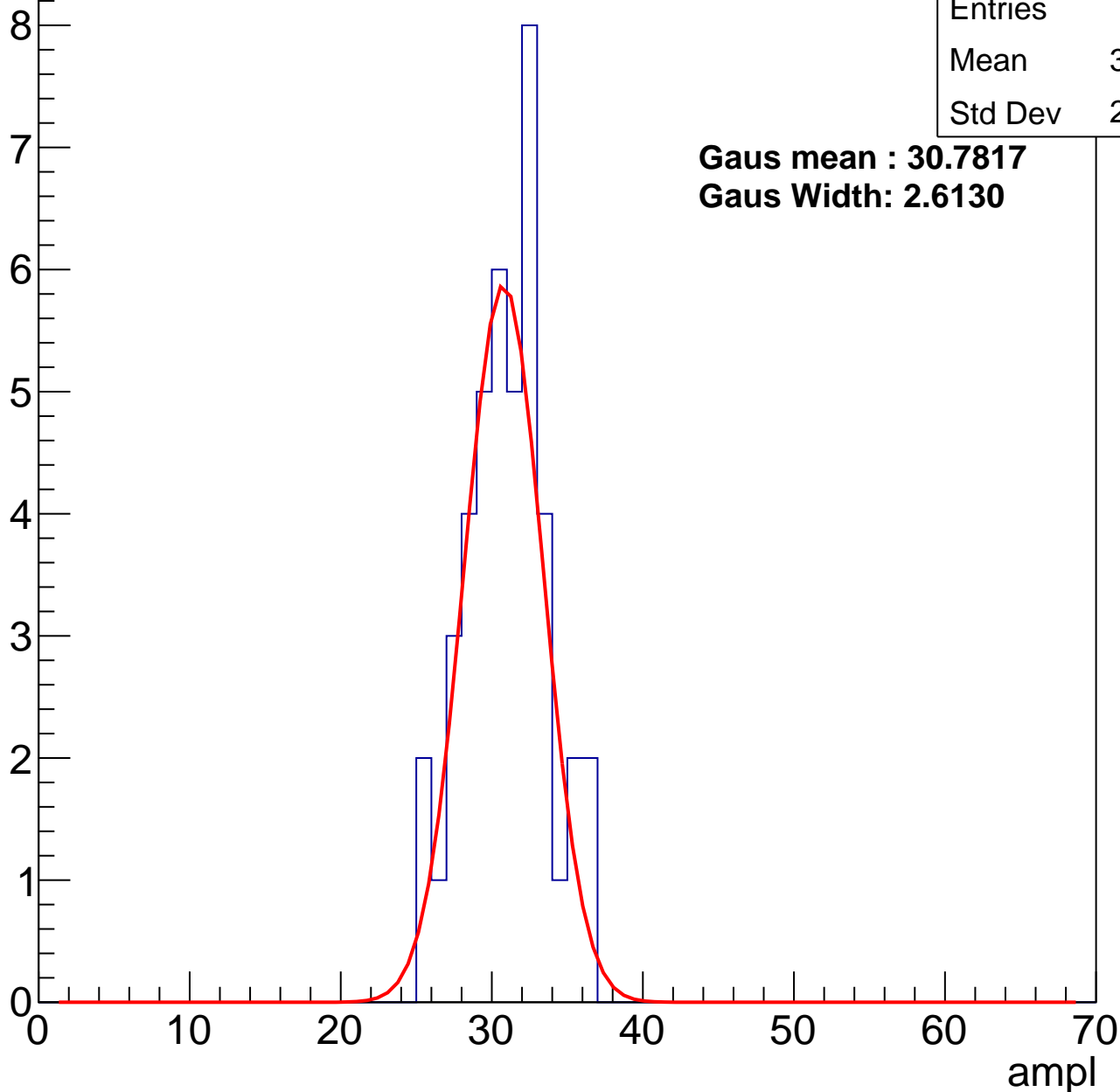
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	30.53
Std Dev	2.697

Gaus mean : 30.7817

Gaus Width: 2.6130



B0L001S, U22-ch11, adc1

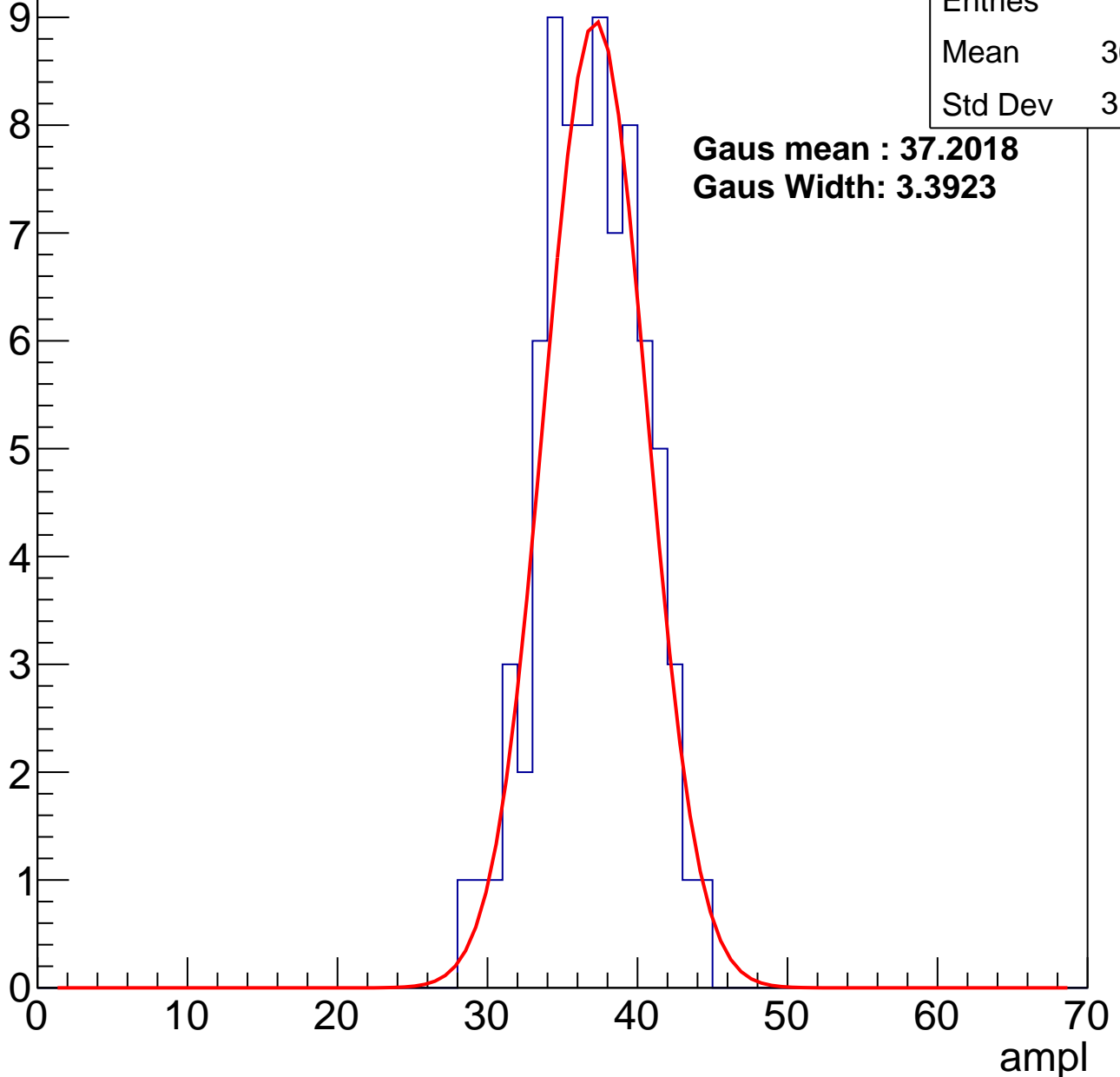
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.52
Std Dev	3.345

Gaus mean : 37.2018

Gaus Width: 3.3923



B0L001S, U22-ch11, adc2

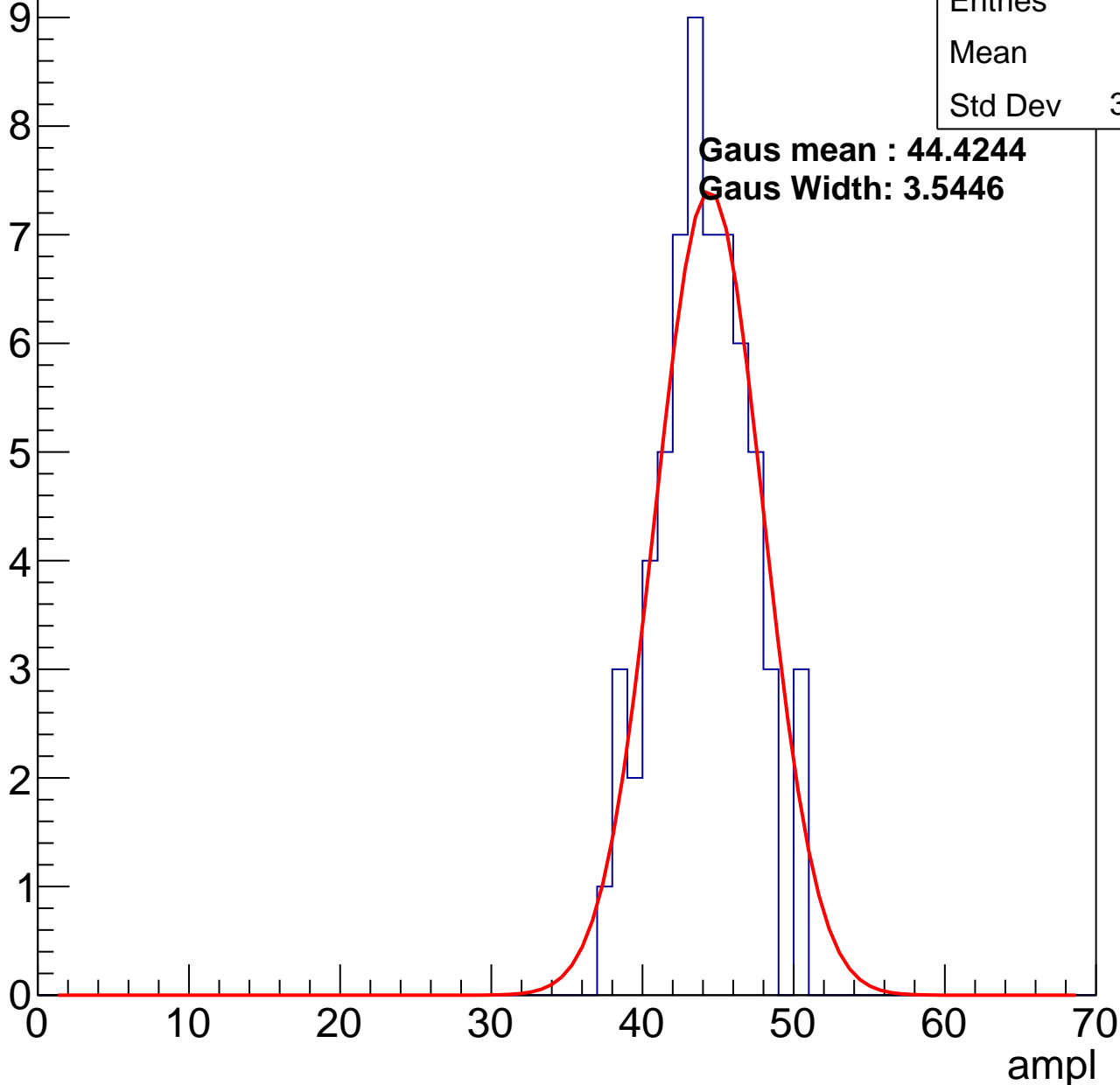
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	43.6
Std Dev	3.045

Gaus mean : 44.4244

Gaus Width: 3.5446

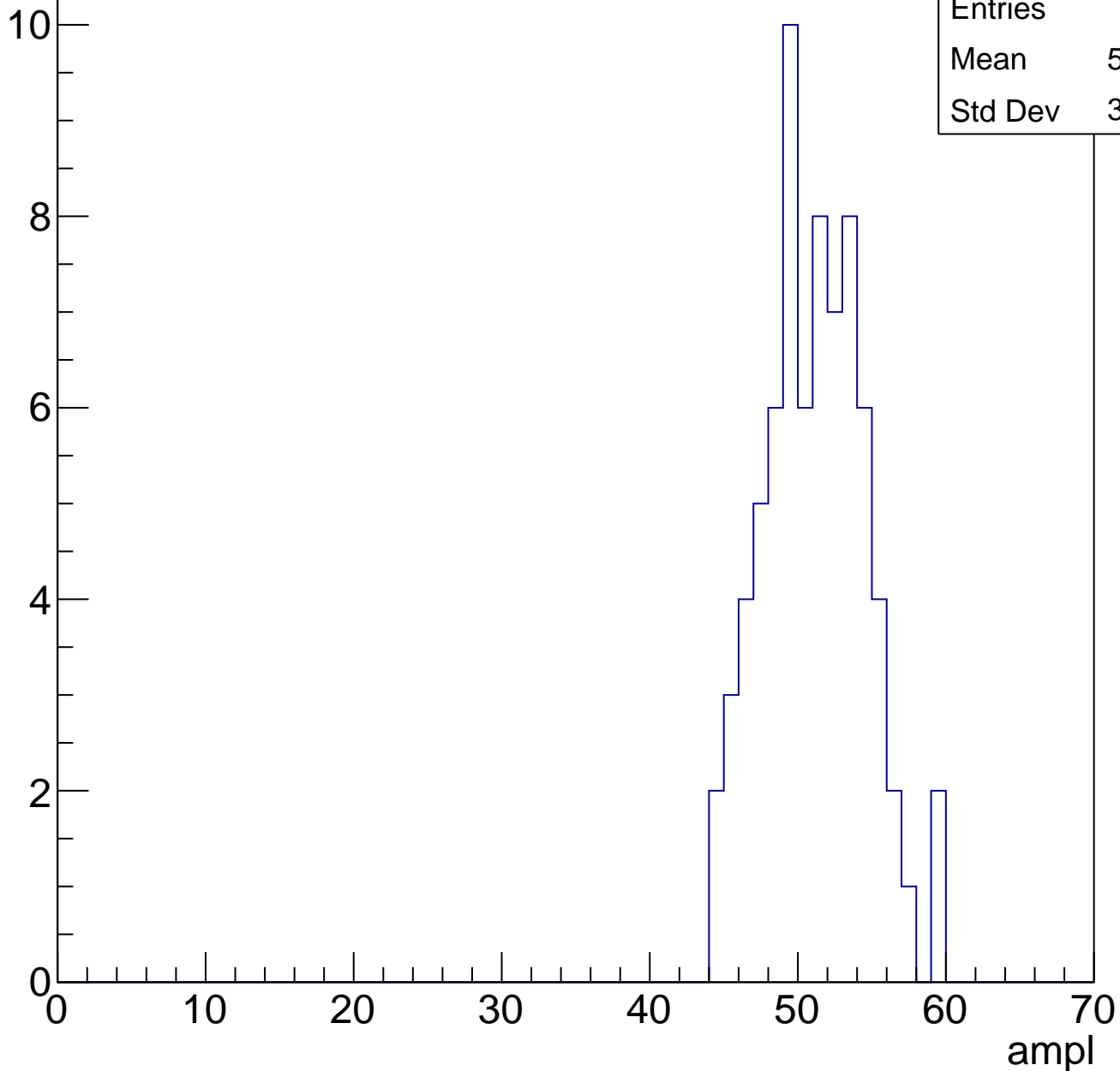


B0L001S, U22-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	50.64
Std Dev	3.383

Entry

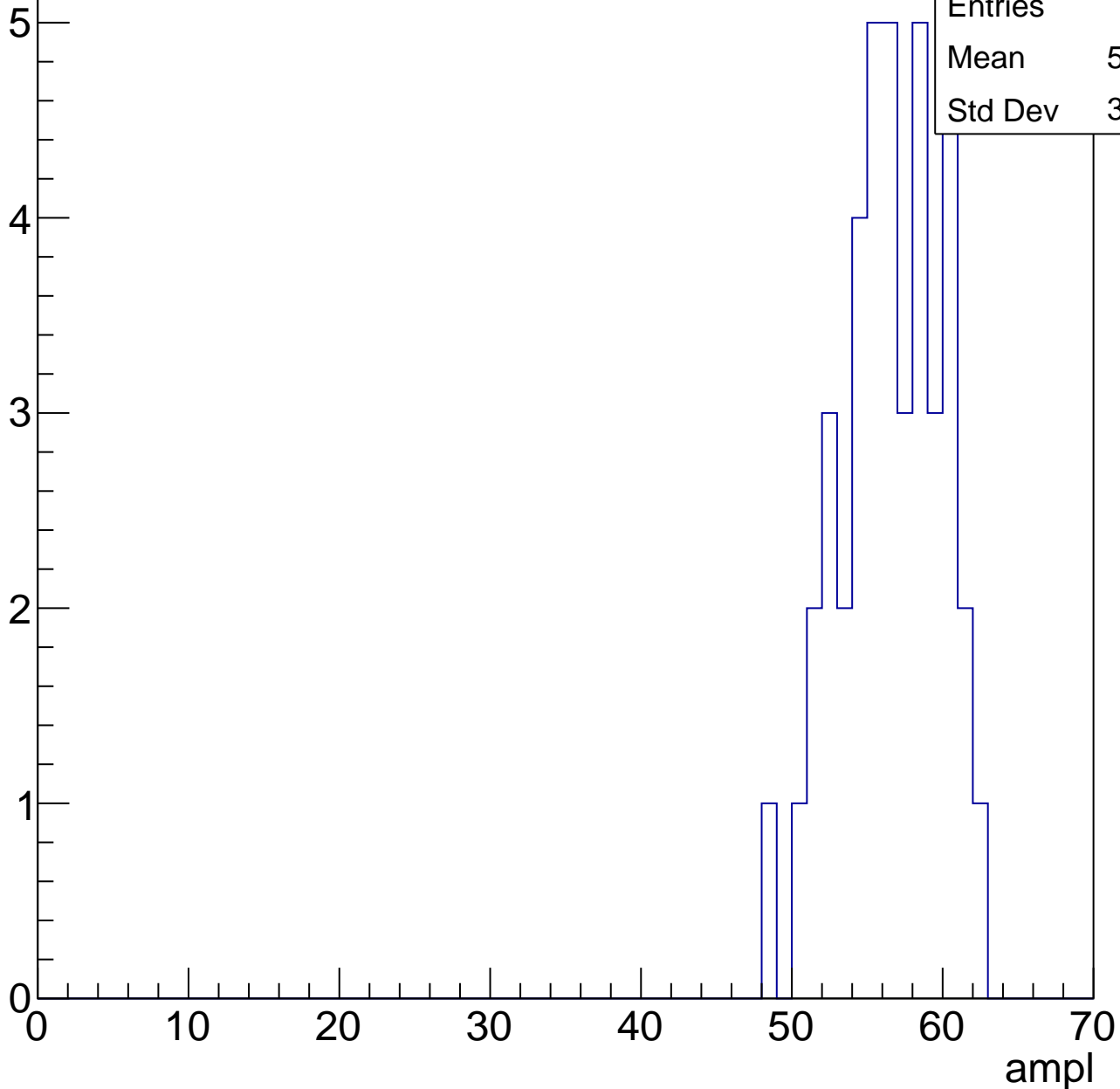


B0L001S, U22-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	56.07
Std Dev	3.283

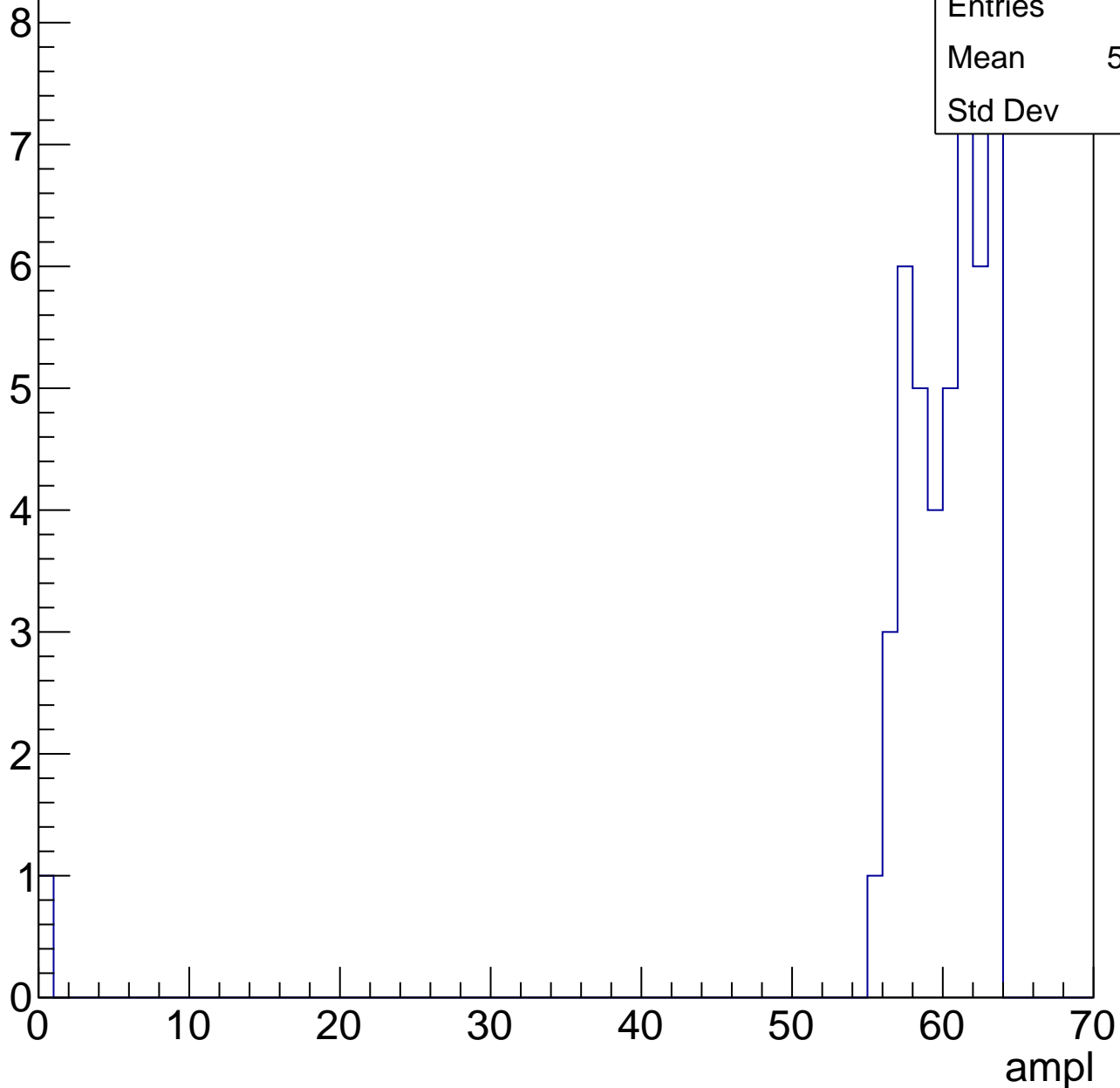


B0L001S, U22-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

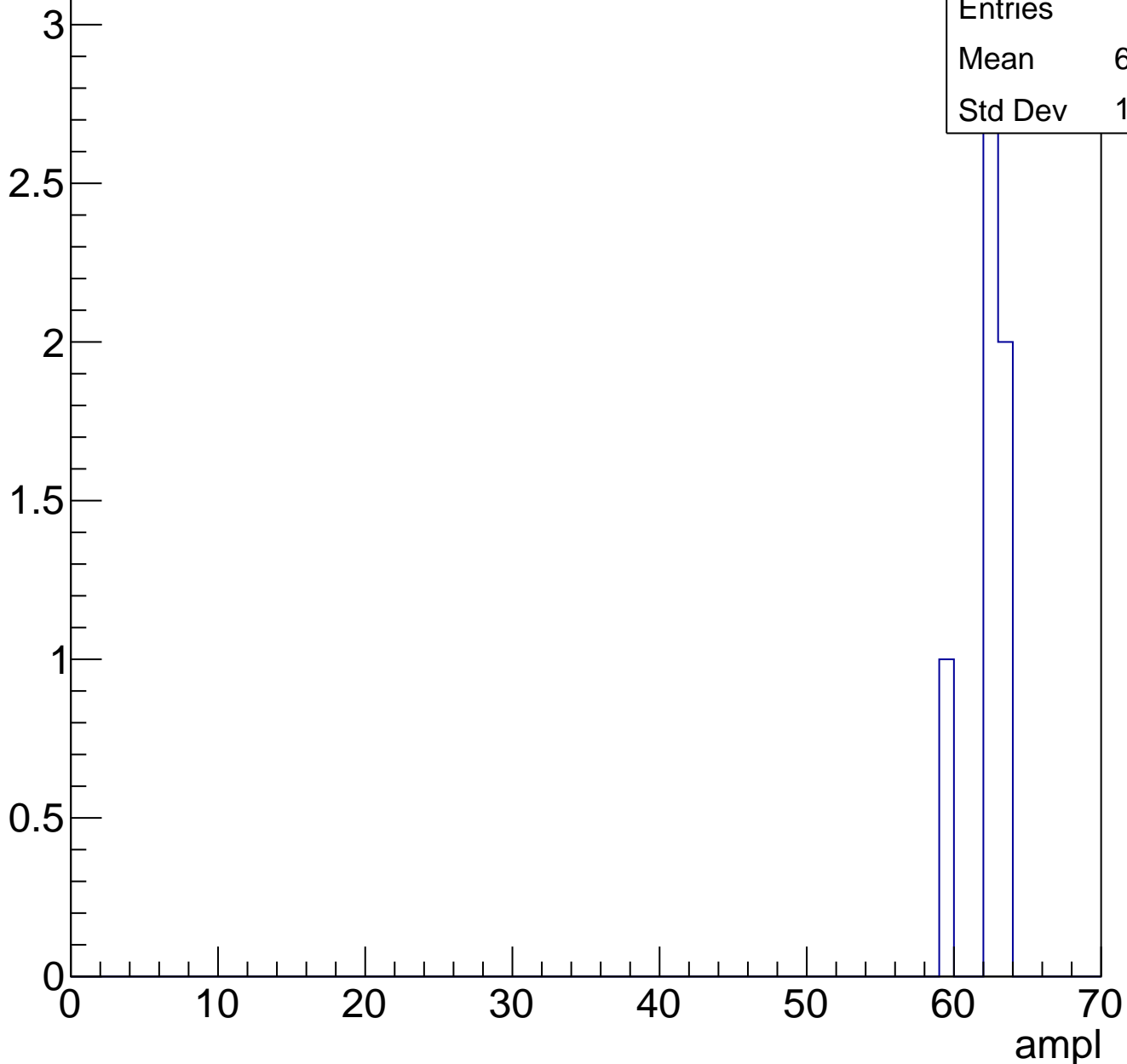
Entries	47
Mean	58.62
Std Dev	8.95



B0L001S, U22-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch12, adc0

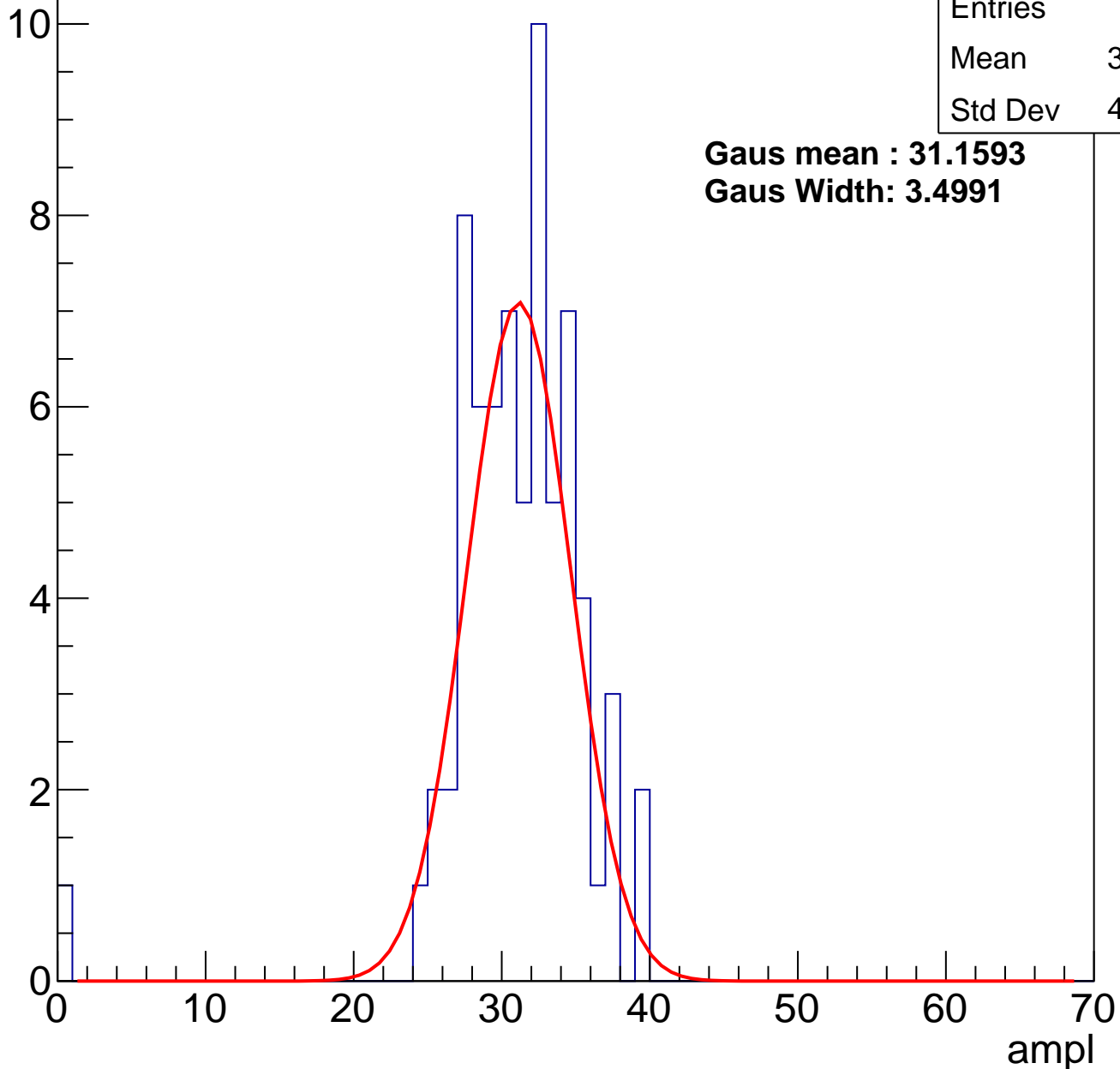
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	30.53
Std Dev	4.996

Gaus mean : 31.1593

Gaus Width: 3.4991

Entry



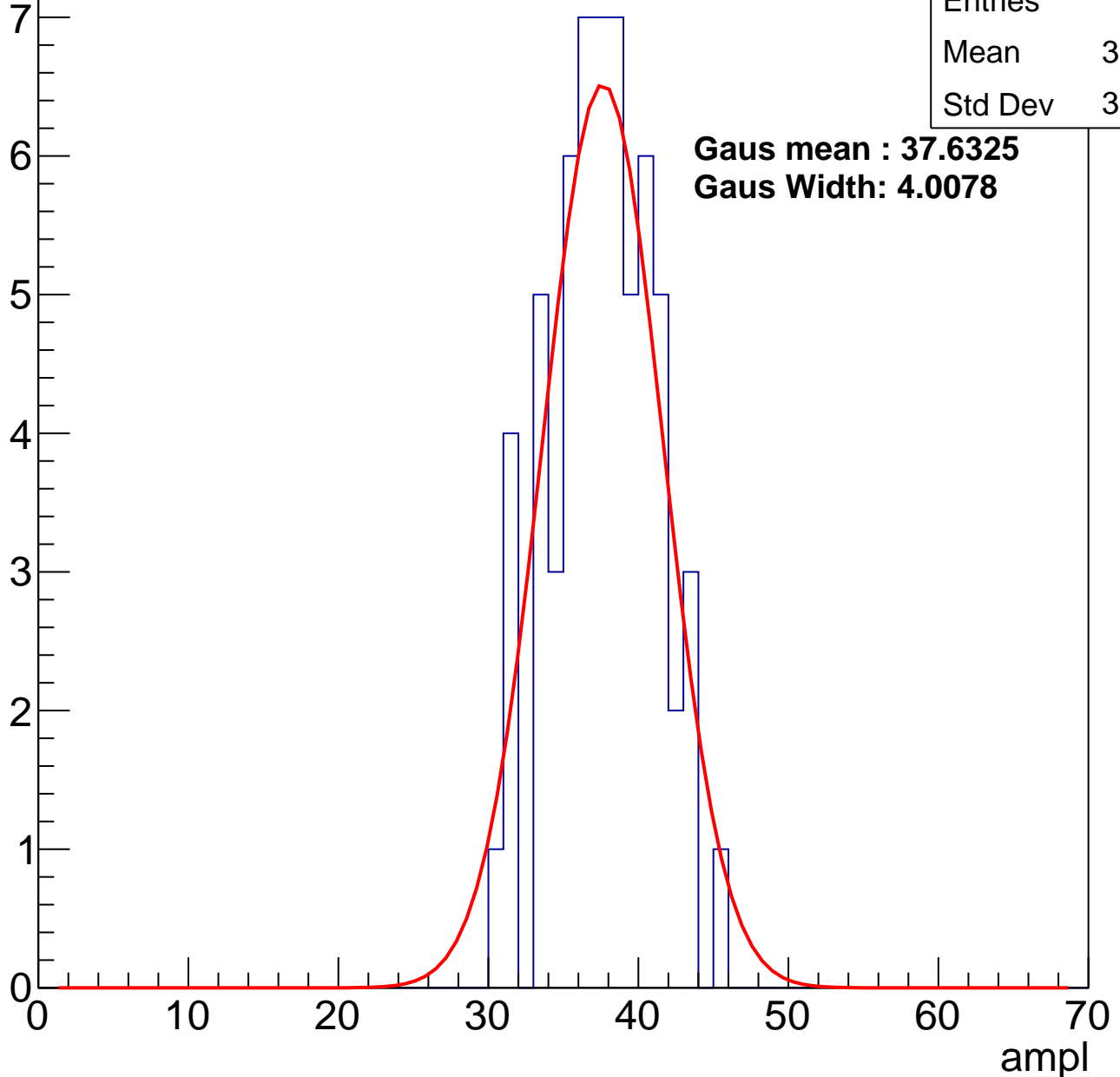
B0L001S, U22-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.19
Std Dev	3.388

Gaus mean : 37.6325
Gaus Width: 4.0078



B0L001S, U22-ch12, adc2

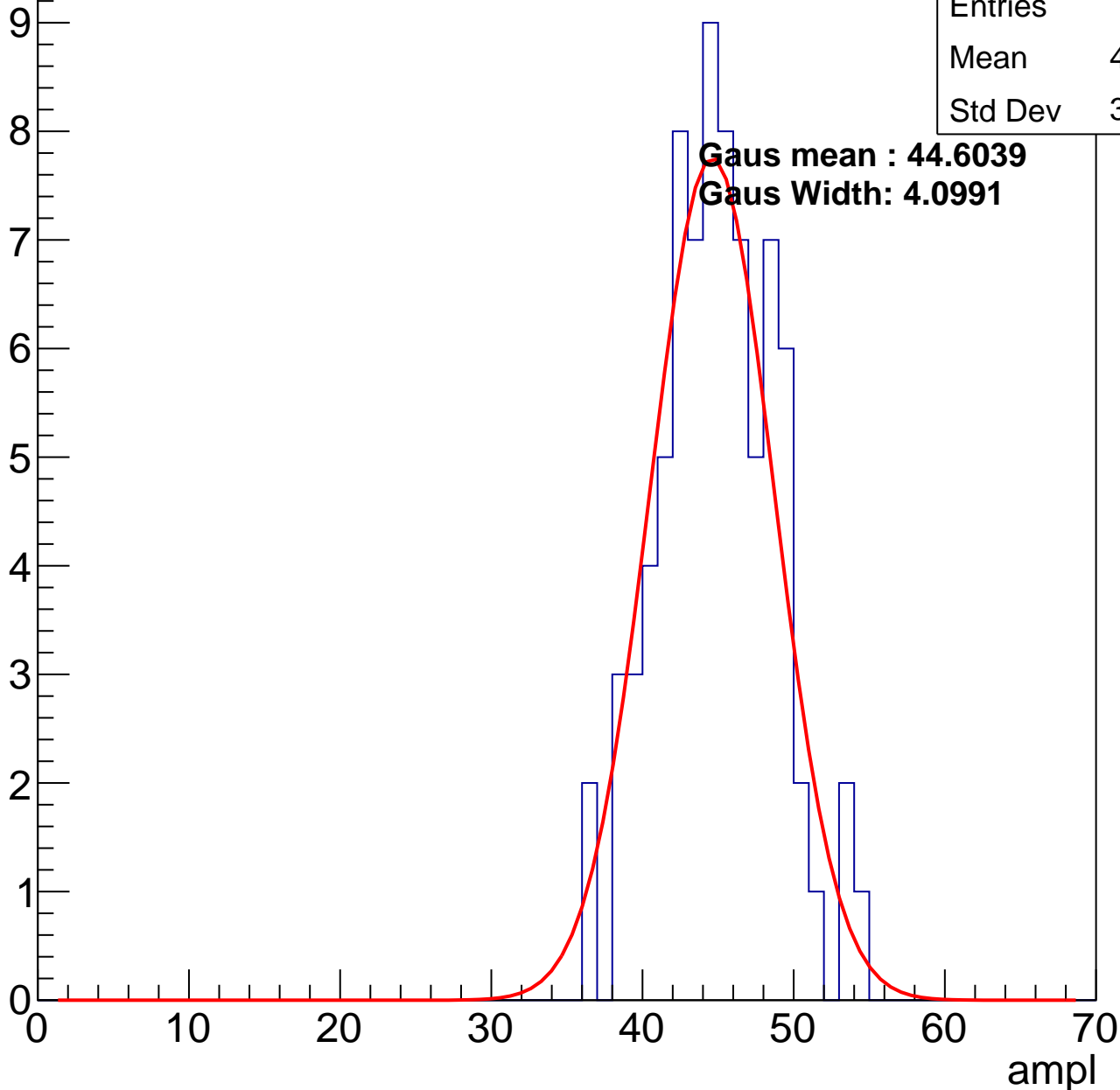
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	44.49
Std Dev	3.818

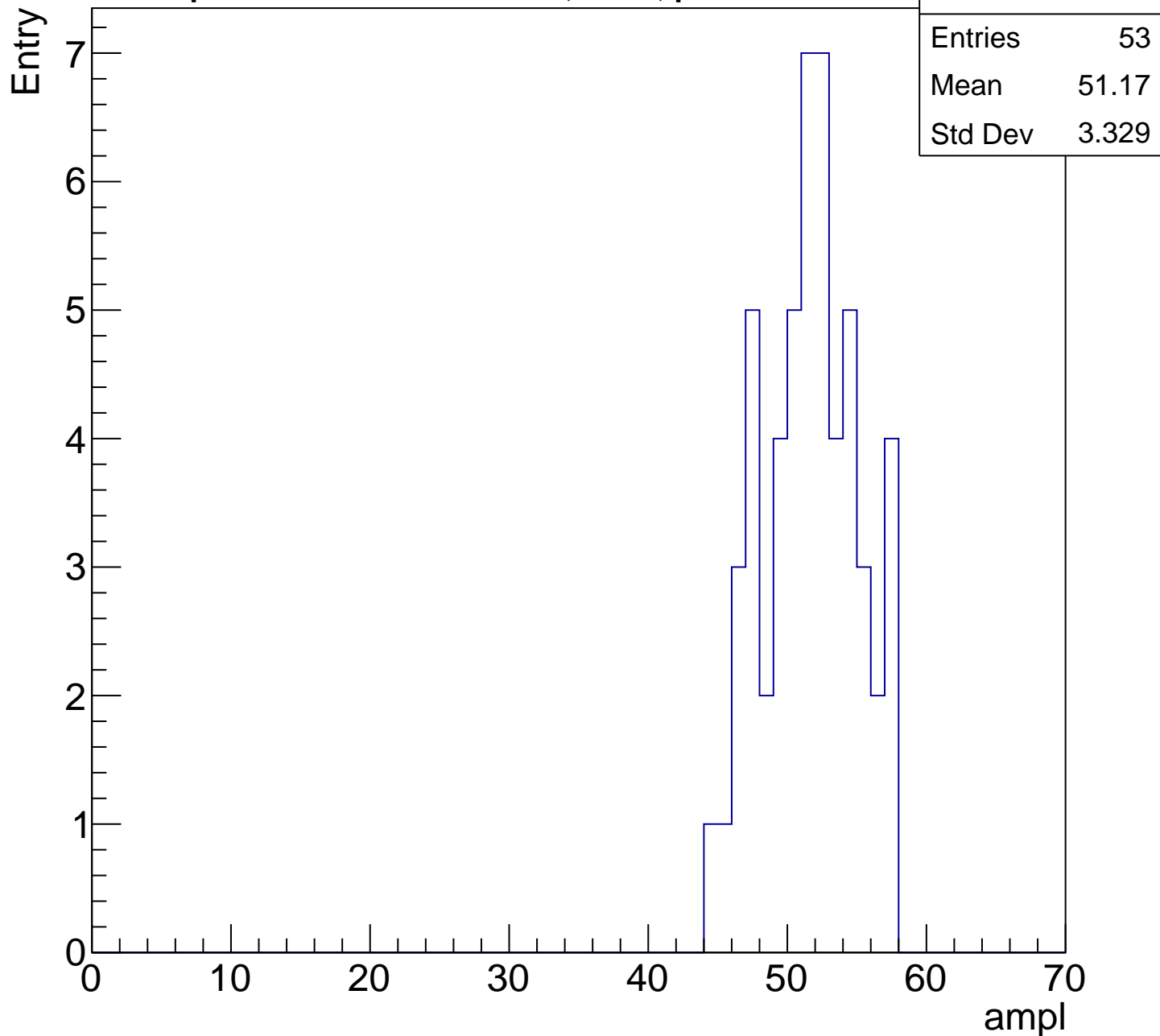
Gaus mean : 44.6039

Gaus Width: 4.0991



B0L001S, U22-ch12, adc3

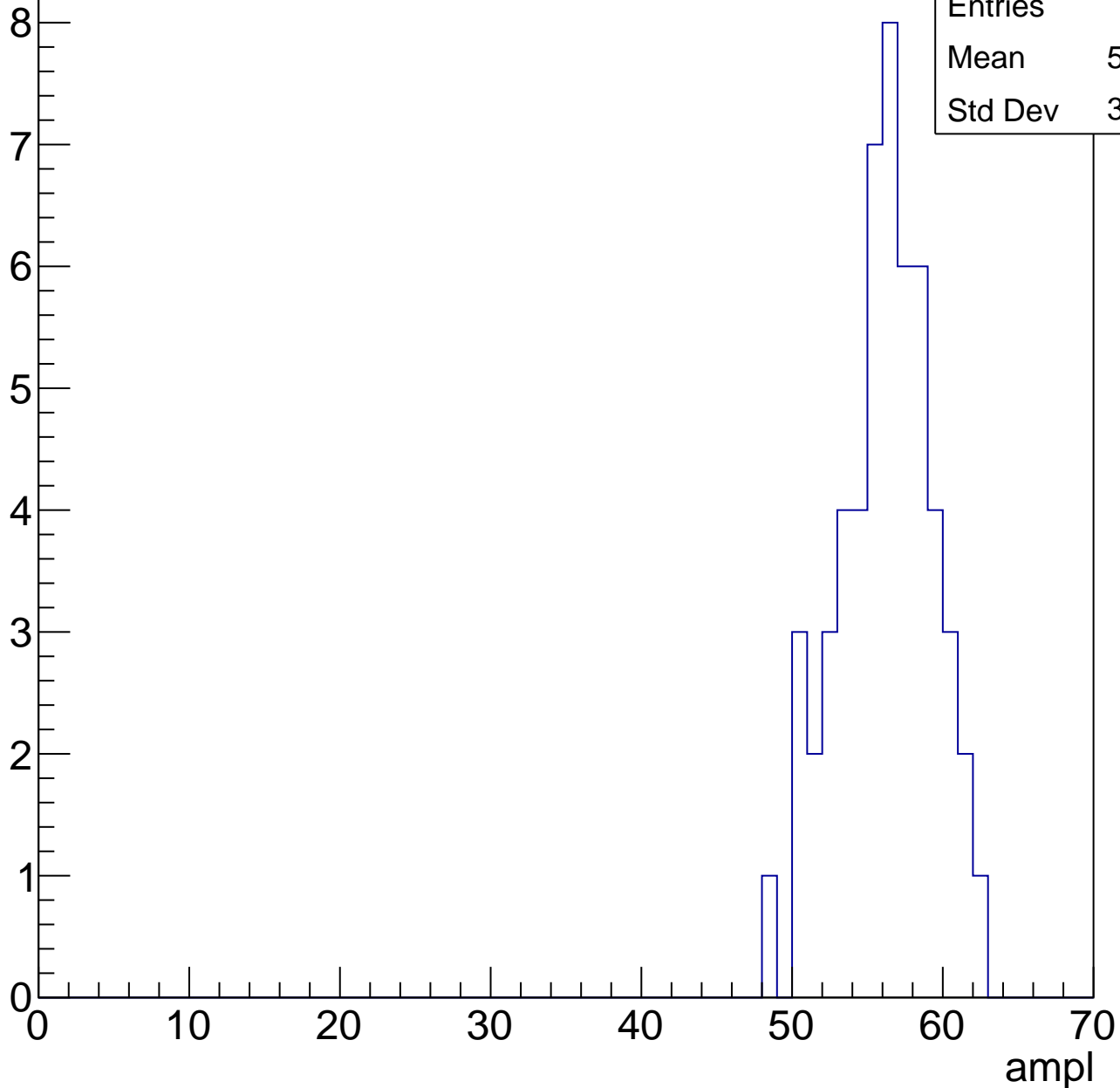
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U22-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

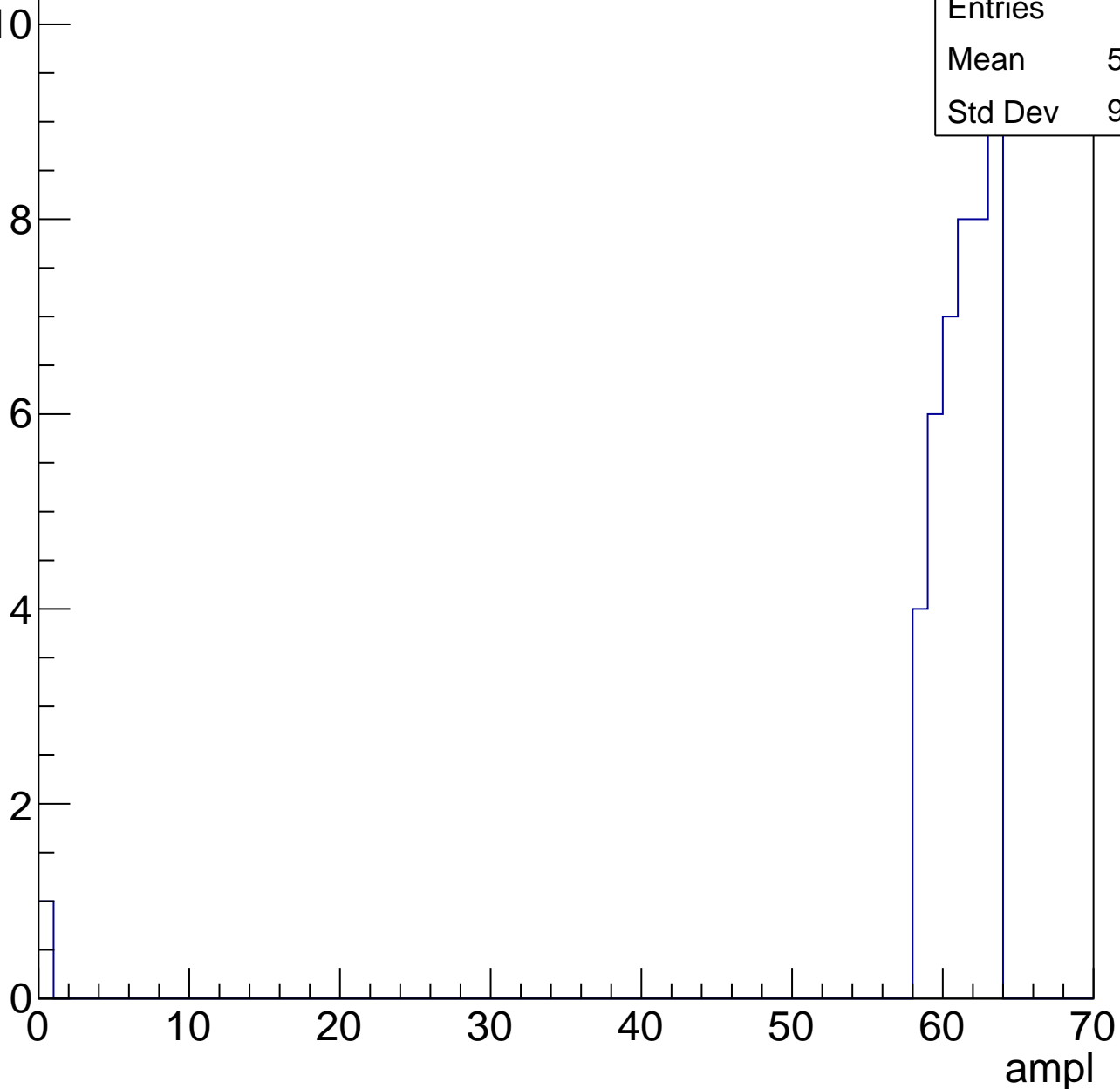


B0L001S, U22-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	59.55
Std Dev	9.223



B0L001S, U22-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch13, adc0

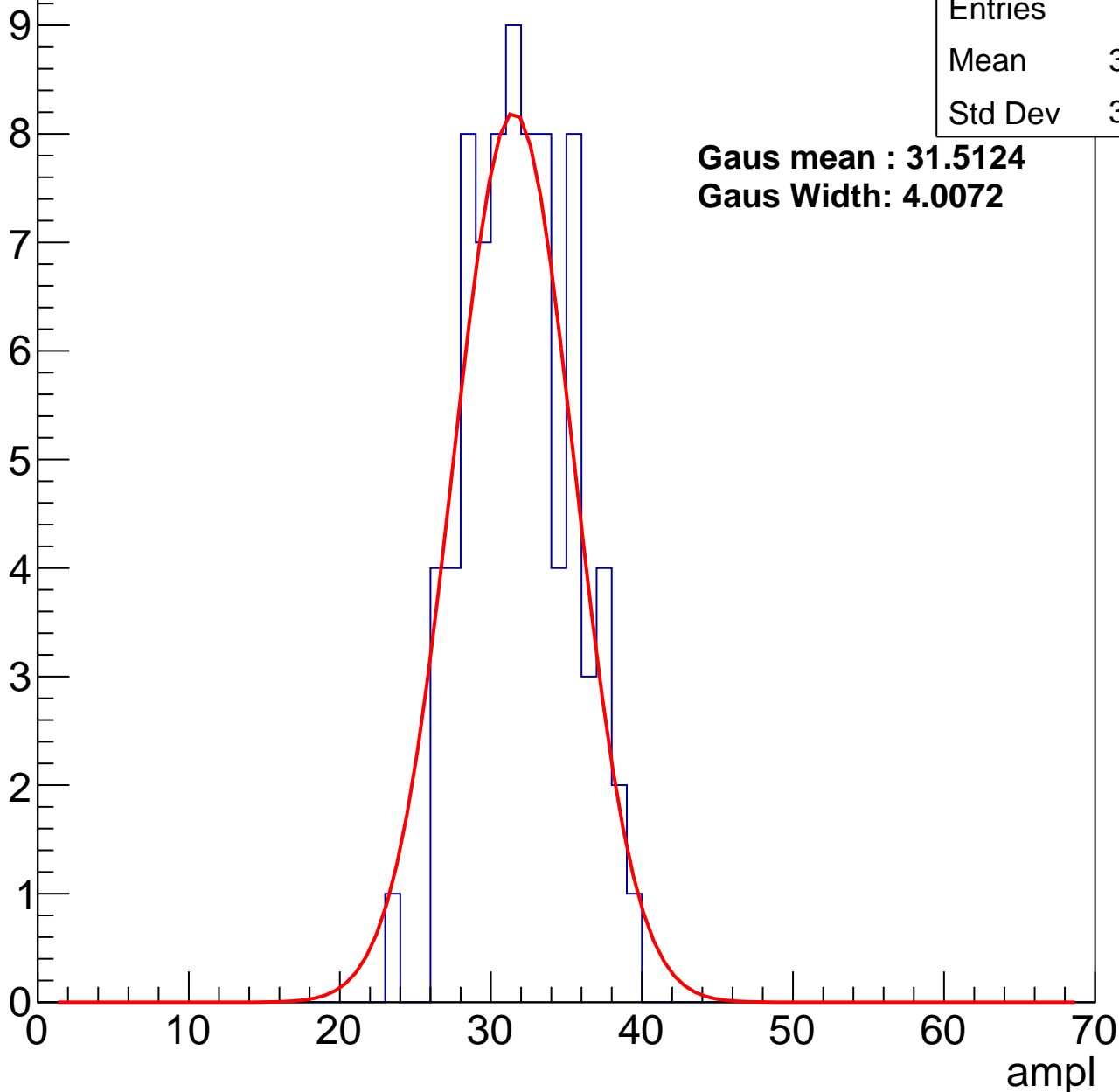
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	31.49
Std Dev	3.379

Gaus mean : 31.5124

Gaus Width: 4.0072



B0L001S, U22-ch13, adc1

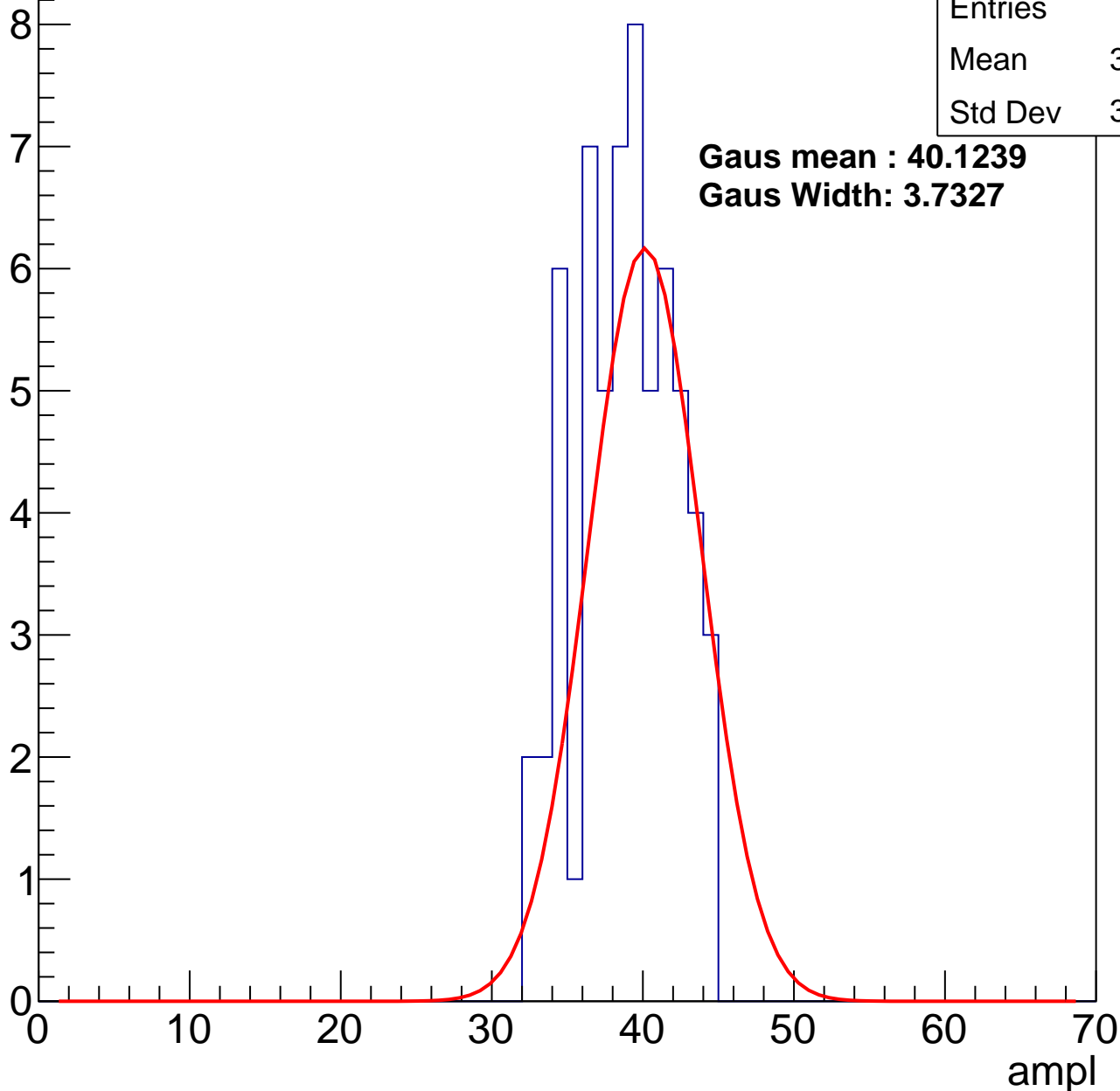
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.43
Std Dev	3.185

Gaus mean : 40.1239

Gaus Width: 3.7327



B0L001S, U22-ch13, adc2

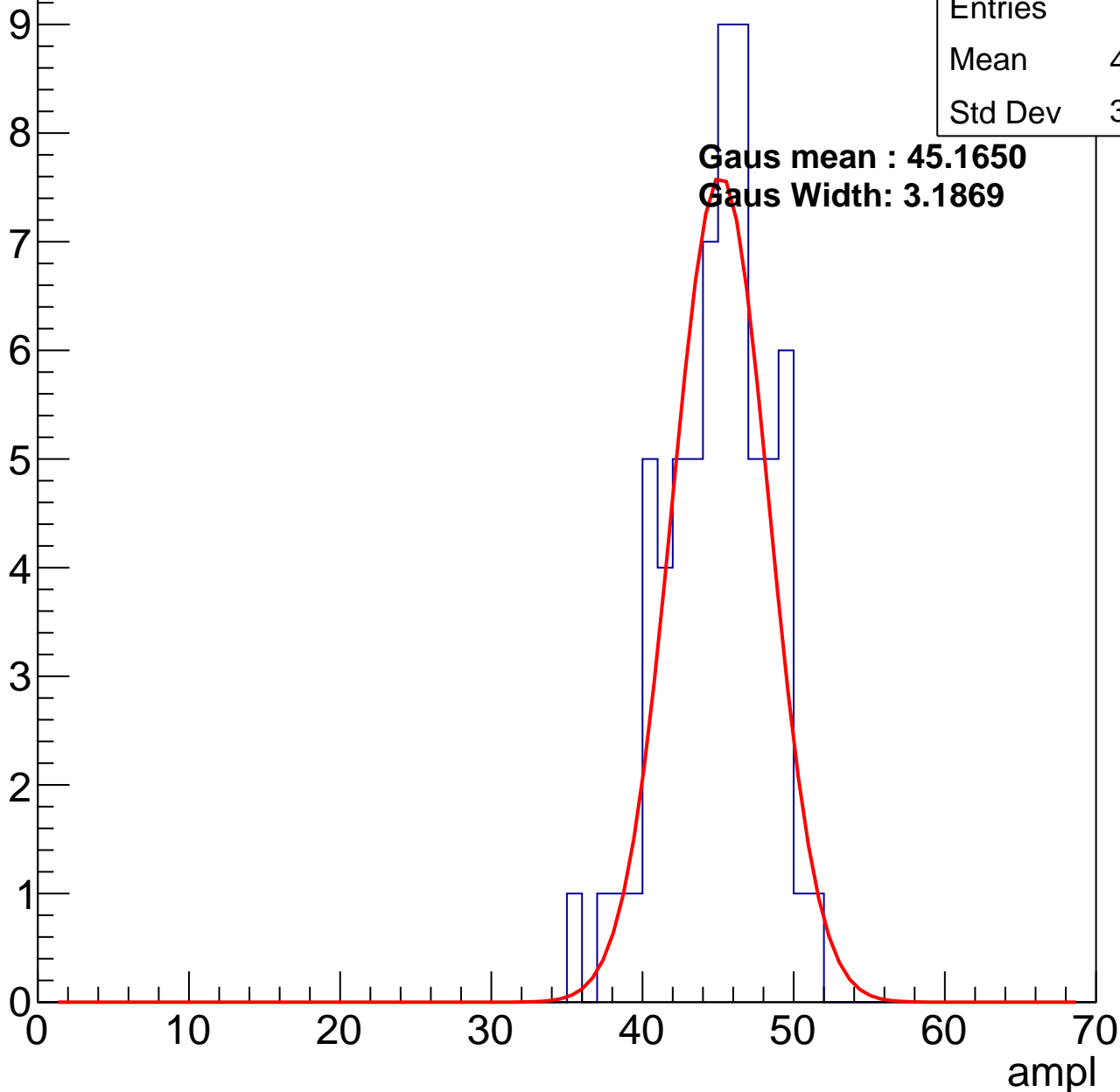
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.47
Std Dev	3.308

Gaus mean : 45.1650

Gaus Width: 3.1869

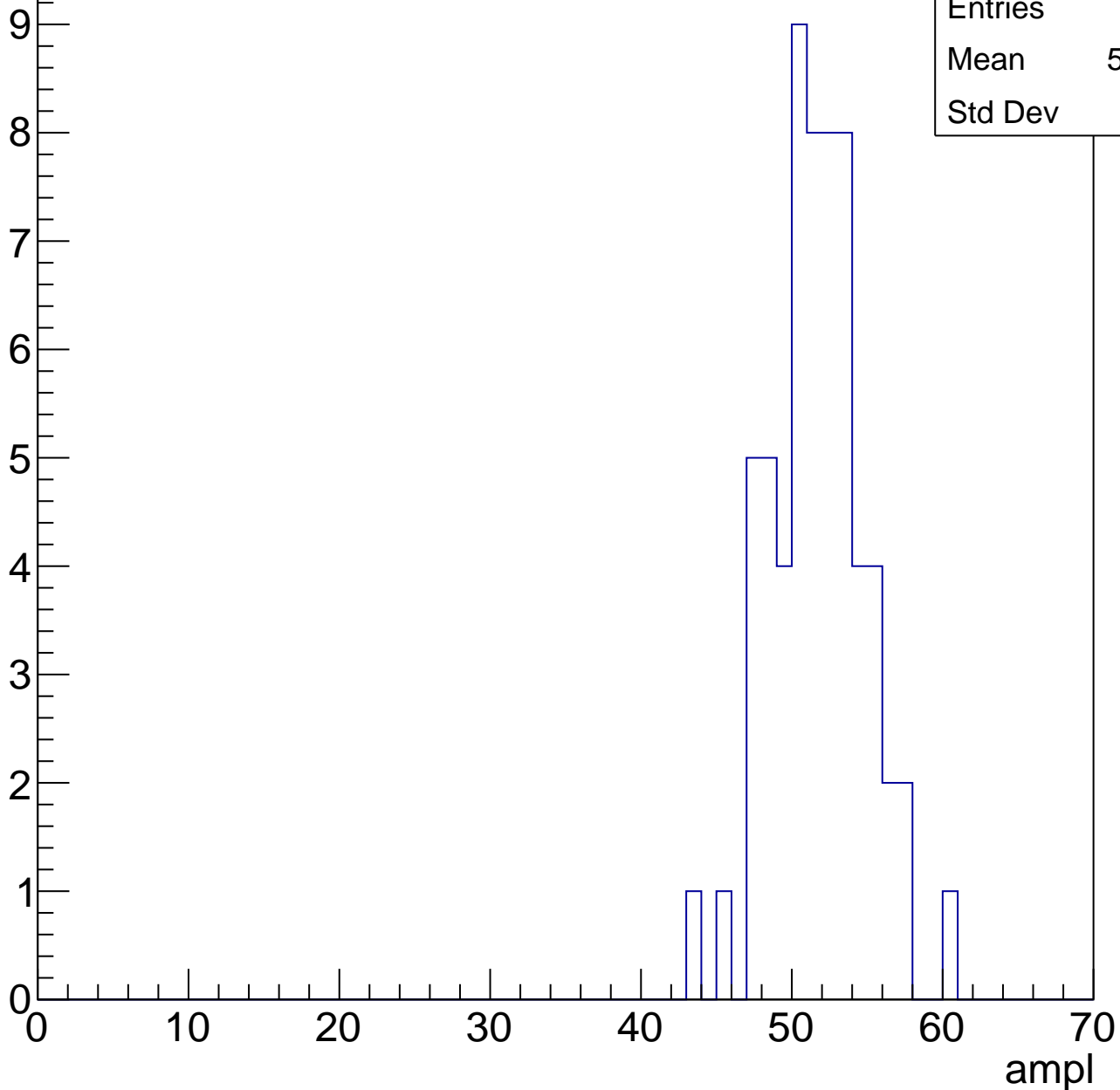


B0L001S, U22-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.27
Std Dev	3.08

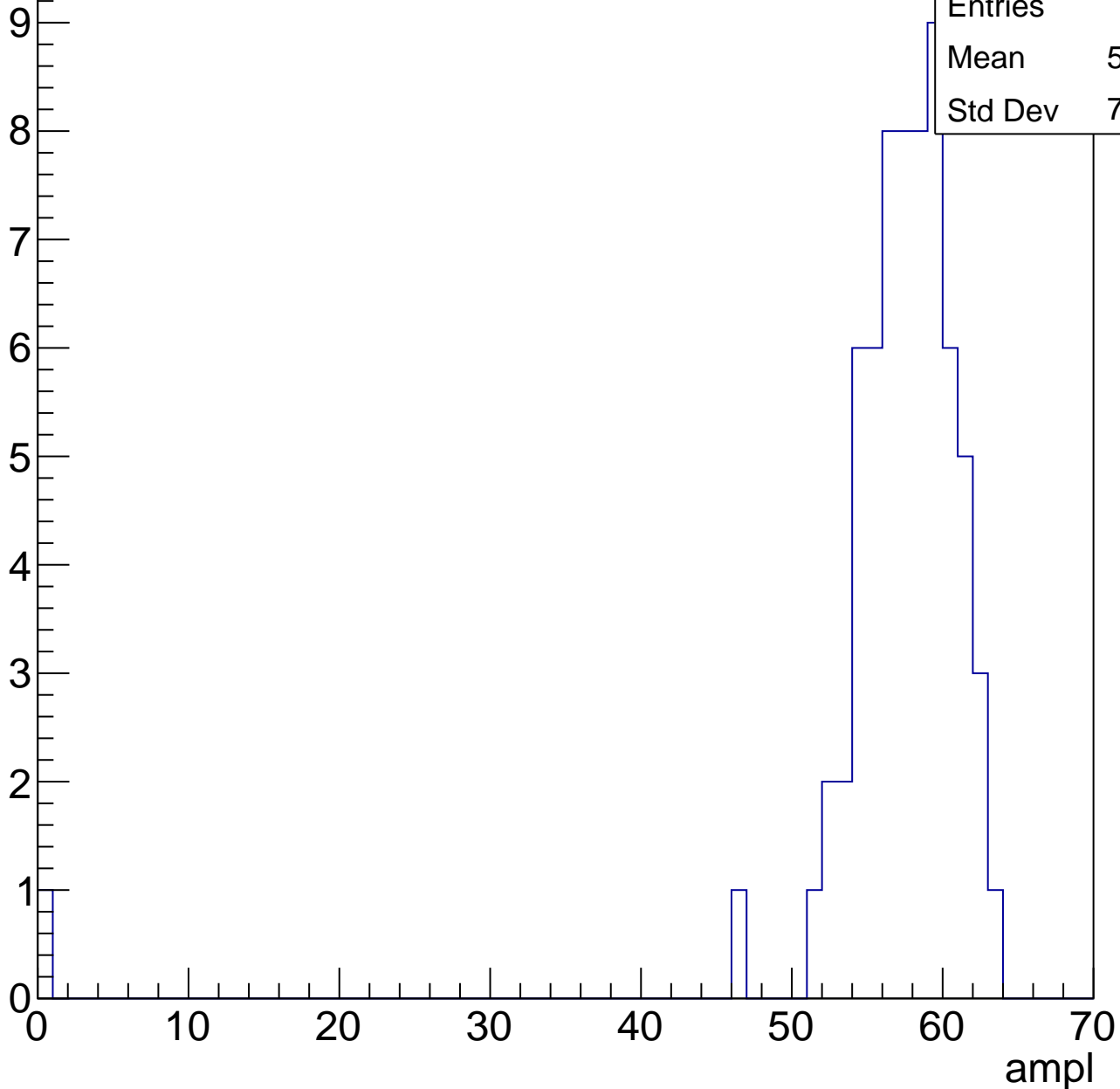


B0L001S, U22-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	56.33
Std Dev	7.563

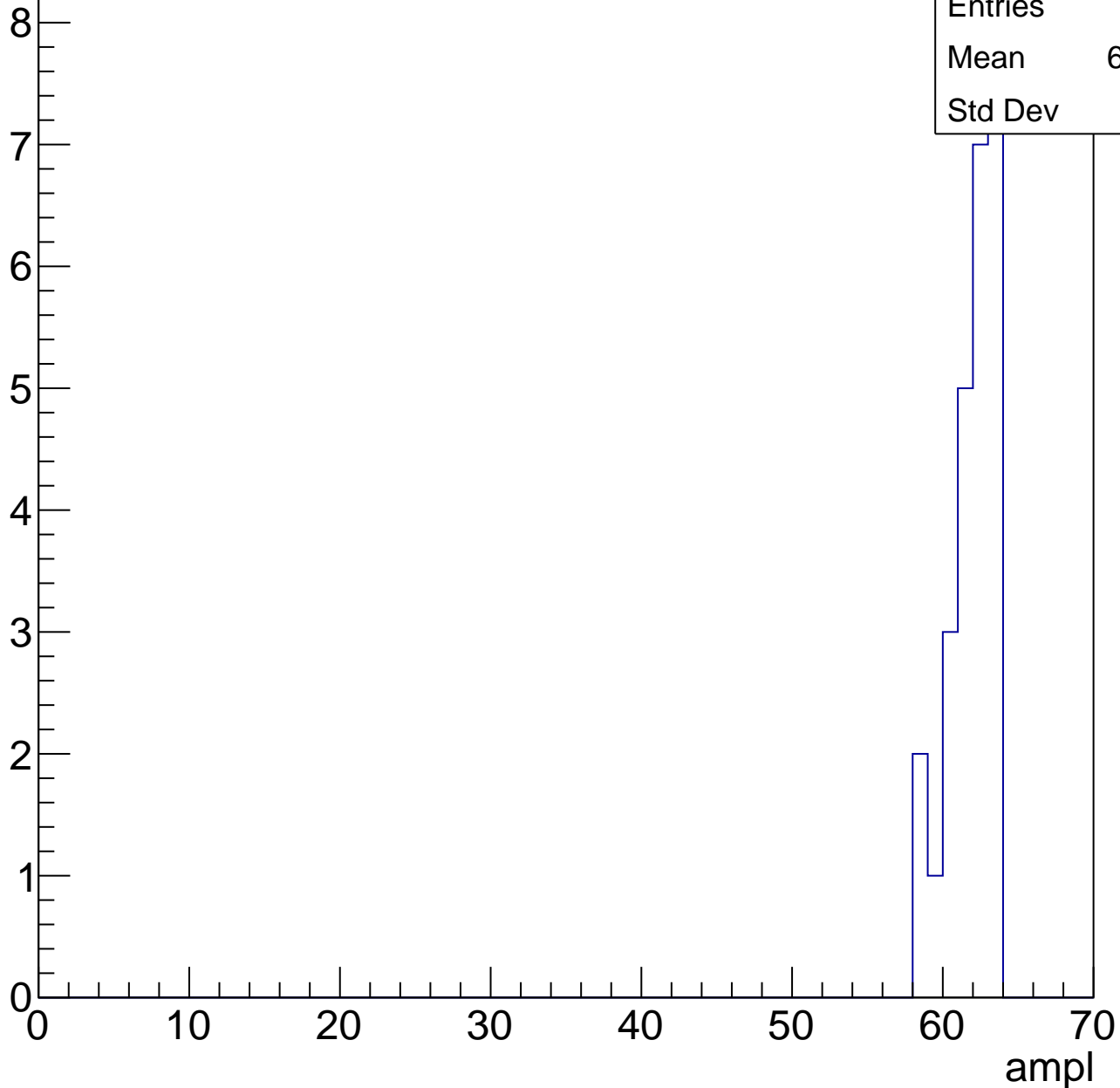


B0L001S, U22-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.46
Std Dev	1.5



B0L001S, U22-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch14, adc0

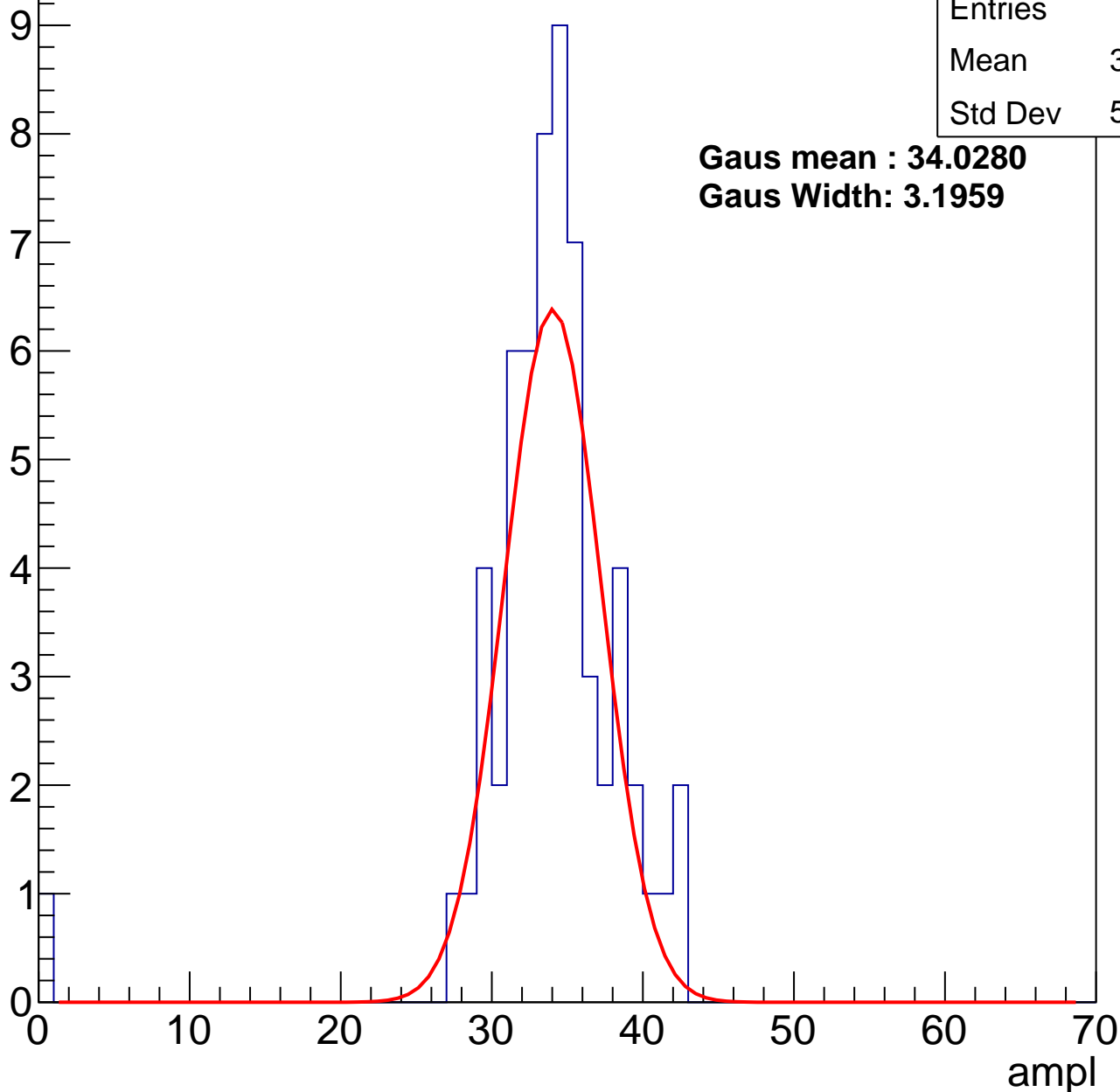
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	33.35
Std Dev	5.474

Gaus mean : 34.0280

Gaus Width: 3.1959



B0L001S, U22-ch14, adc1

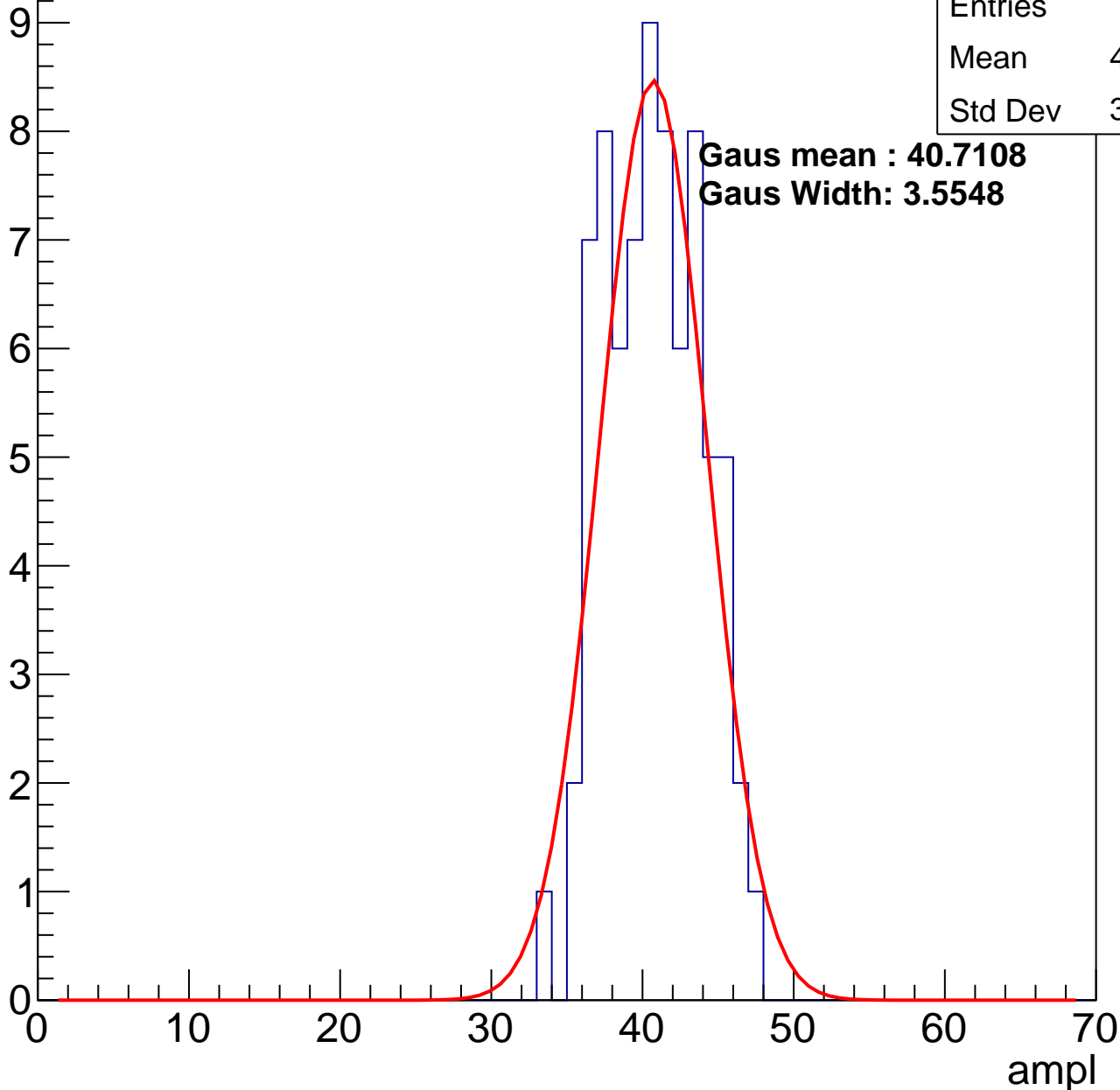
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	40.27
Std Dev	3.138

Gaus mean : 40.7108

Gaus Width: 3.5548



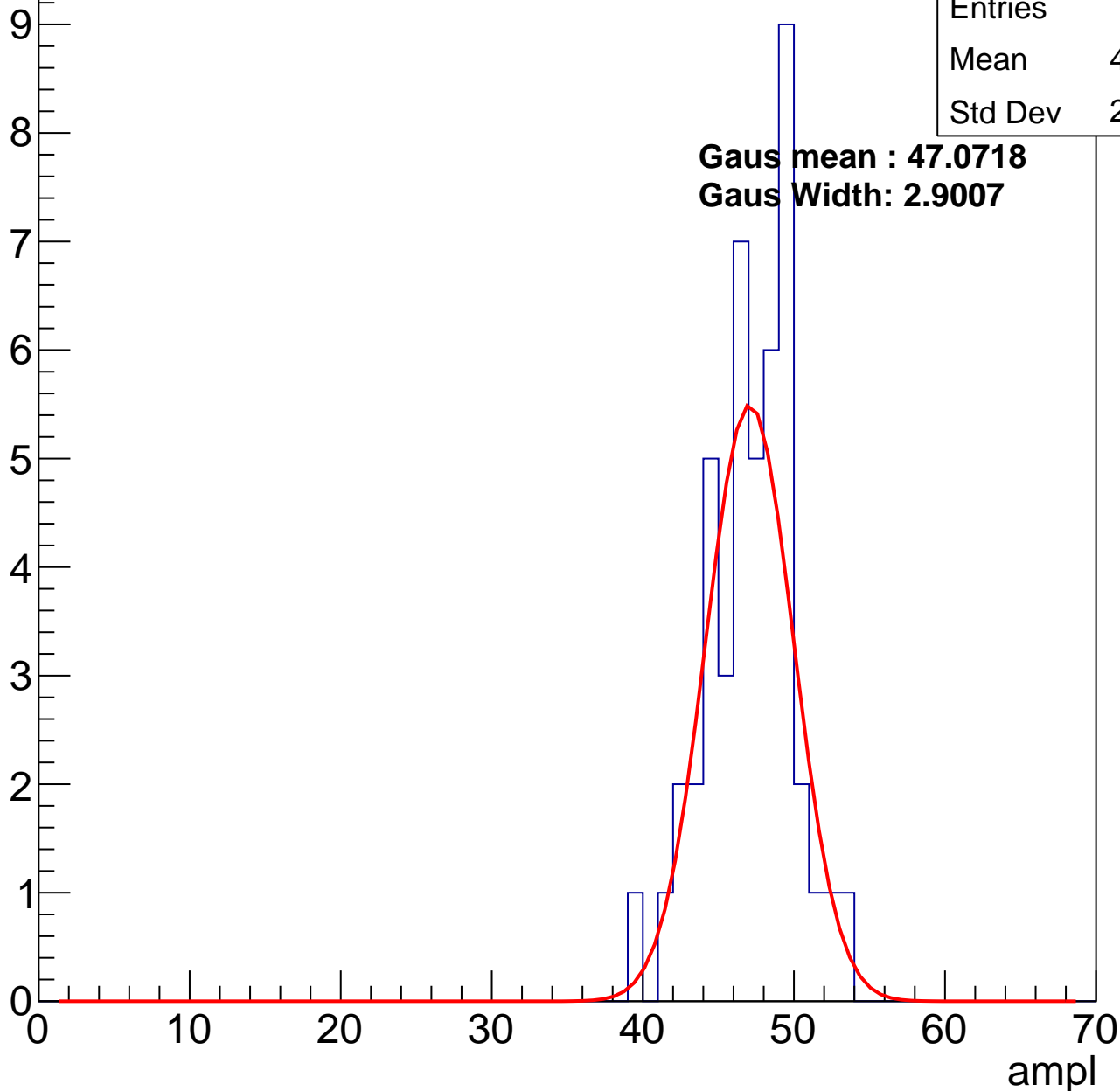
B0L001S, U22-ch14, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	46.67
Std Dev	2.867

Gaus mean : 47.0718
Gaus Width: 2.9007

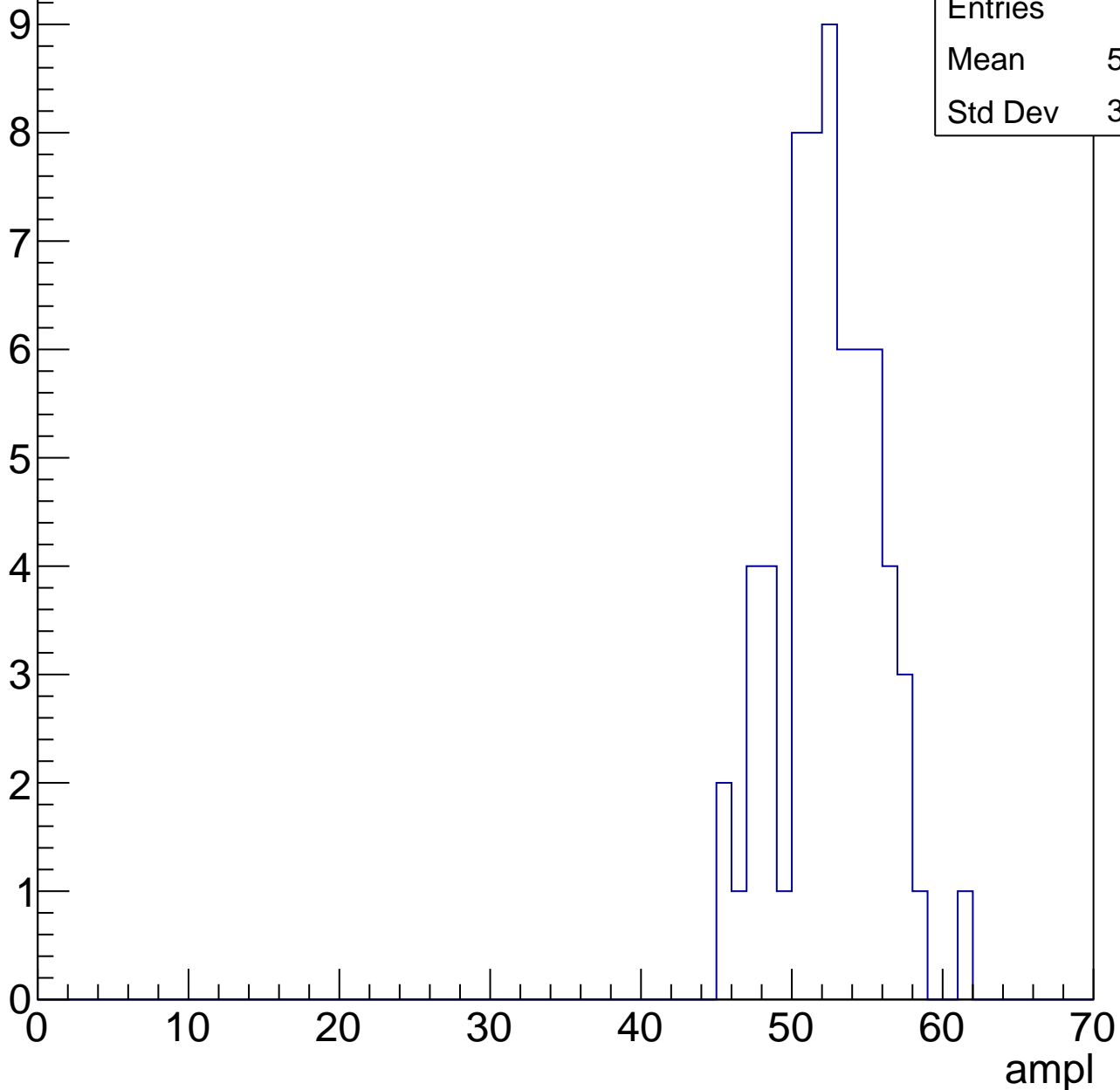


B0L001S, U22-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.98
Std Dev	3.276

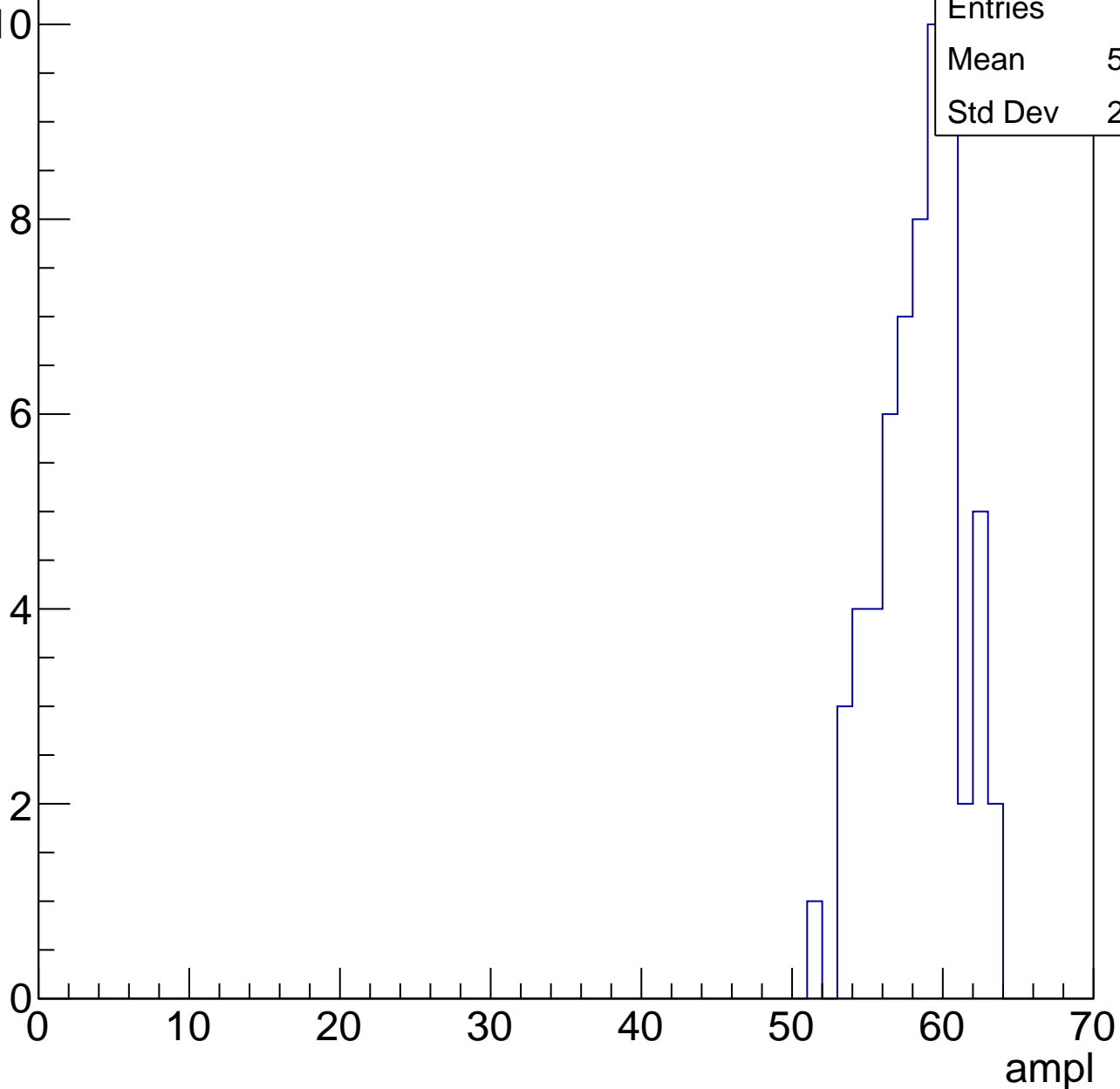


B0L001S, U22-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	57.92
Std Dev	2.712

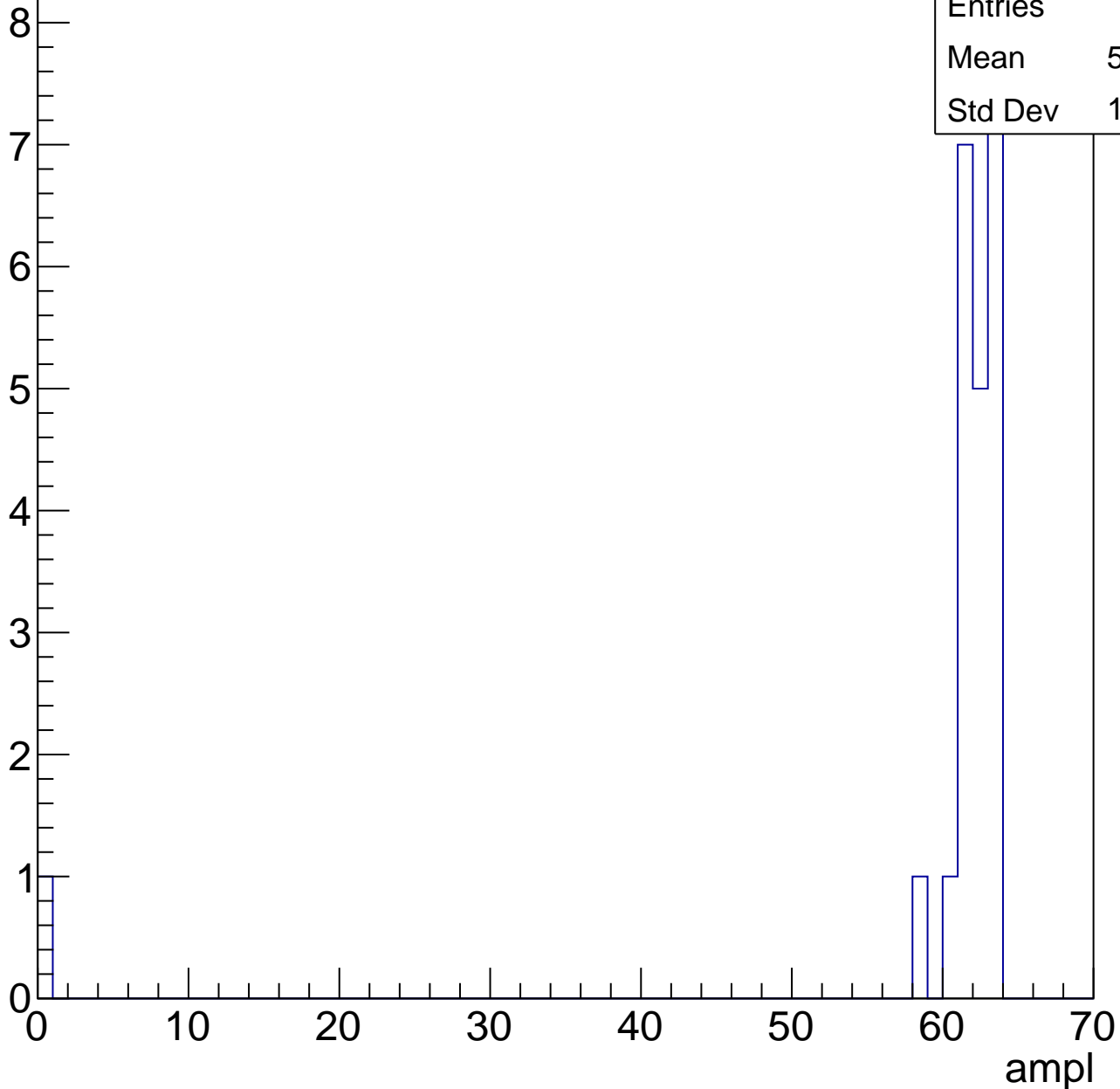


B0L001S, U22-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	59.09
Std Dev	12.66



B0L001S, U22-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch15, adc0

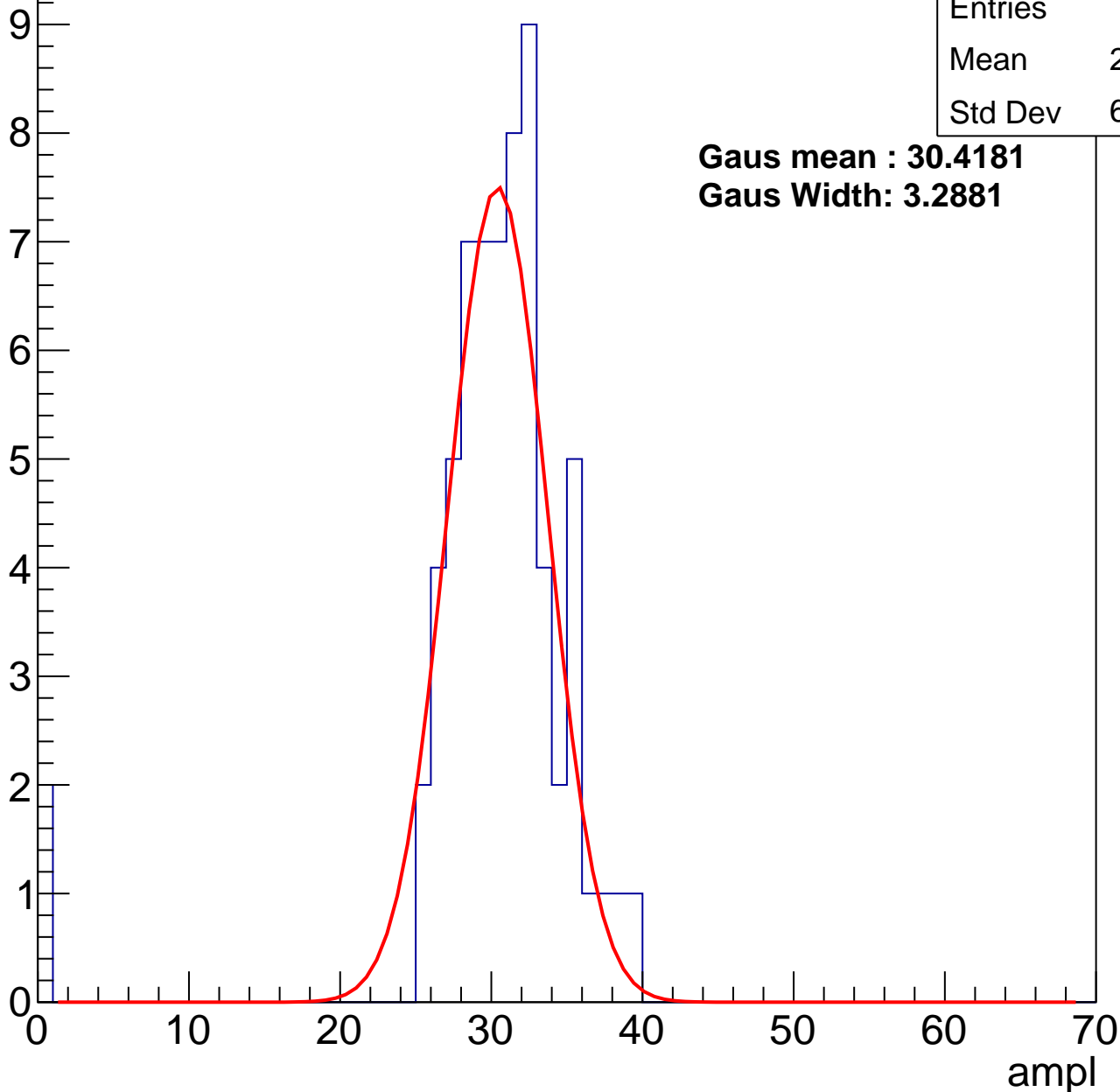
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.68
Std Dev	6.098

Gaus mean : 30.4181

Gaus Width: 3.2881



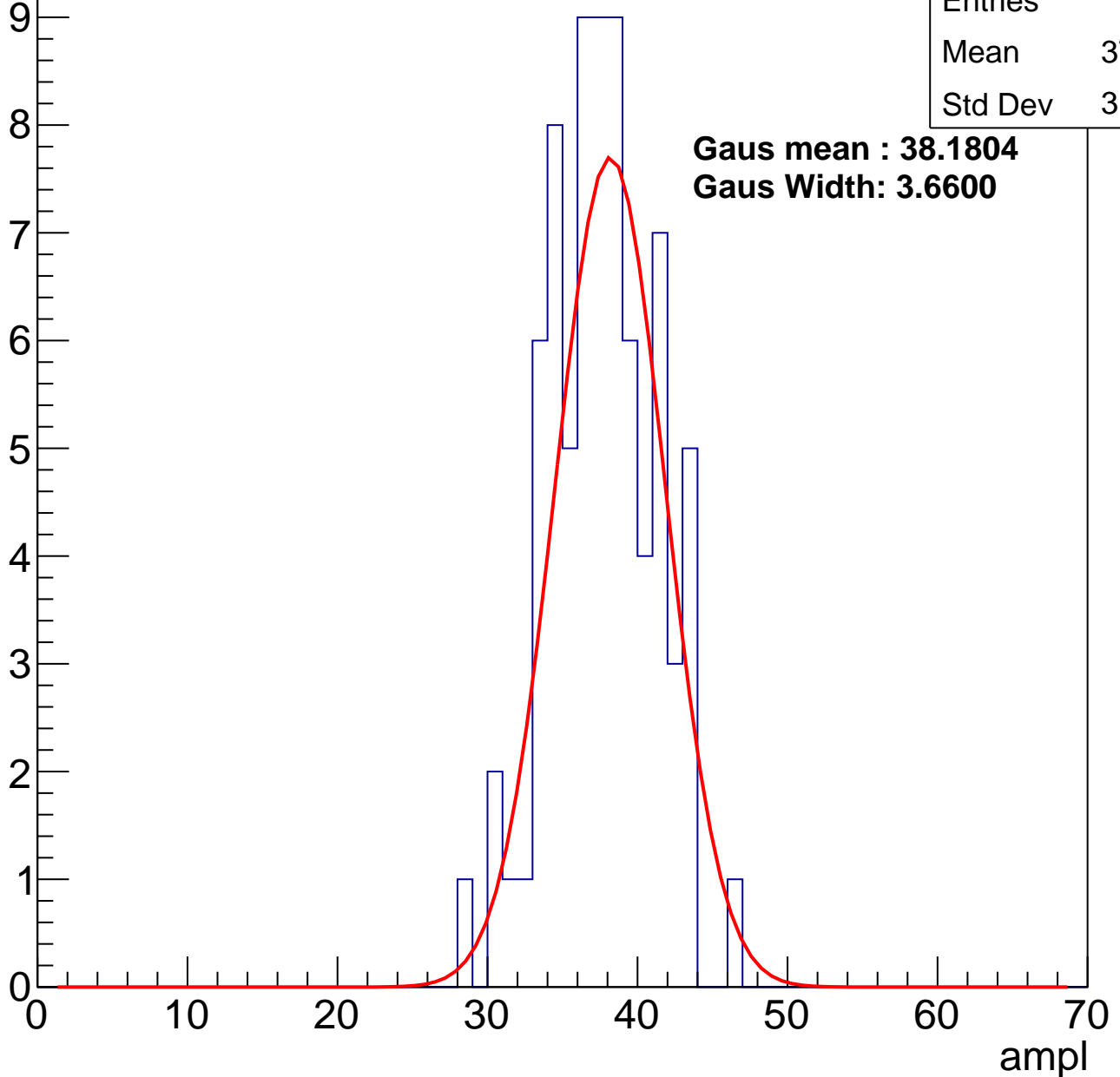
B0L001S, U22-ch15, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.18
Std Dev	3.515

Gaus mean : 38.1804
Gaus Width: 3.6600



B0L001S, U22-ch15, adc2

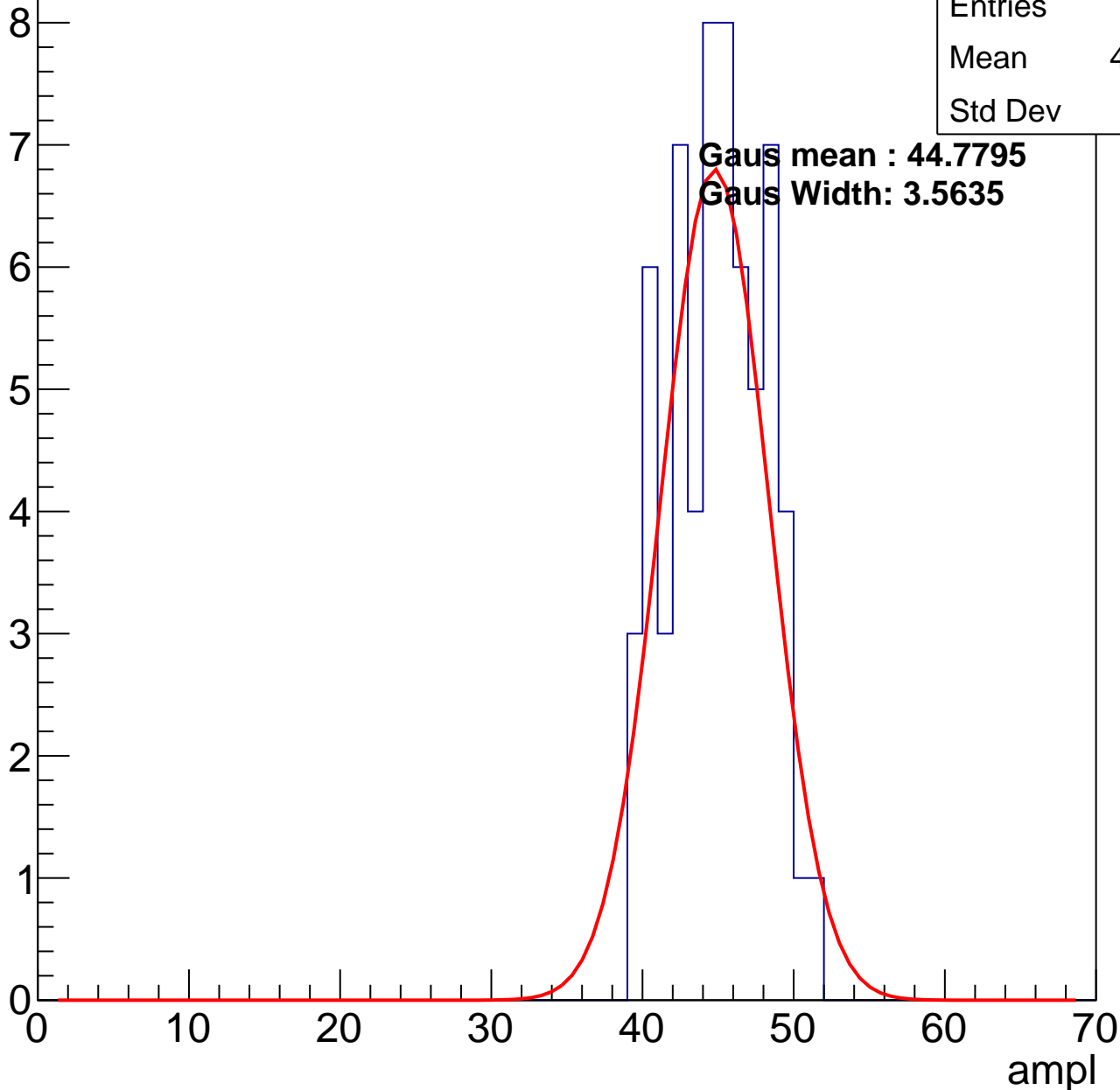
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.48
Std Dev	3.06

Gaus mean : 44.7795

Gaus Width: 3.5635

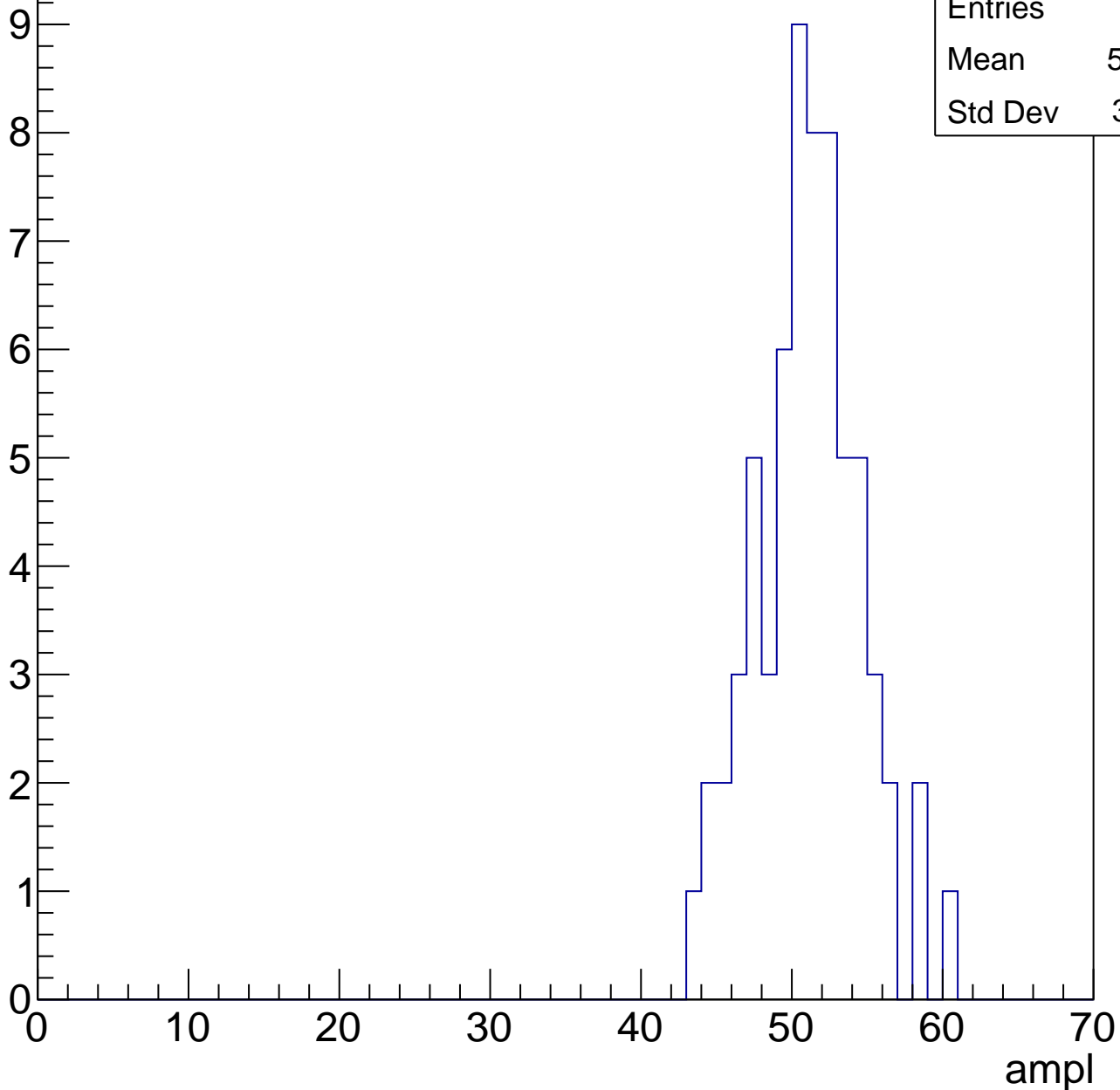


B0L001S, U22-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

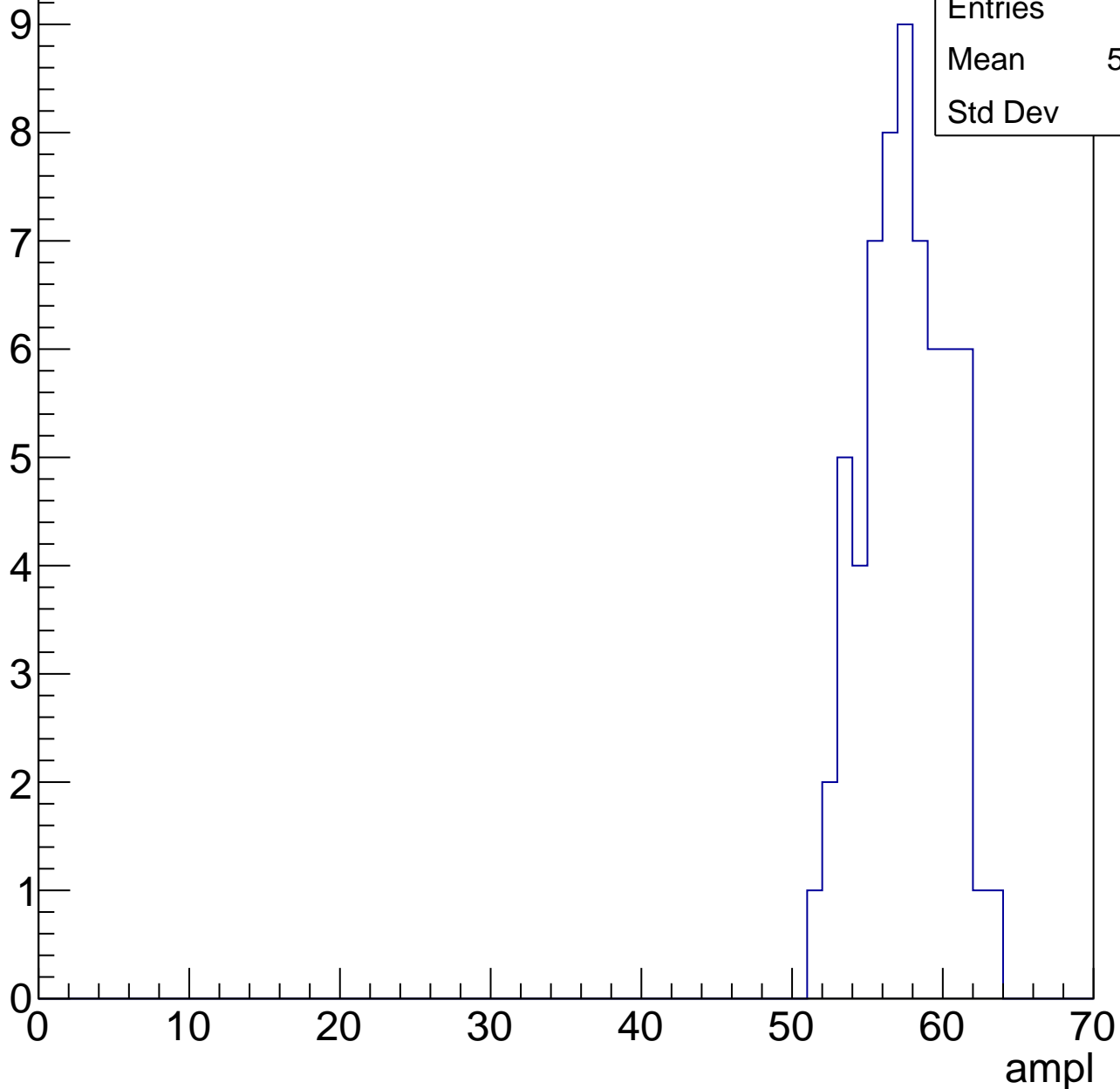
Entries	65
Mean	50.68
Std Dev	3.491



B0L001S, U22-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

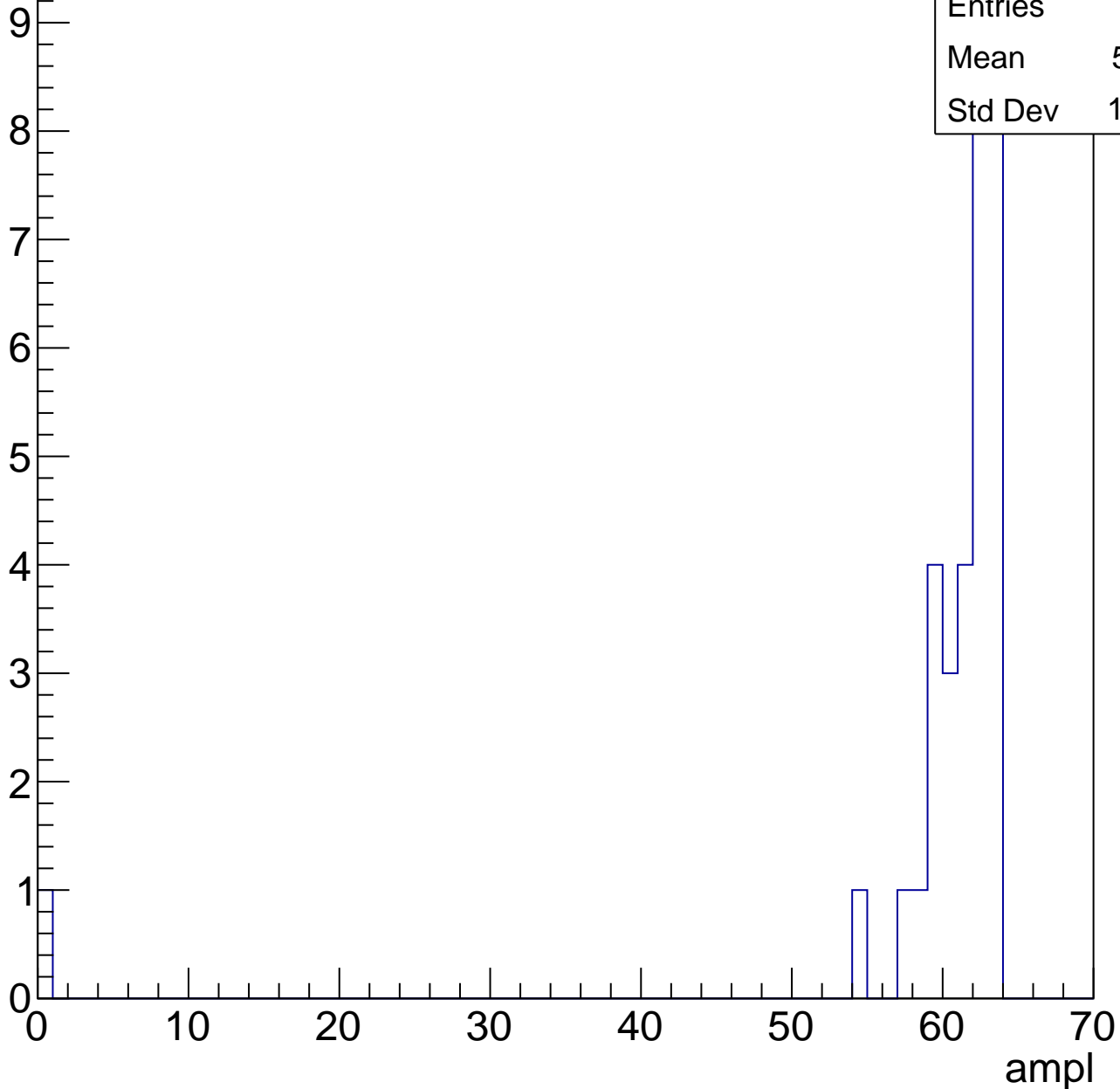


B0L001S, U22-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.21
Std Dev	10.67



B0L001S, U22-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L001S, U22-ch16, adc0

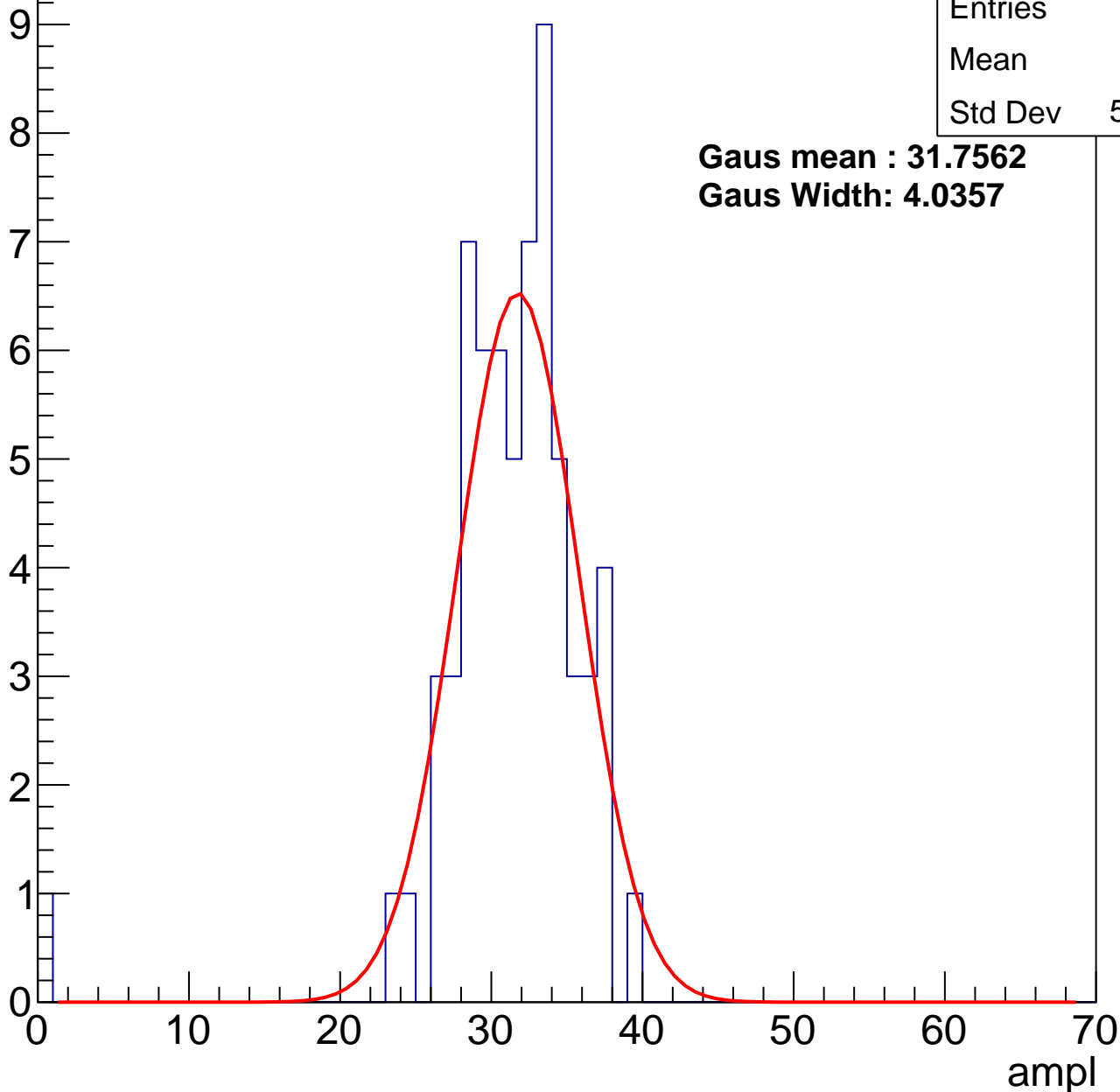
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.8
Std Dev	5.124

Gaus mean : 31.7562

Gaus Width: 4.0357



B0L001S, U22-ch16, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	37.47
Std Dev	3.269

Gaus mean : 38.3475

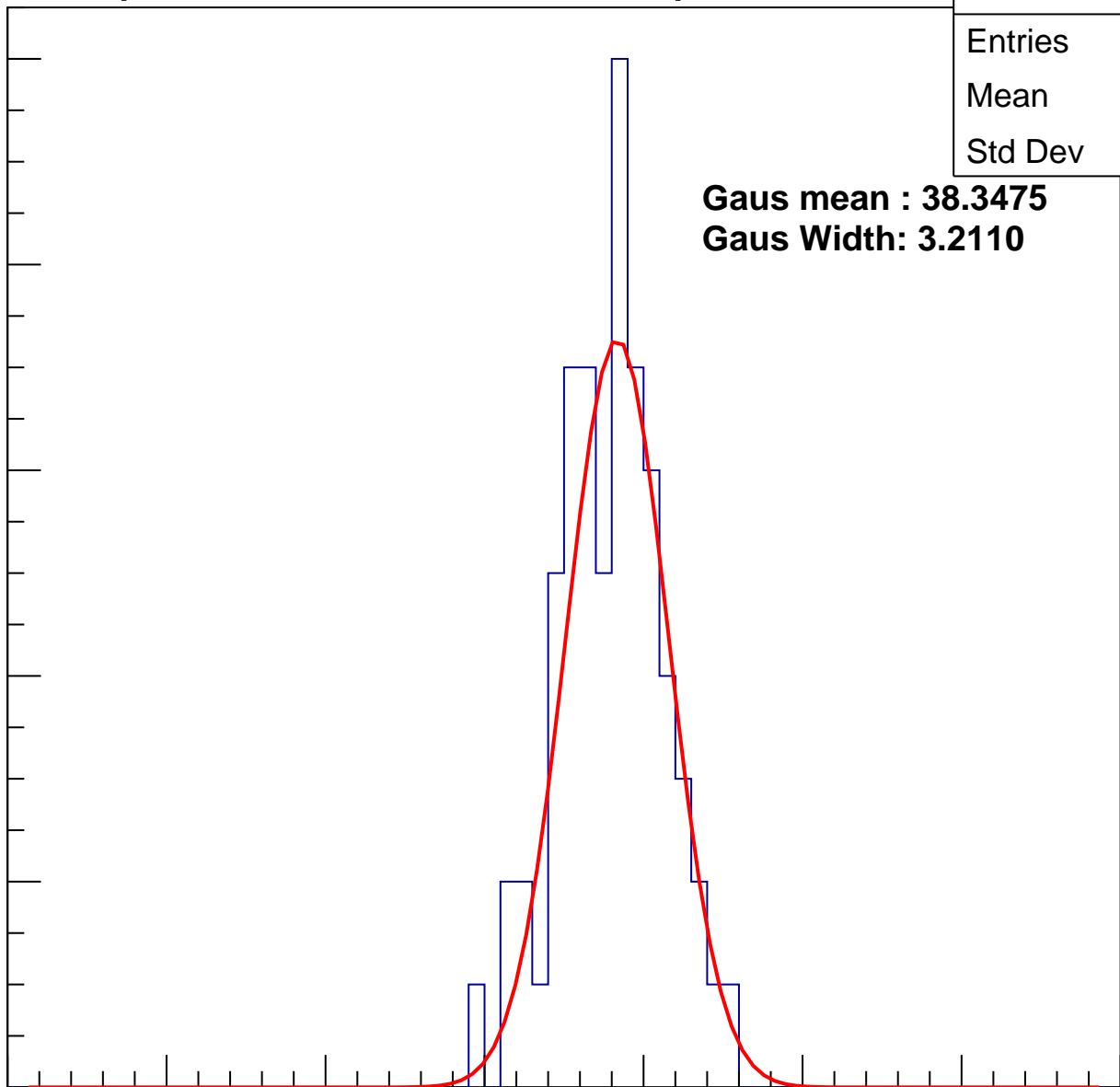
Gaus Width: 3.2110

Entry

10
8
6
4
2
0

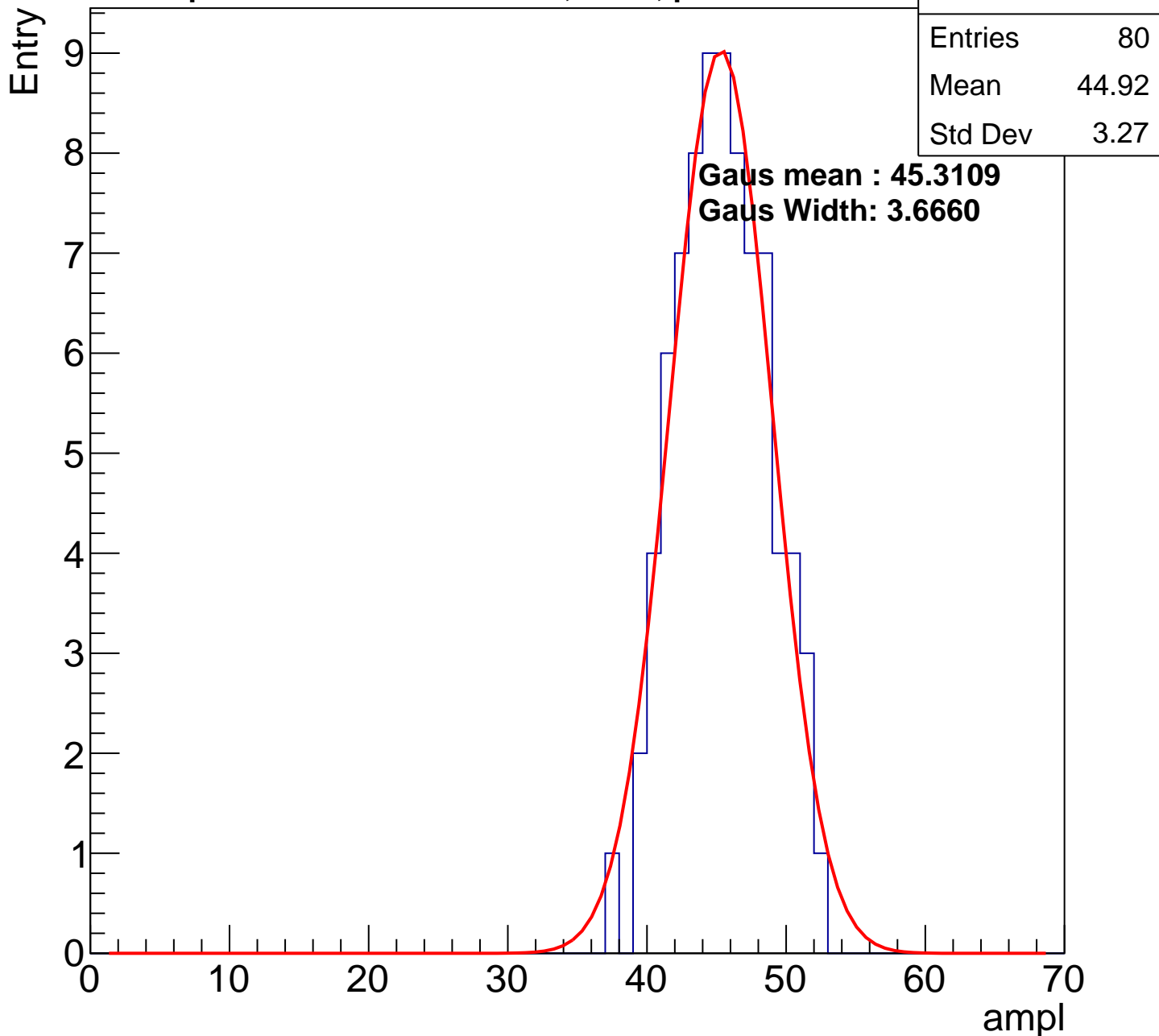
ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch16, adc2

calib_packv5_042523_0143.root, FC#9, port A1

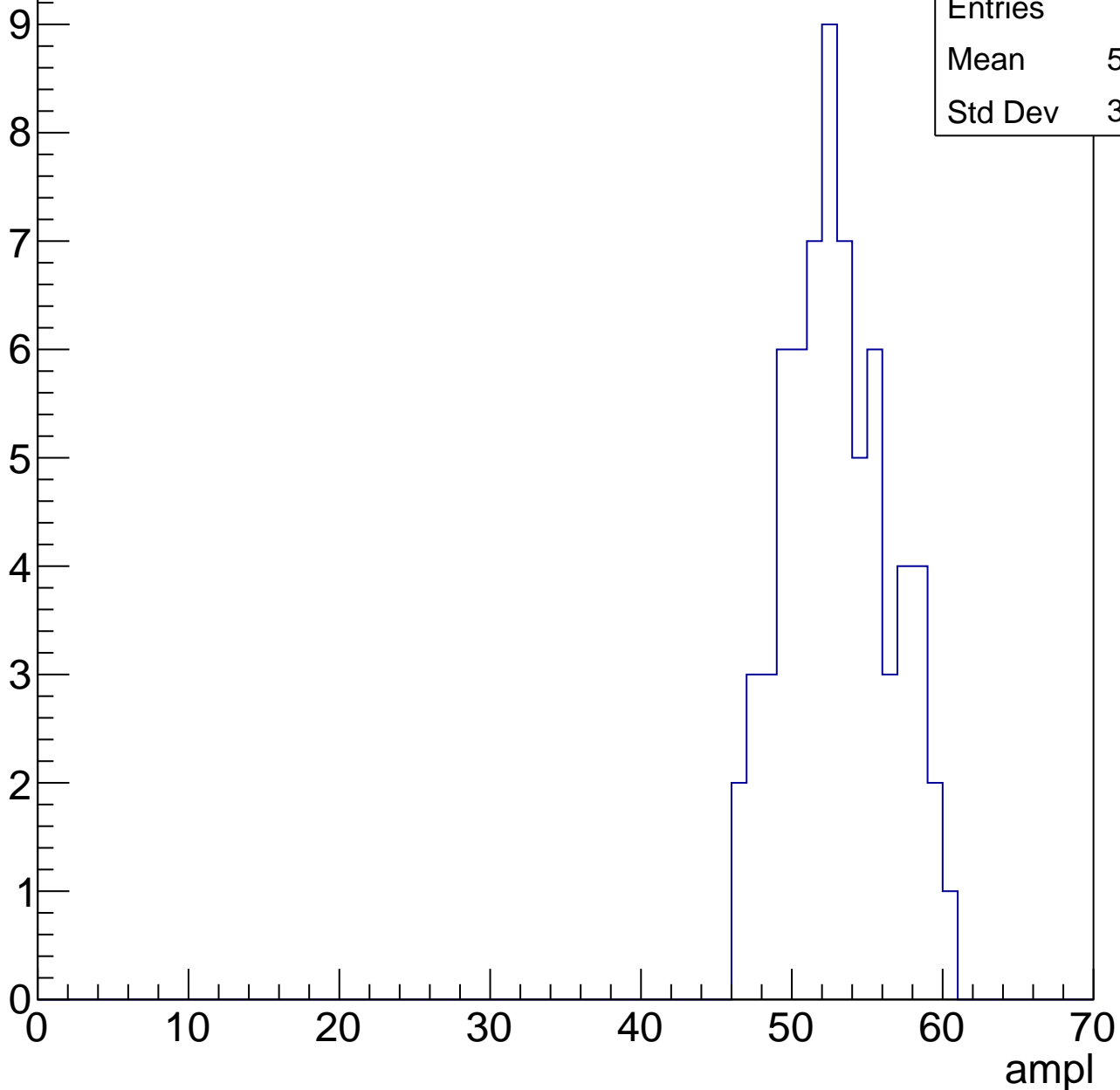


B0L001S, U22-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

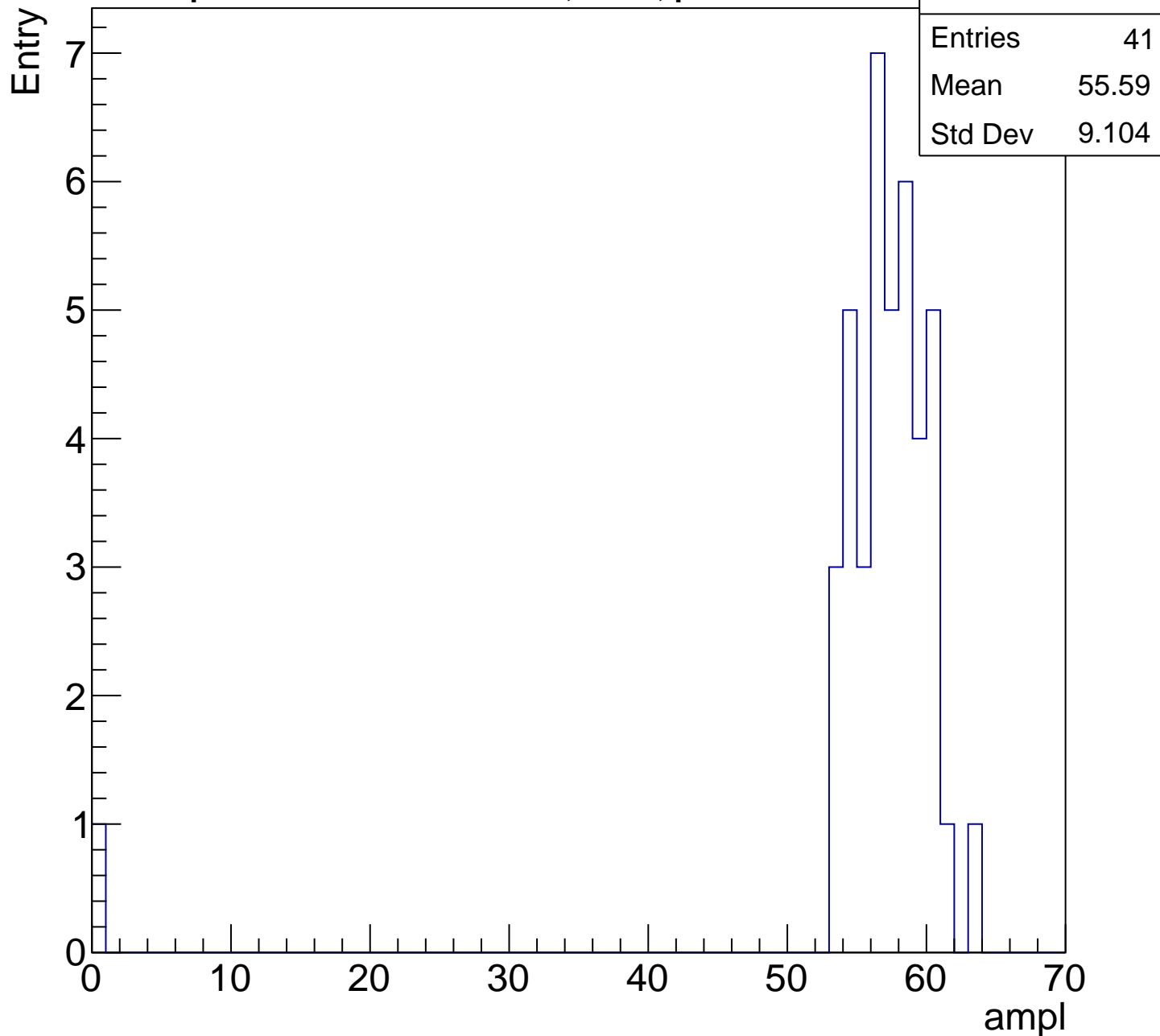
Entry

Entries	68
Mean	52.54
Std Dev	3.419



B0L001S, U22-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

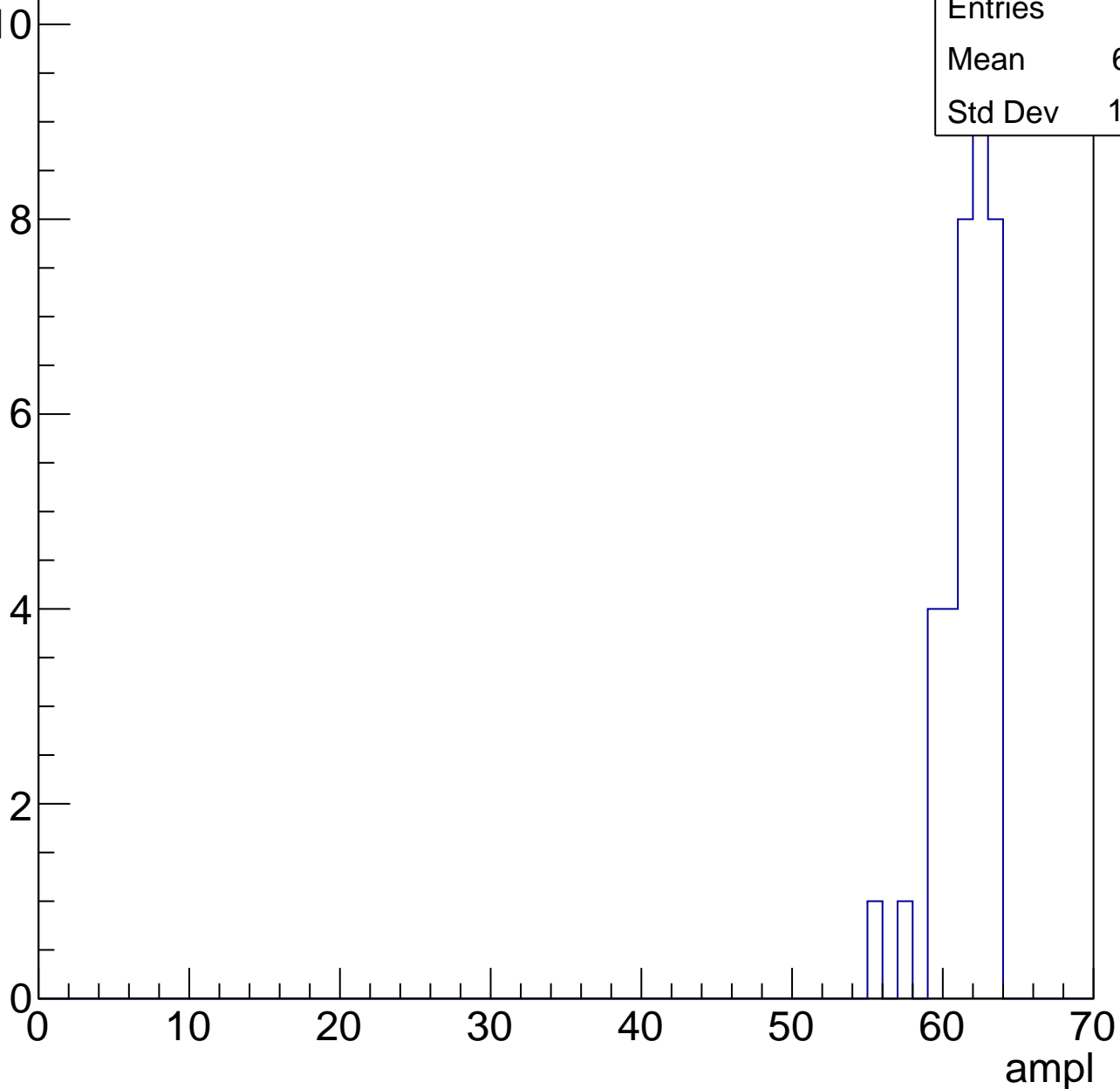


B0L001S, U22-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	61.11
Std Dev	1.776



B0L001S, U22-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch17, adc0

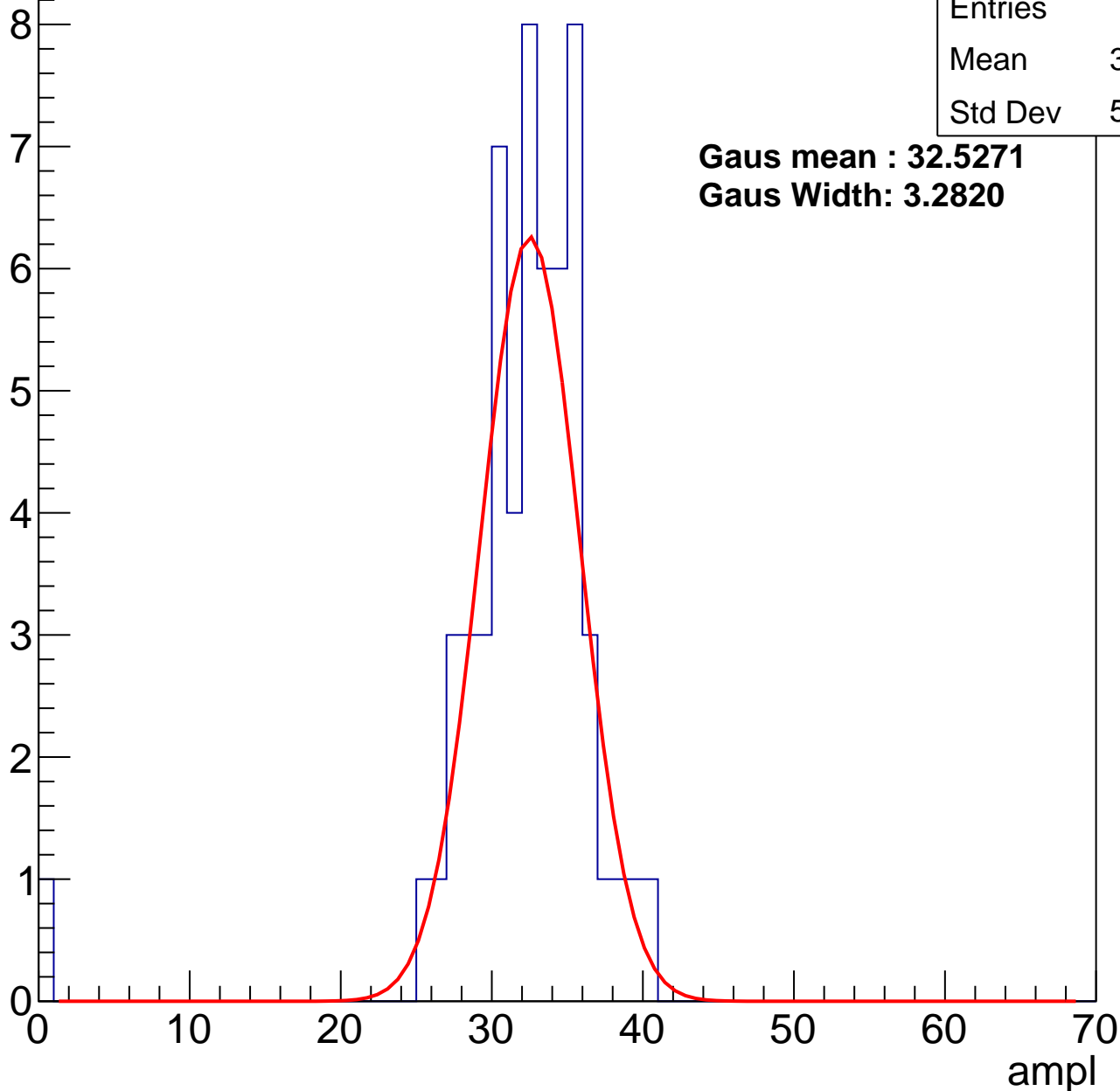
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.67
Std Dev	5.267

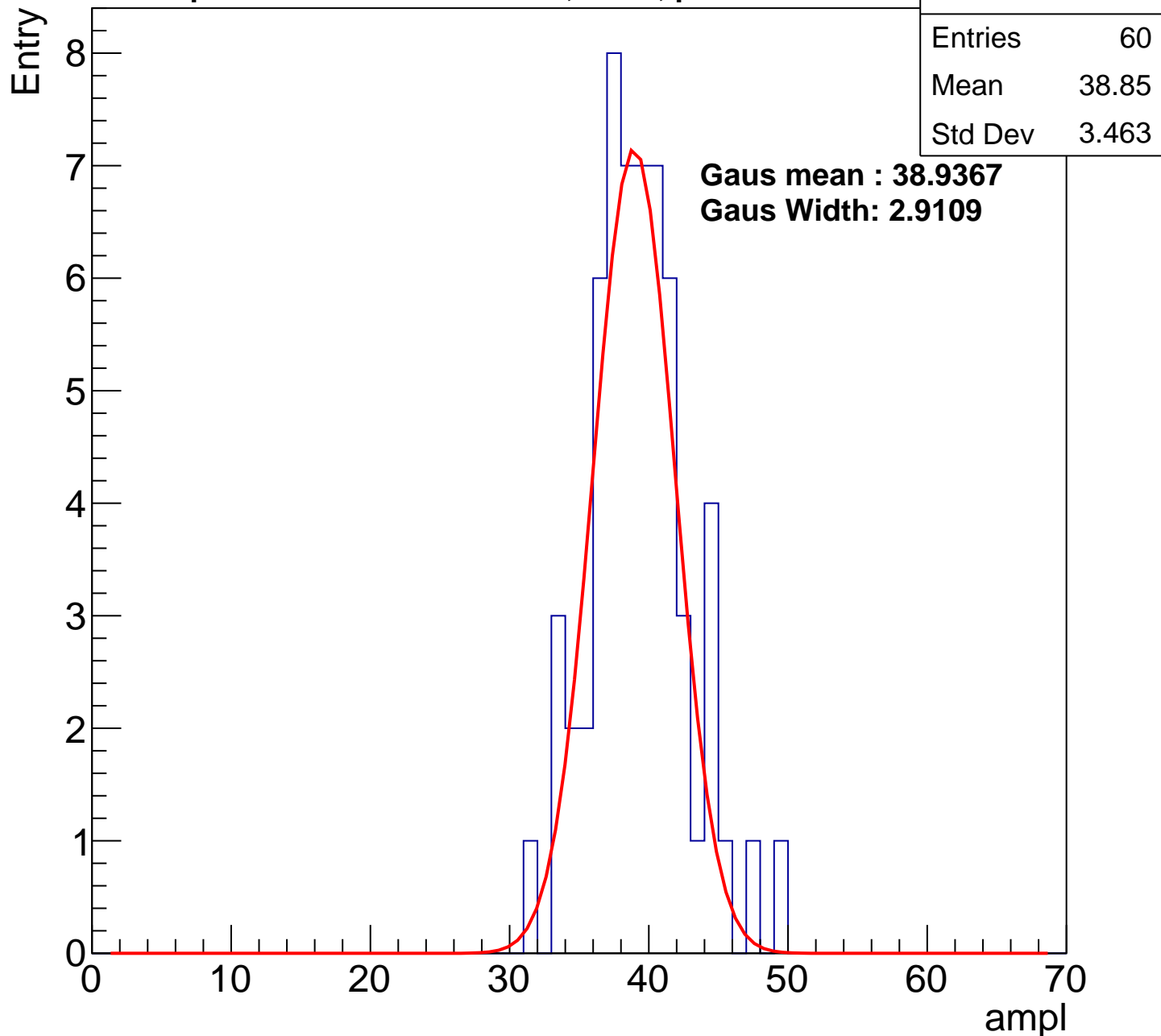
Gaus mean : 32.5271

Gaus Width: 3.2820



B0L001S, U22-ch17, adc1

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U22-ch17, adc2

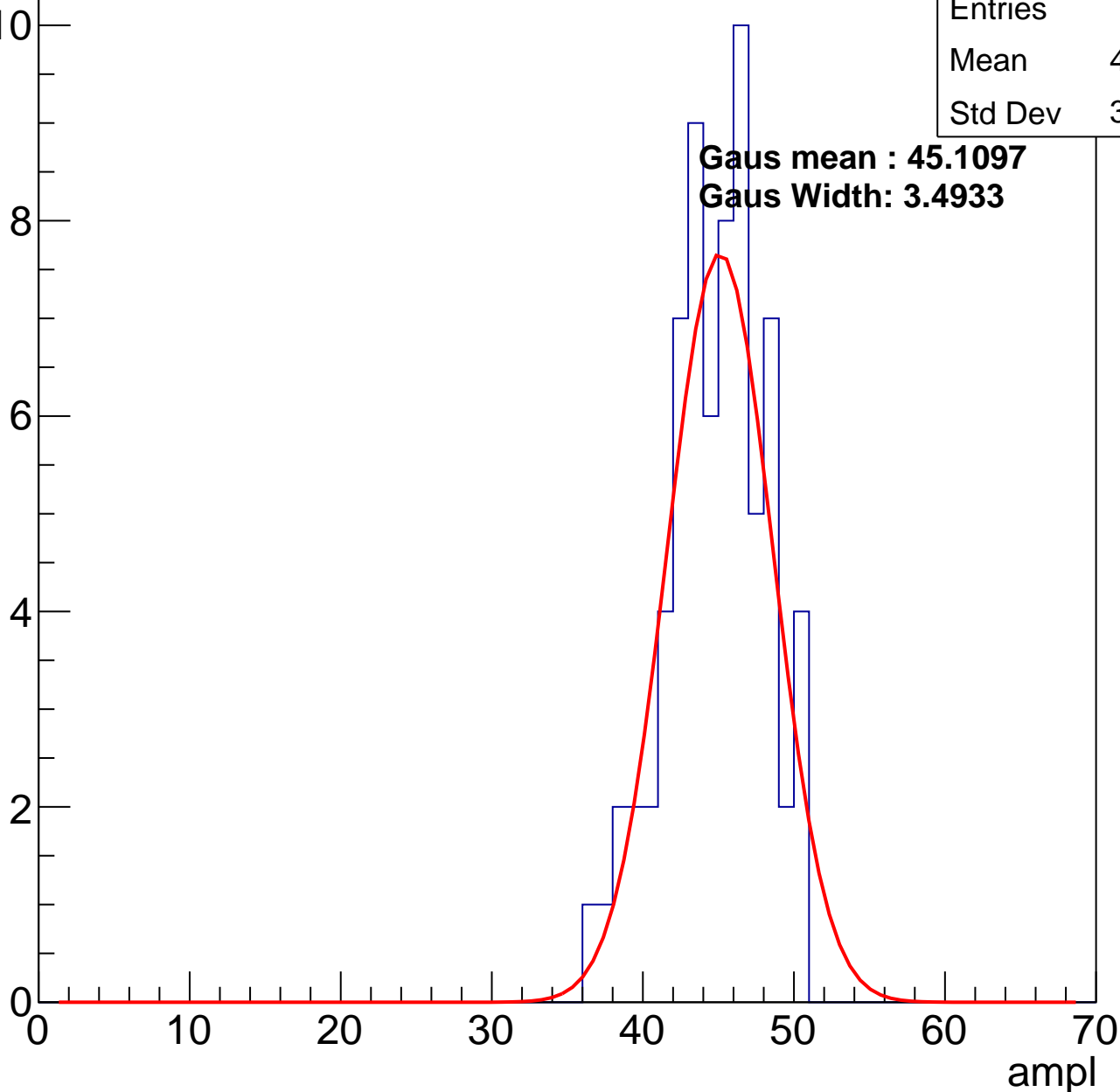
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.36
Std Dev	3.229

Gaus mean : 45.1097

Gaus Width: 3.4933

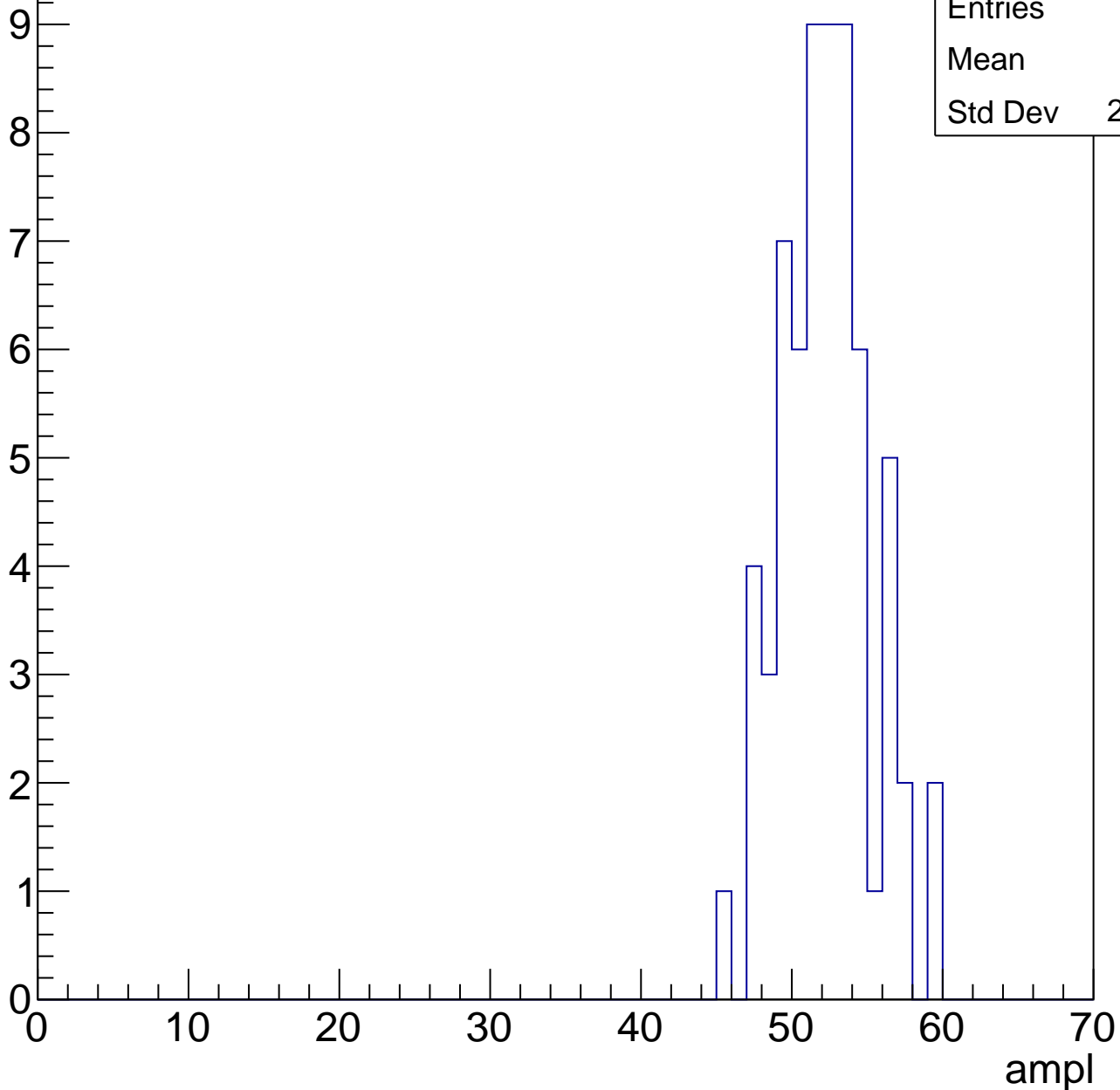


B0L001S, U22-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.8
Std Dev	2.959

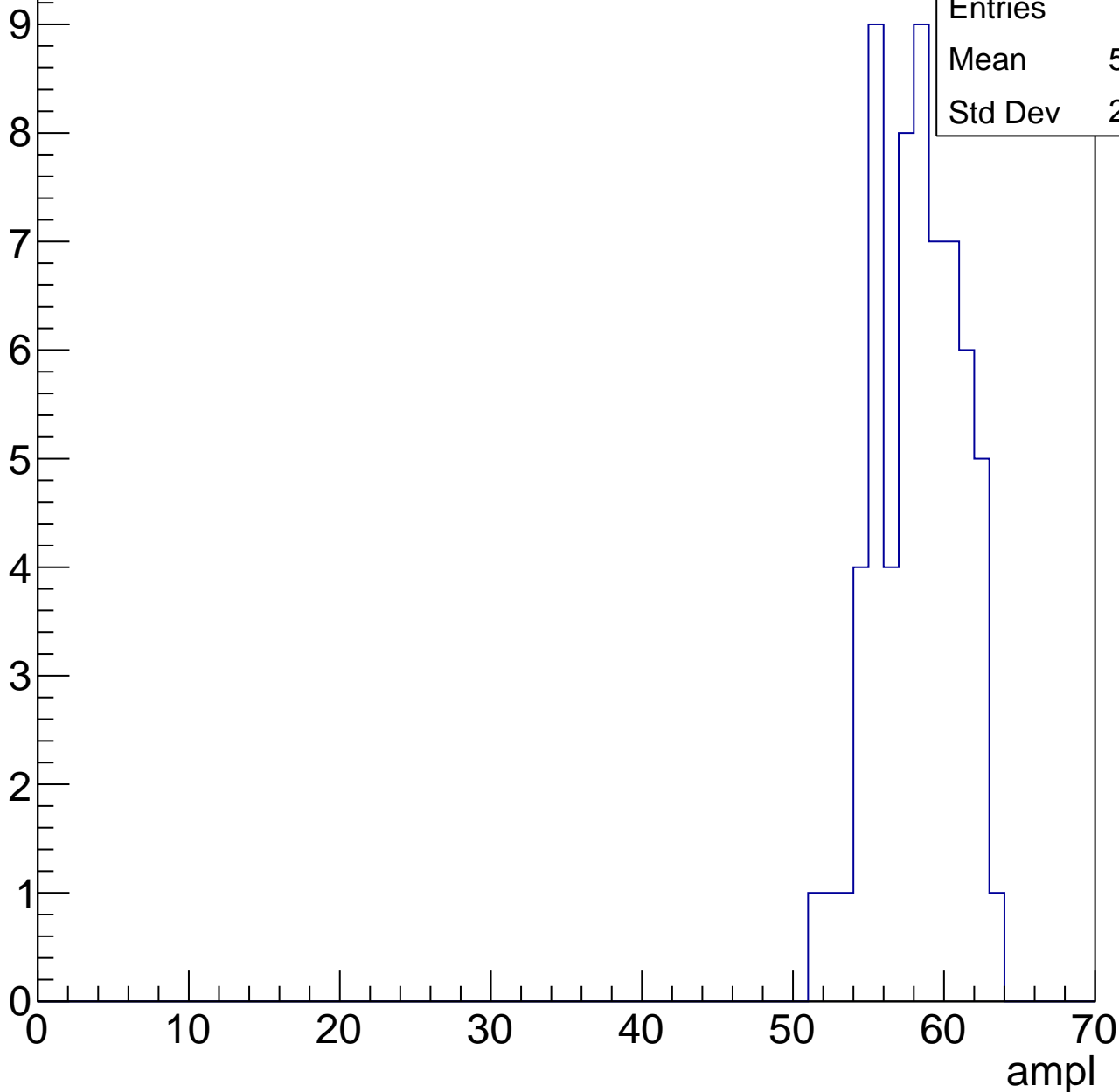


B0L001S, U22-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	57.79
Std Dev	2.732

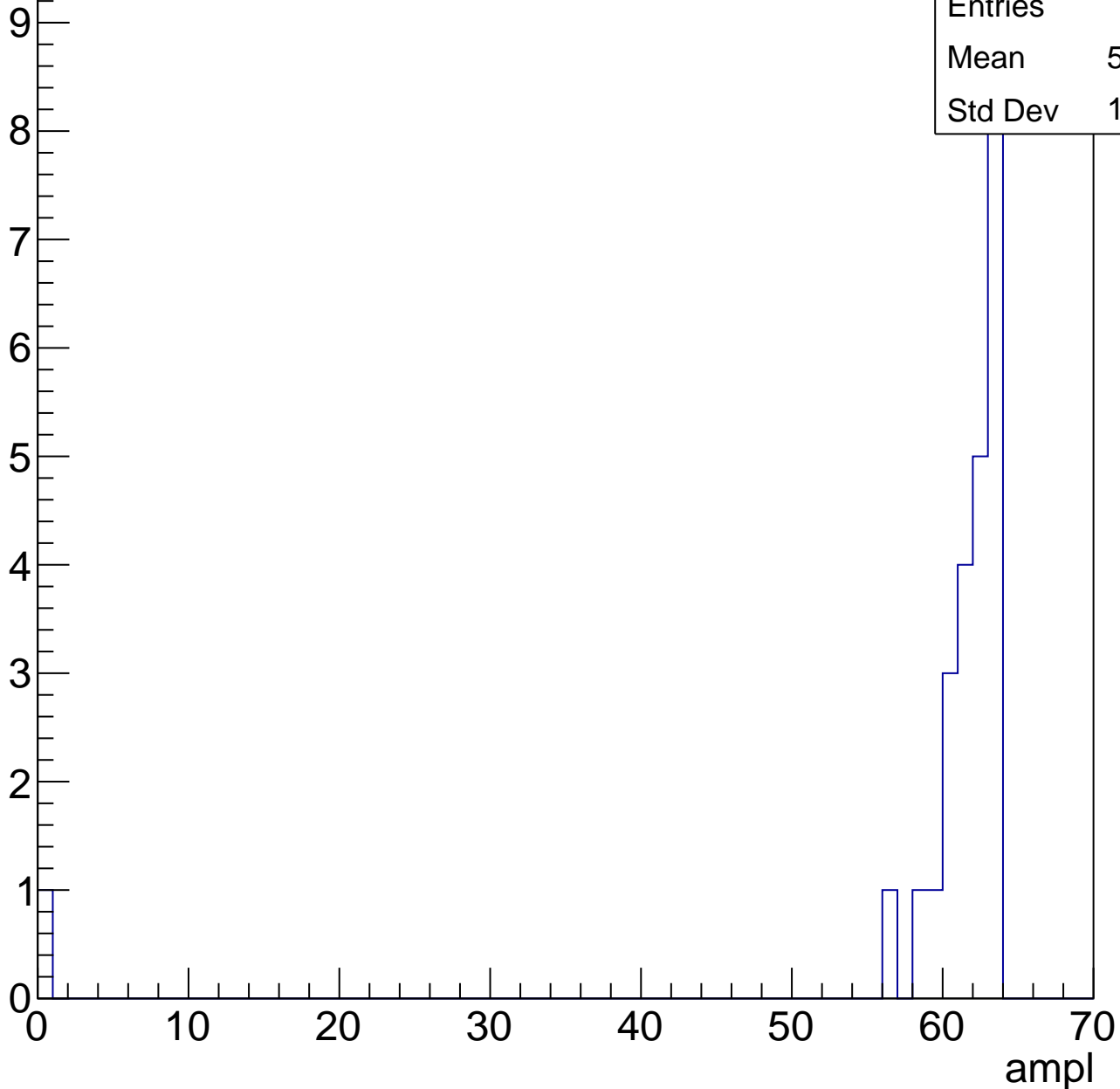


B0L001S, U22-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.96
Std Dev	12.16



B0L001S, U22-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L001S, U22-ch18, adc0

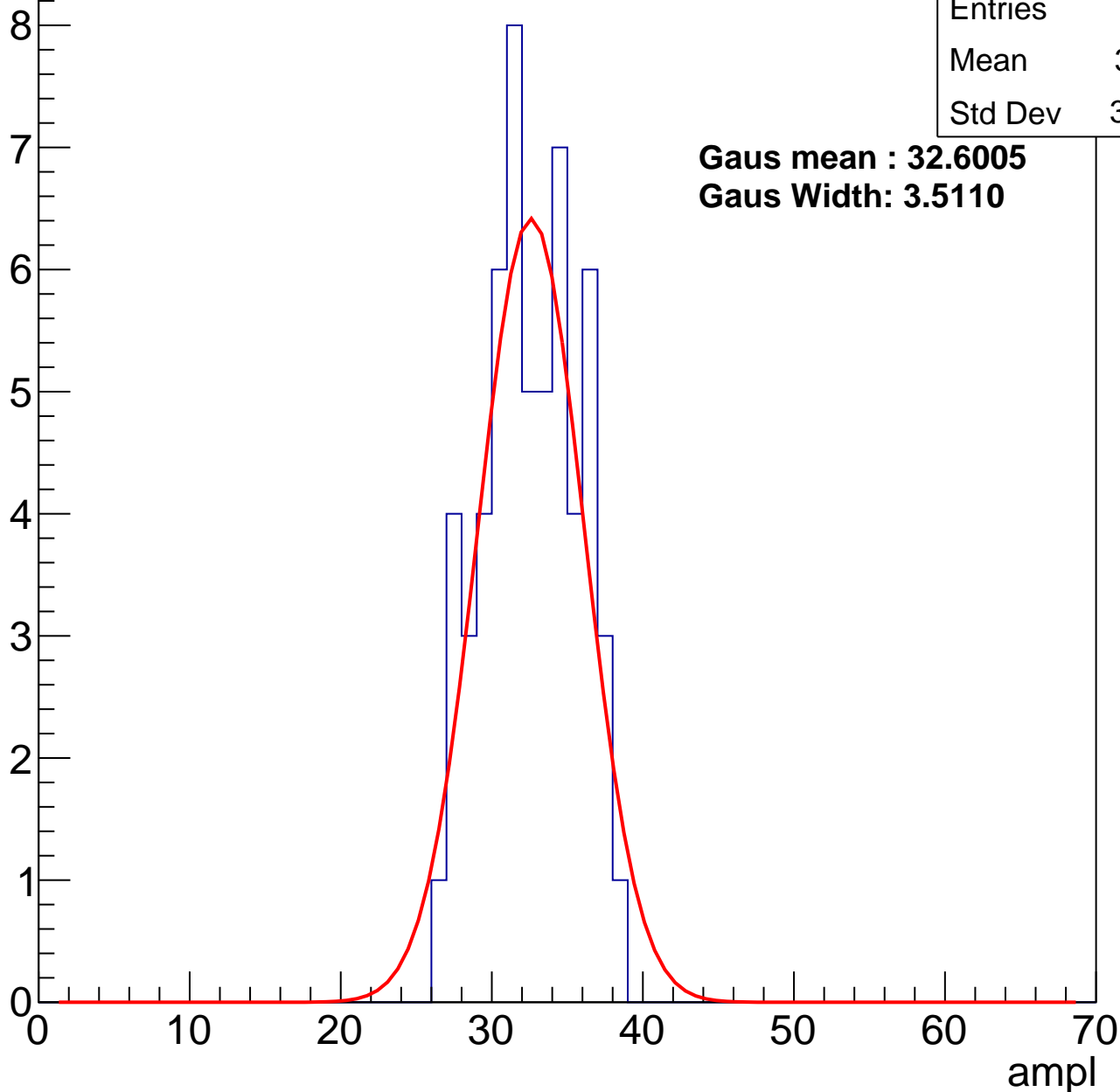
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	32.11
Std Dev	3.042

Gaus mean : 32.6005

Gaus Width: 3.5110



B0L001S, U22-ch18, adc1

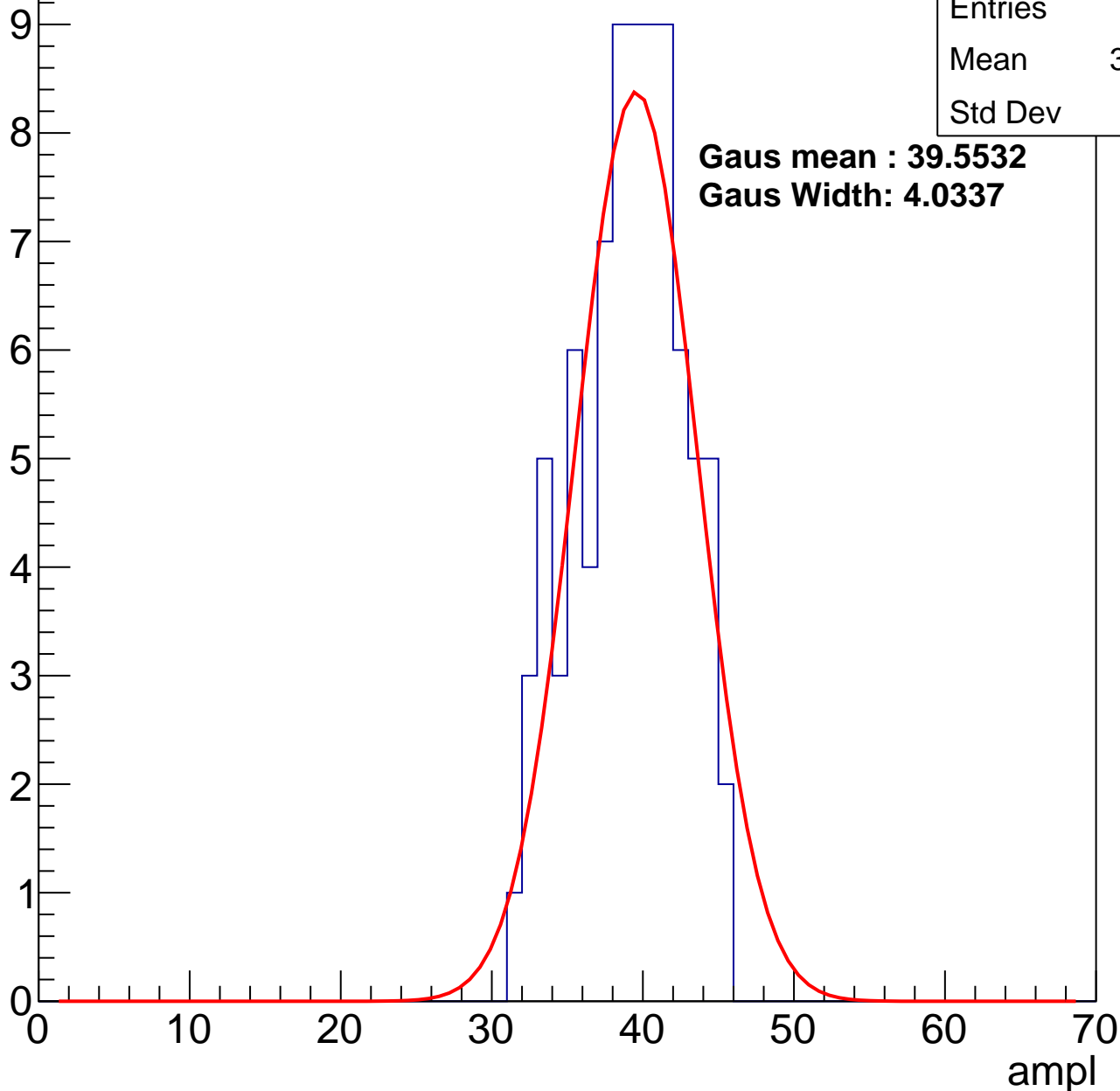
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	38.63
Std Dev	3.47

Gaus mean : 39.5532

Gaus Width: 4.0337



B0L001S, U22-ch18, adc2

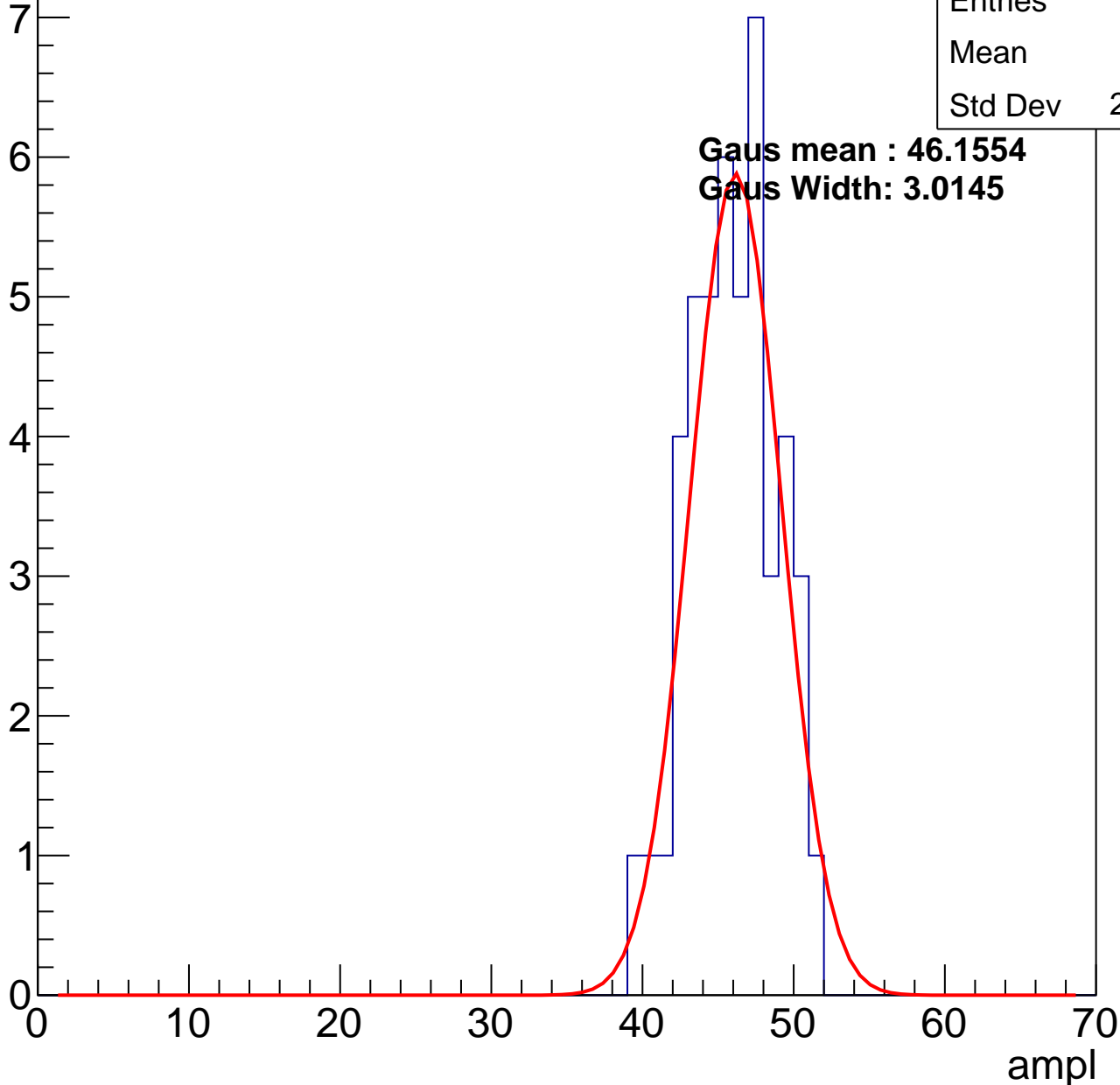
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	45.5
Std Dev	2.803

Gaus mean : 46.1554

Gaus Width: 3.0145

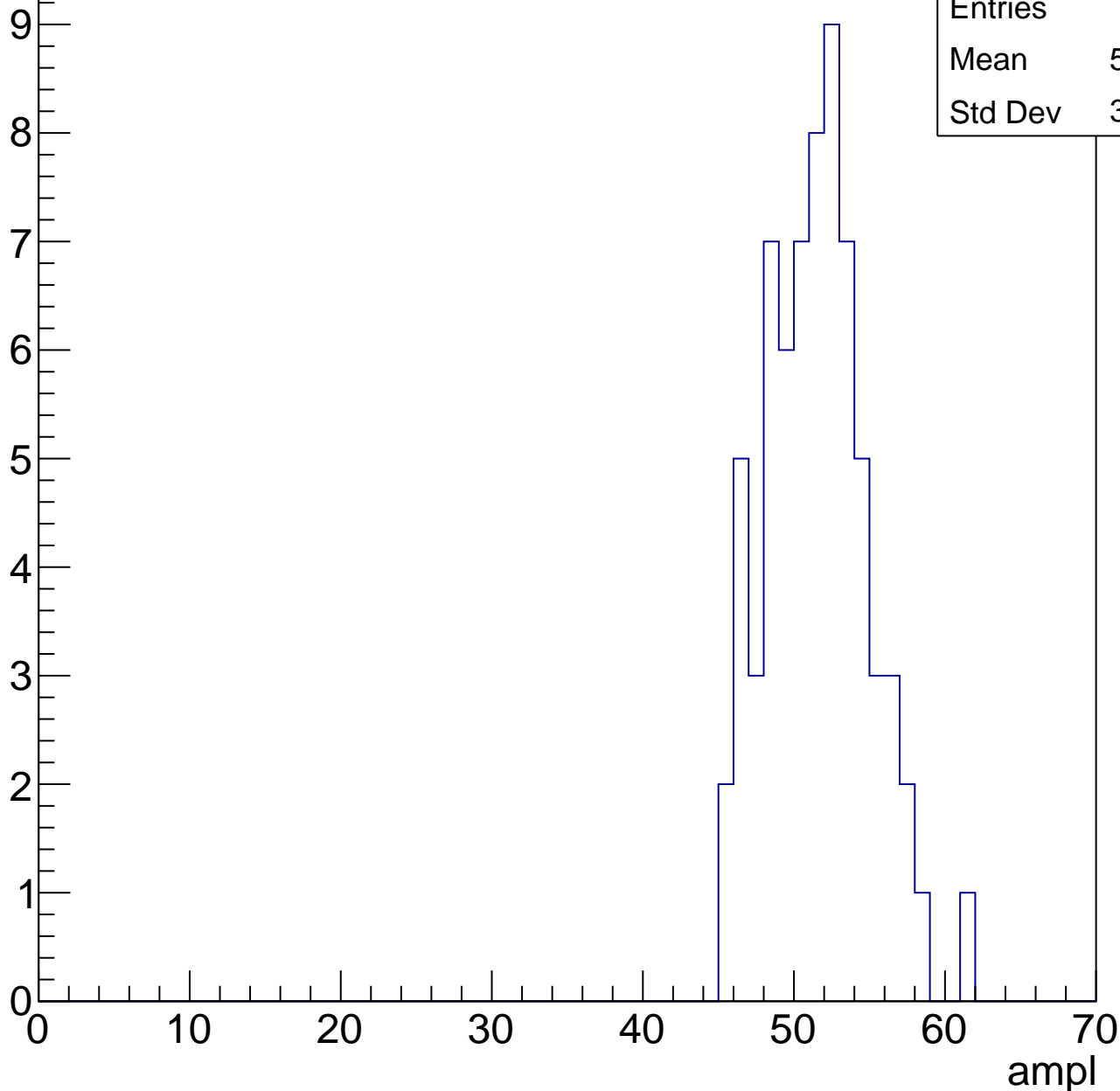


B0L001S, U22-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.07
Std Dev	3.329



B0L001S, U22-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	57.12
Std Dev	7.278

Entry

10

8

6

4

2

0

0

10

20

30

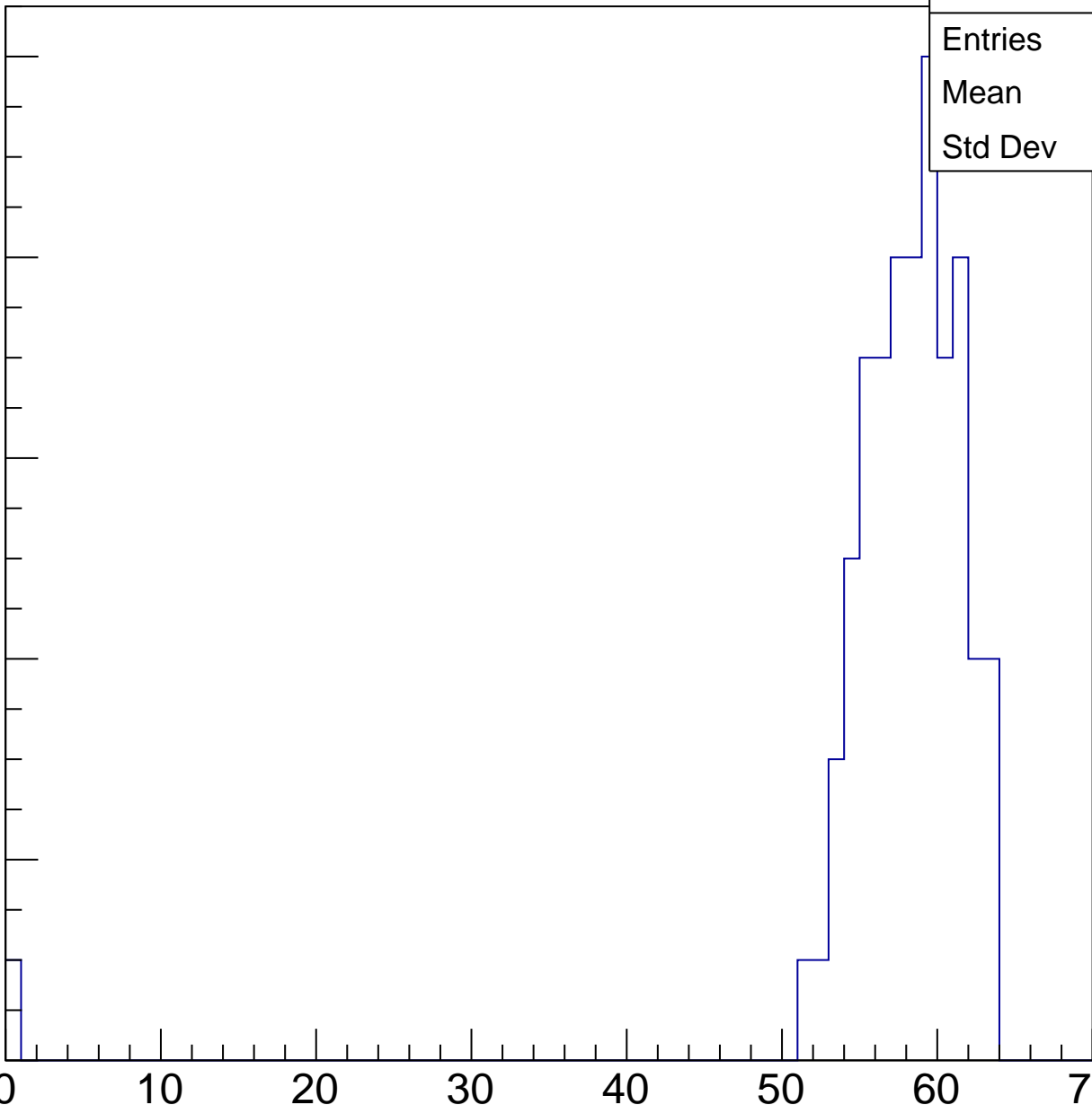
40

50

60

70

ampl

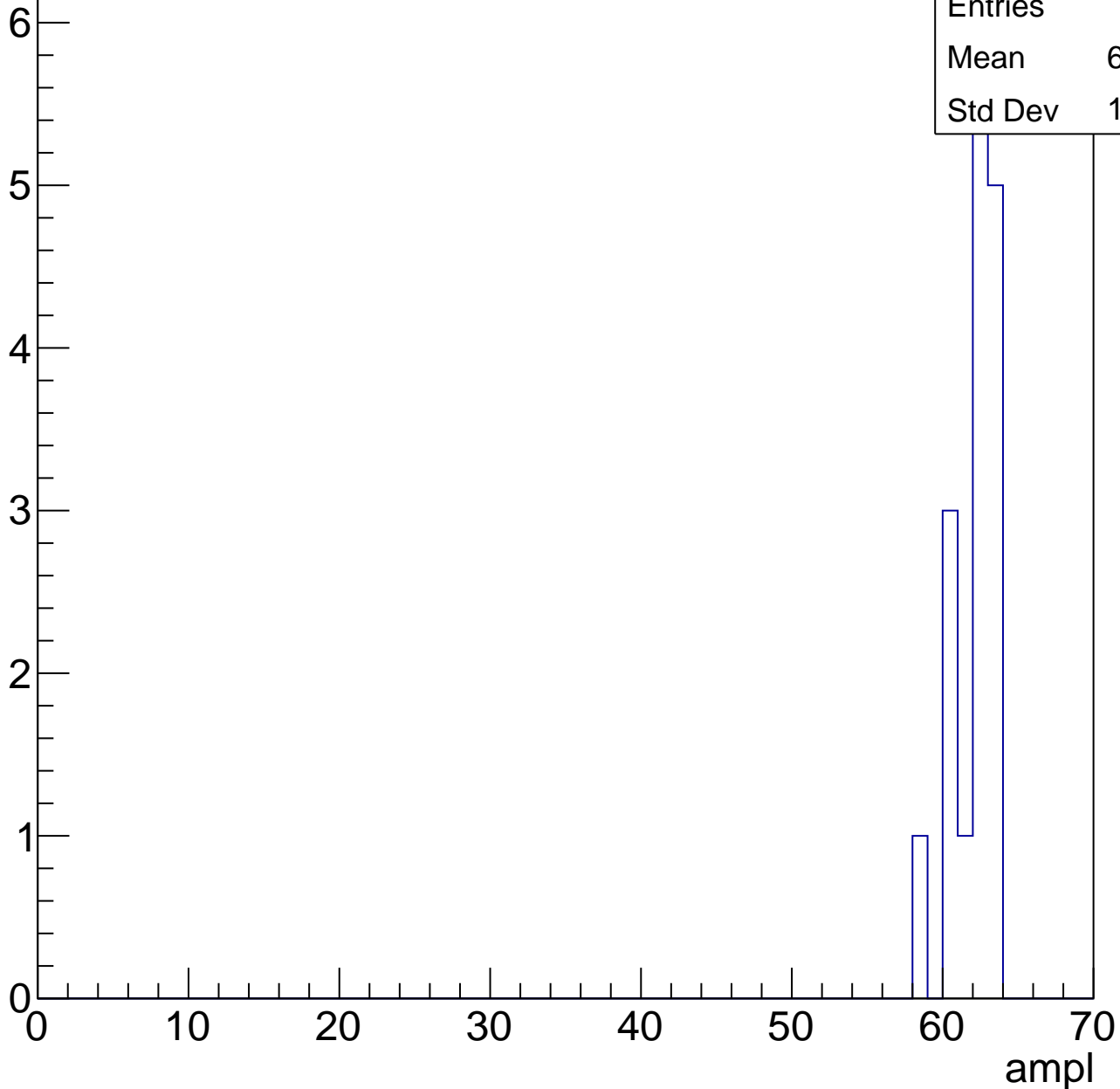


B0L001S, U22-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	61.62
Std Dev	1.409



B0L001S, U22-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch19, adc0

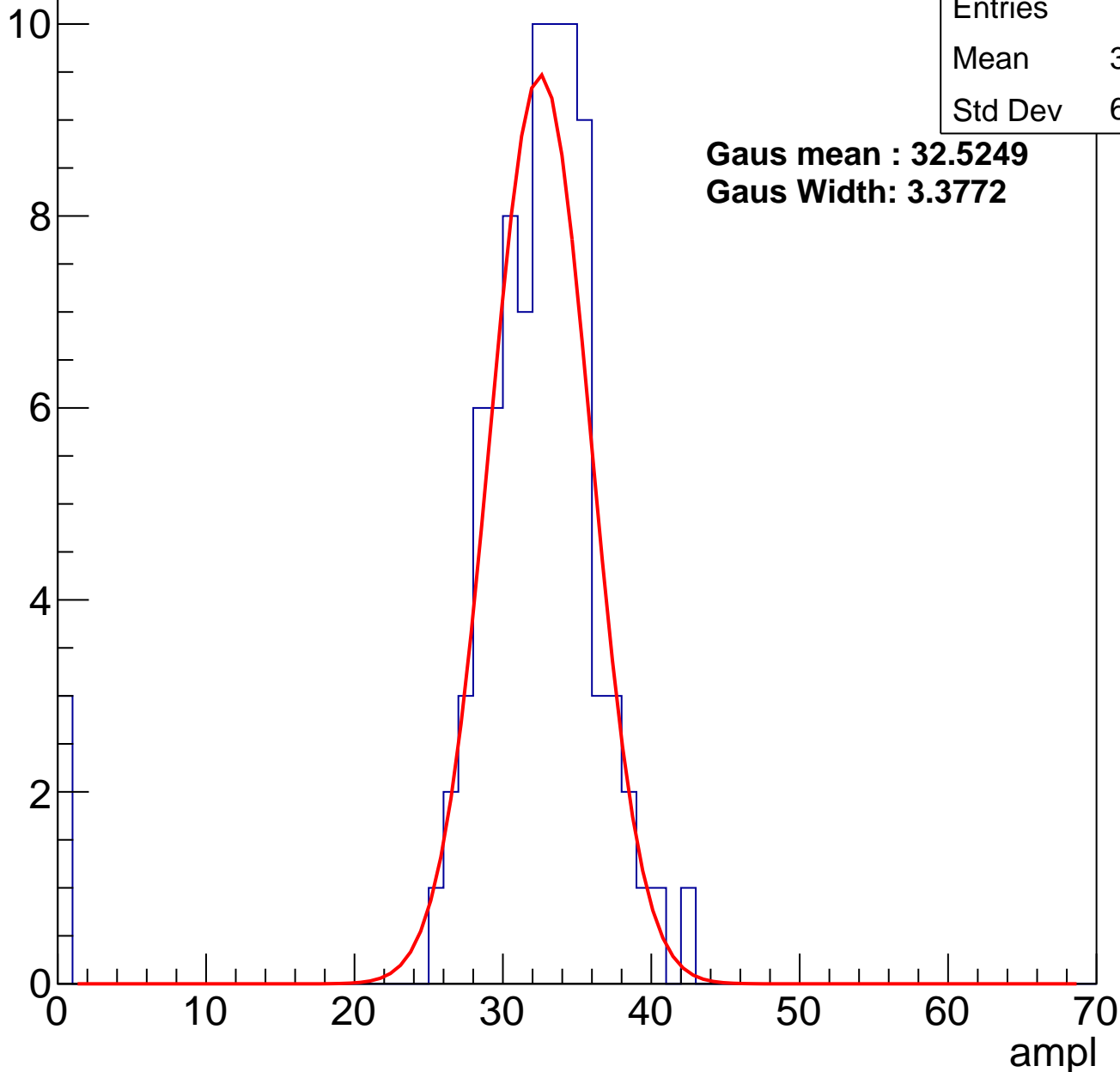
calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	31.14
Std Dev	6.762

Gaus mean : 32.5249

Gaus Width: 3.3772

Entry



B0L001S, U22-ch19, adc1

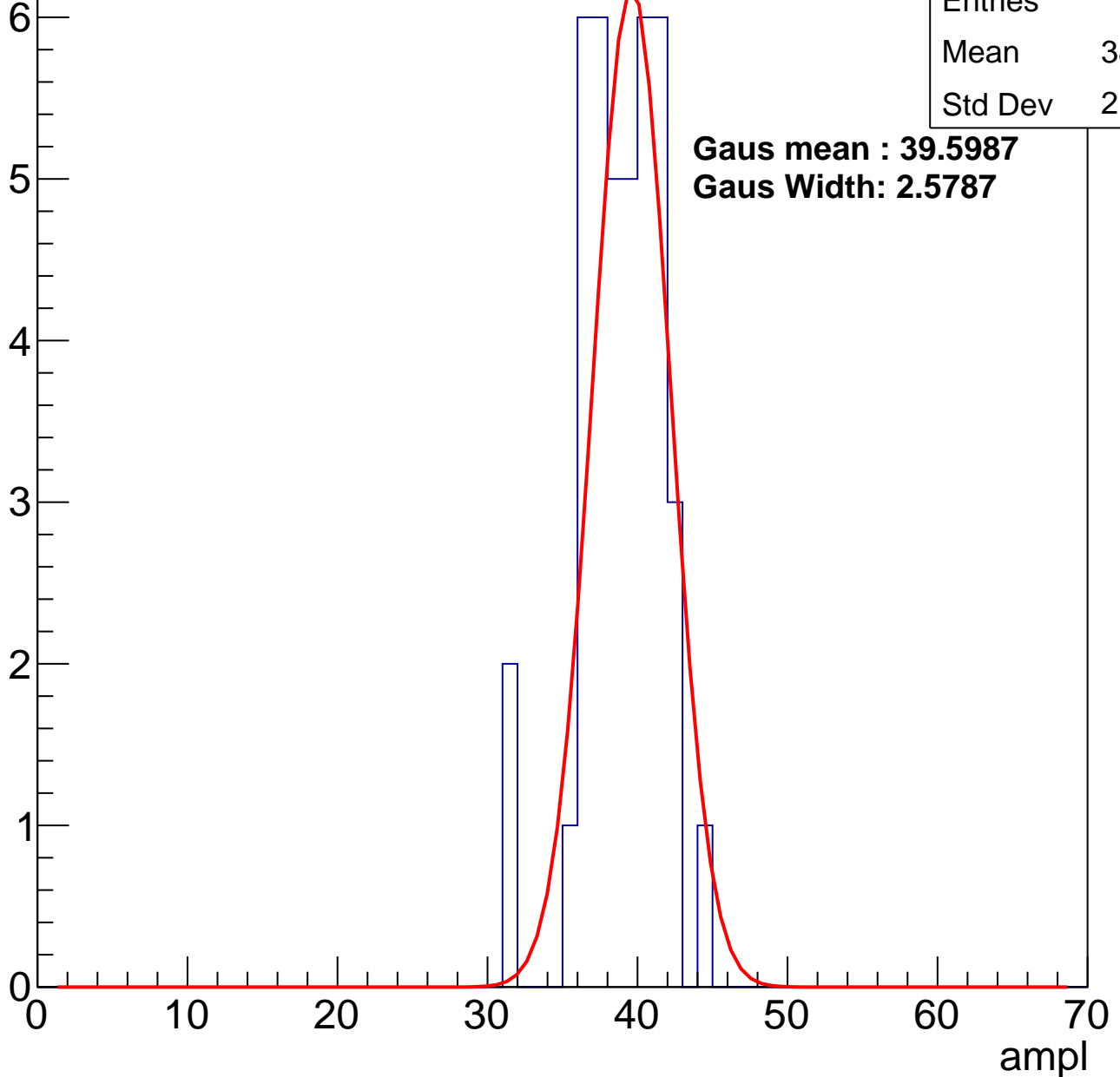
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	38.44
Std Dev	2.687

Gaus mean : 39.5987

Gaus Width: 2.5787



B0L001S, U22-ch19, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	44.26
Std Dev	3.532

Gaus mean : 44.7539

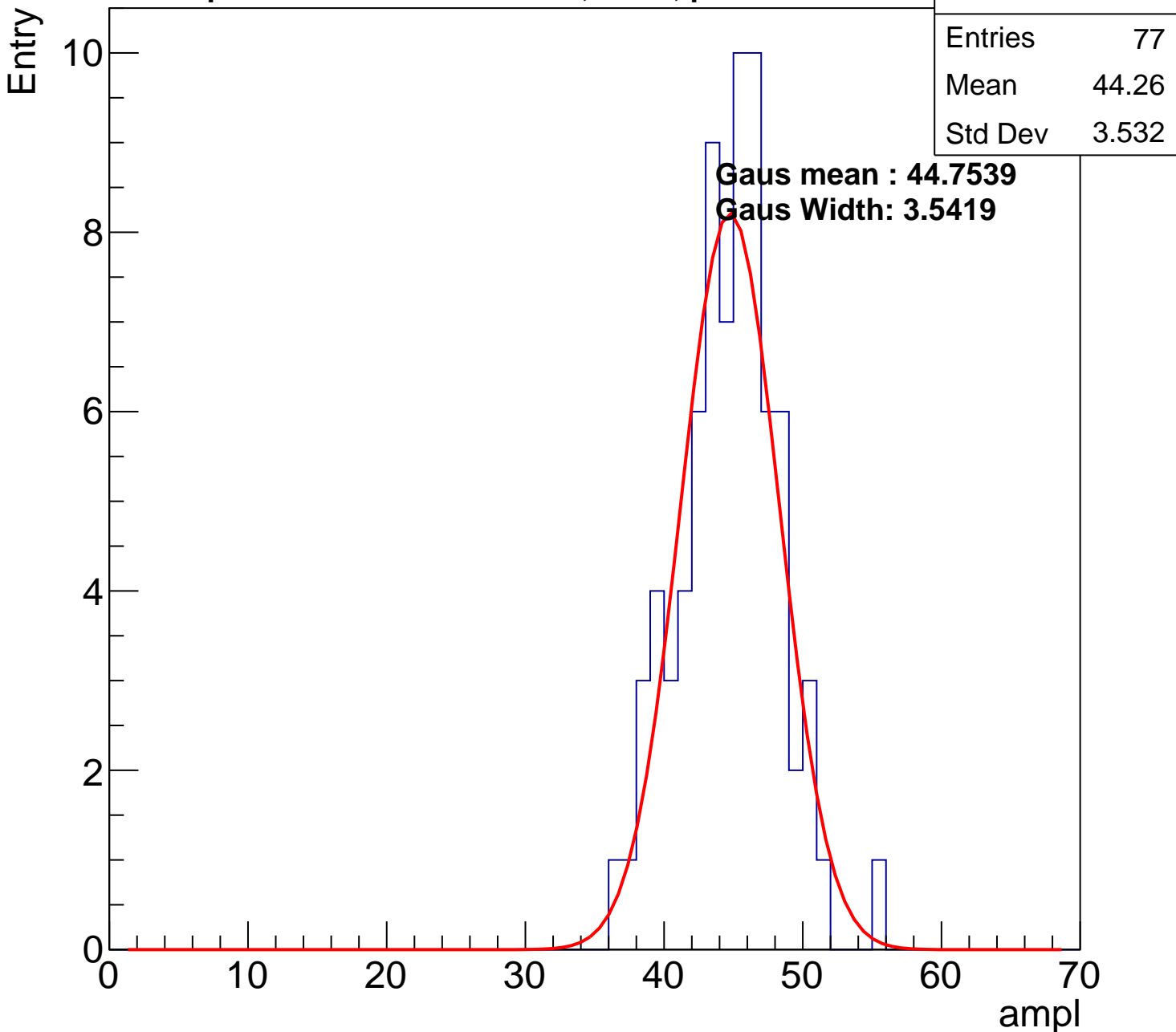
Gaus Width: 3.5419

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

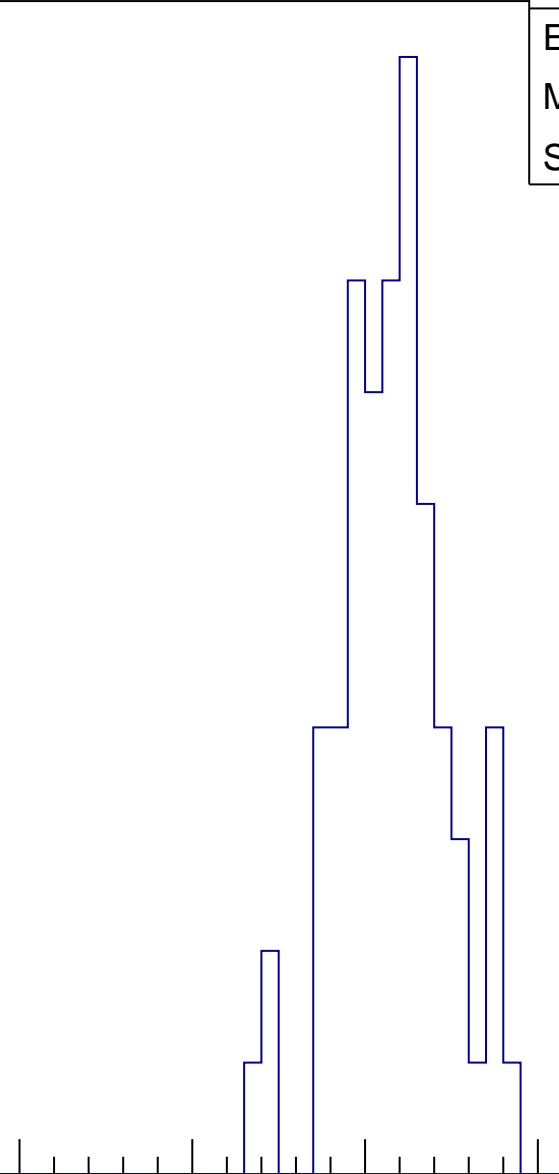
Entries	63
Mean	51.14
Std Dev	3.177

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

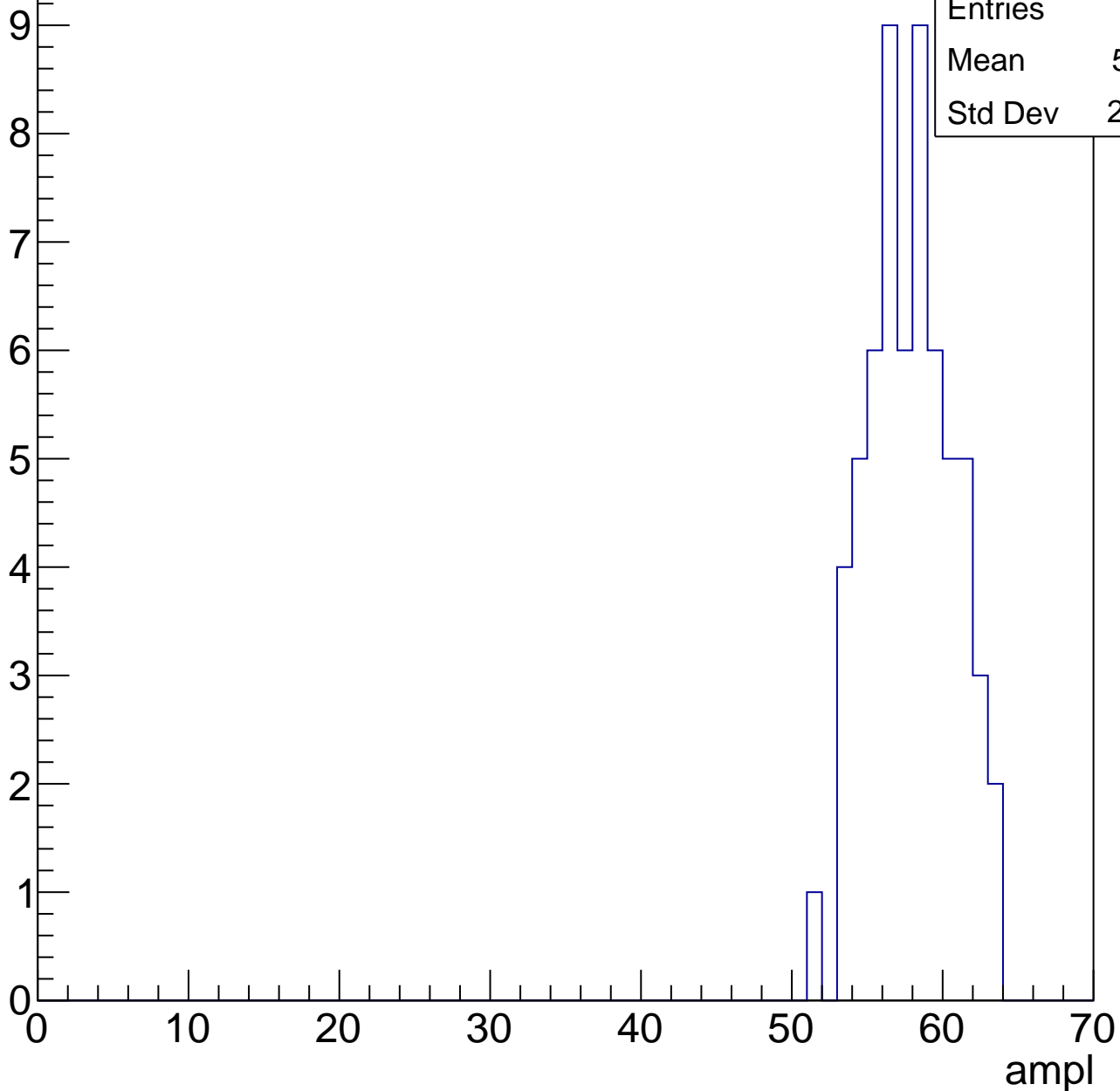


B0L001S, U22-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	57.41
Std Dev	2.784

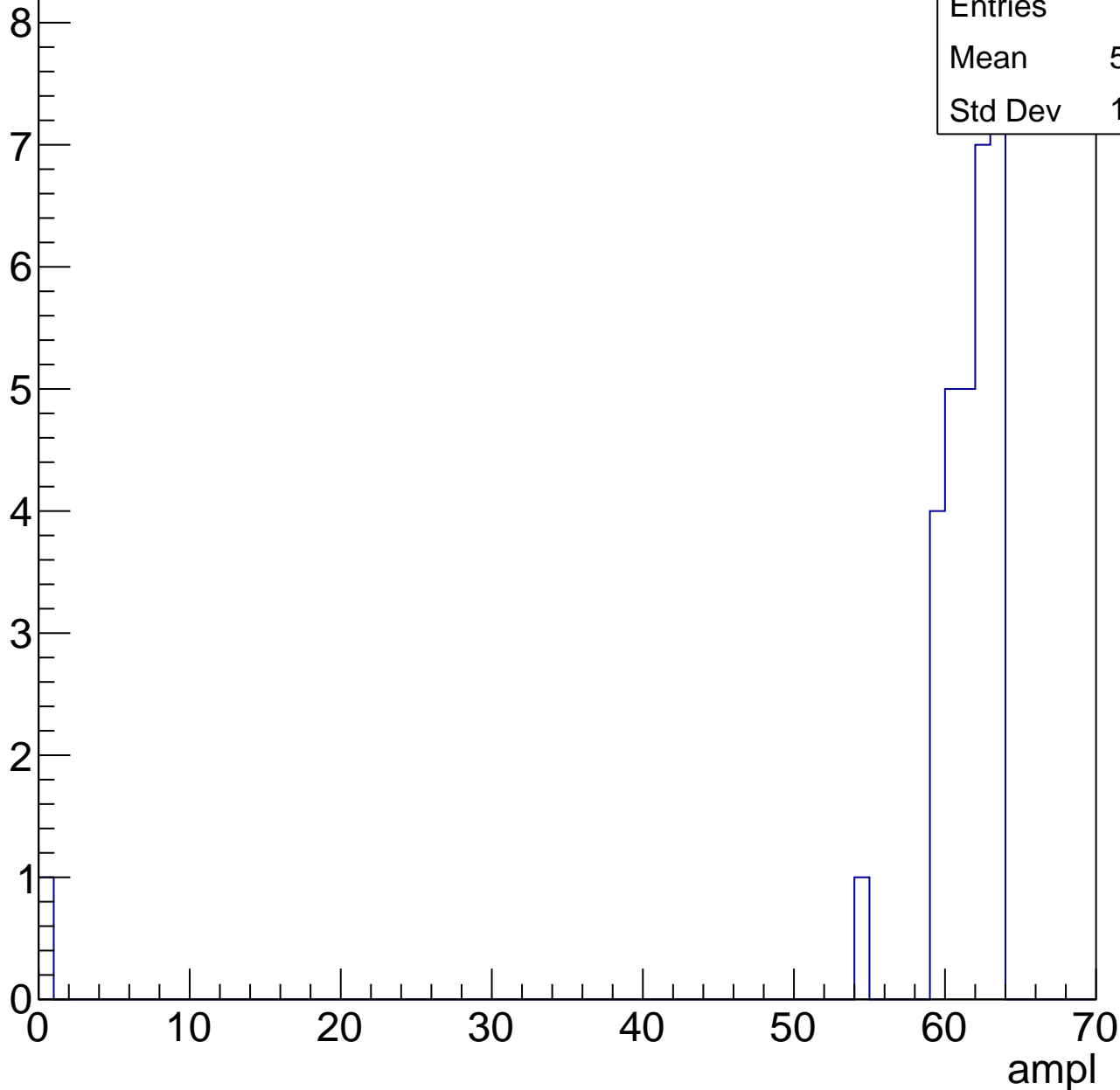


B0L001S, U22-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.13
Std Dev	10.96



B0L001S, U22-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch20, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	84
Mean	30.31
Std Dev	4.978

Gaus mean : 31.3465

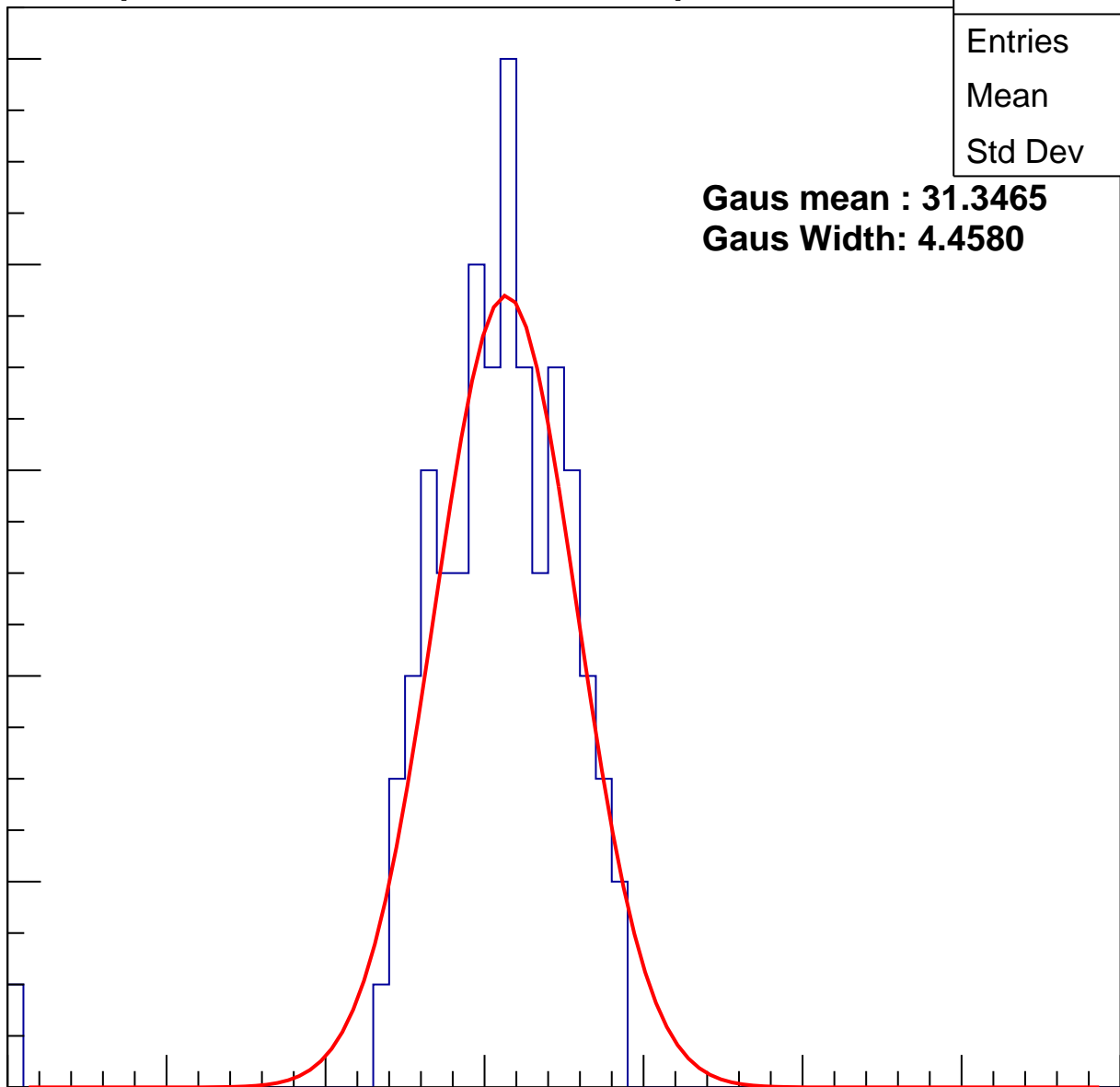
Gaus Width: 4.4580

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch20, adc1

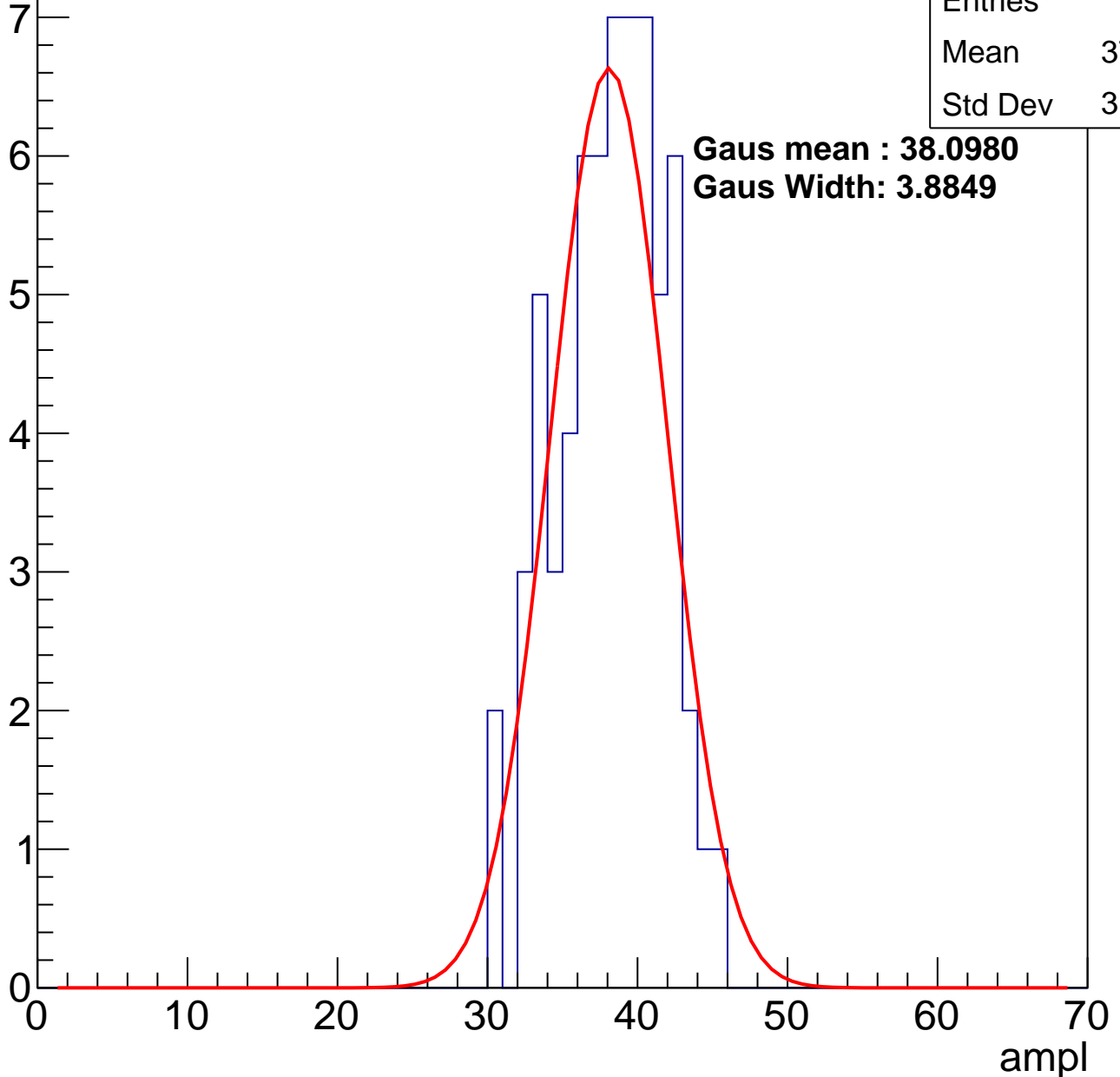
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.72
Std Dev	3.466

Gaus mean : 38.0980

Gaus Width: 3.8849



B0L001S, U22-ch20, adc2

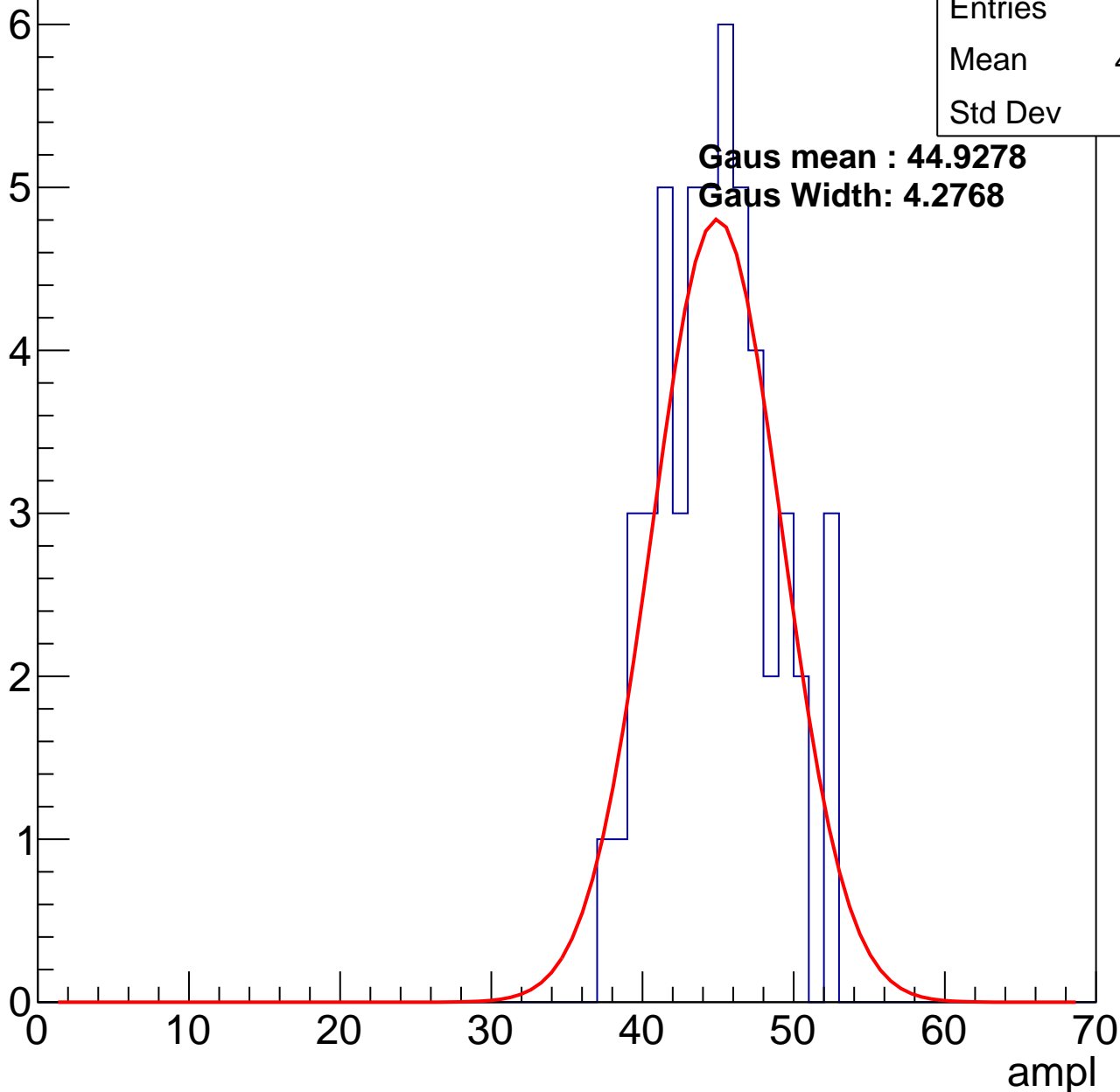
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.41
Std Dev	3.69

Gaus mean : 44.9278

Gaus Width: 4.2768

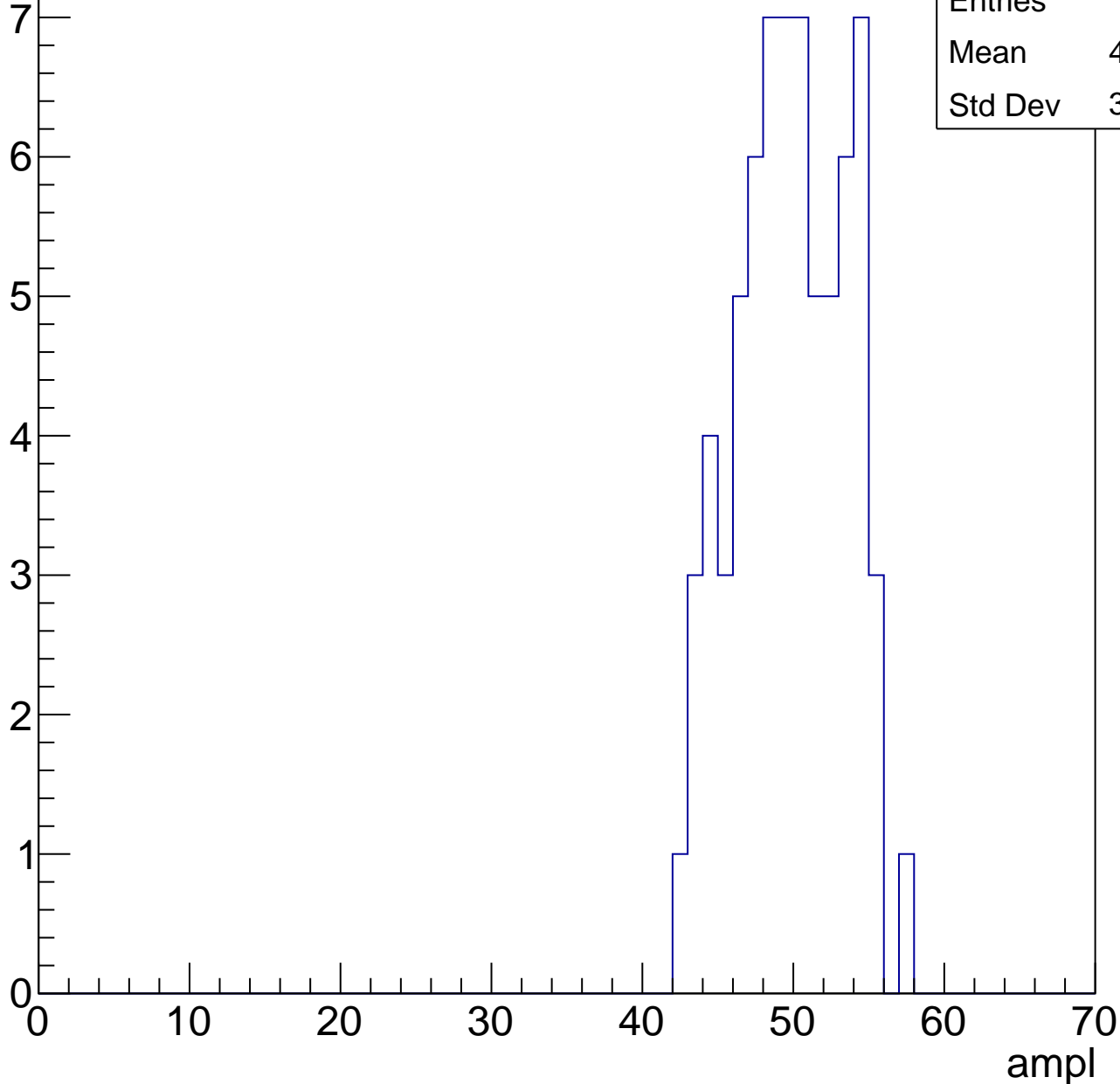


B0L001S, U22-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.37
Std Dev	3.558

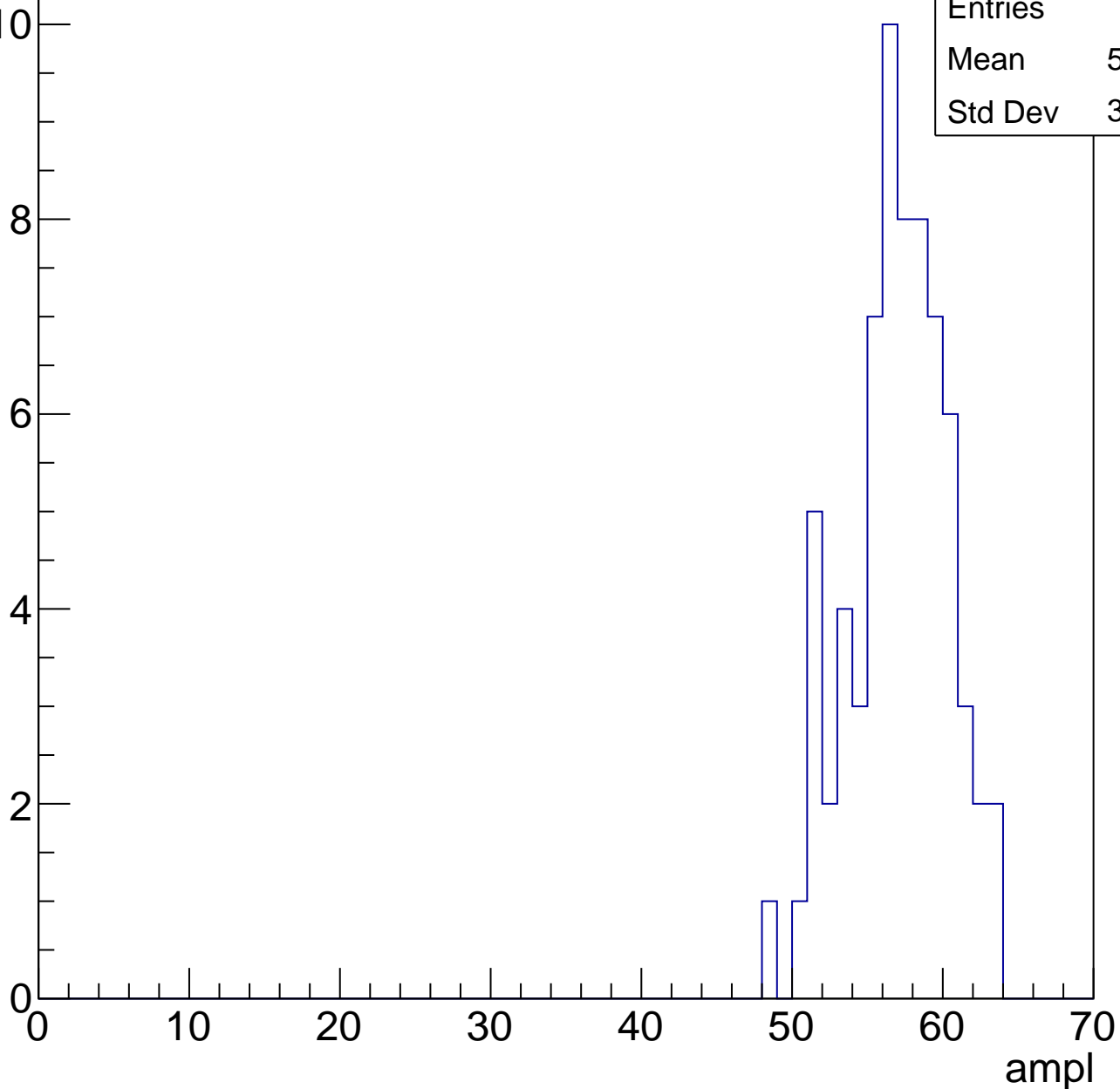


B0L001S, U22-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	56.55
Std Dev	3.264

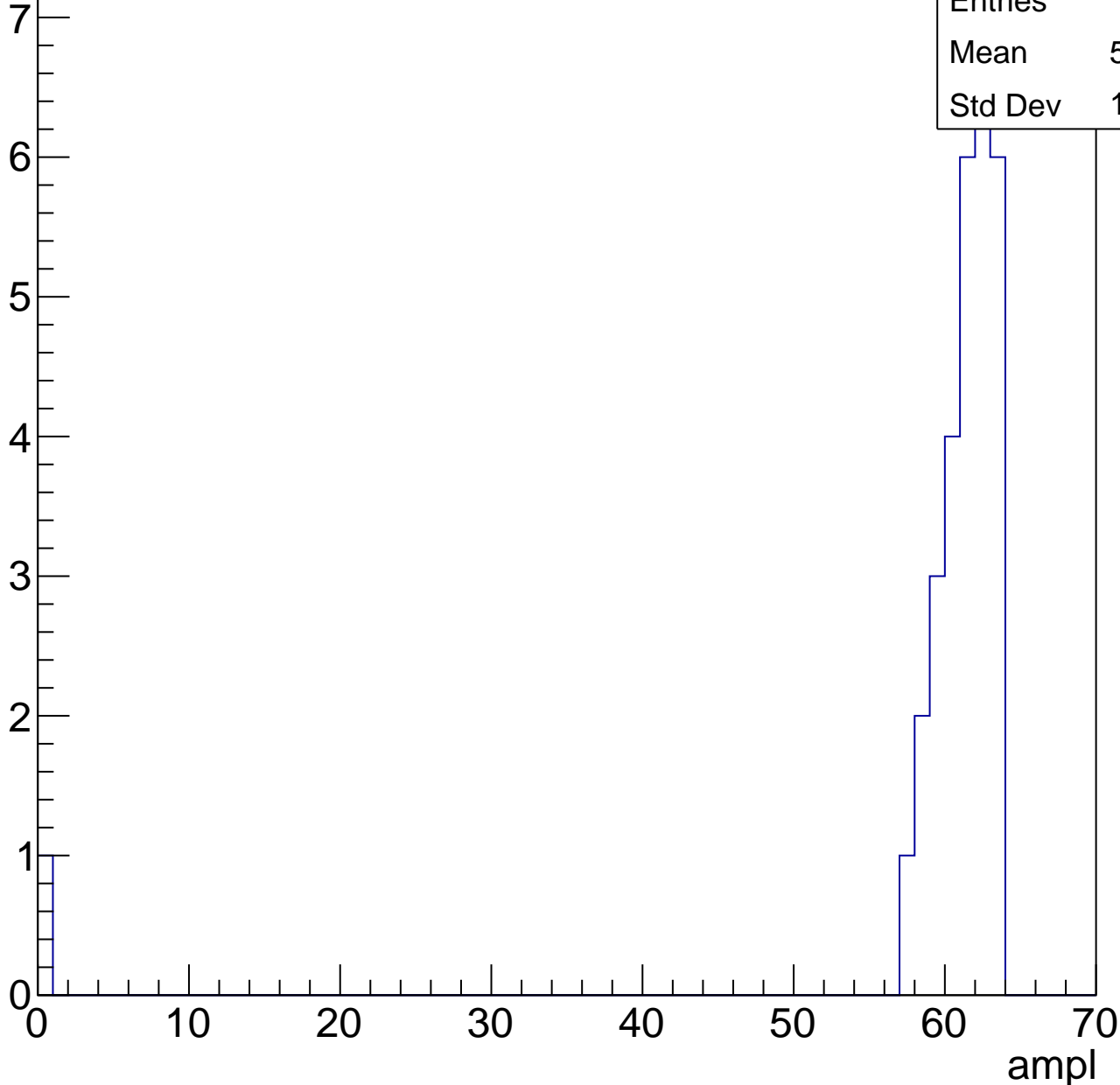


B0L001S, U22-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.93
Std Dev	11.07



B0L001S, U22-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch21, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	99
Mean	30.95
Std Dev	4.253

Gaus mean : 31.3453

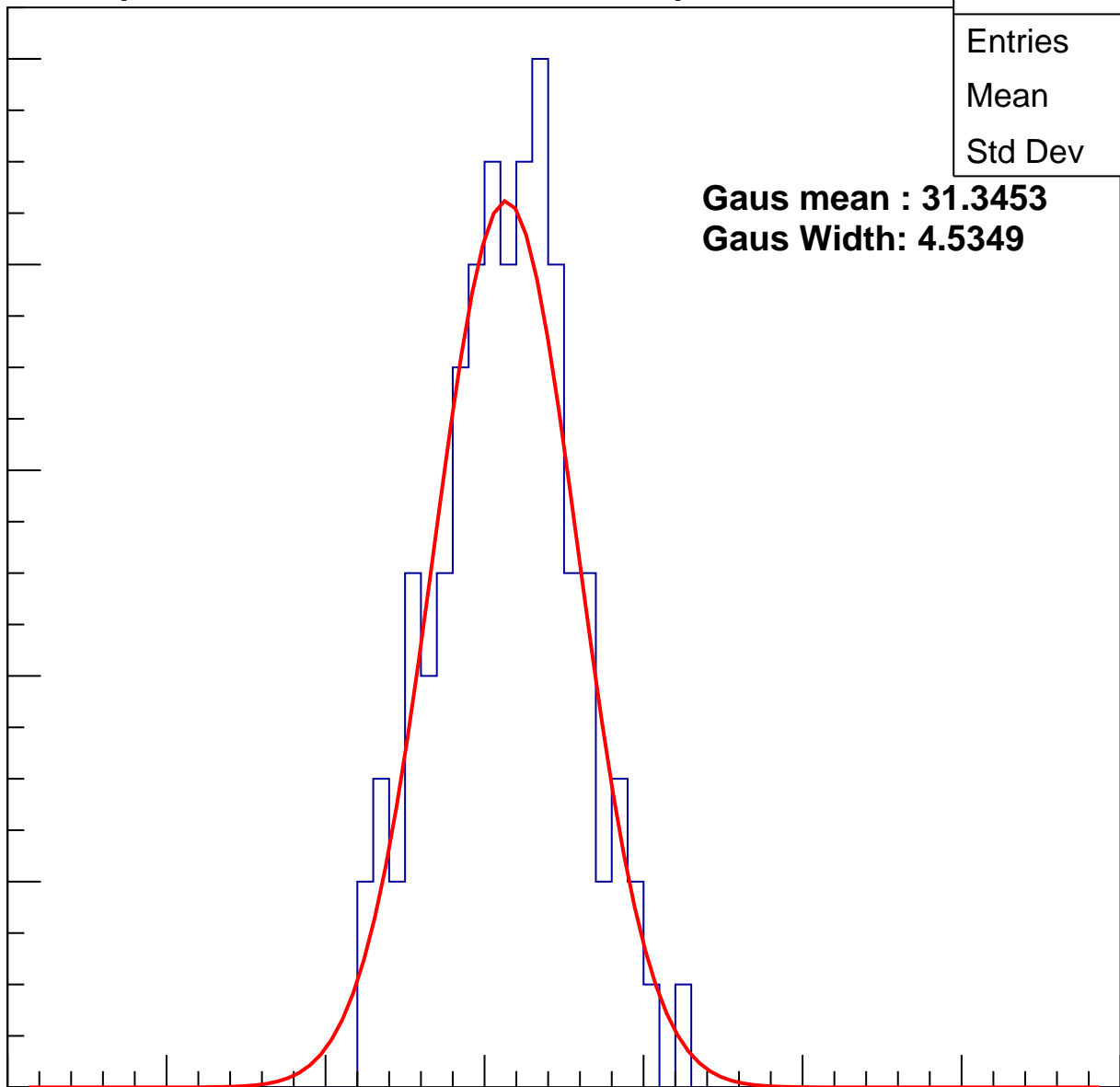
Gaus Width: 4.5349

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



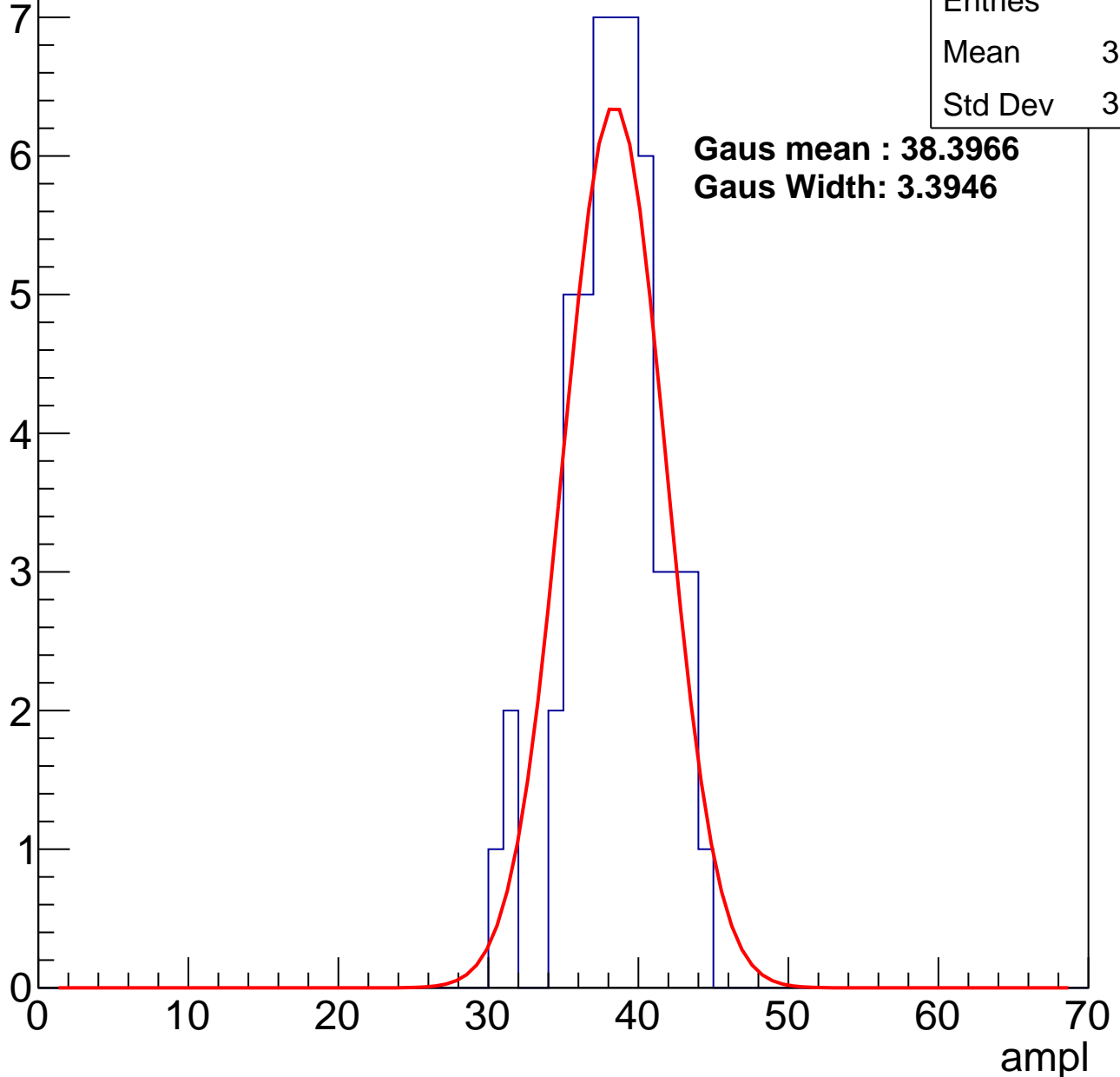
B0L001S, U22-ch21, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	37.98
Std Dev	3.048

Gaus mean : 38.3966
Gaus Width: 3.3946



B0L001S, U22-ch21, adc2

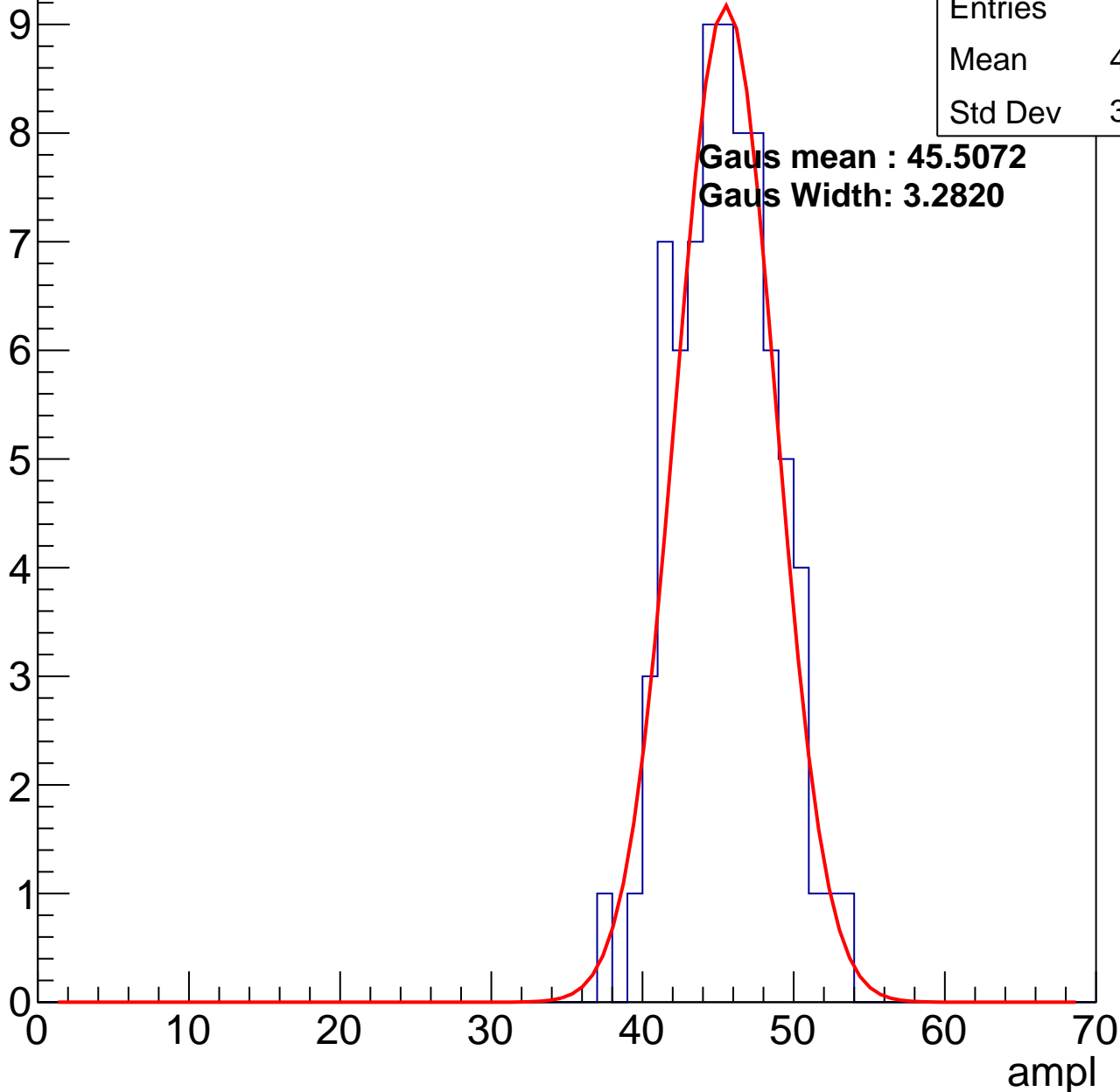
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	45.06
Std Dev	3.225

Gaus mean : 45.5072

Gaus Width: 3.2820

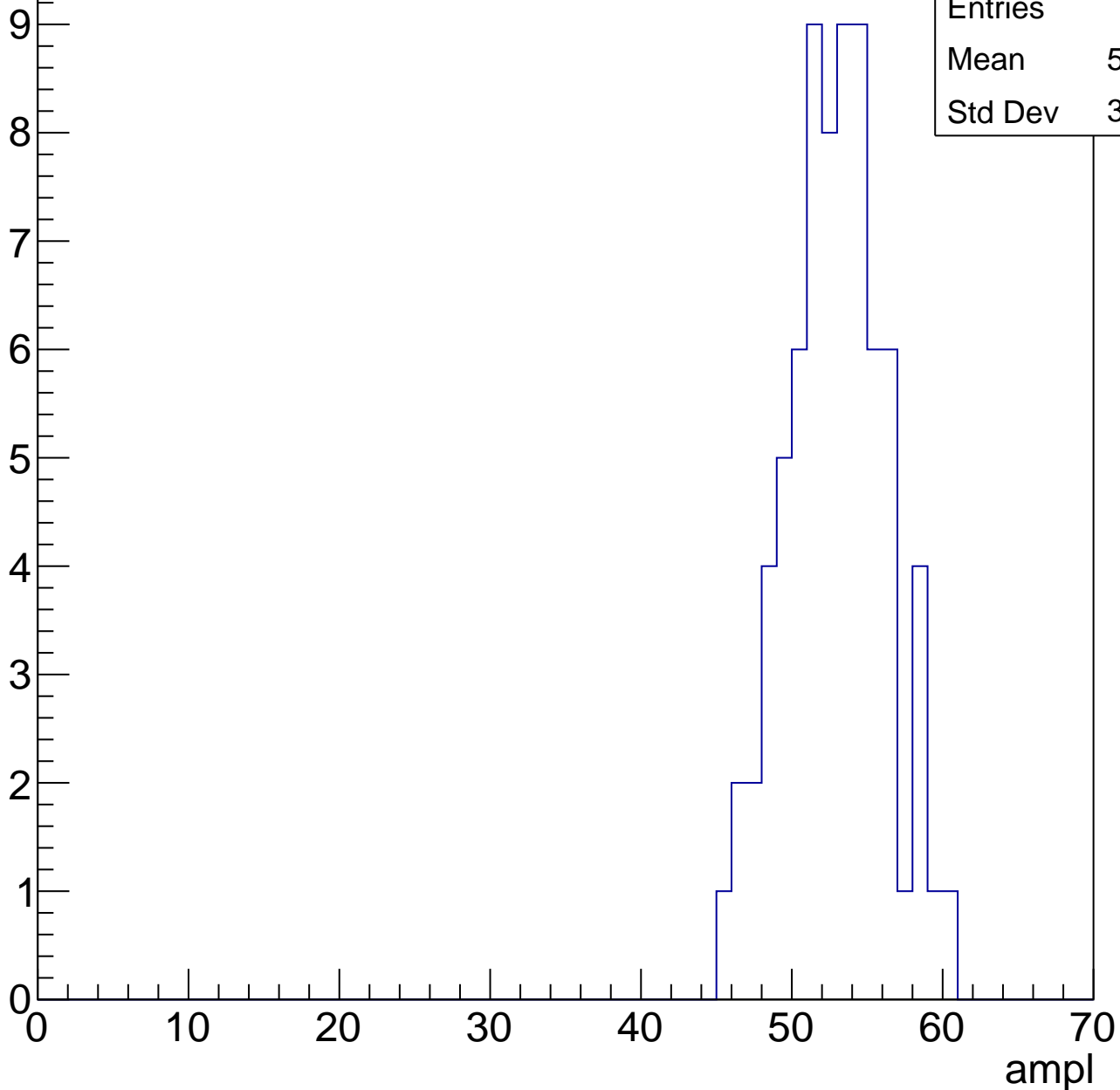


B0L001S, U22-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

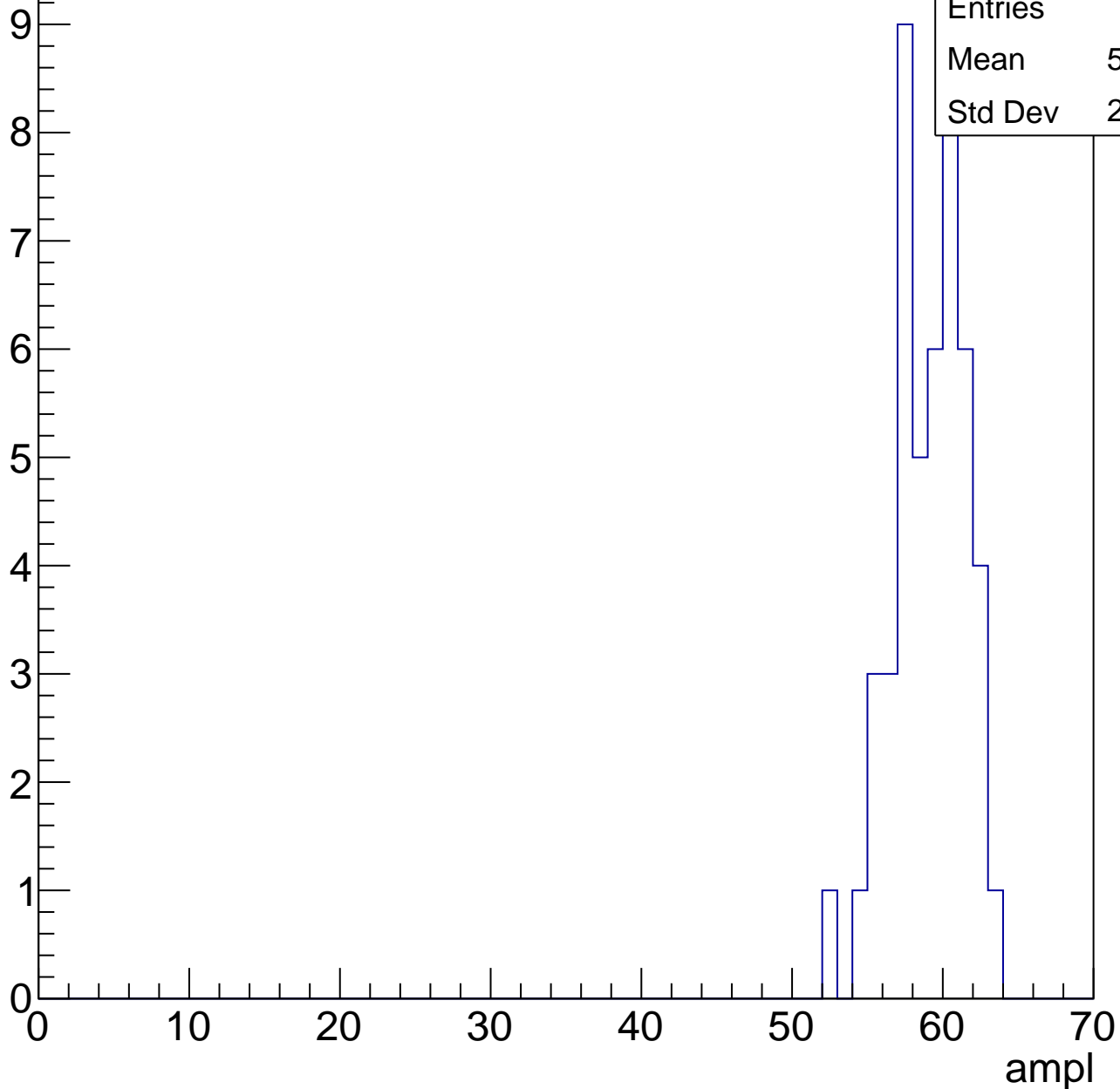
Entries	74
Mean	52.43
Std Dev	3.239



B0L001S, U22-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



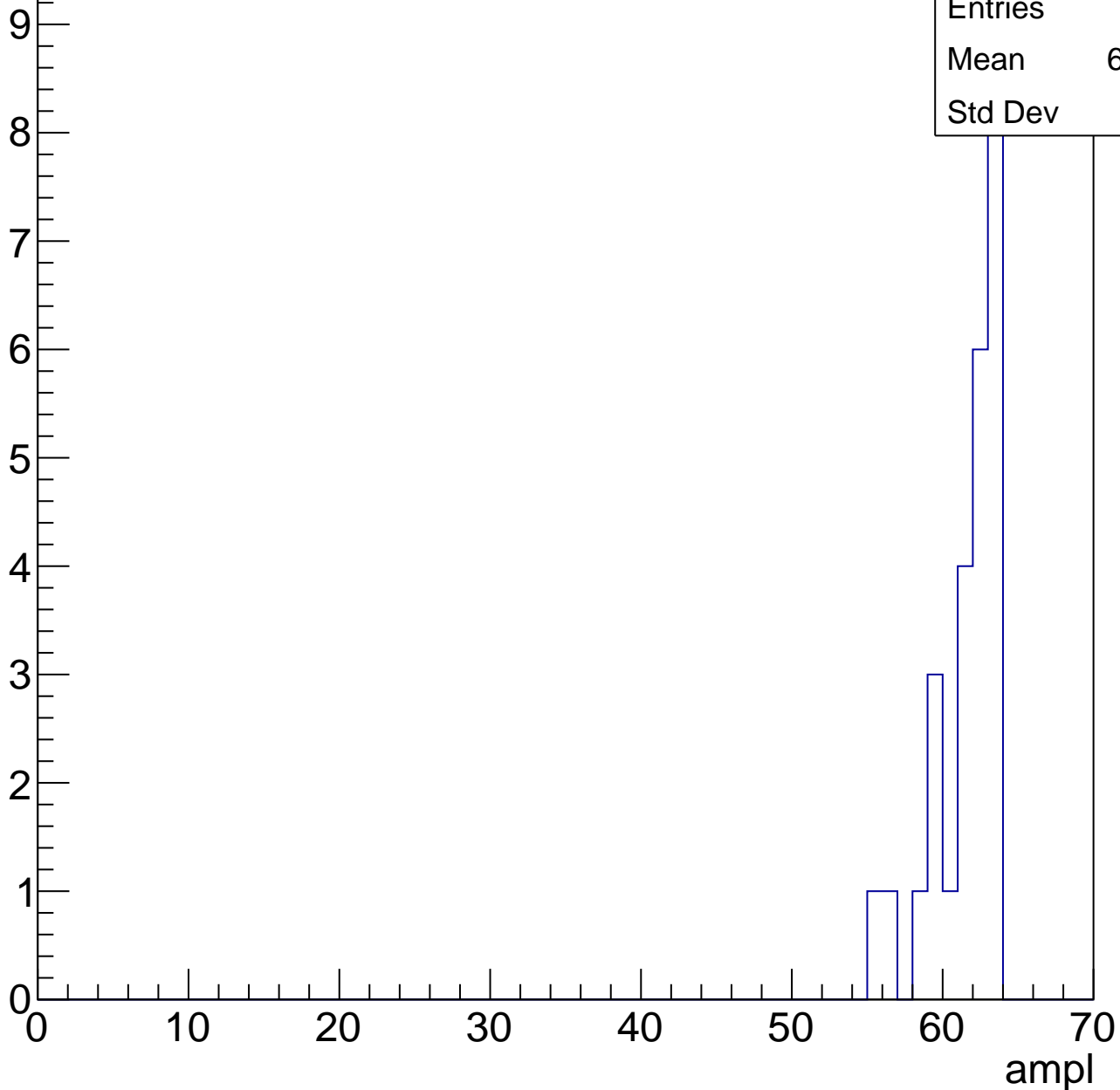
Entries	47
Mean	58.57
Std Dev	2.377

B0L001S, U22-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	61.12
Std Dev	2.19



B0L001S, U22-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch22, adc0

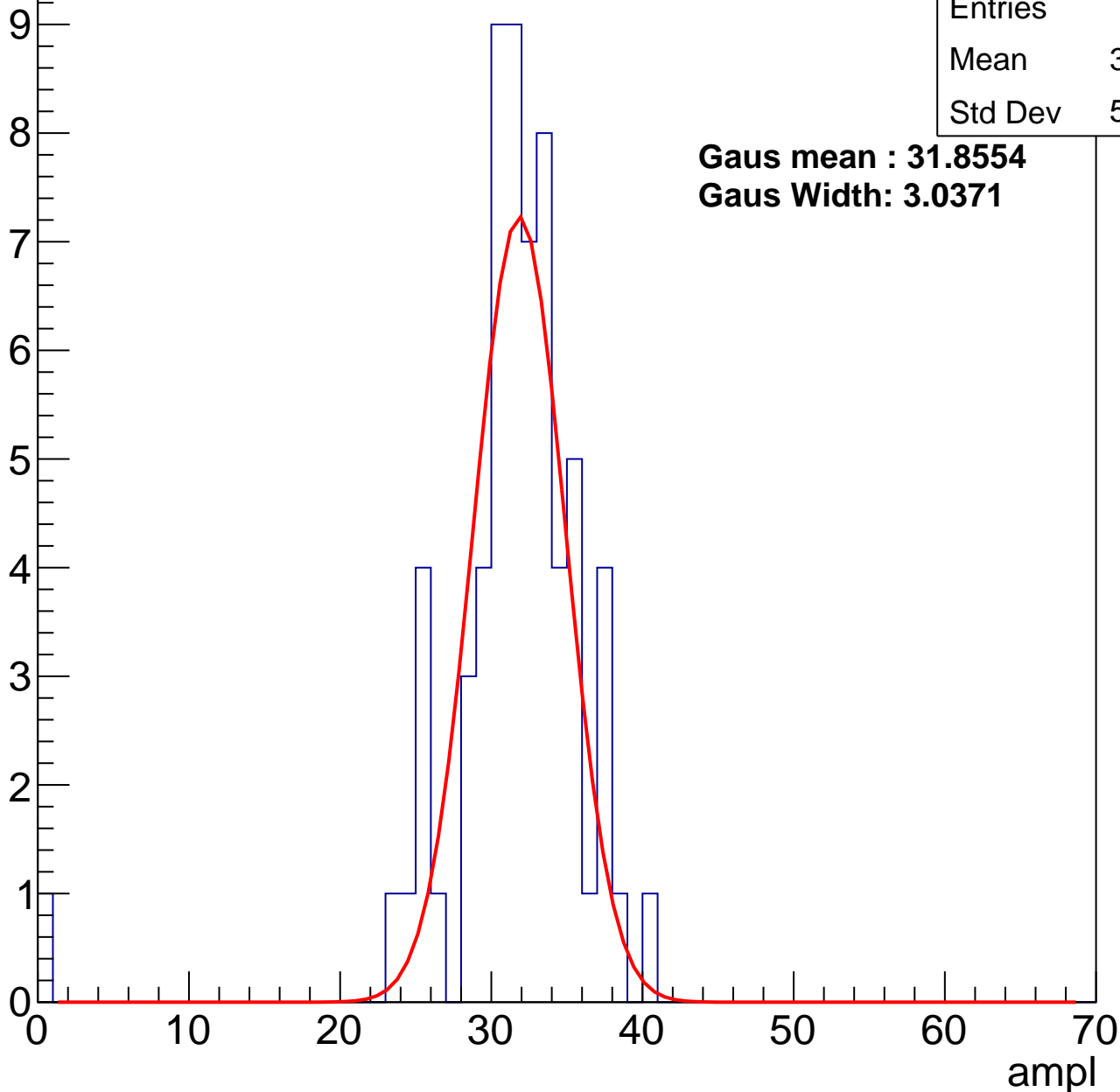
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.98
Std Dev	5.228

Gaus mean : 31.8554

Gaus Width: 3.0371



B0L001S, U22-ch22, adc1

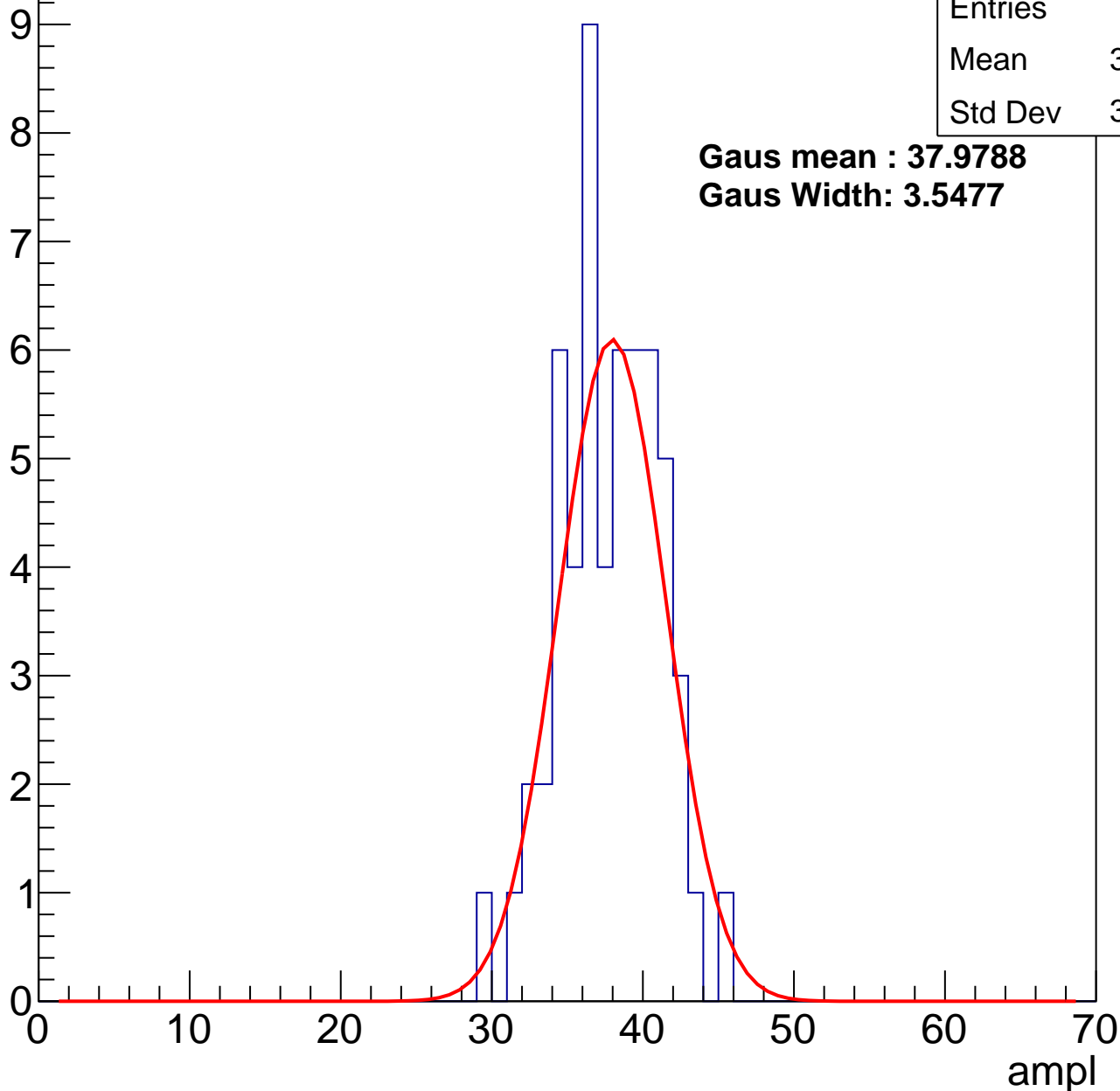
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.32
Std Dev	3.229

Gaus mean : 37.9788

Gaus Width: 3.5477



B0L001S, U22-ch22, adc2

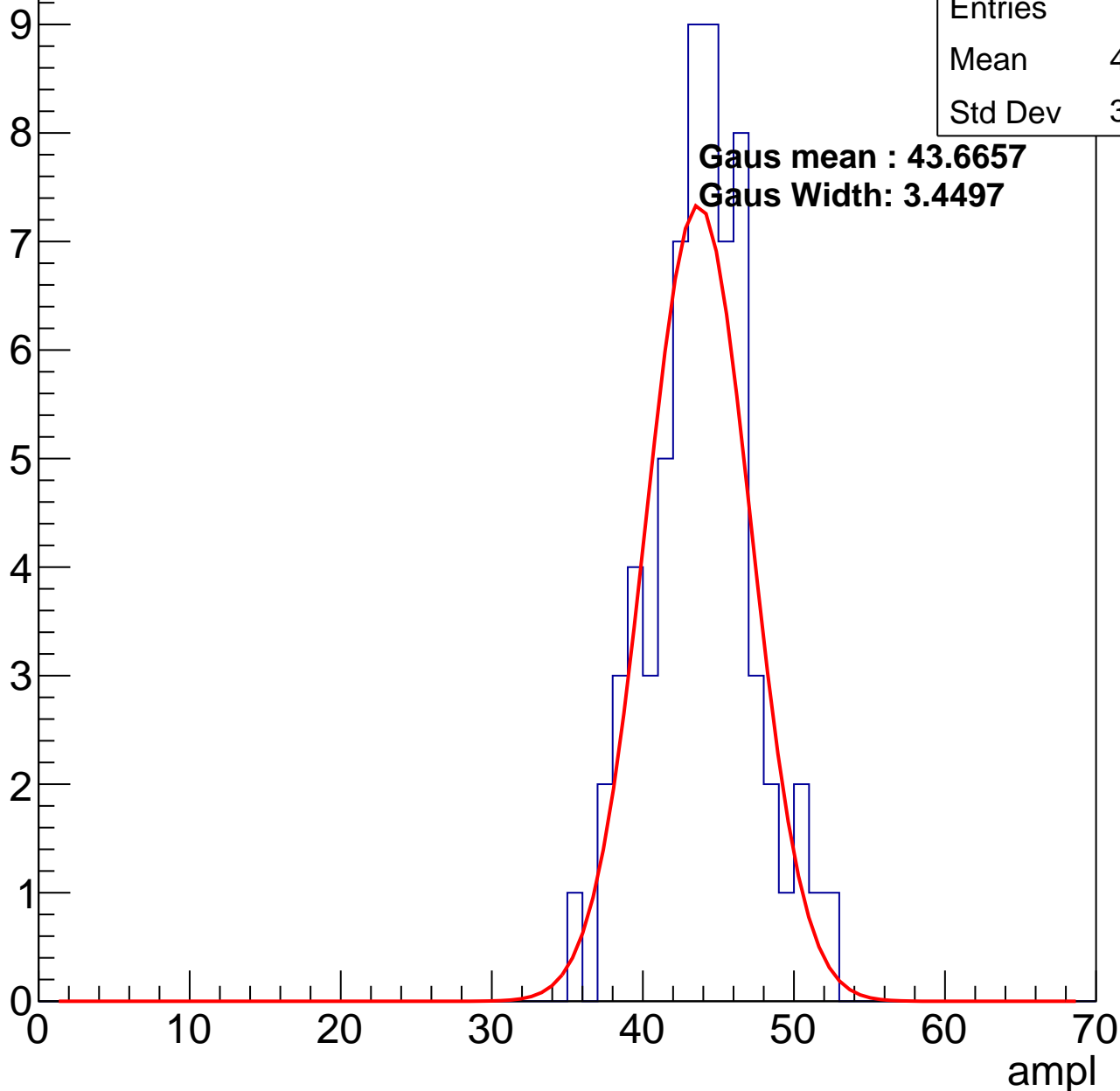
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.43
Std Dev	3.436

Gaus mean : 43.6657

Gaus Width: 3.4497

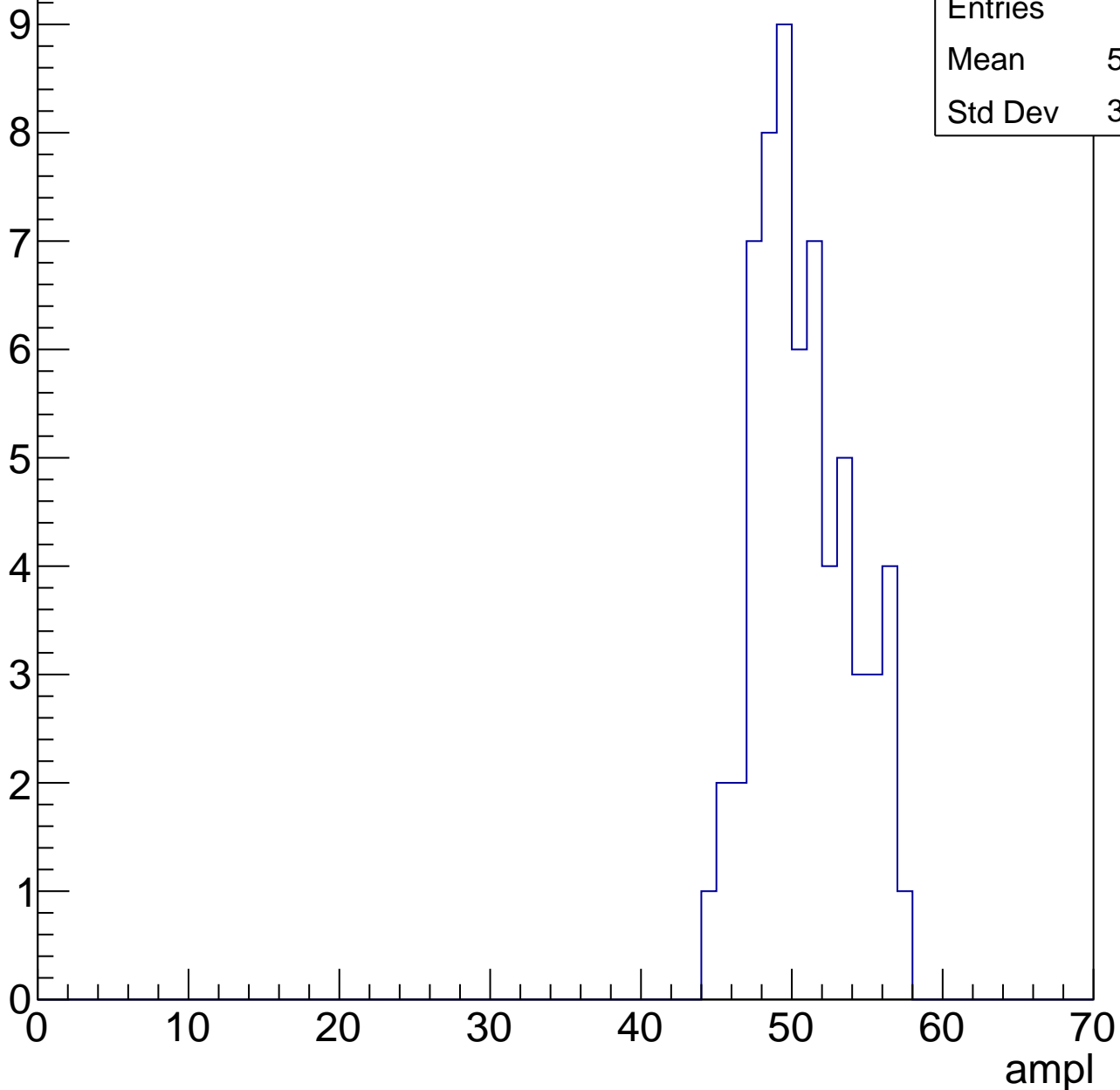


B0L001S, U22-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.29
Std Dev	3.113

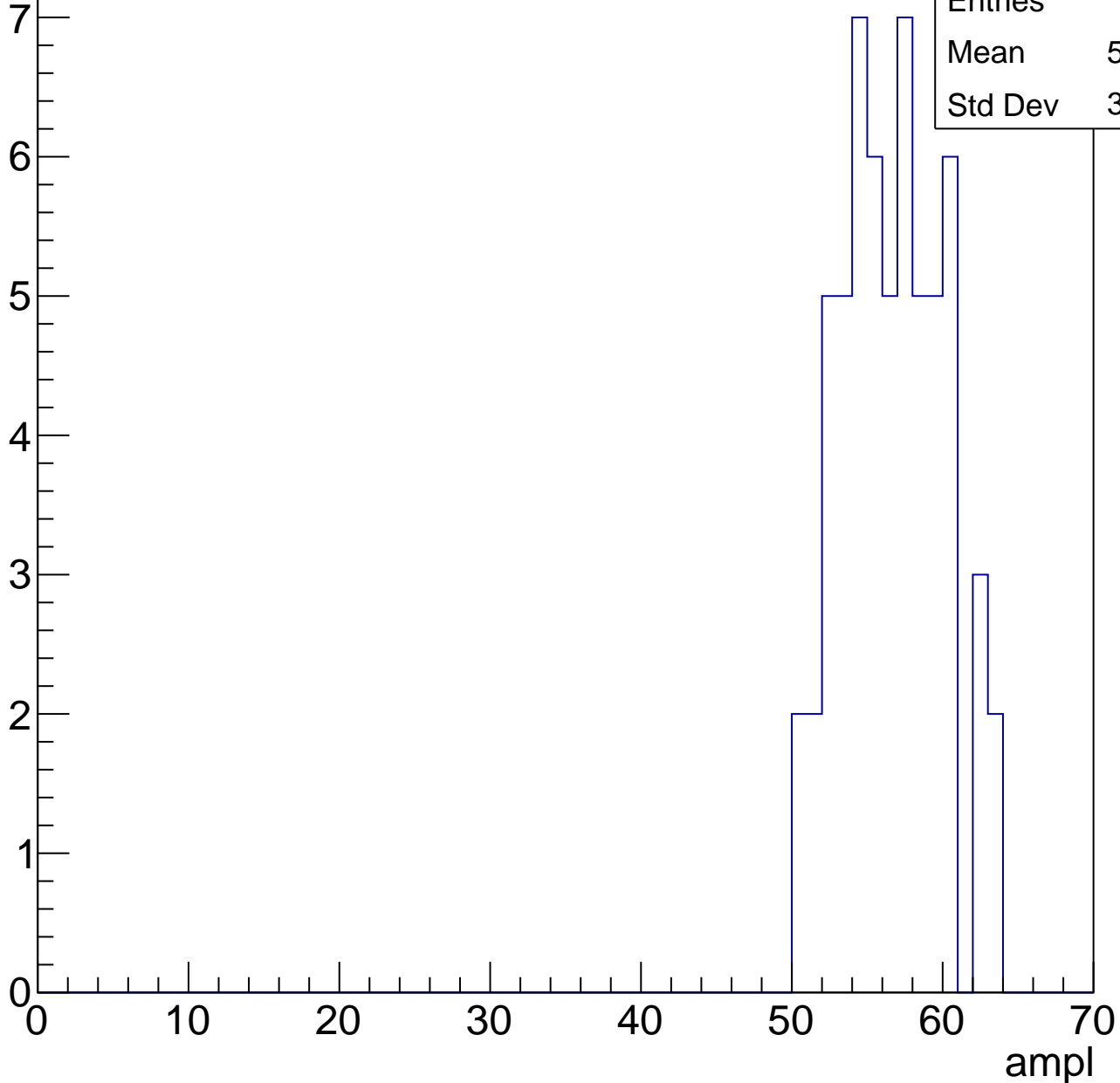


B0L001S, U22-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.18
Std Dev	3.299

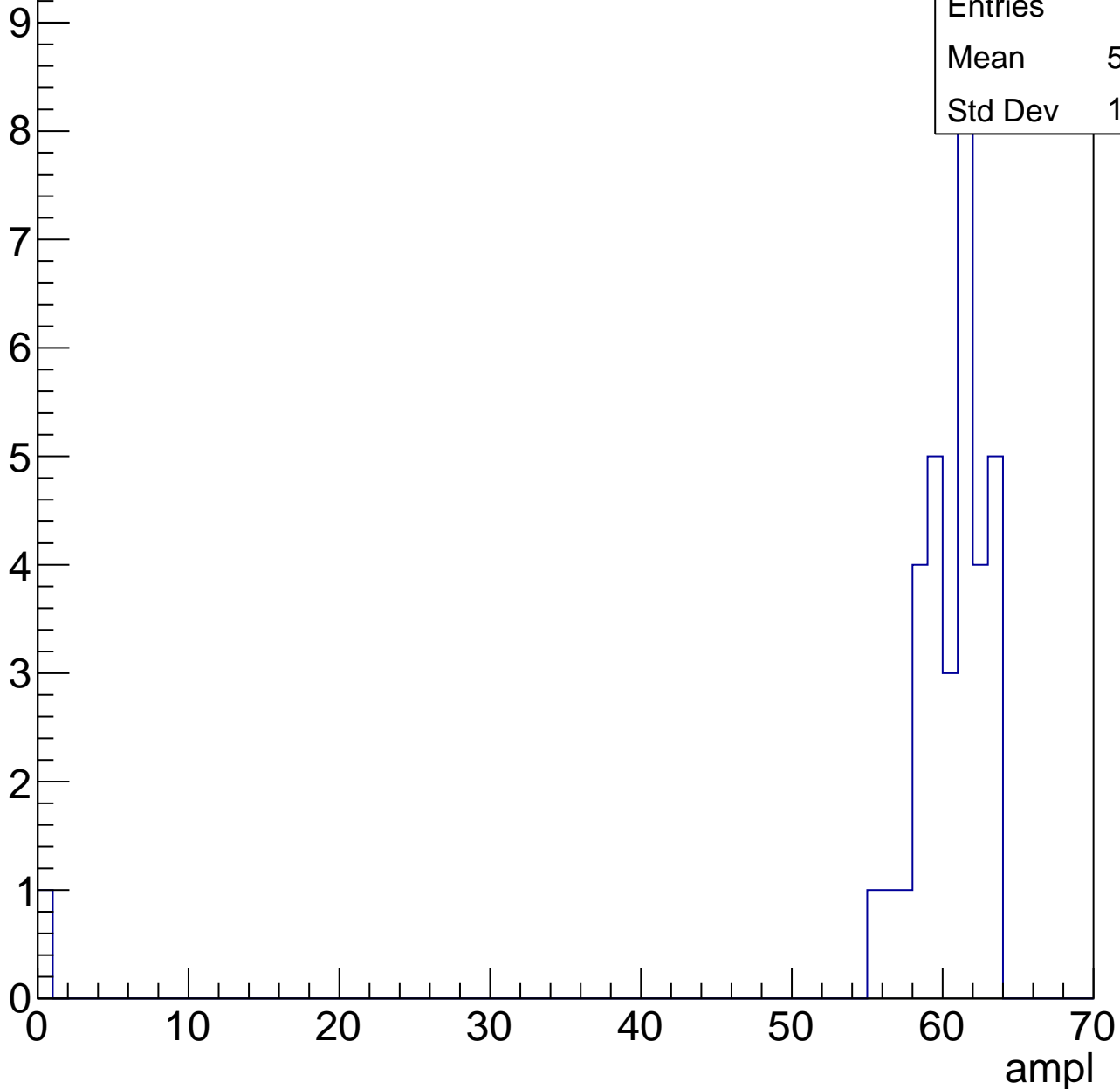


B0L001S, U22-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

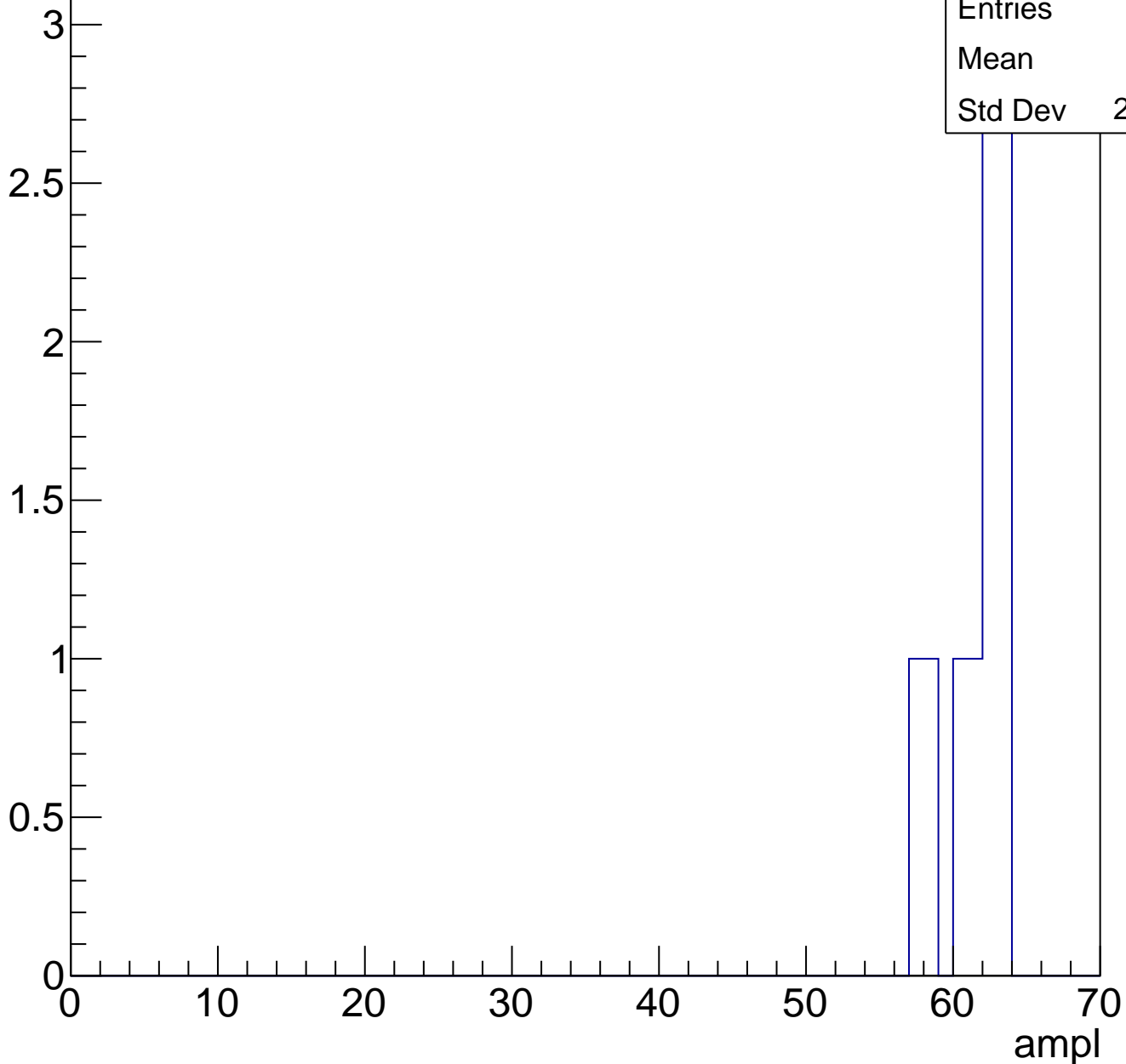
Entries	34
Mean	58.44
Std Dev	10.37



B0L001S, U22-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch23, adc0

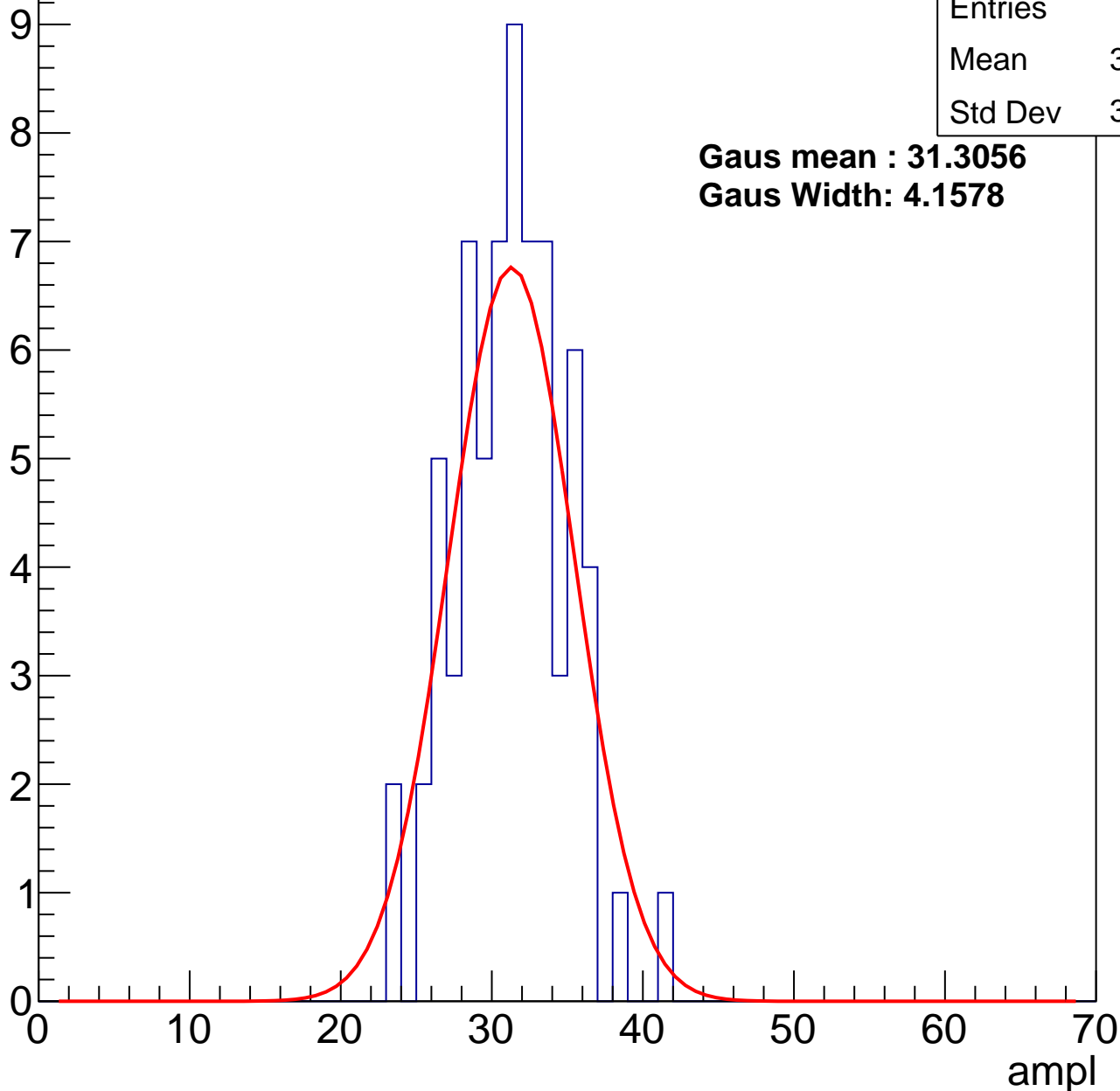
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.83
Std Dev	3.547

Gaus mean : 31.3056

Gaus Width: 4.1578

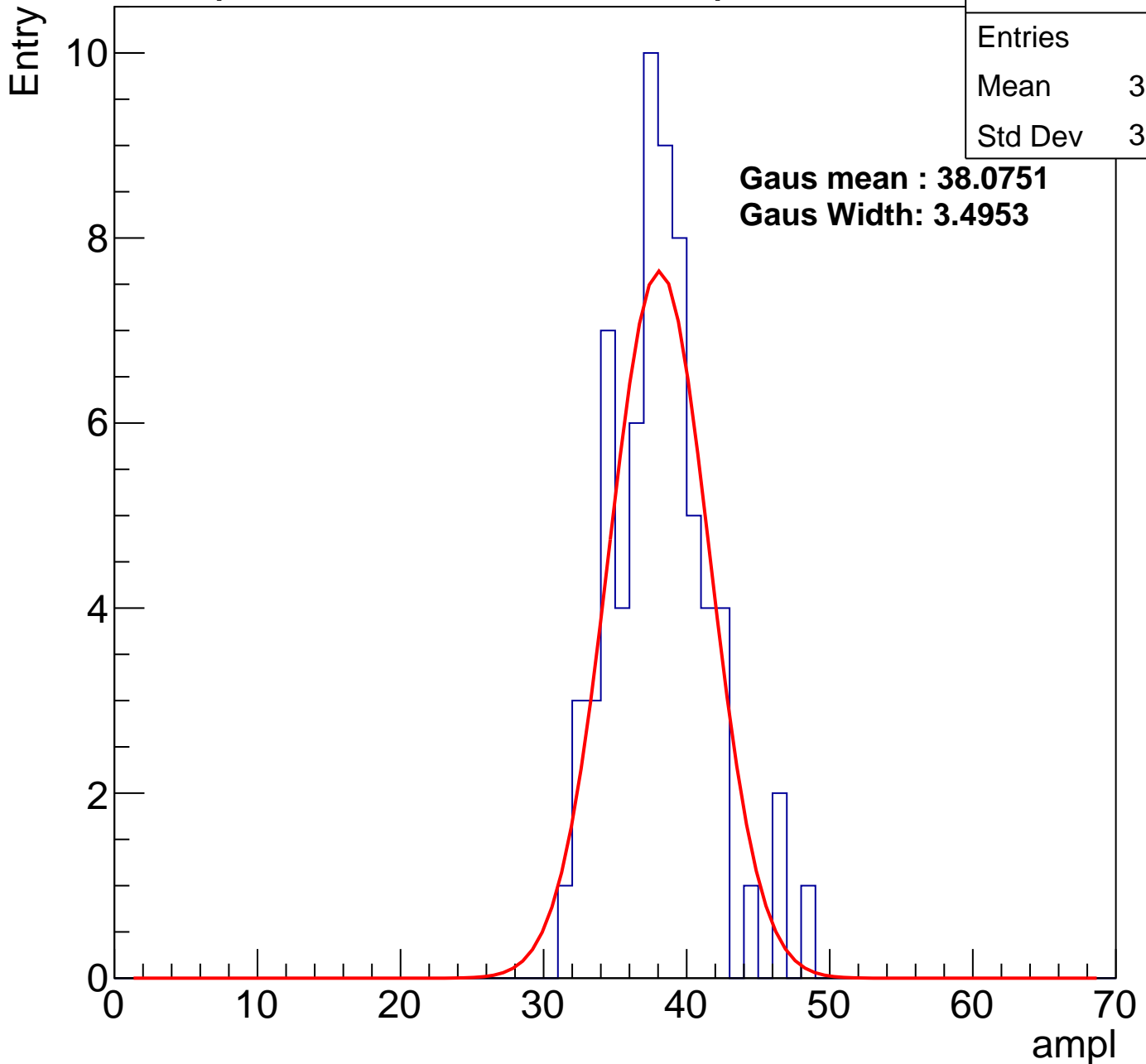


B0L001S, U22-ch23, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	37.65
Std Dev	3.425

Gaus mean : 38.0751
Gaus Width: 3.4953



B0L001S, U22-ch23, adc2

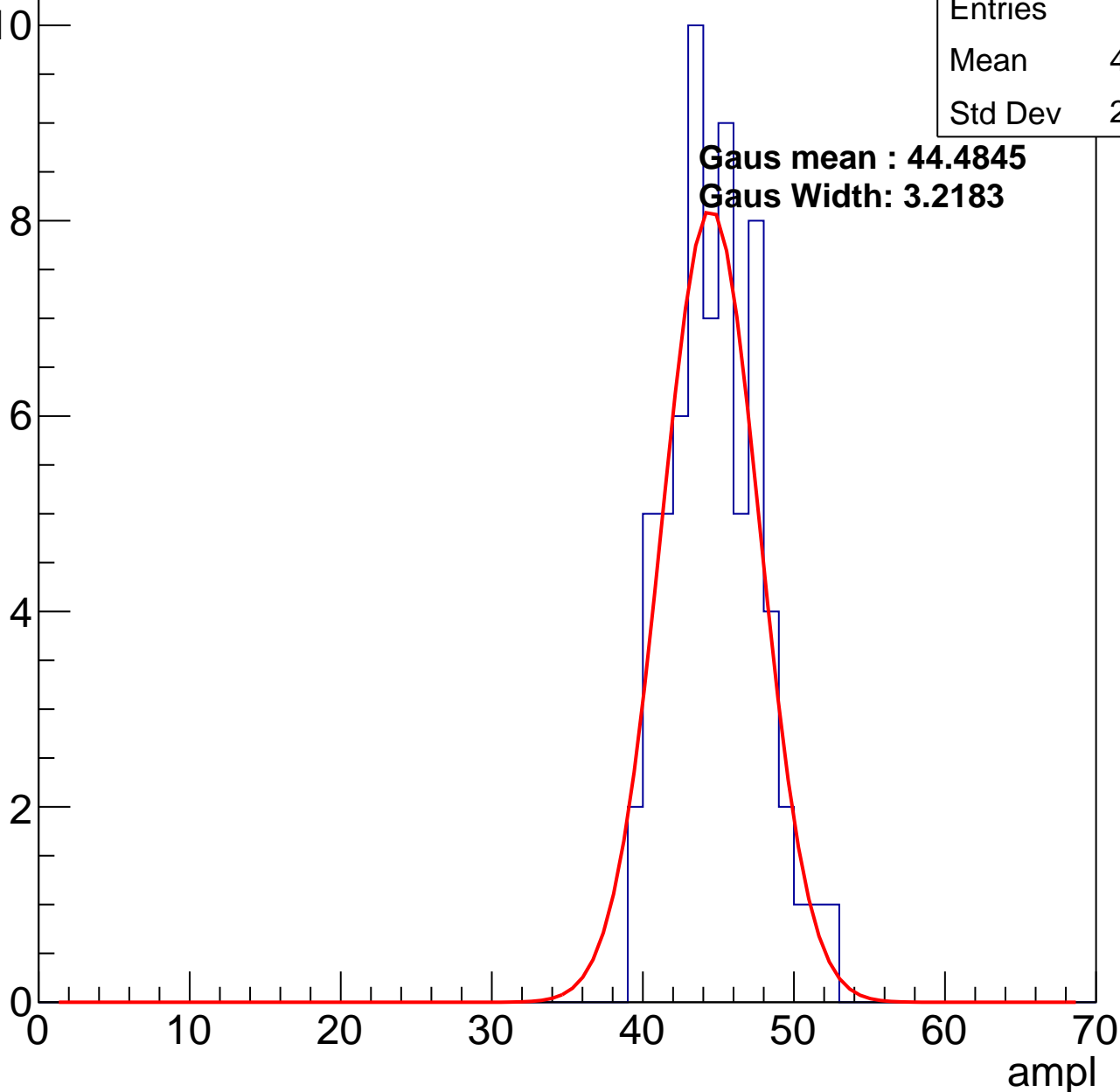
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	44.35
Std Dev	2.926

Gaus mean : 44.4845

Gaus Width: 3.2183

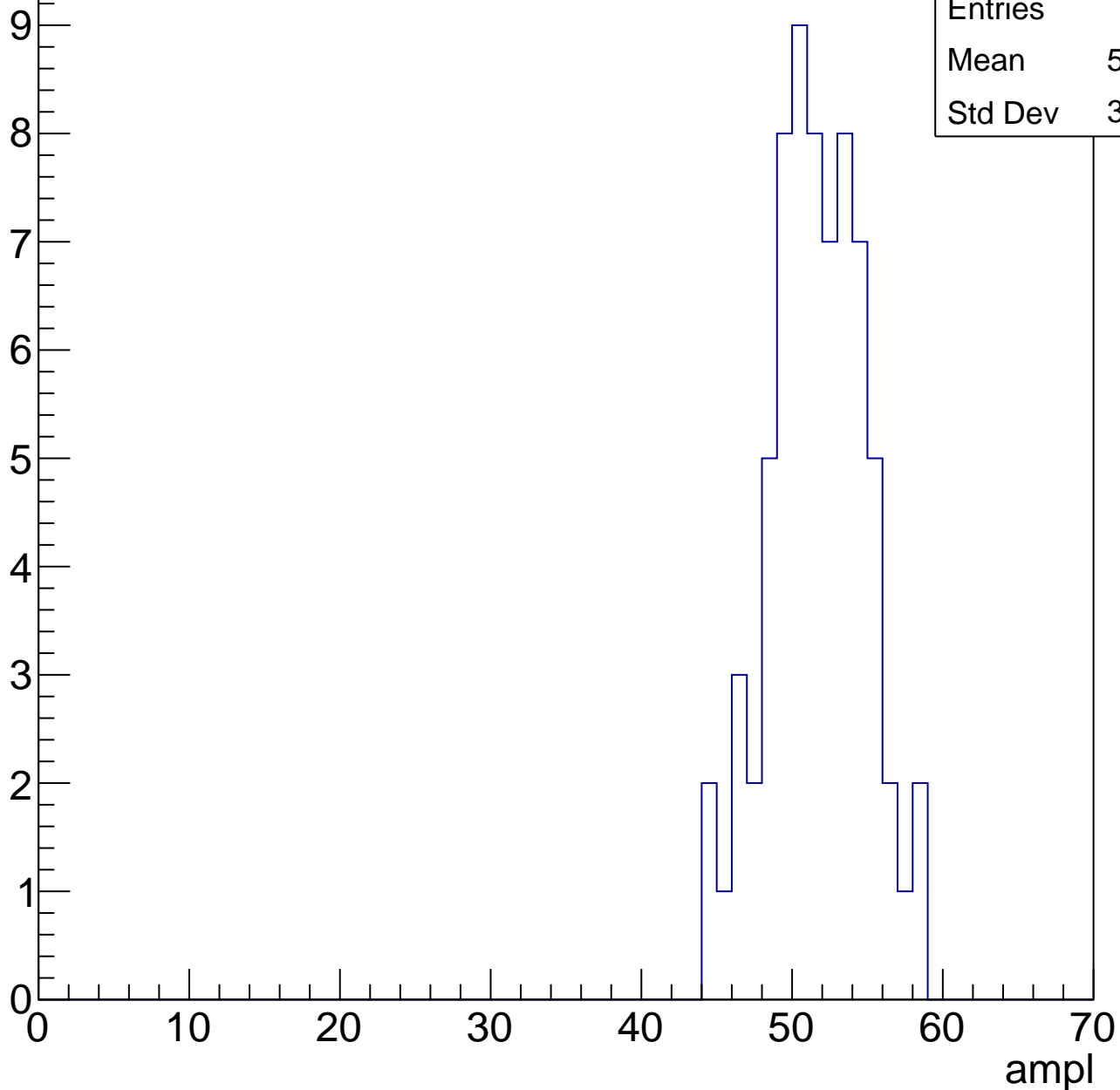


B0L001S, U22-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

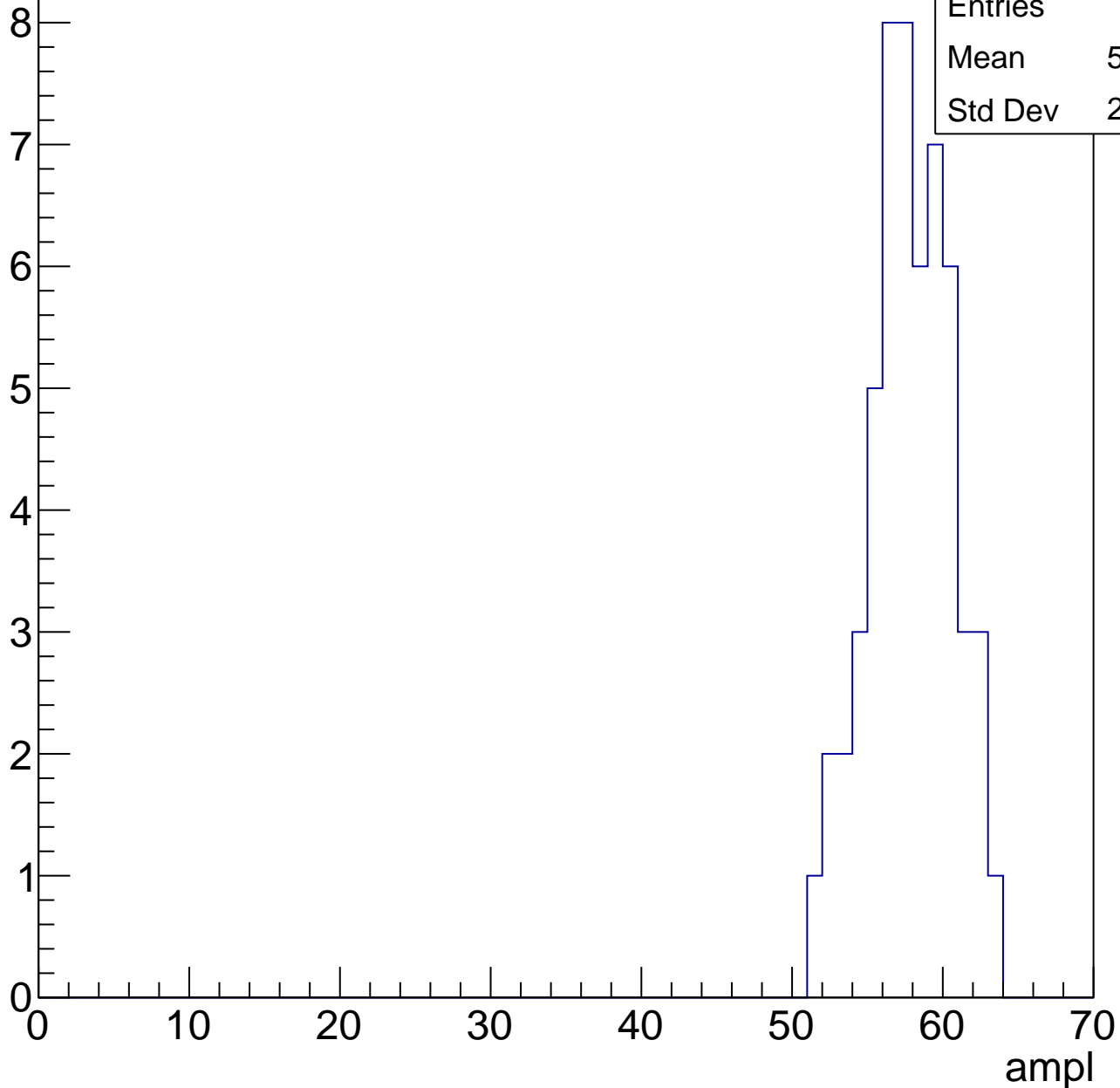
Entries	70
Mean	51.16
Std Dev	3.142



B0L001S, U22-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

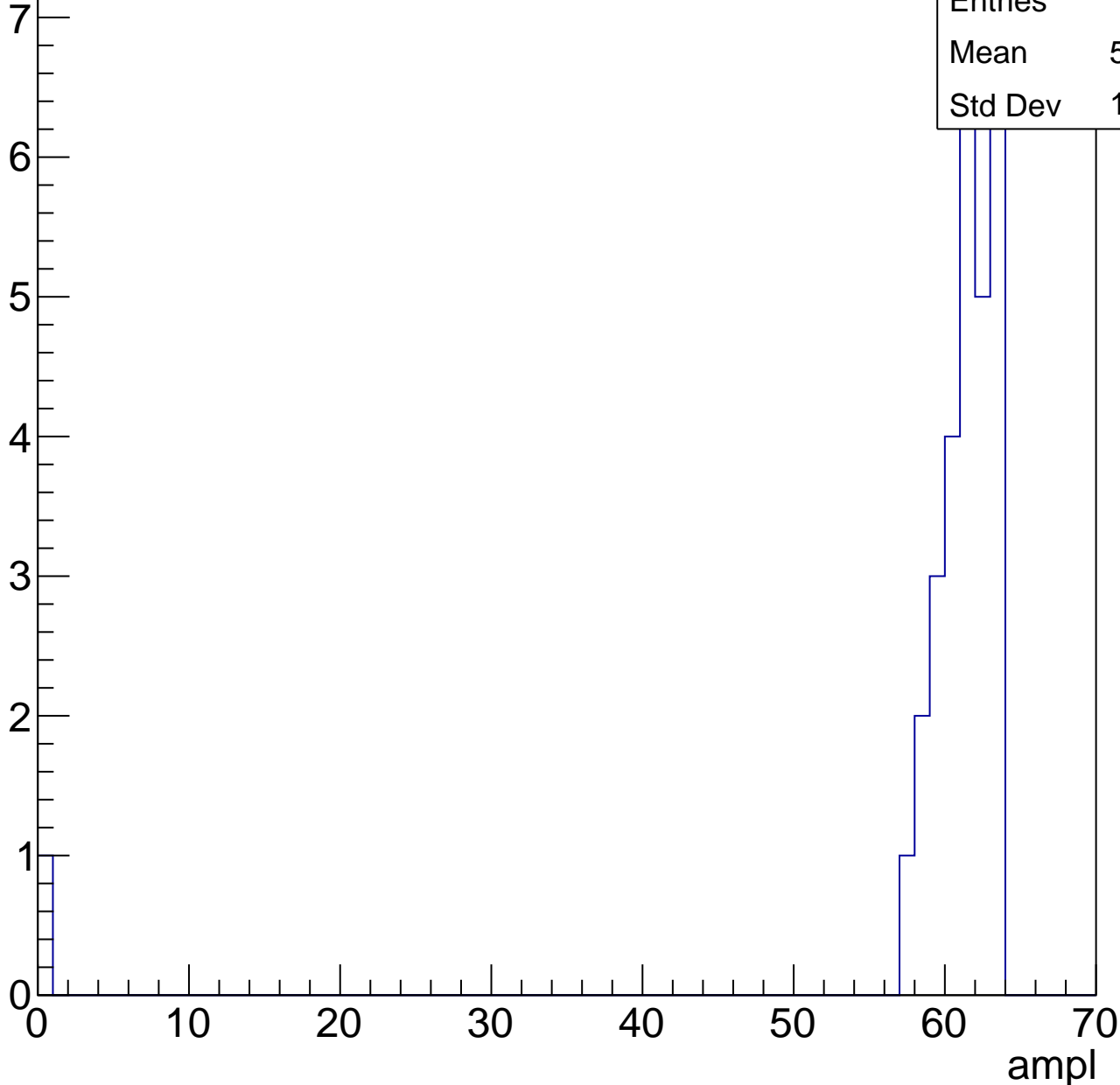


B0L001S, U22-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.93
Std Dev	11.07



B0L001S, U22-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch24, adc0

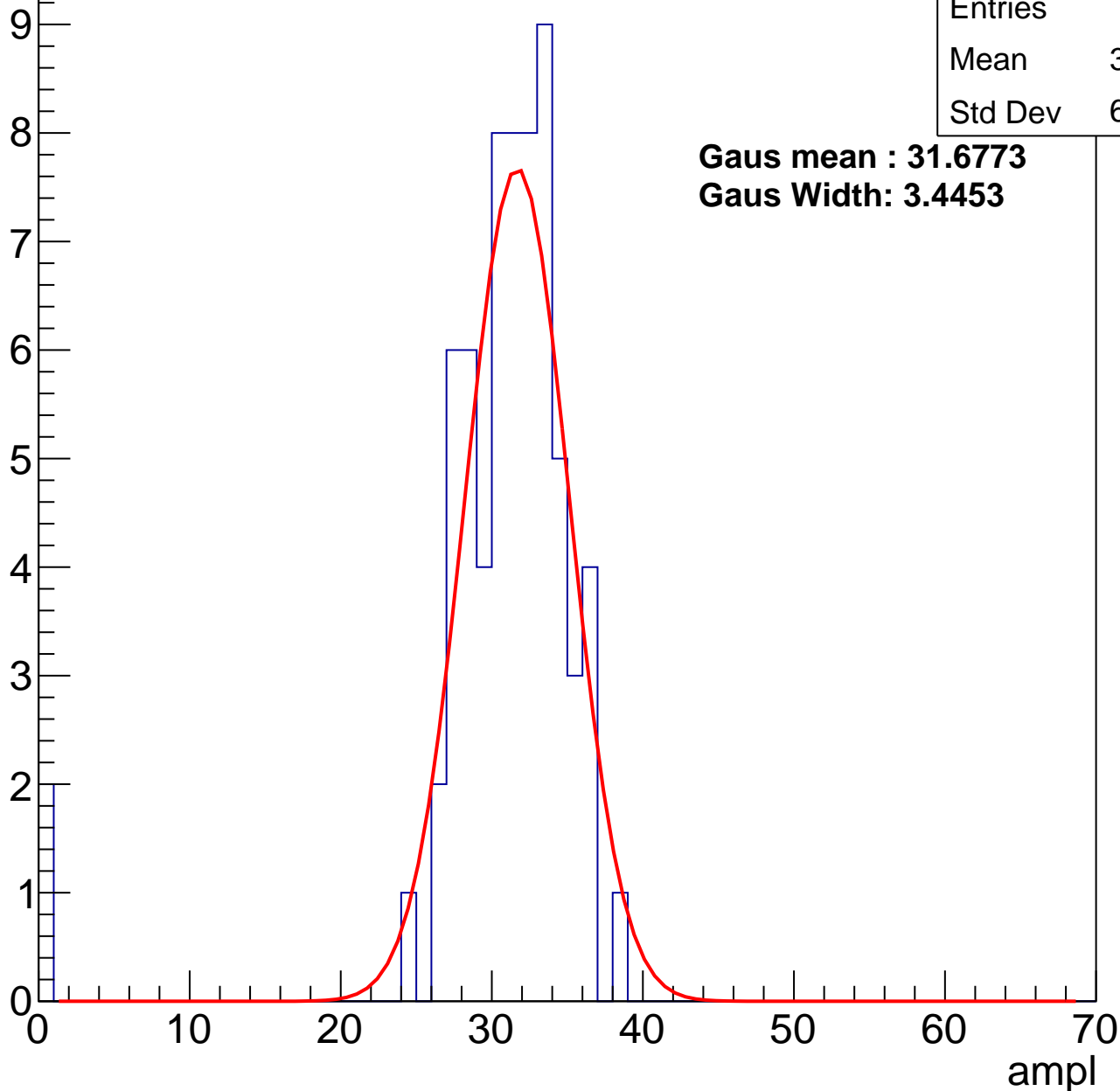
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.15
Std Dev	6.025

Gaus mean : 31.6773

Gaus Width: 3.4453



B0L001S, U22-ch24, adc1

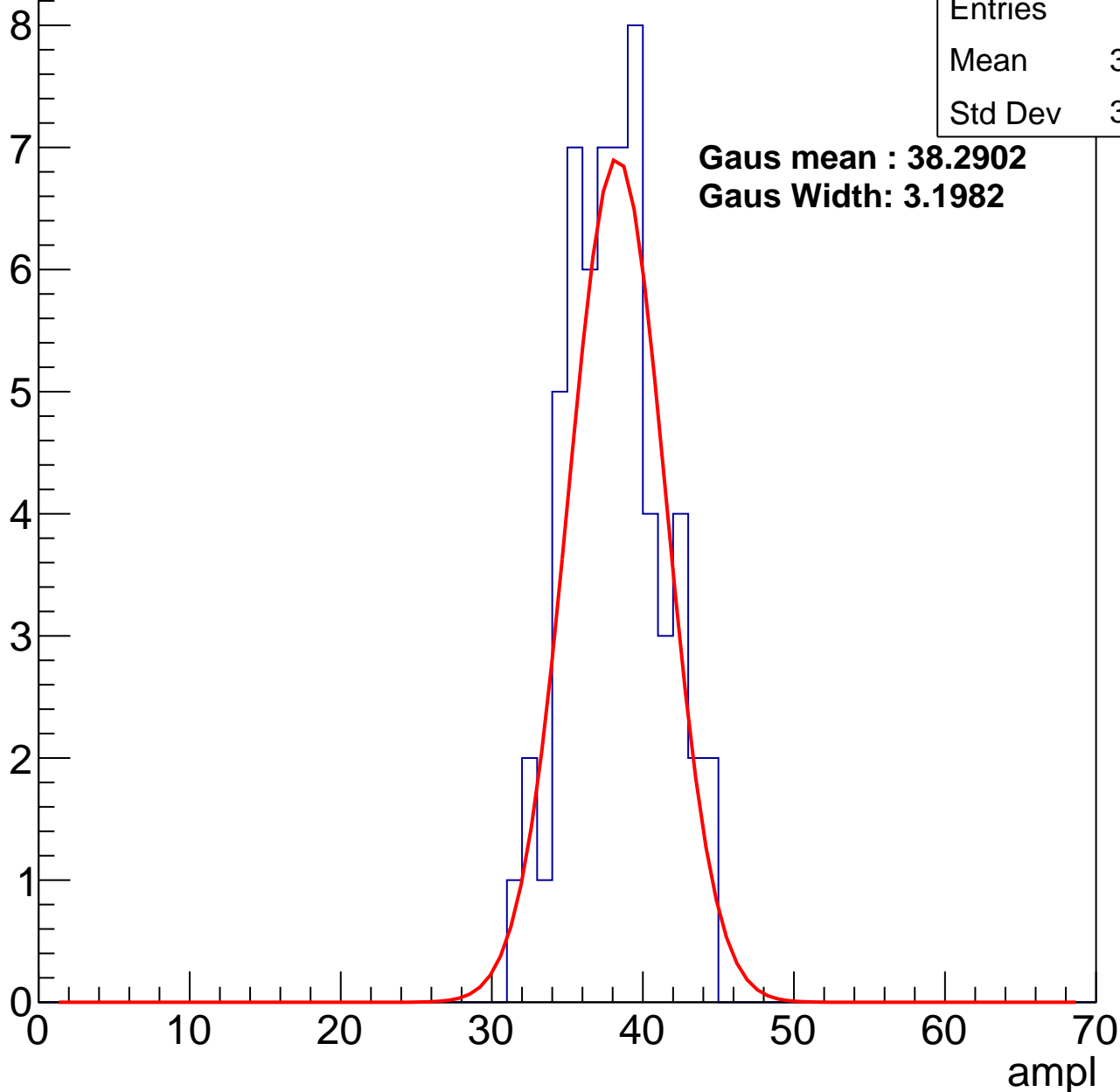
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.64
Std Dev	3.052

Gaus mean : 38.2902

Gaus Width: 3.1982



B0L001S, U22-ch24, adc2

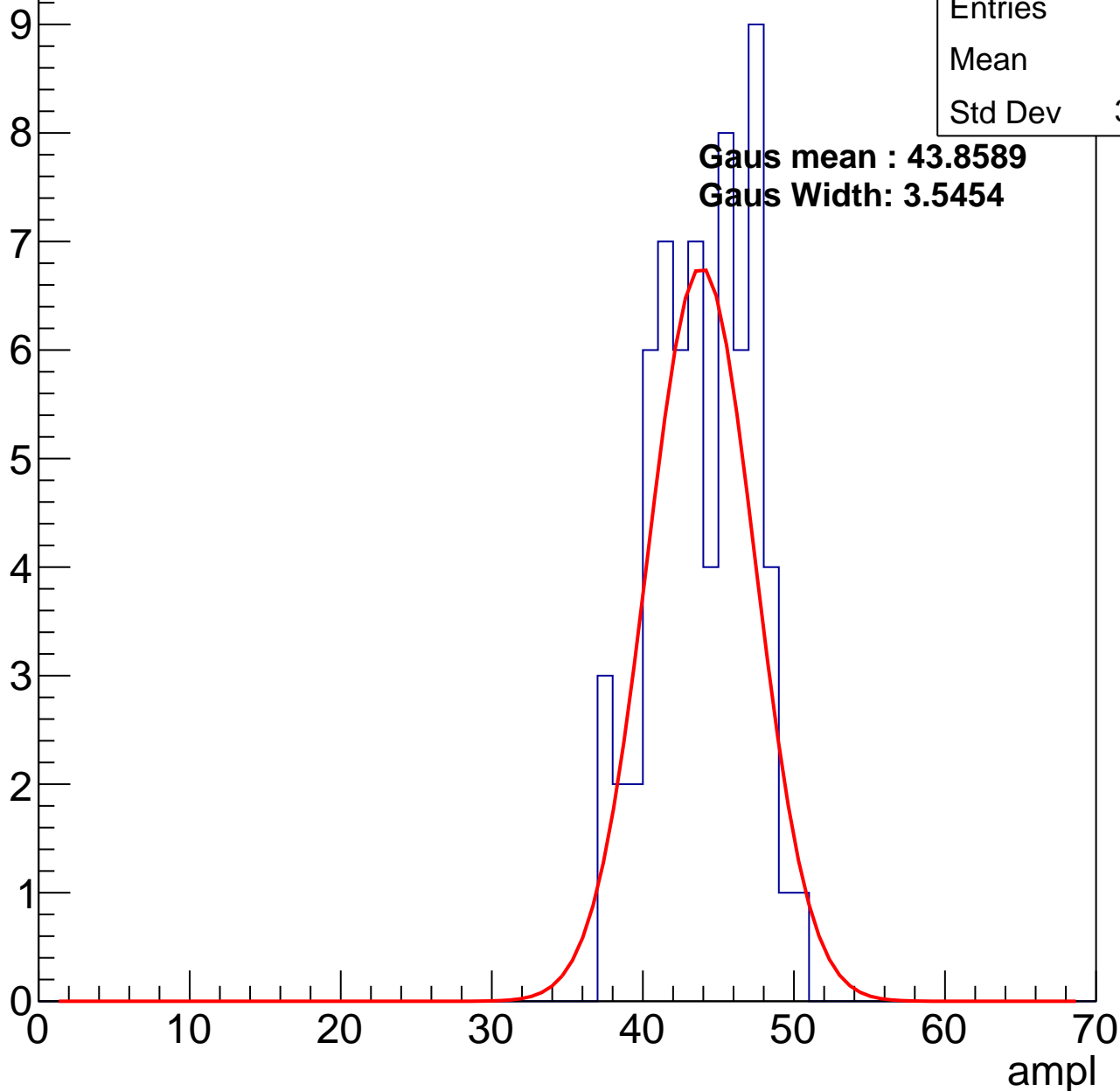
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.5
Std Dev	3.211

Gaus mean : 43.8589

Gaus Width: 3.5454

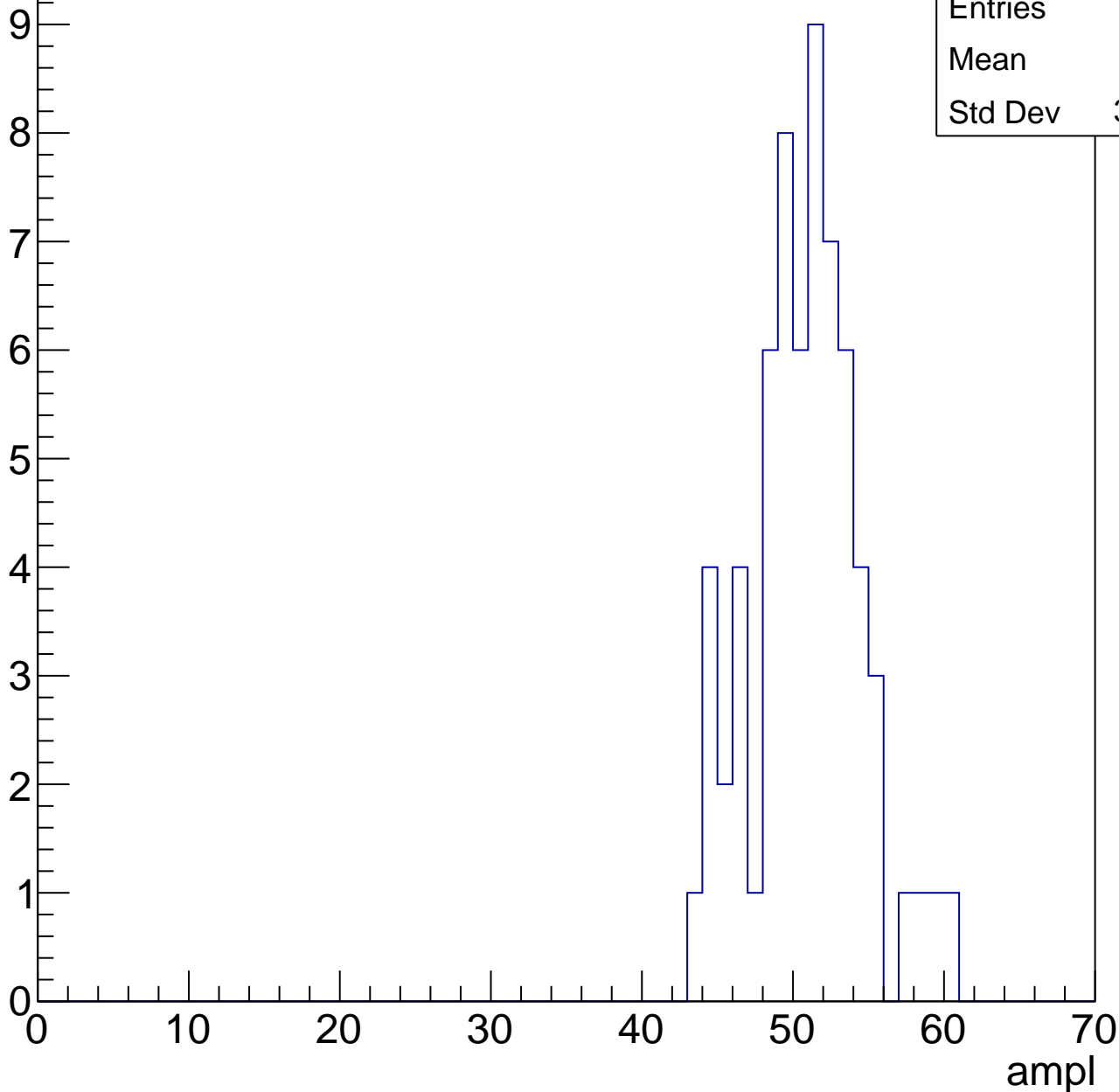


B0L001S, U22-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

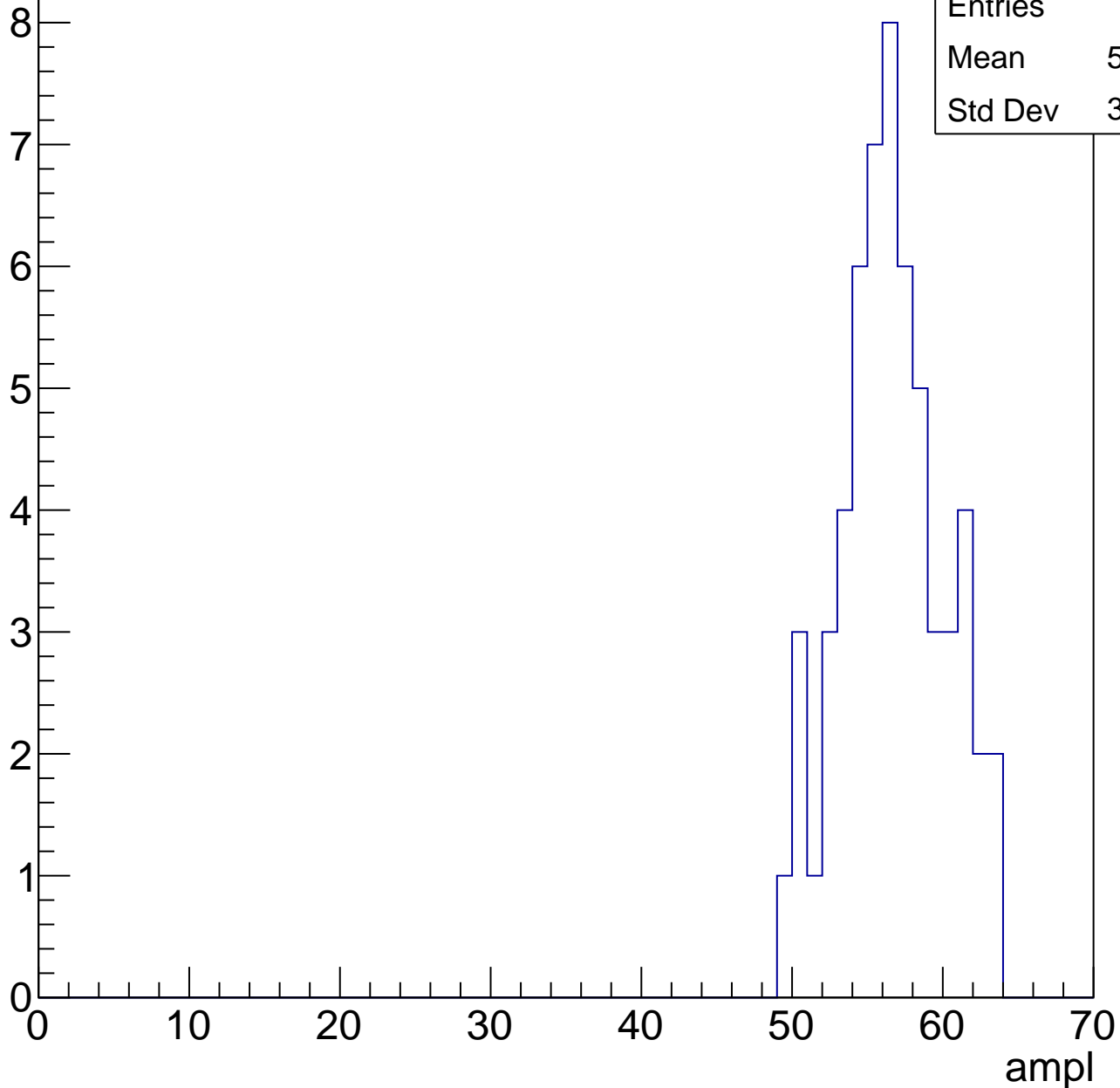
Entries	65
Mean	50.4
Std Dev	3.641



B0L001S, U22-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

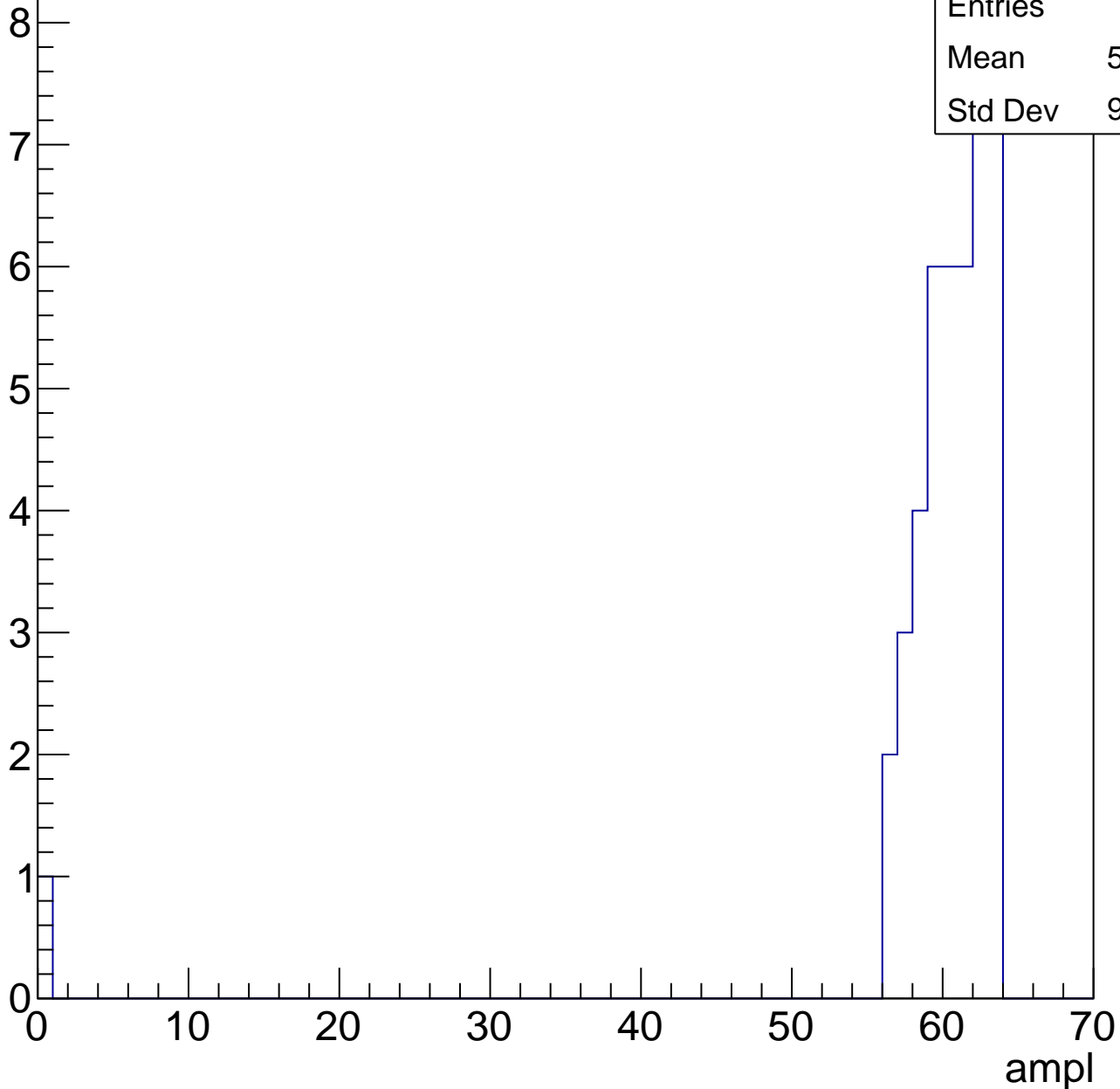


B0L001S, U22-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.98
Std Dev	9.226



B0L001S, U22-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch25, adc0

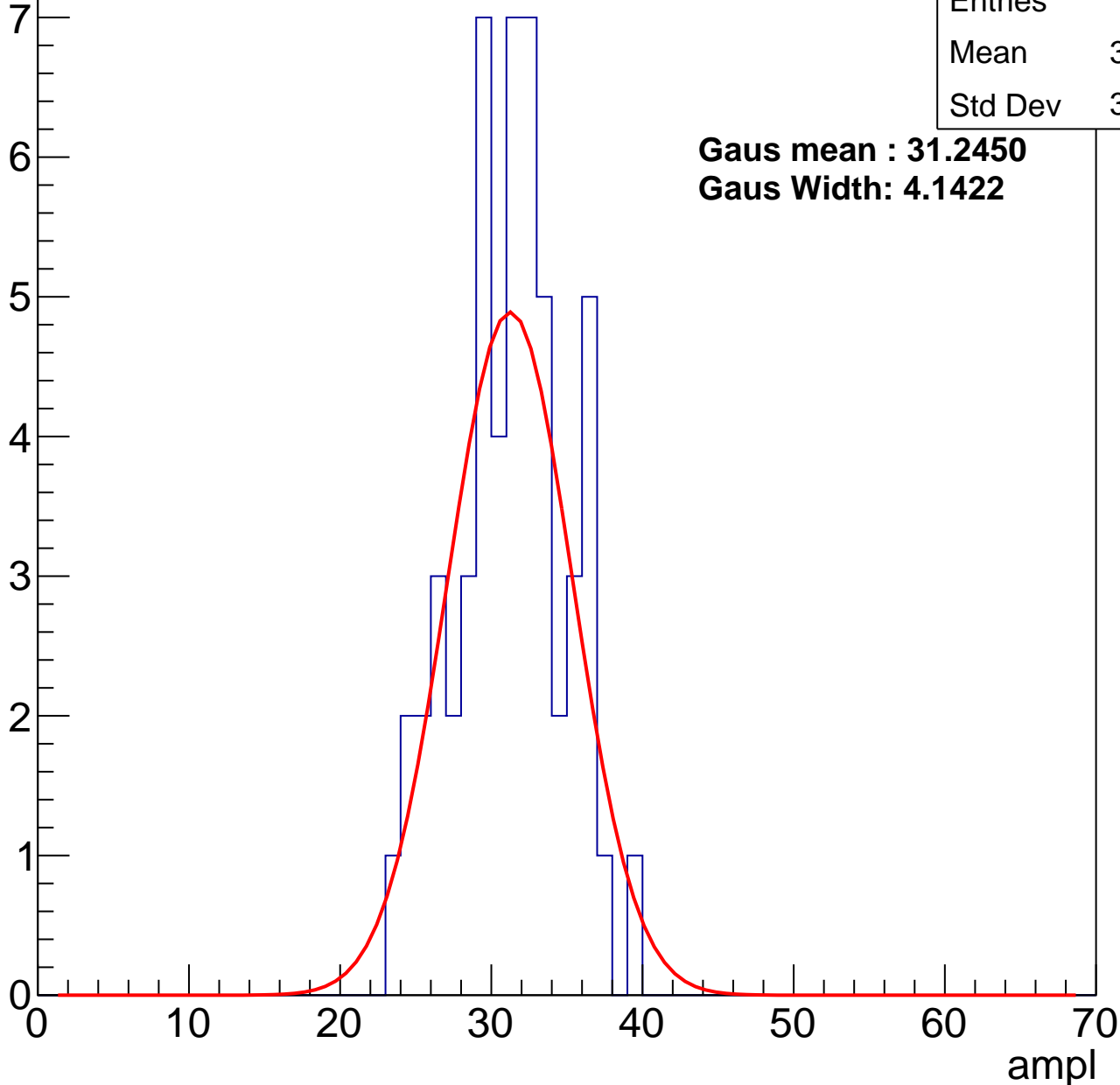
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	30.82
Std Dev	3.609

Gaus mean : 31.2450

Gaus Width: 4.1422



B0L001S, U22-ch25, adc1

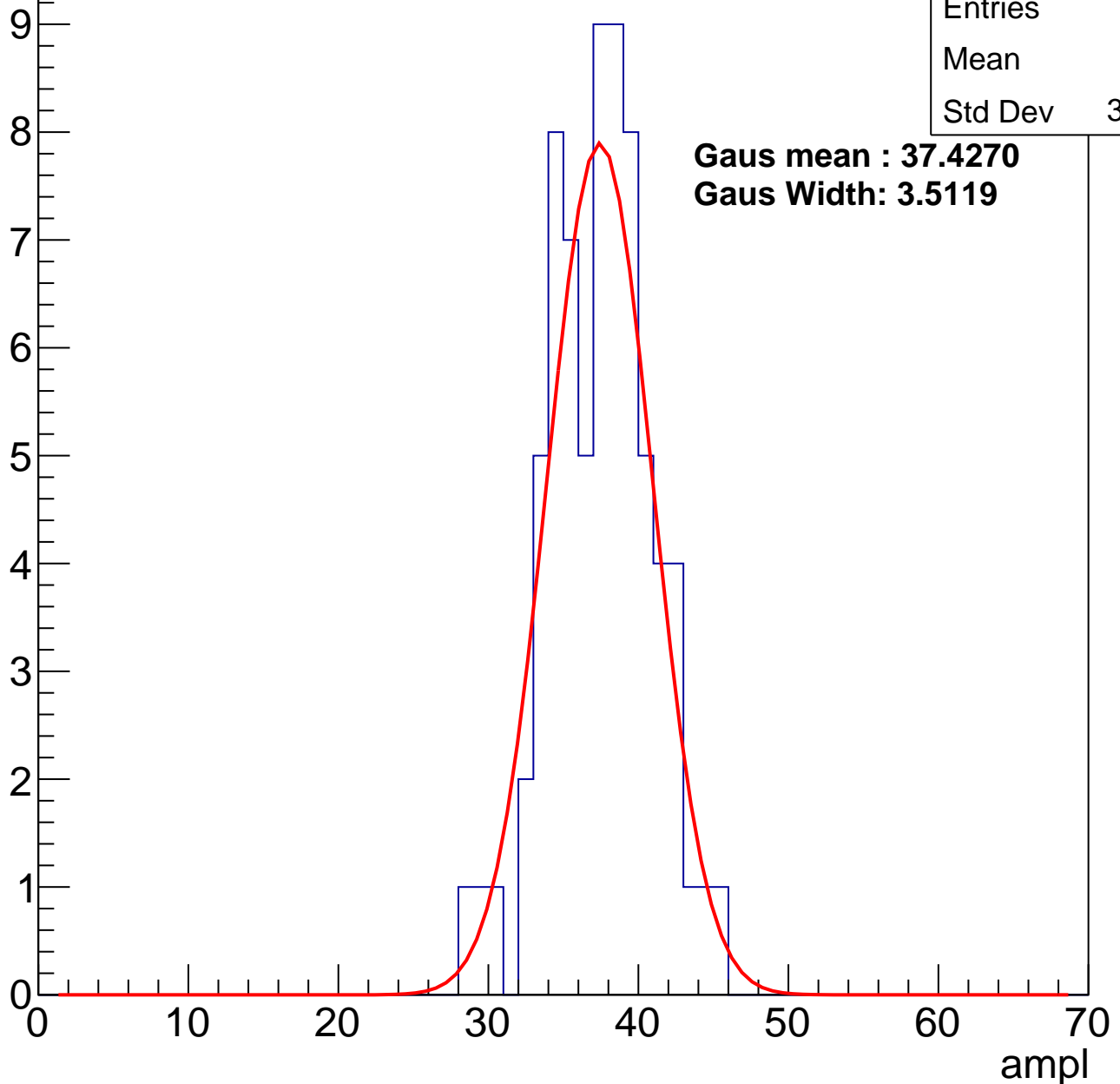
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37
Std Dev	3.391

Gaus mean : 37.4270

Gaus Width: 3.5119



B0L001S, U22-ch25, adc2

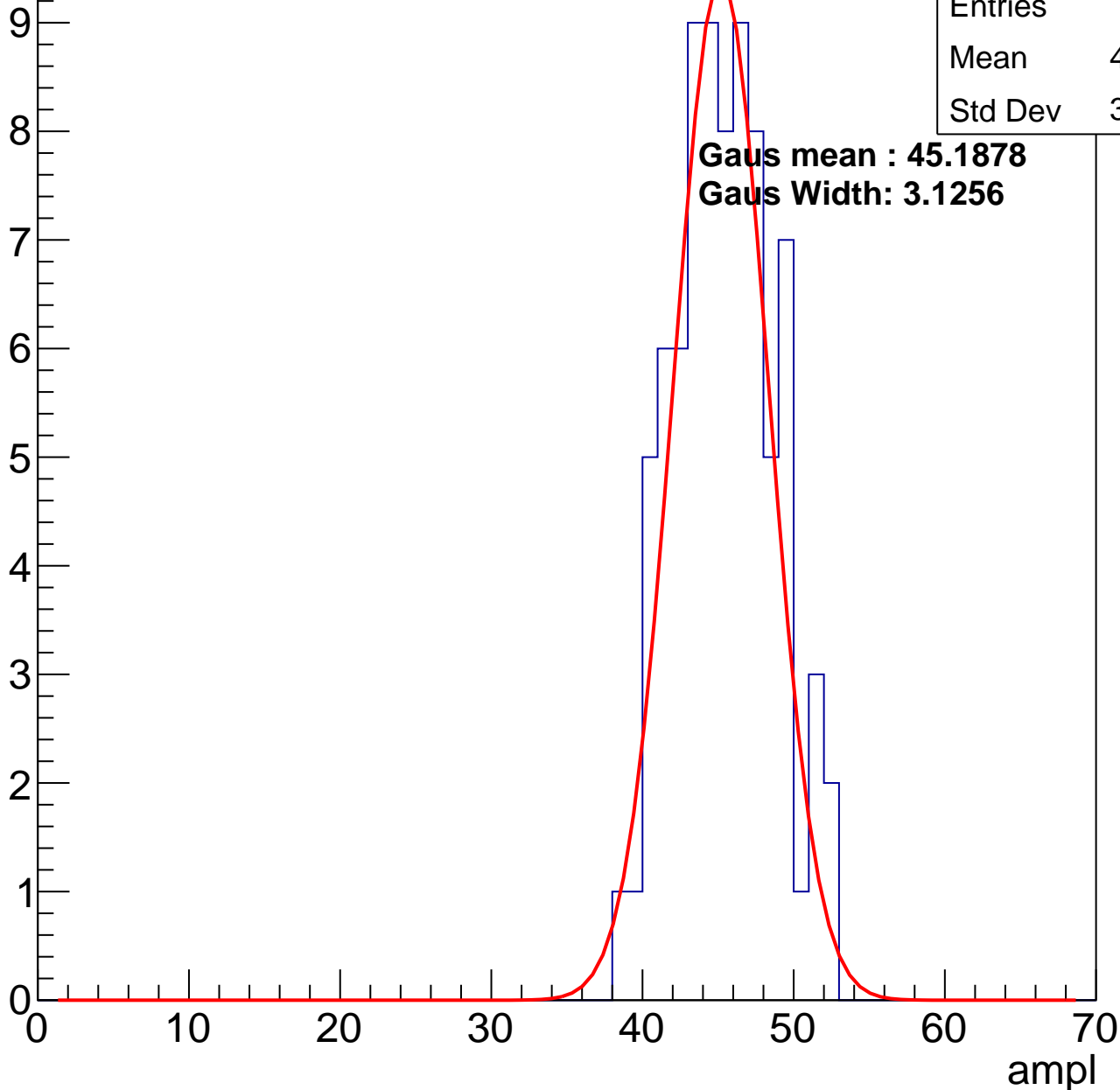
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	44.98
Std Dev	3.229

Gaus mean : 45.1878

Gaus Width: 3.1256

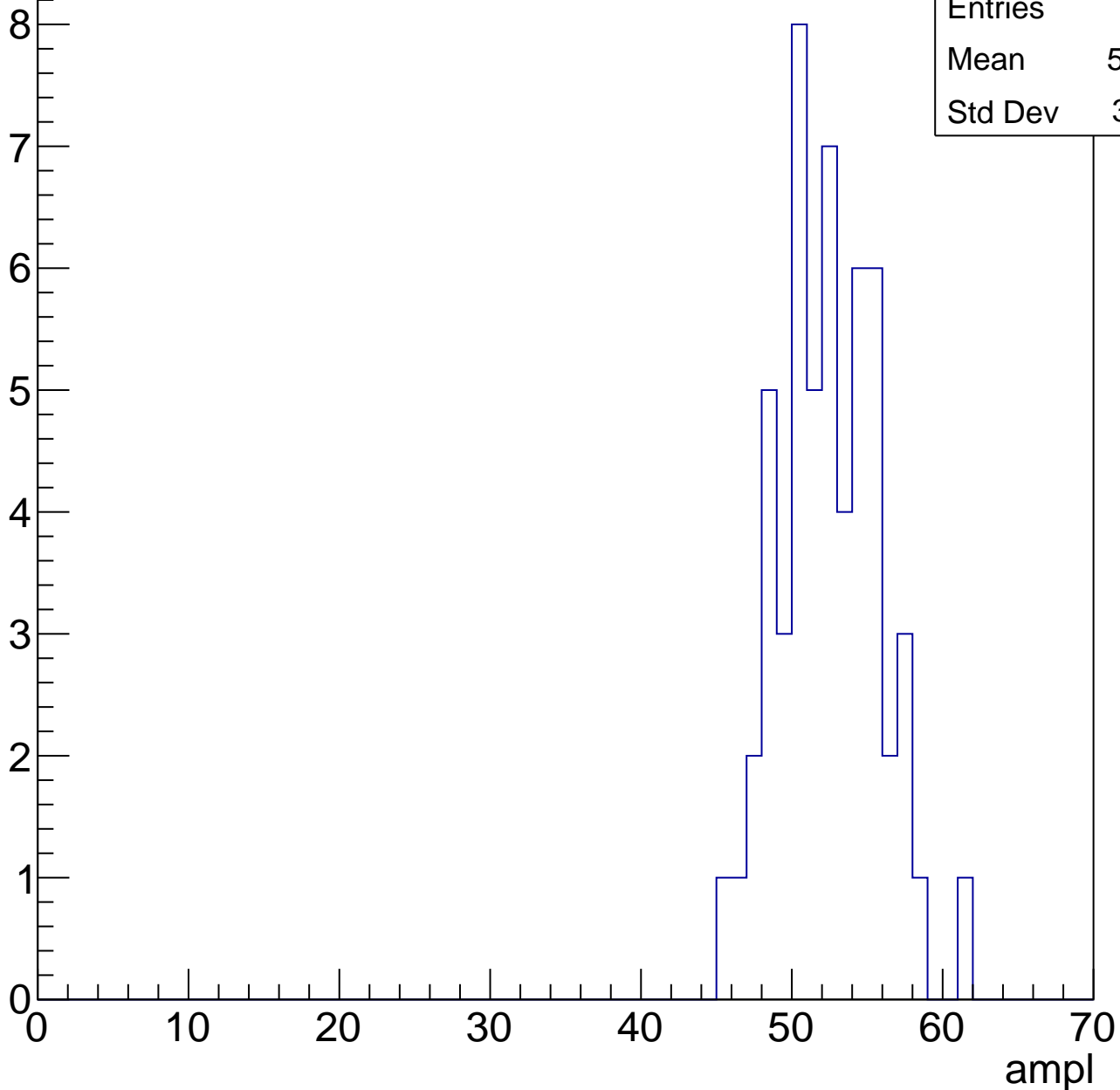


B0L001S, U22-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.98
Std Dev	3.261

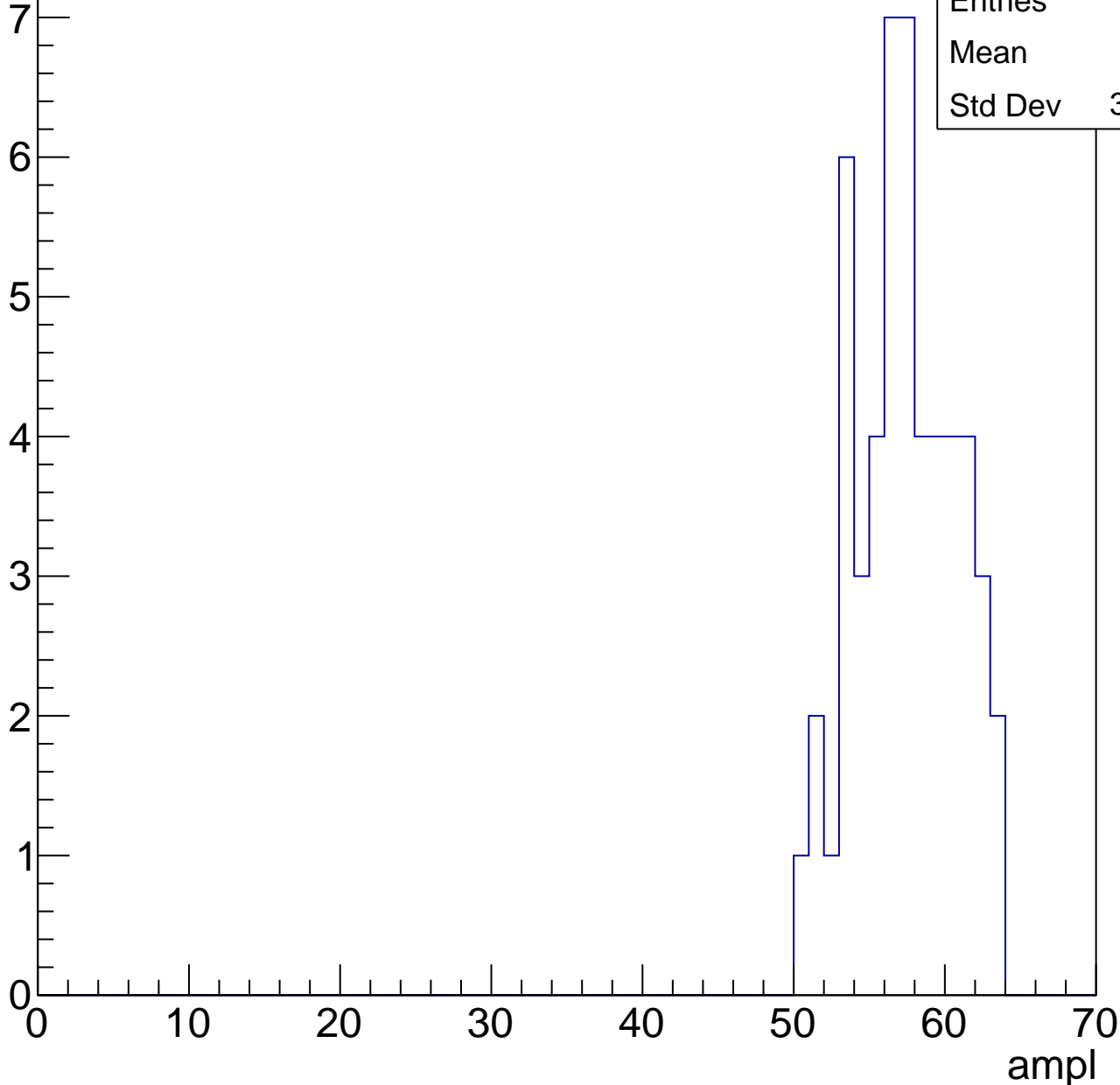


B0L001S, U22-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.9
Std Dev	3.277

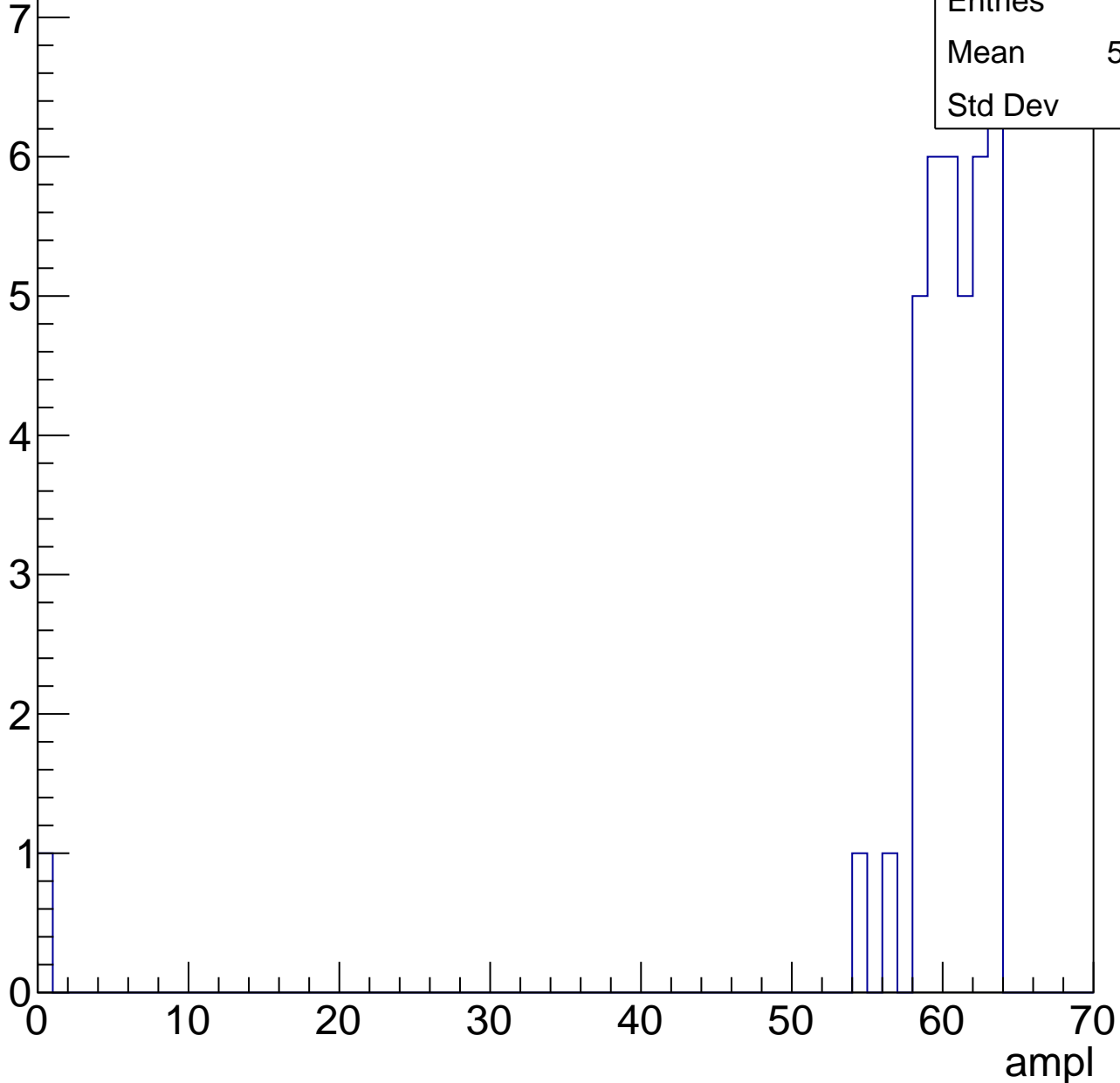


B0L001S, U22-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.74
Std Dev	9.88



B0L001S, U22-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch26, adc0

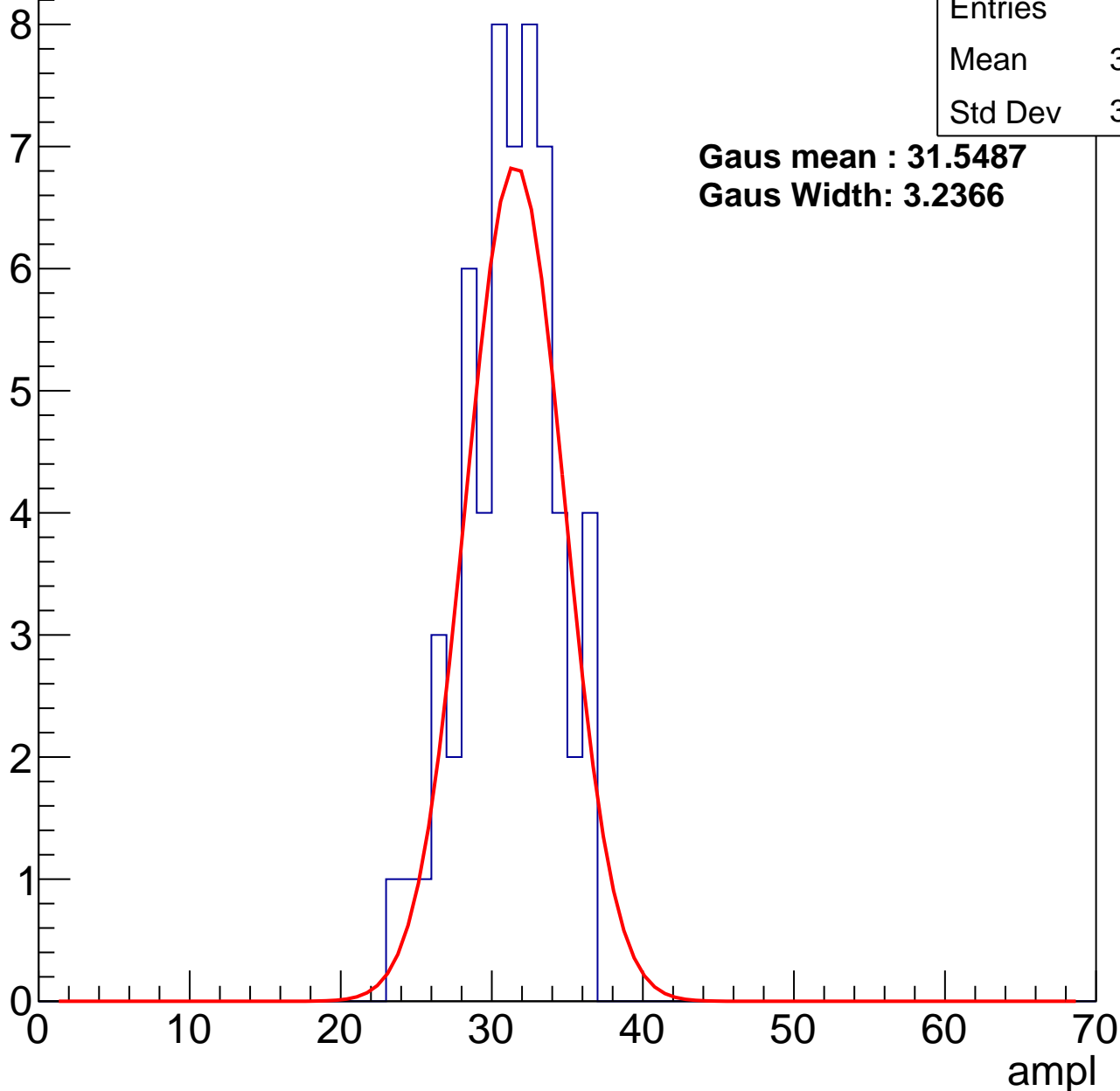
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	30.72
Std Dev	3.033

Gaus mean : 31.5487

Gaus Width: 3.2366



B0L001S, U22-ch26, adc1

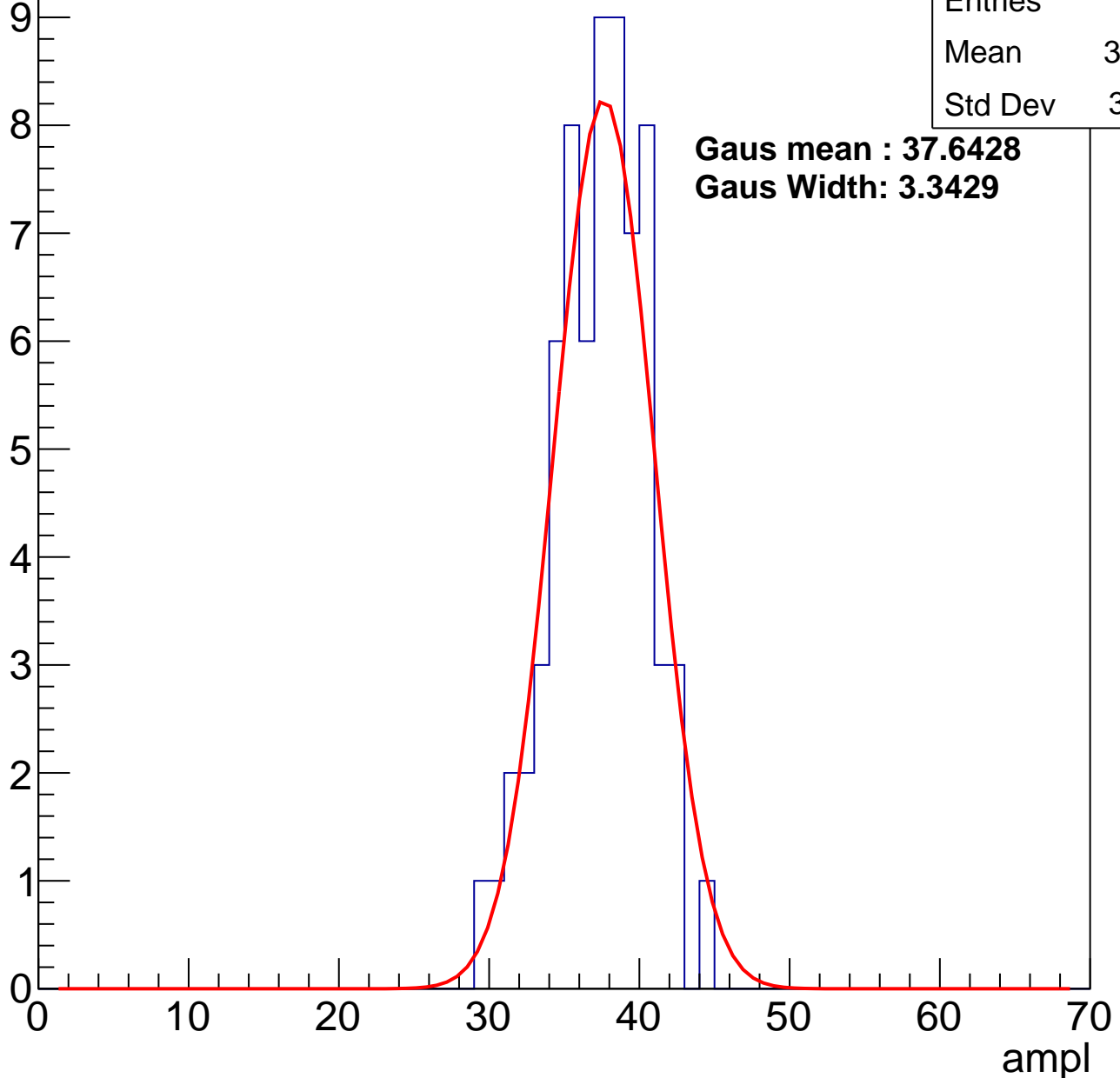
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36.88
Std Dev	3.081

Gaus mean : 37.6428

Gaus Width: 3.3429



B0L001S, U22-ch26, adc2

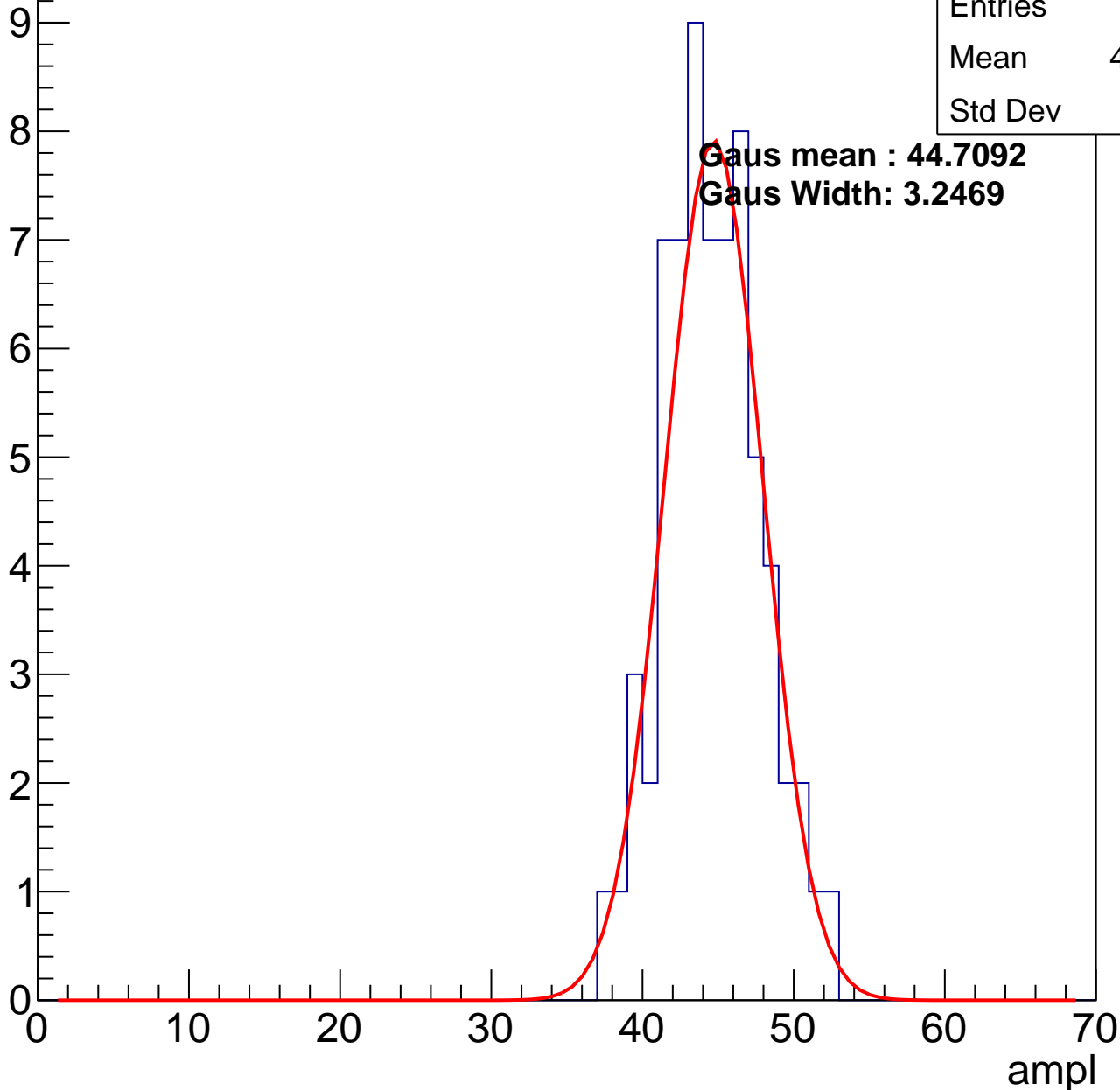
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.16
Std Dev	3.17

Gaus mean : 44.7092

Gaus Width: 3.2469

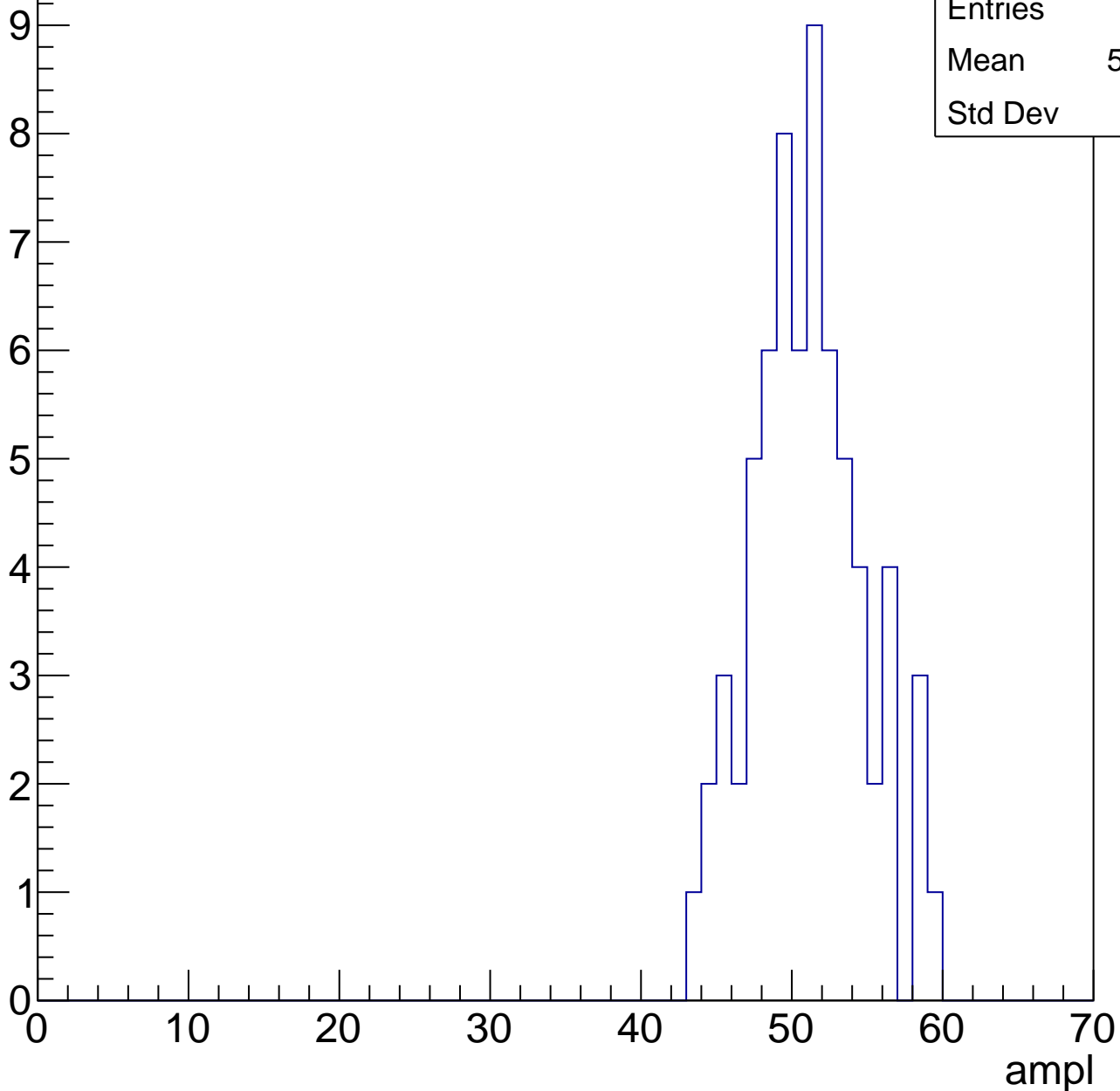


B0L001S, U22-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.63
Std Dev	3.64



B0L001S, U22-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	56.69
Std Dev	3

Entry

10

8

6

4

2

0

0

10

20

30

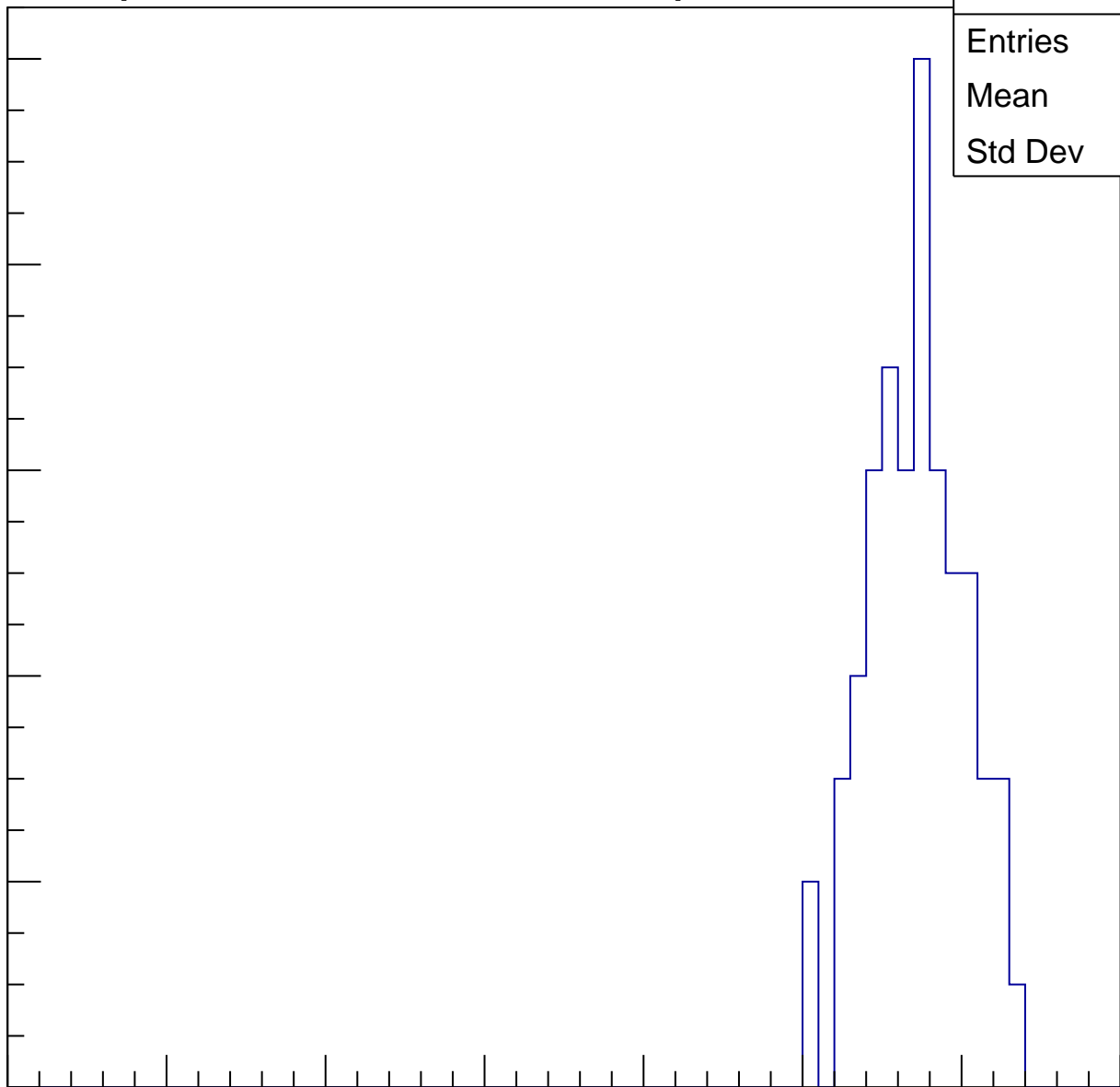
40

50

60

70

ampl

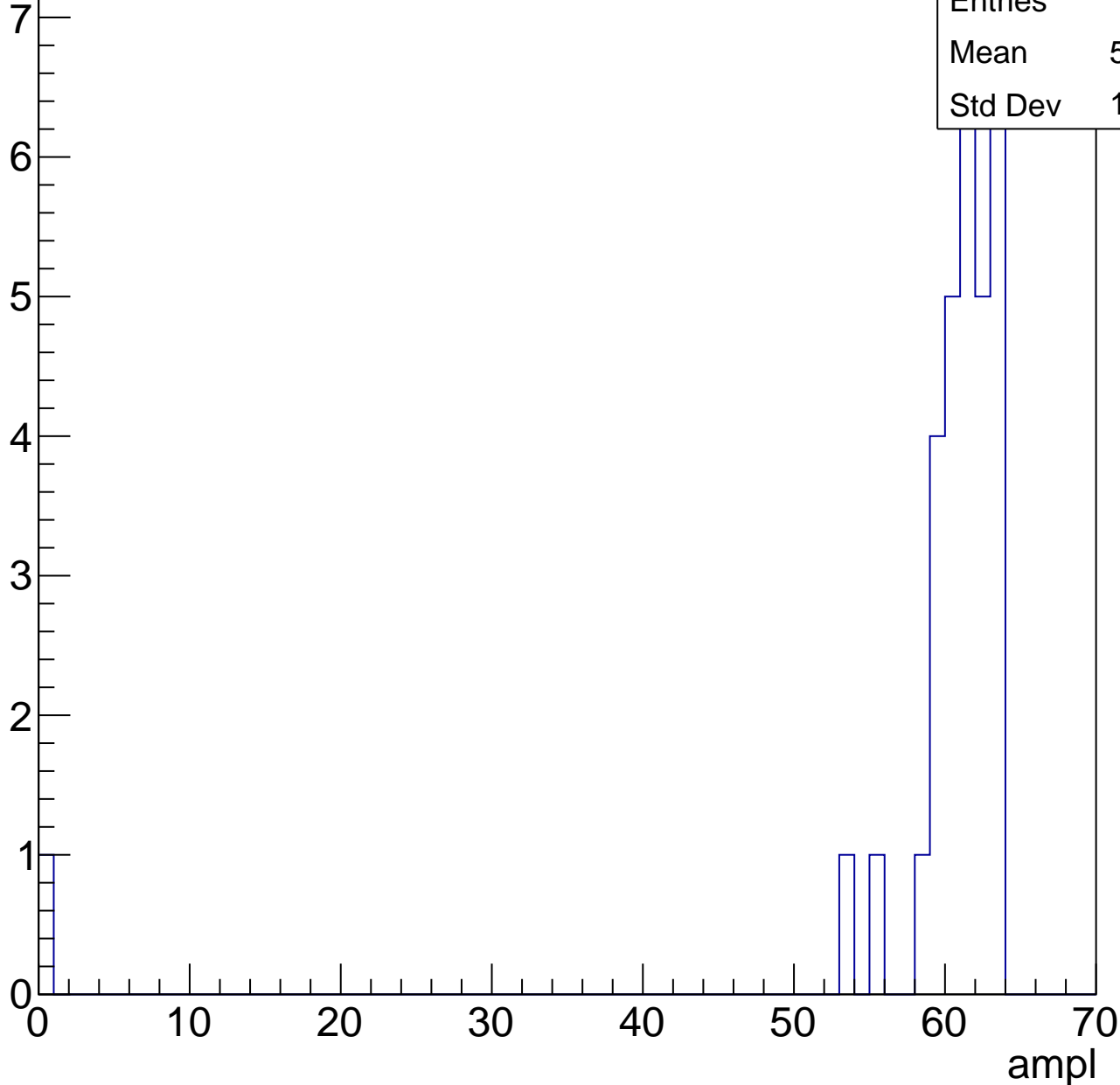


B0L001S, U22-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.75
Std Dev	10.78



B0L001S, U22-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch27, adc0

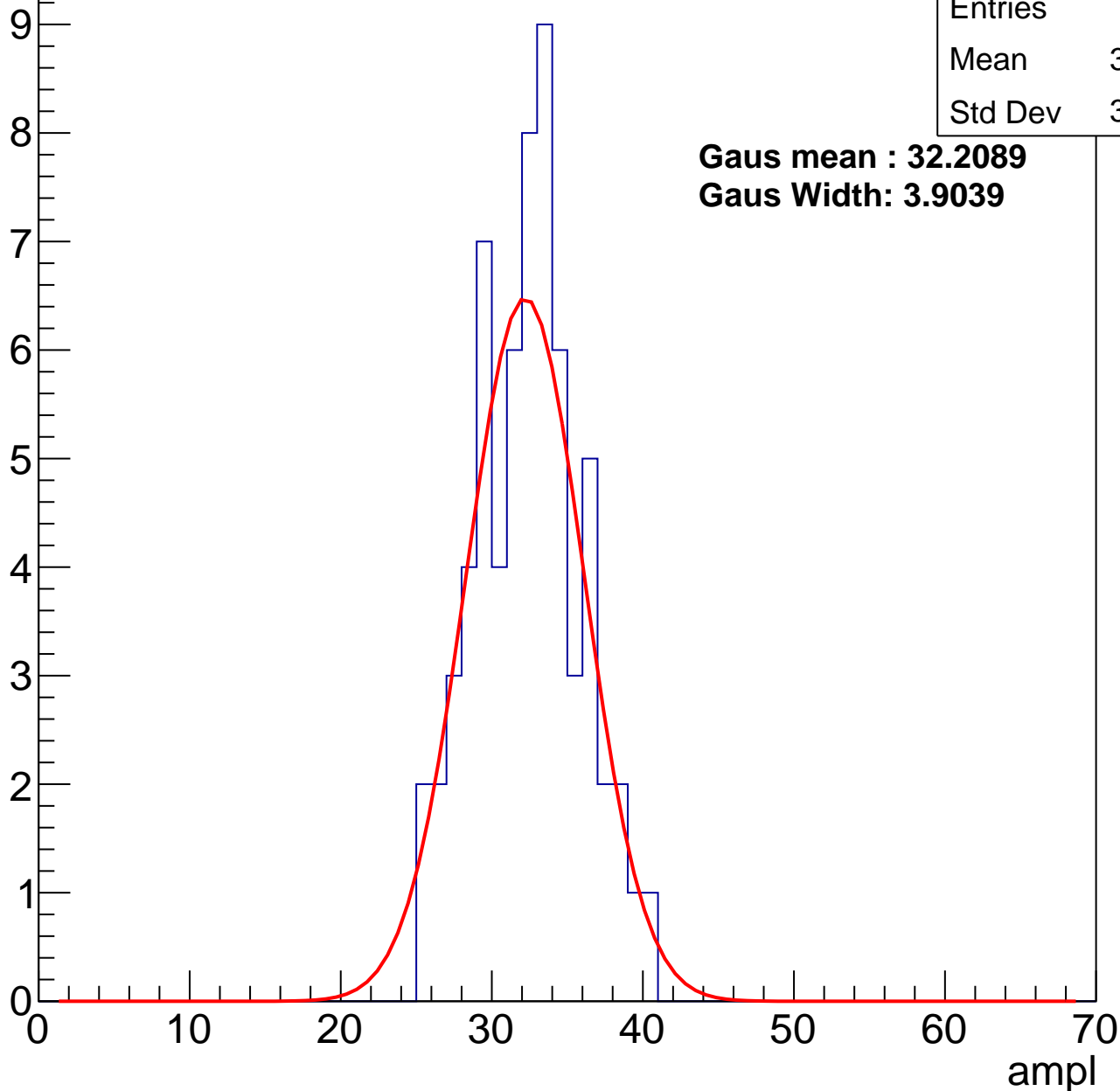
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.92
Std Dev	3.439

Gaus mean : 32.2089

Gaus Width: 3.9039



B0L001S, U22-ch27, adc1

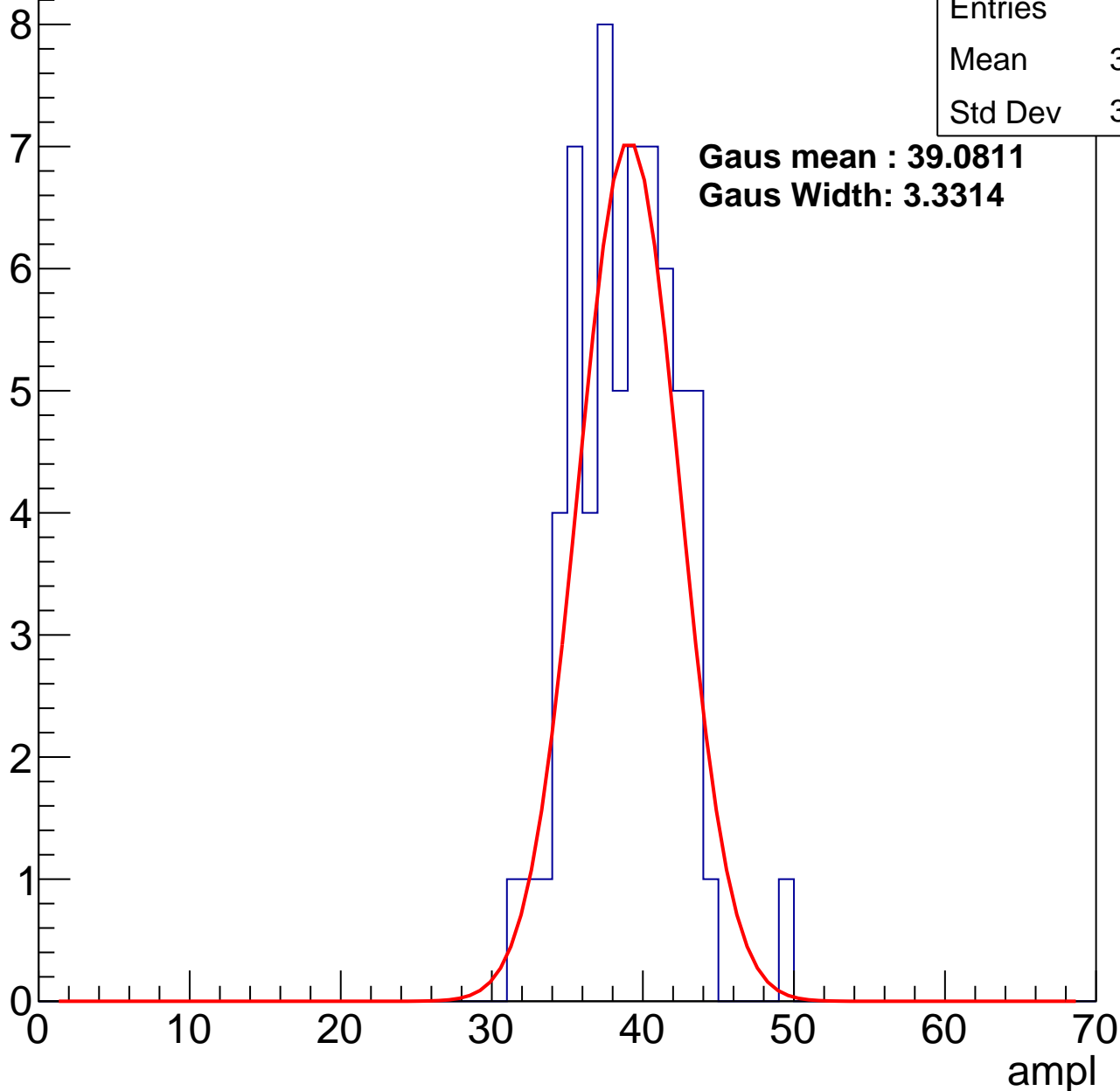
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.48
Std Dev	3.328

Gaus mean : 39.0811

Gaus Width: 3.3314



B0L001S, U22-ch27, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	45.19
Std Dev	3.67

Gaus mean : 45.8940

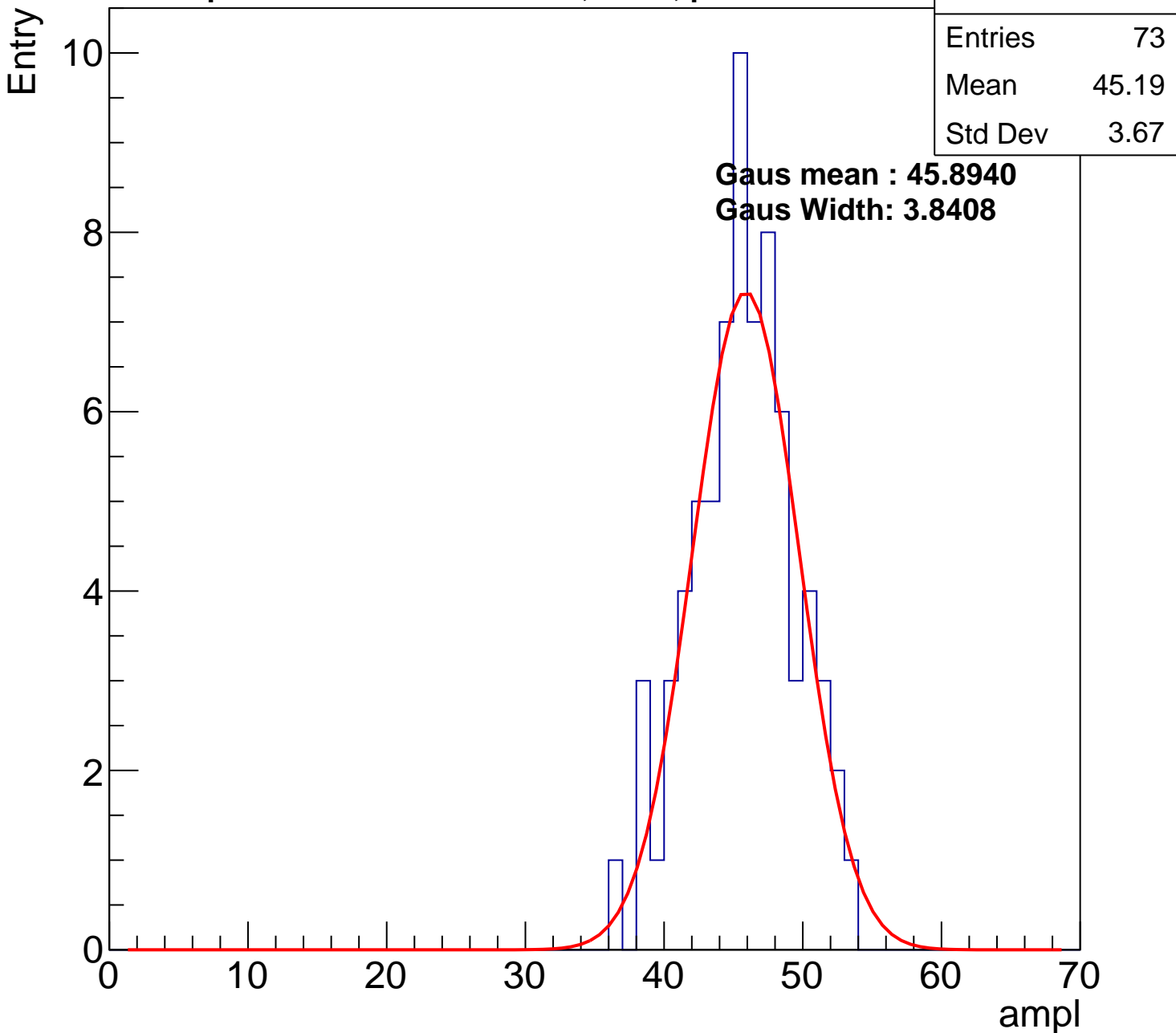
Gaus Width: 3.8408

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

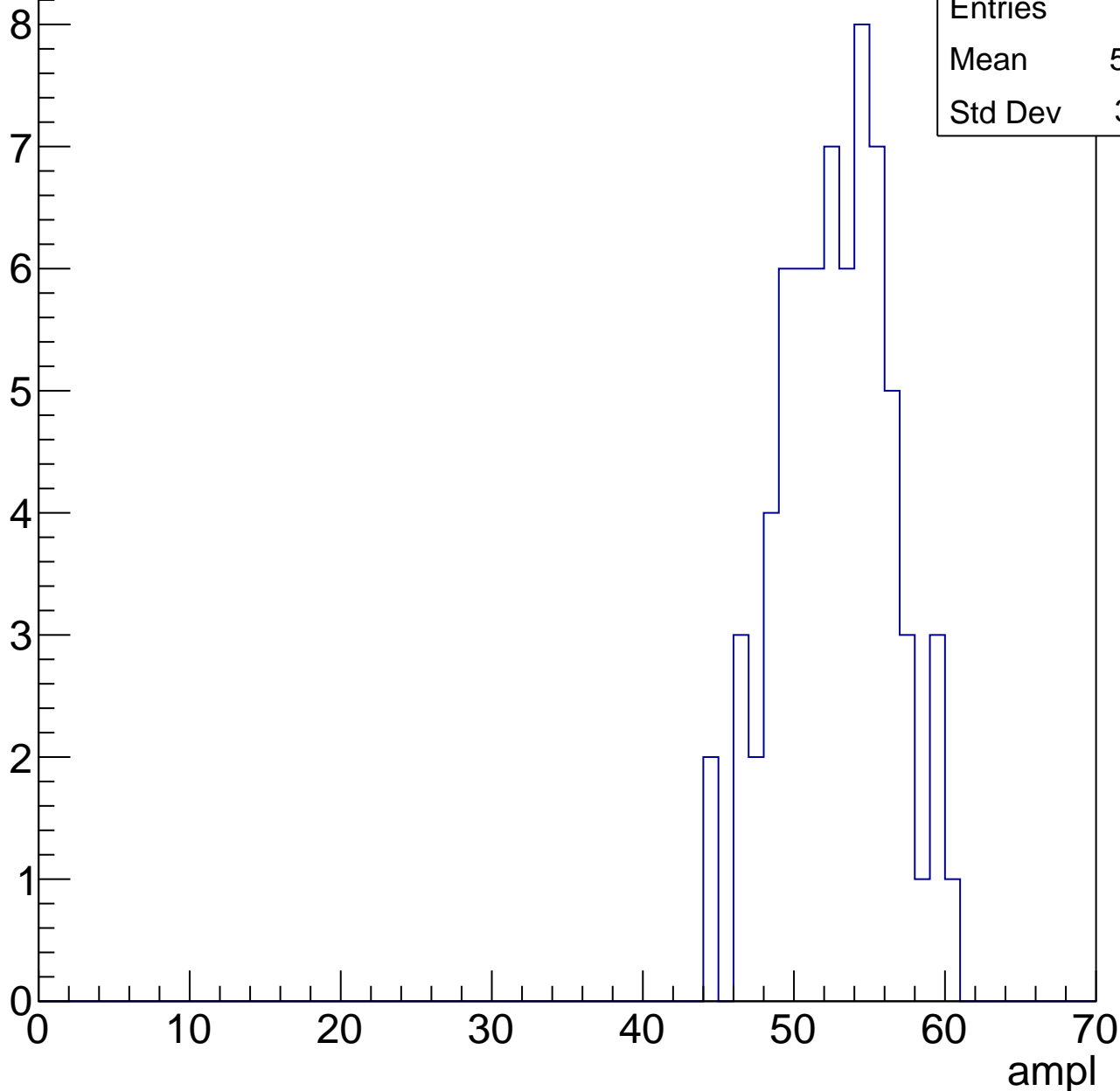


B0L001S, U22-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.24
Std Dev	3.651

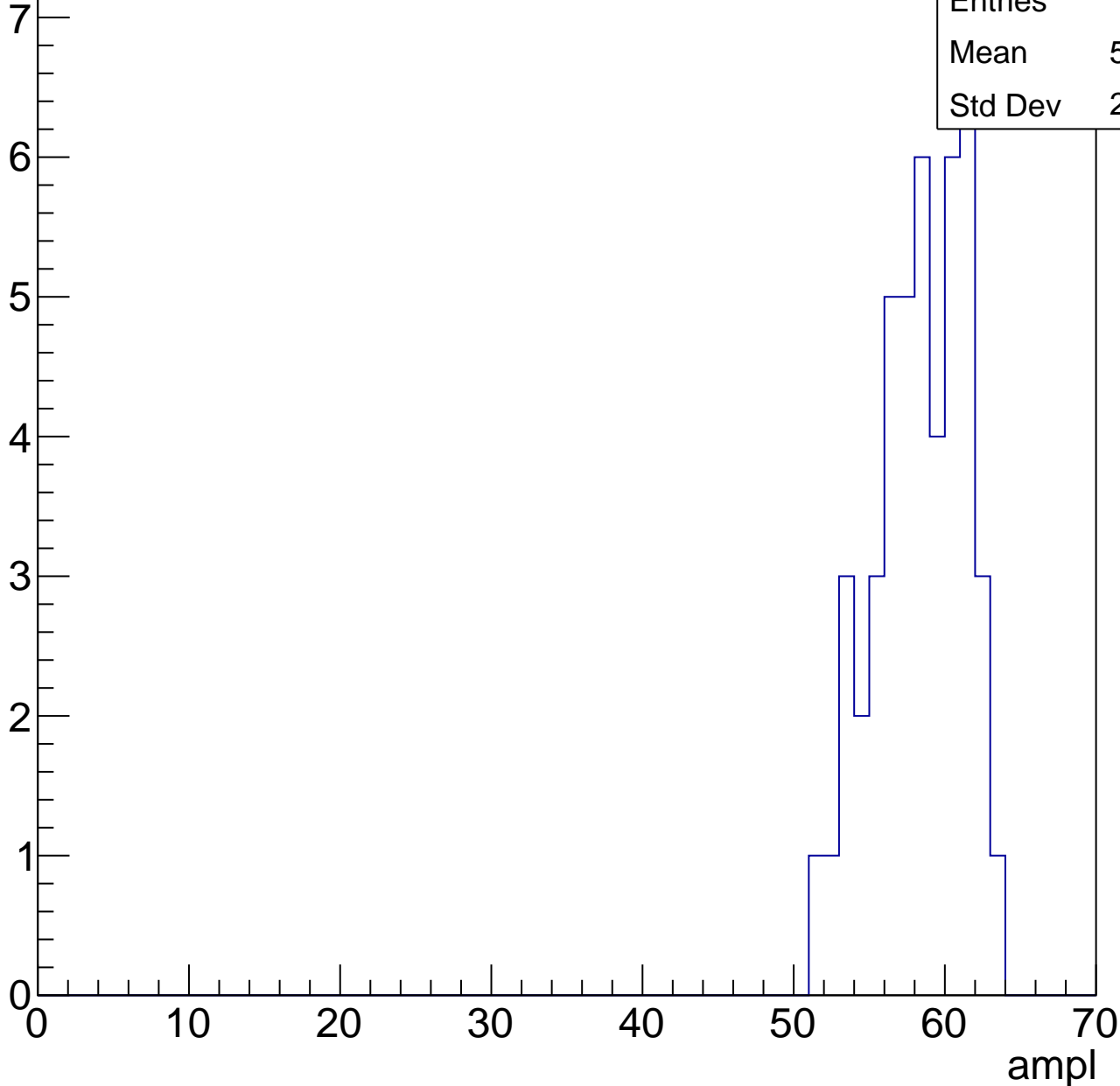


B0L001S, U22-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	57.87
Std Dev	2.944

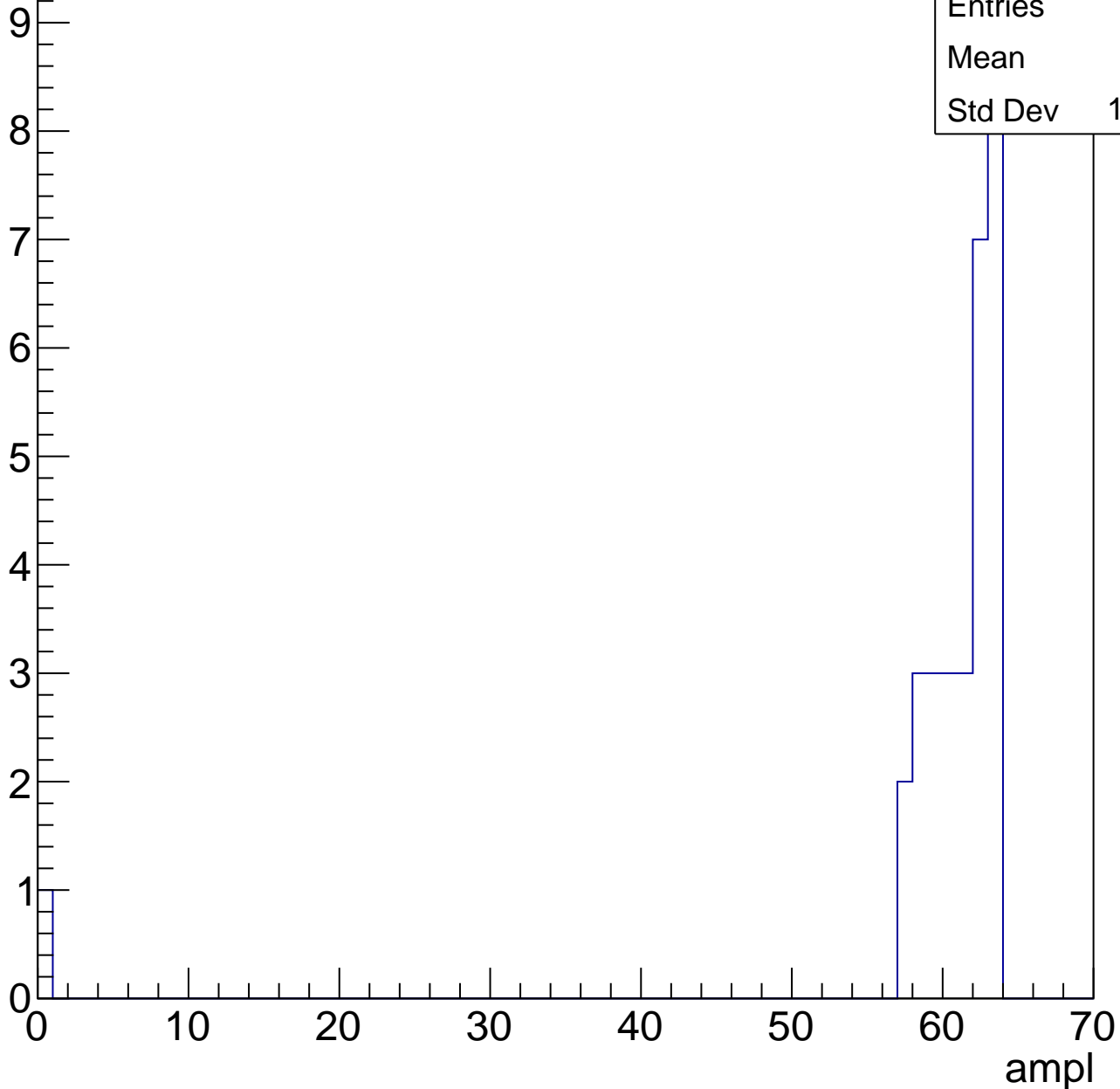


B0L001S, U22-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59
Std Dev	10.95



B0L001S, U22-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch27, adc7

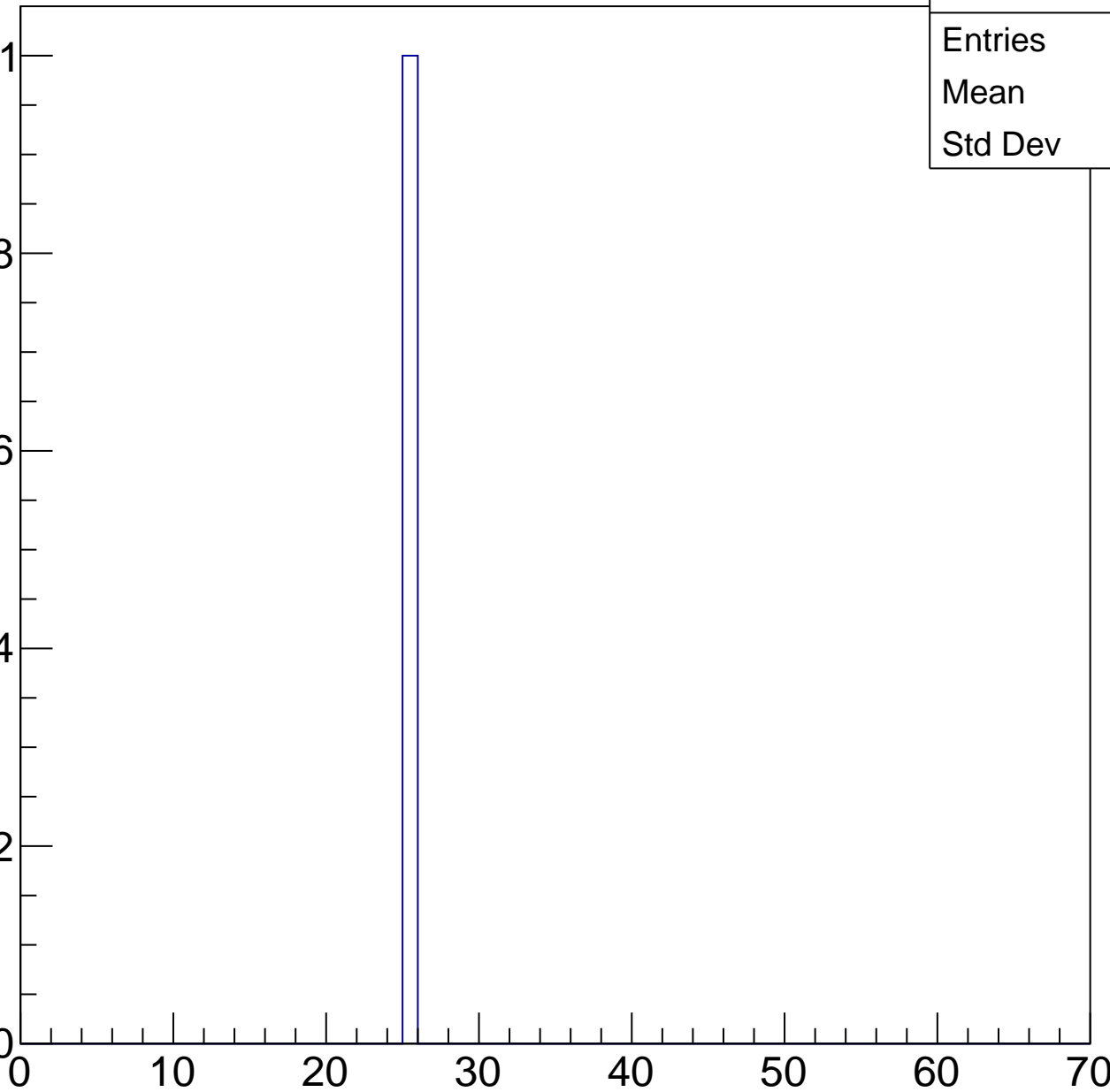
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L001S, U22-ch28, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	28.55
Std Dev	5.892

Gaus mean : 29.8261

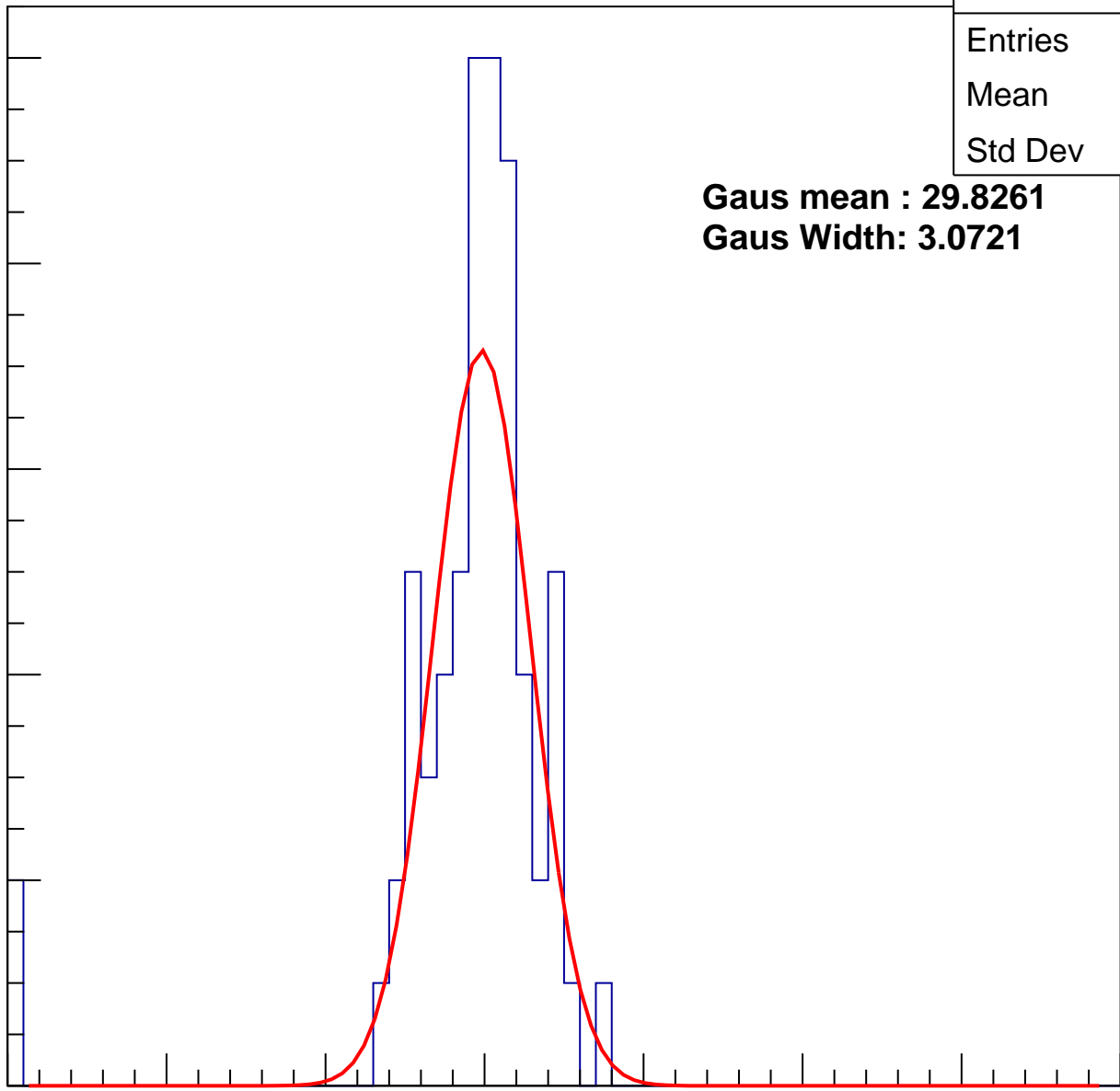
Gaus Width: 3.0721

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



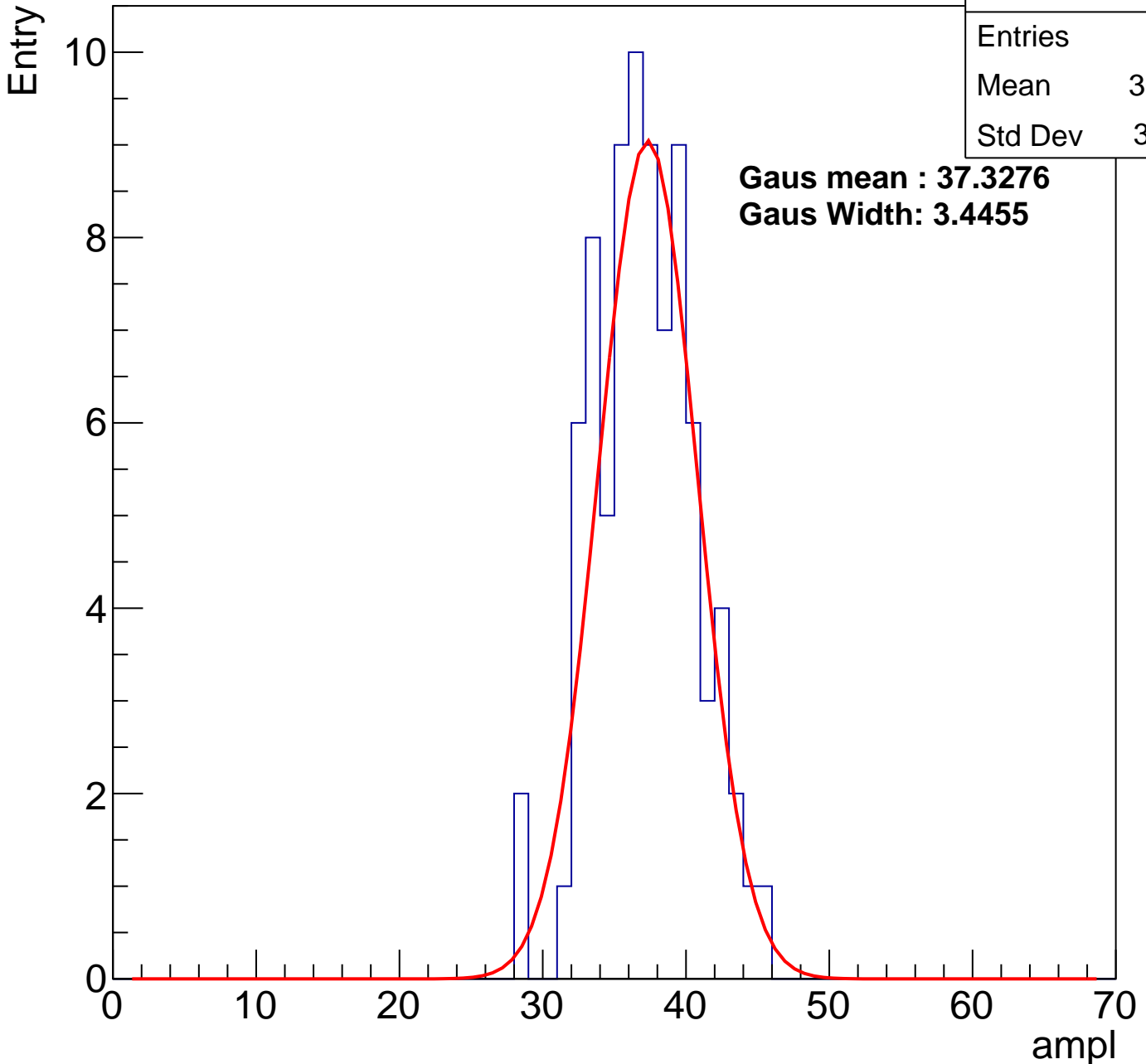
B0L001S, U22-ch28, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	36.67
Std Dev	3.451

Gaus mean : 37.3276

Gaus Width: 3.4455



B0L001S, U22-ch28, adc2

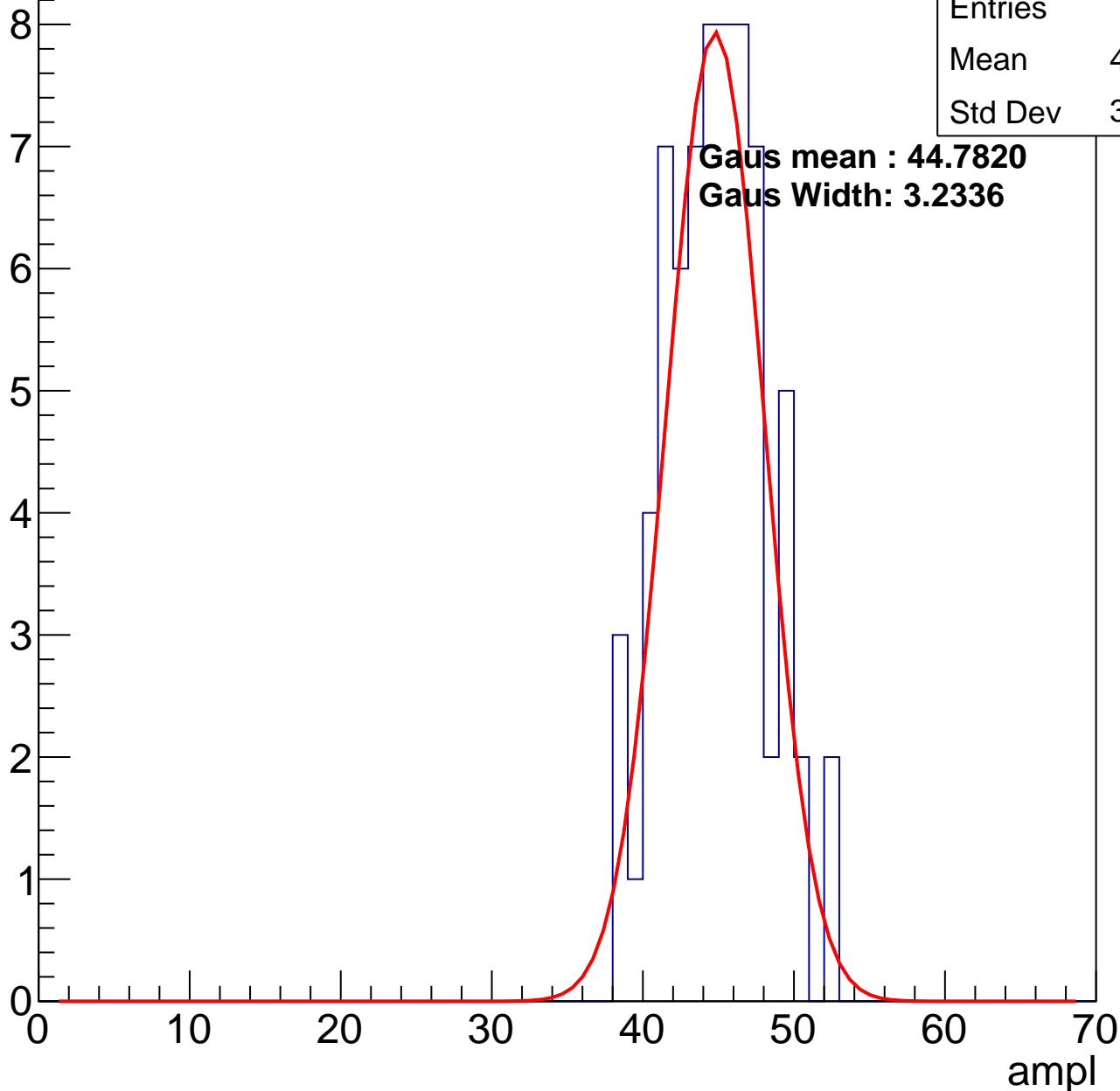
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.39
Std Dev	3.253

Gaus mean : 44.7820

Gaus Width: 3.2336

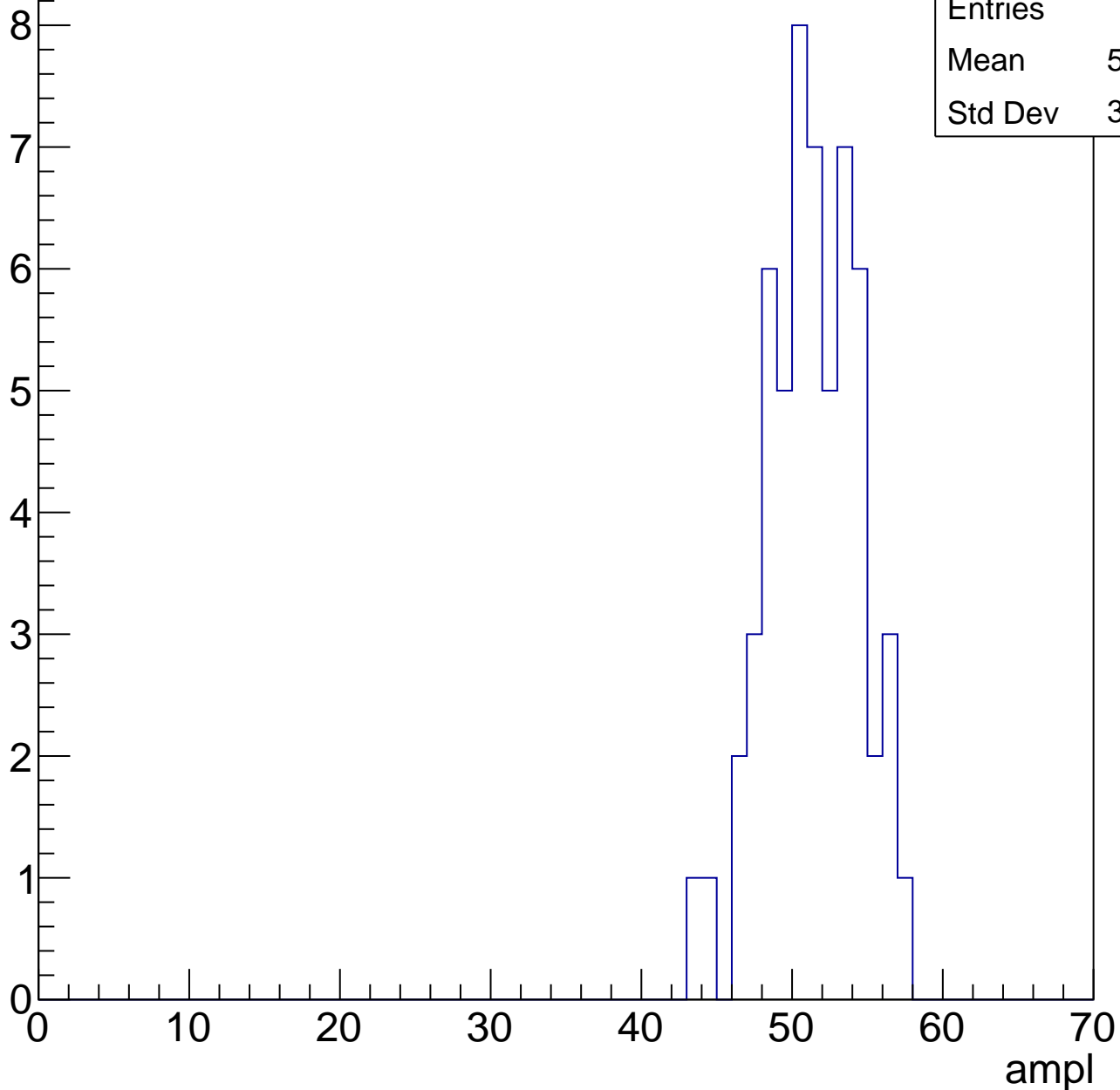


B0L001S, U22-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.88
Std Dev	3.027

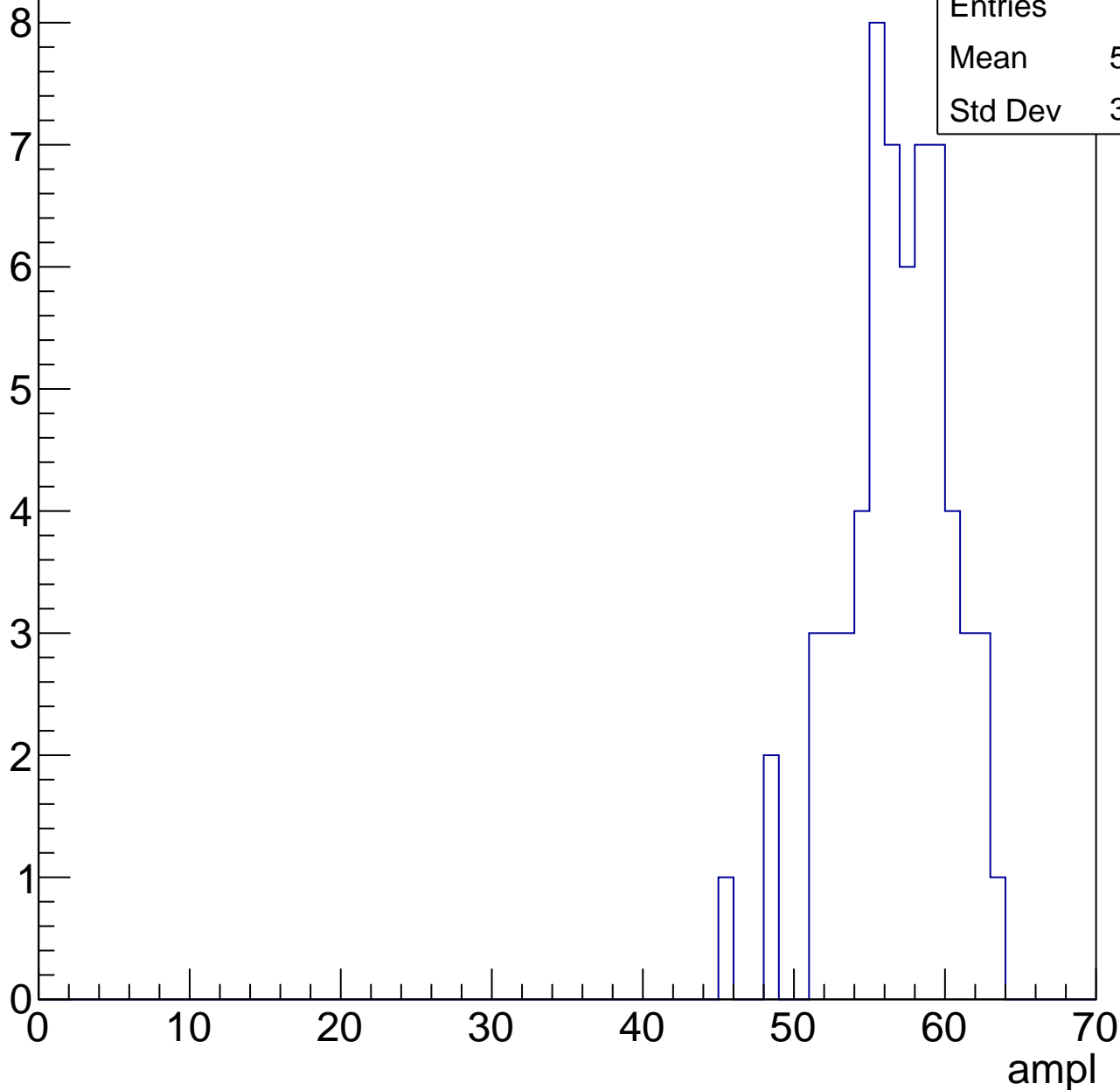


B0L001S, U22-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.29
Std Dev	3.616

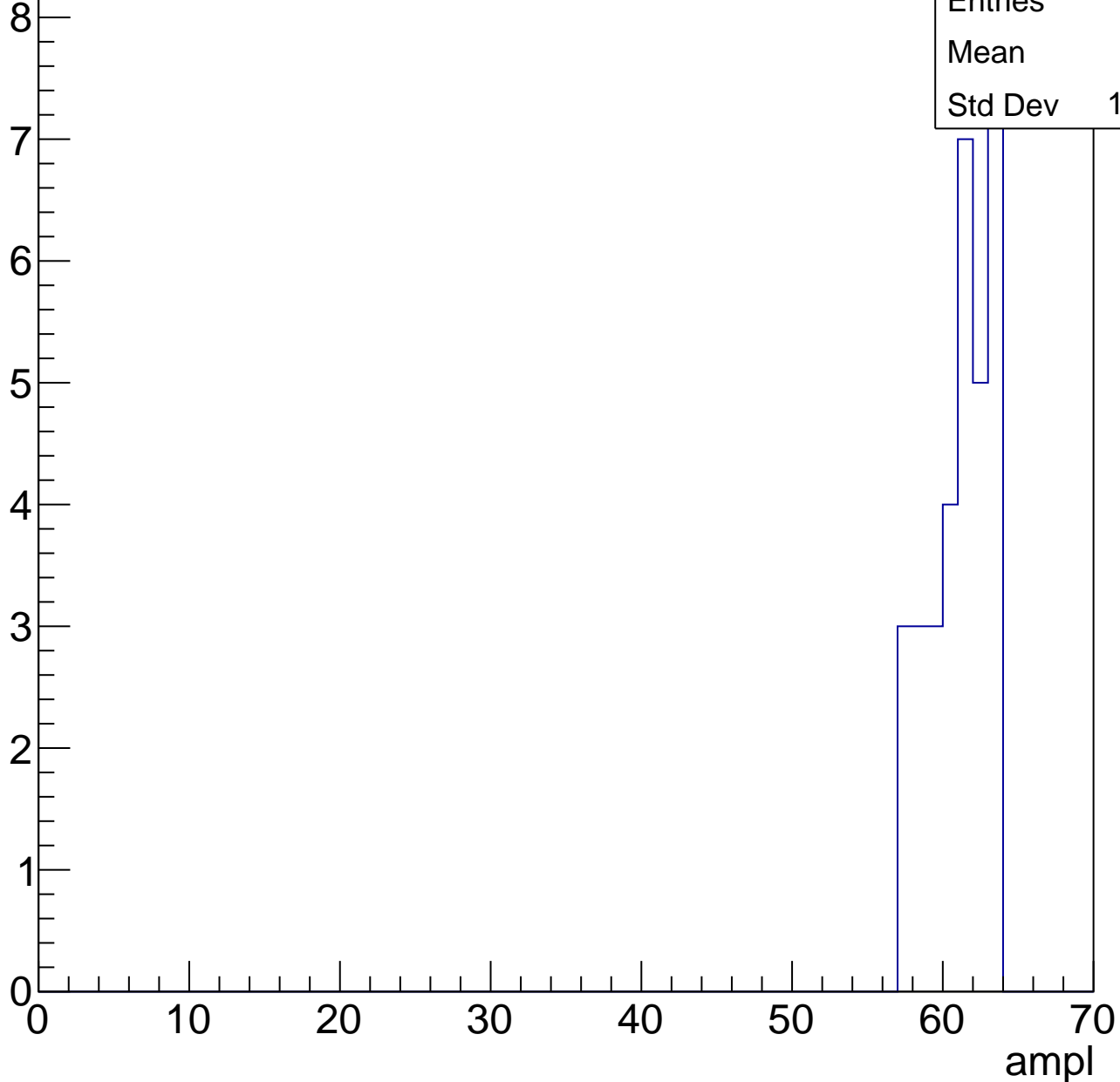


B0L001S, U22-ch28, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	60.7
Std Dev	1.946



B0L001S, U22-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

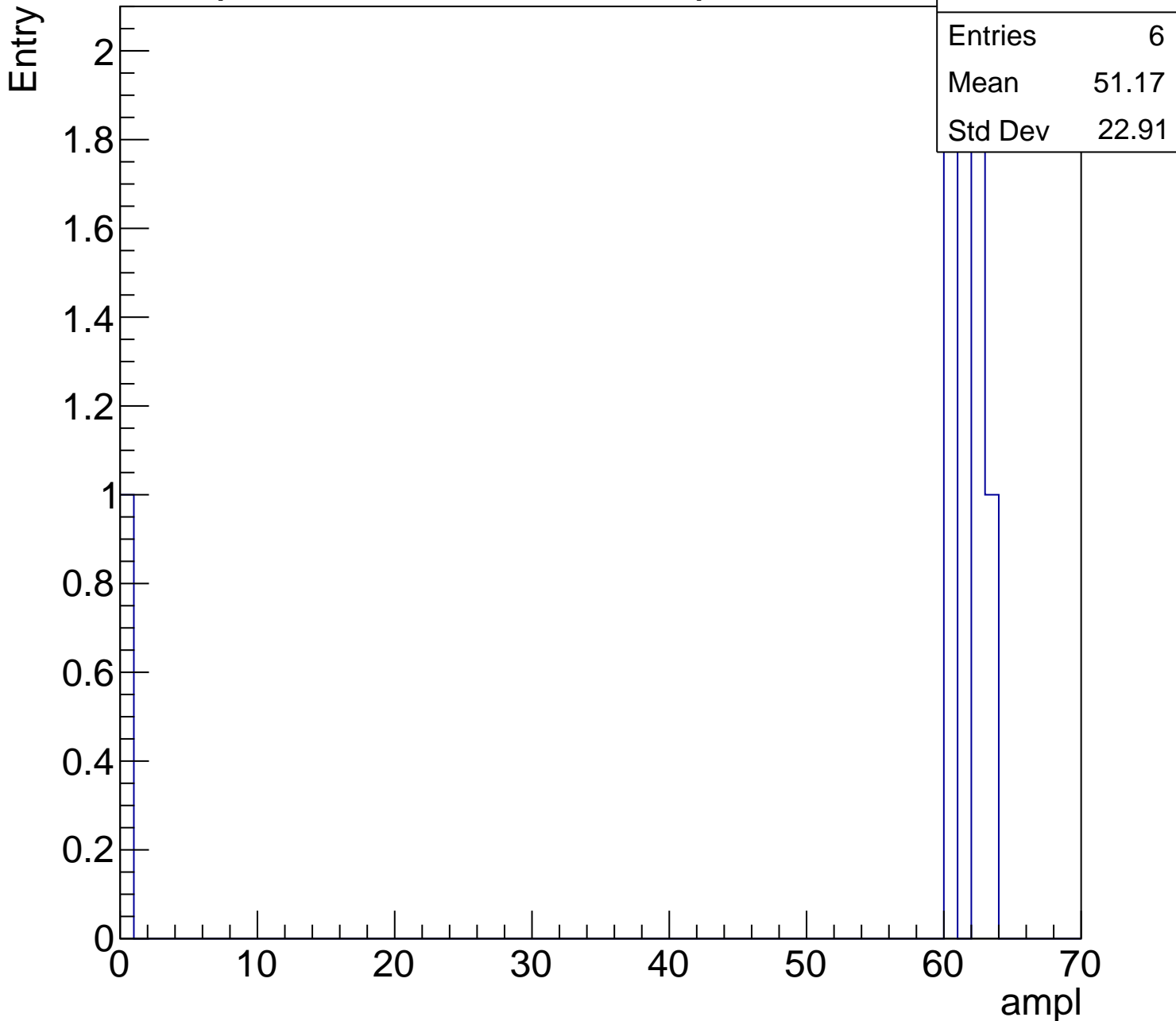
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.91

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch29, adc0

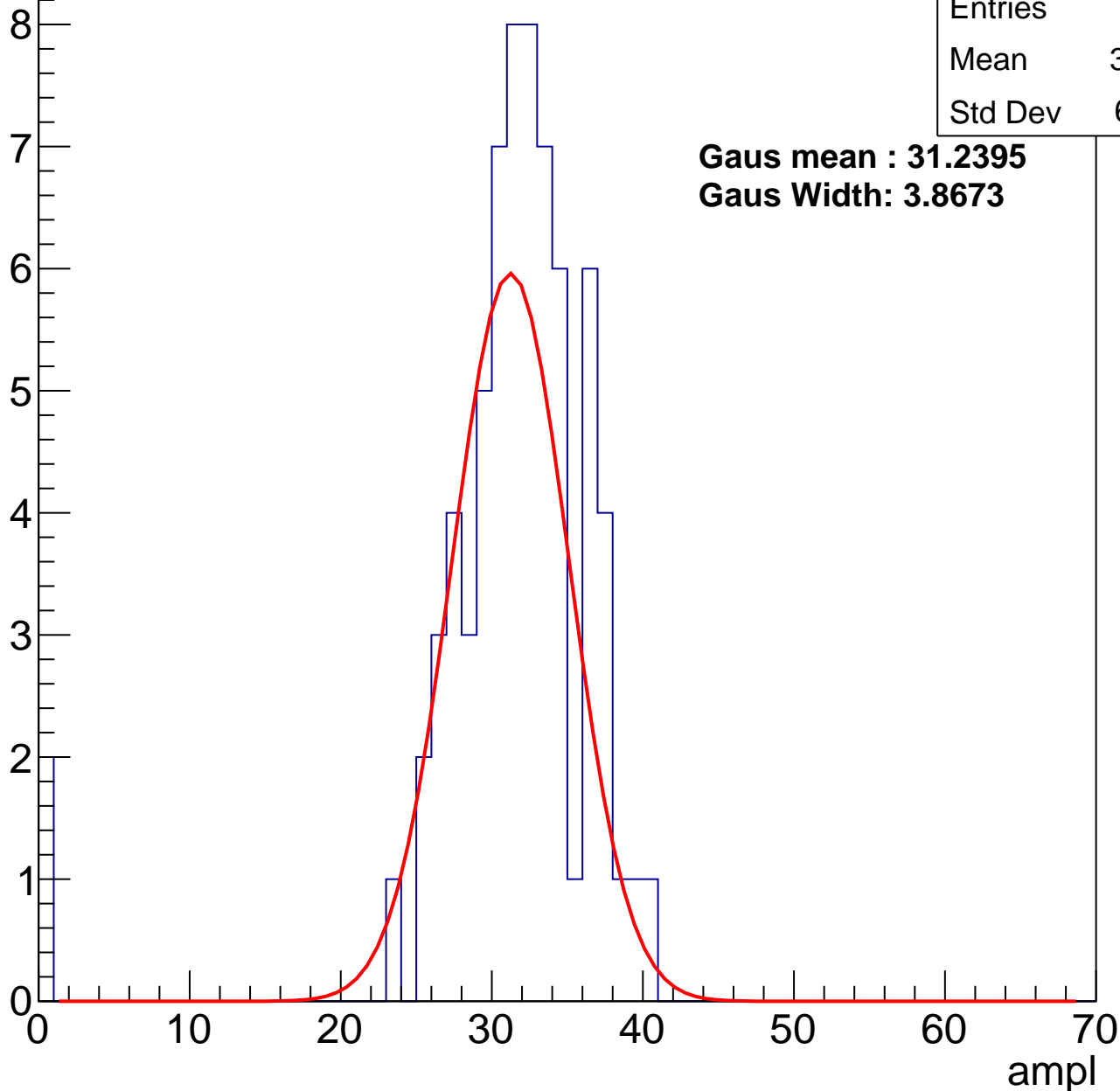
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	30.76
Std Dev	6.371

Gaus mean : 31.2395

Gaus Width: 3.8673



B0L001S, U22-ch29, adc1

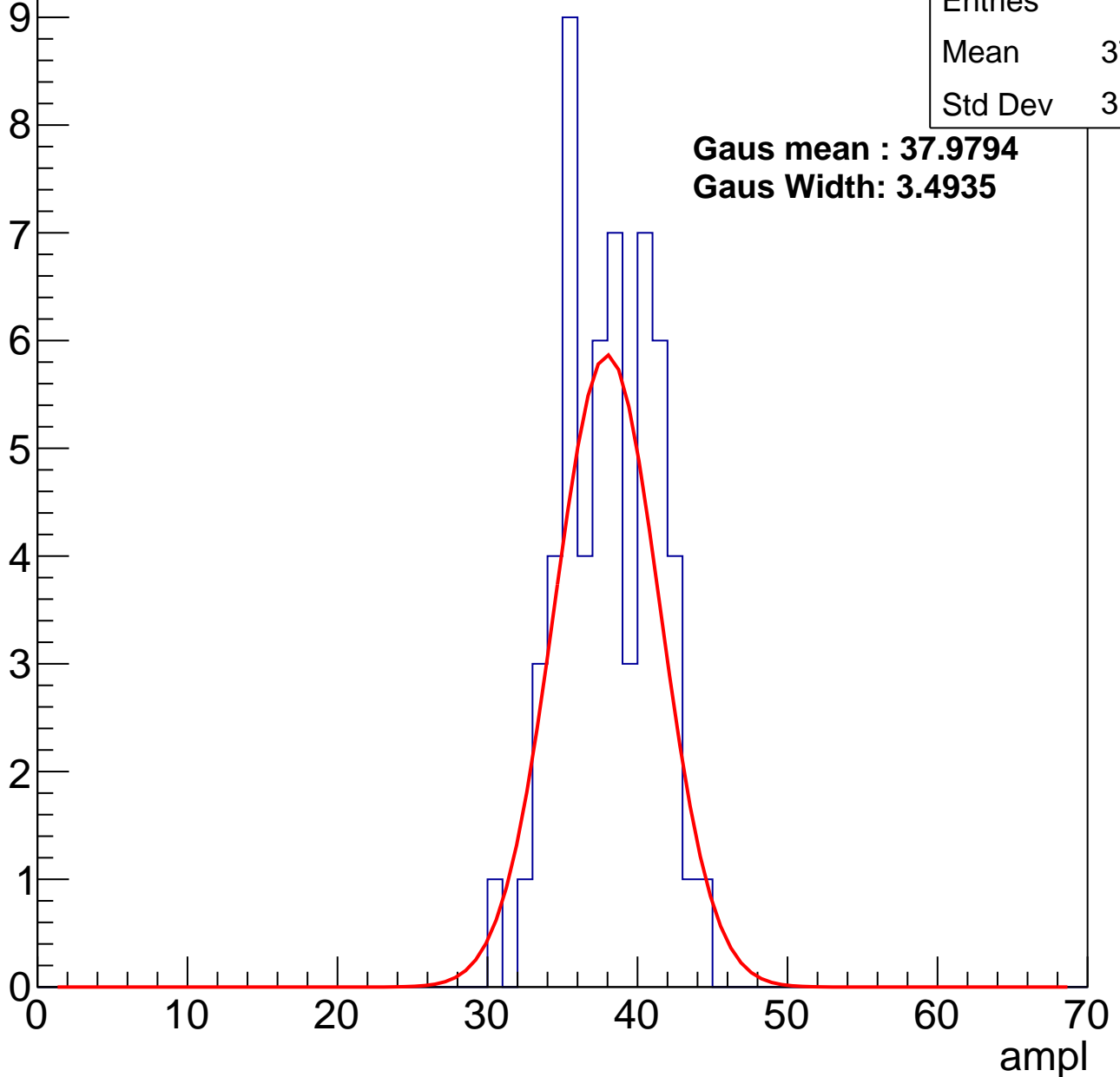
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.58
Std Dev	3.083

Gaus mean : 37.9794

Gaus Width: 3.4935



B0L001S, U22-ch29, adc2

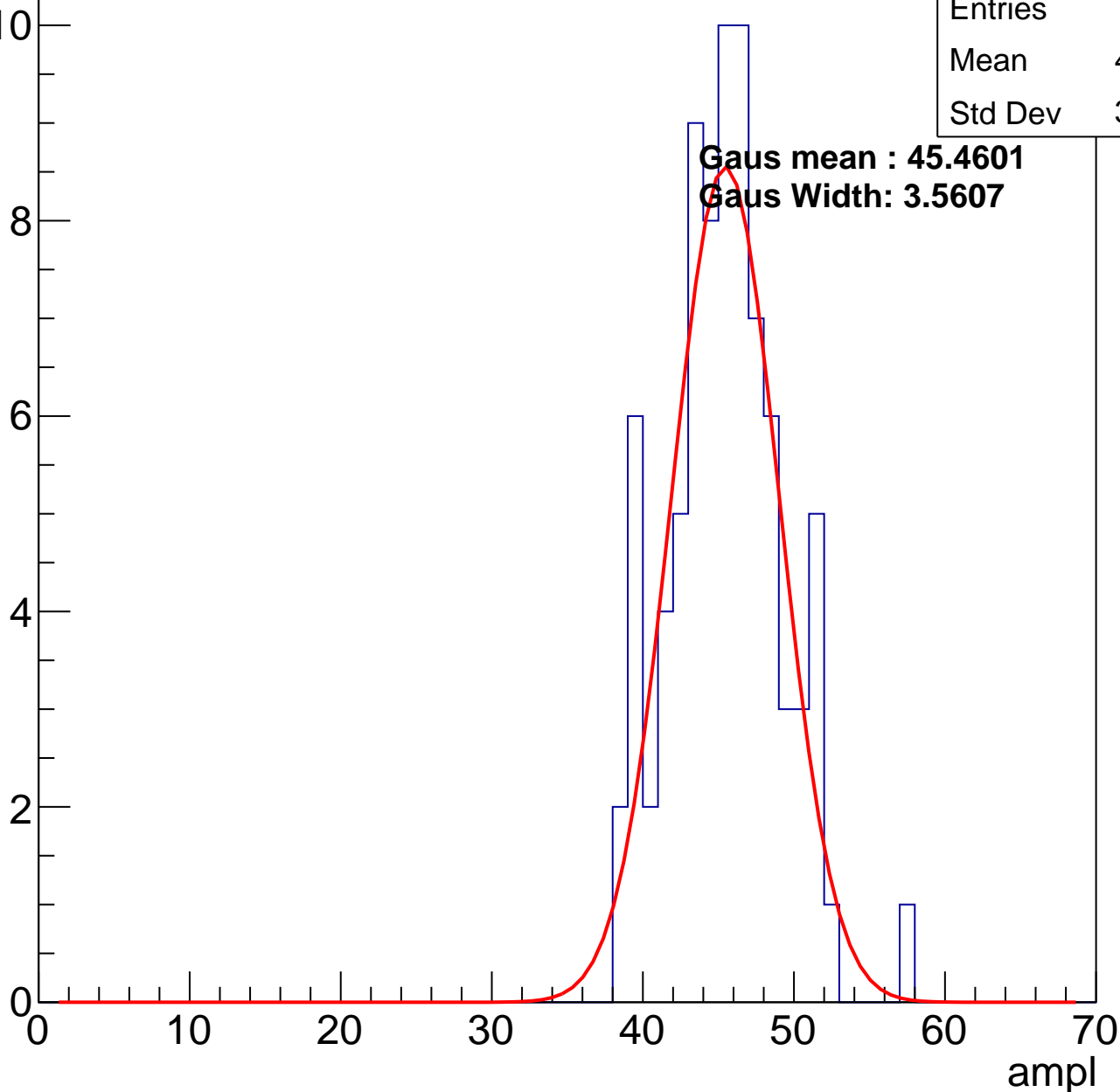
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	45.01
Std Dev	3.671

Gaus mean : 45.4601

Gaus Width: 3.5607

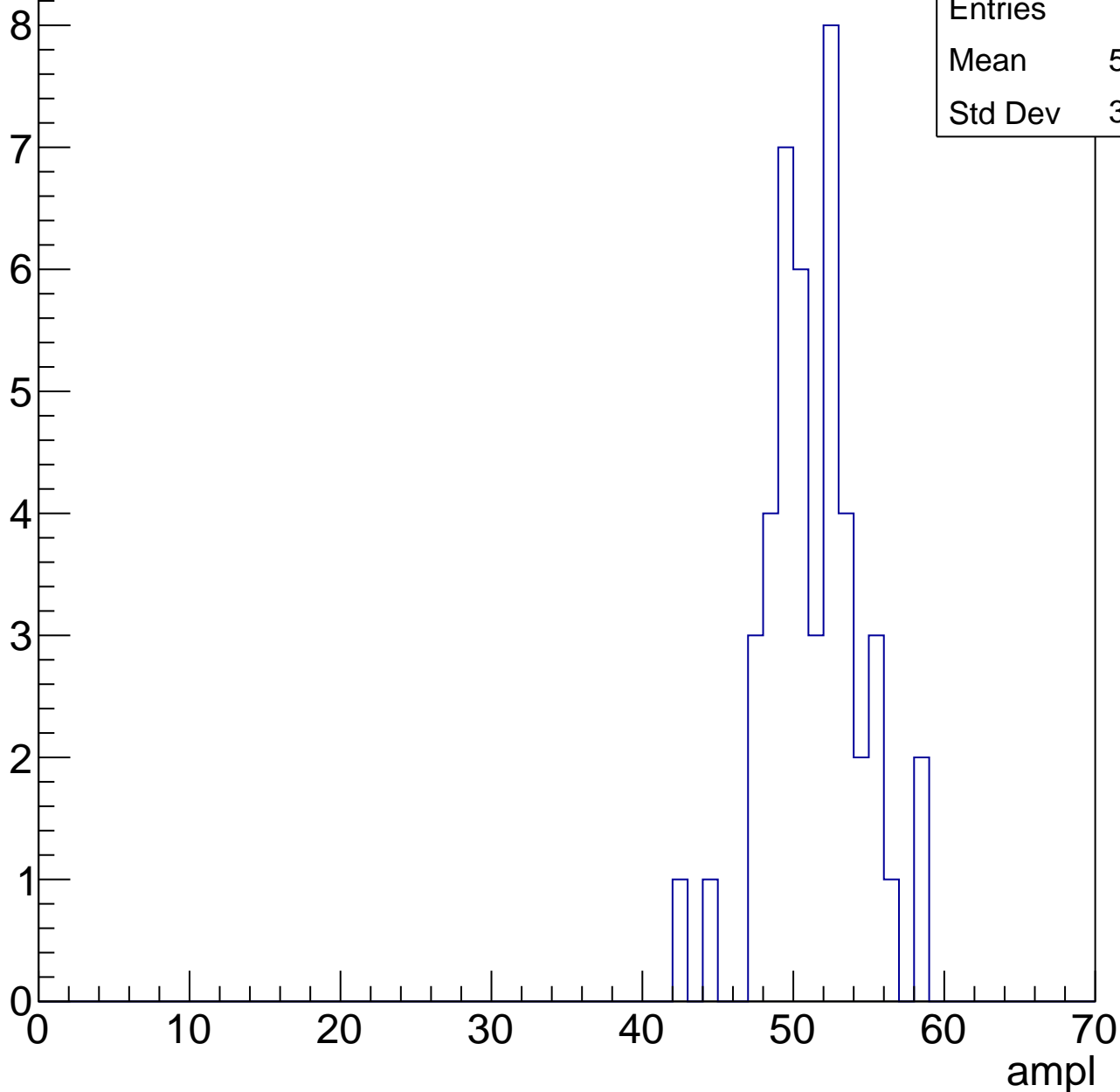


B0L001S, U22-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	50.84
Std Dev	3.197



B0L001S, U22-ch29, adc4

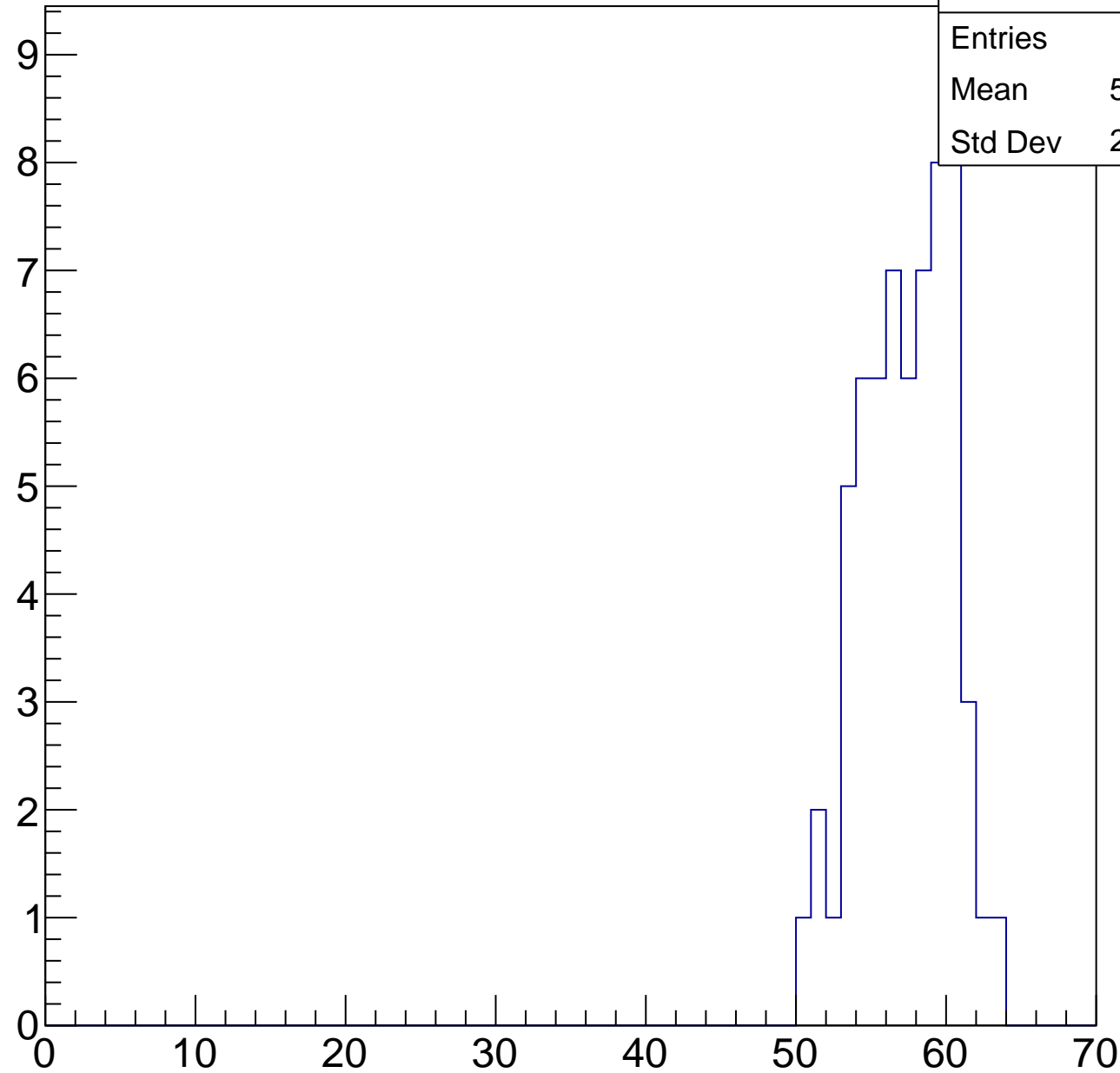
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	56.87
Std Dev	2.925

ampl

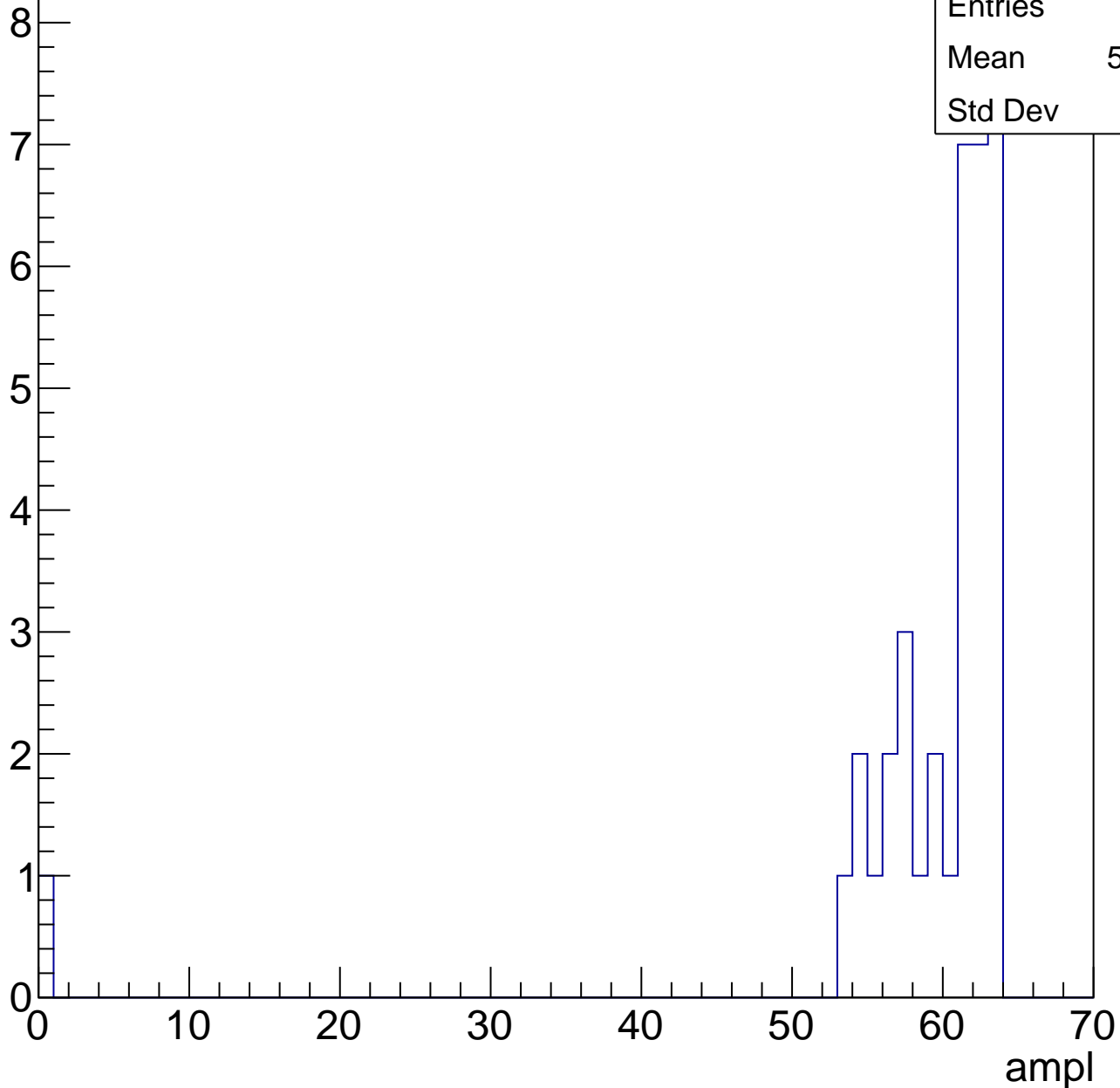


B0L001S, U22-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.33
Std Dev	10.3



B0L001S, U22-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B0L001S, U22-ch29, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch30, adc0

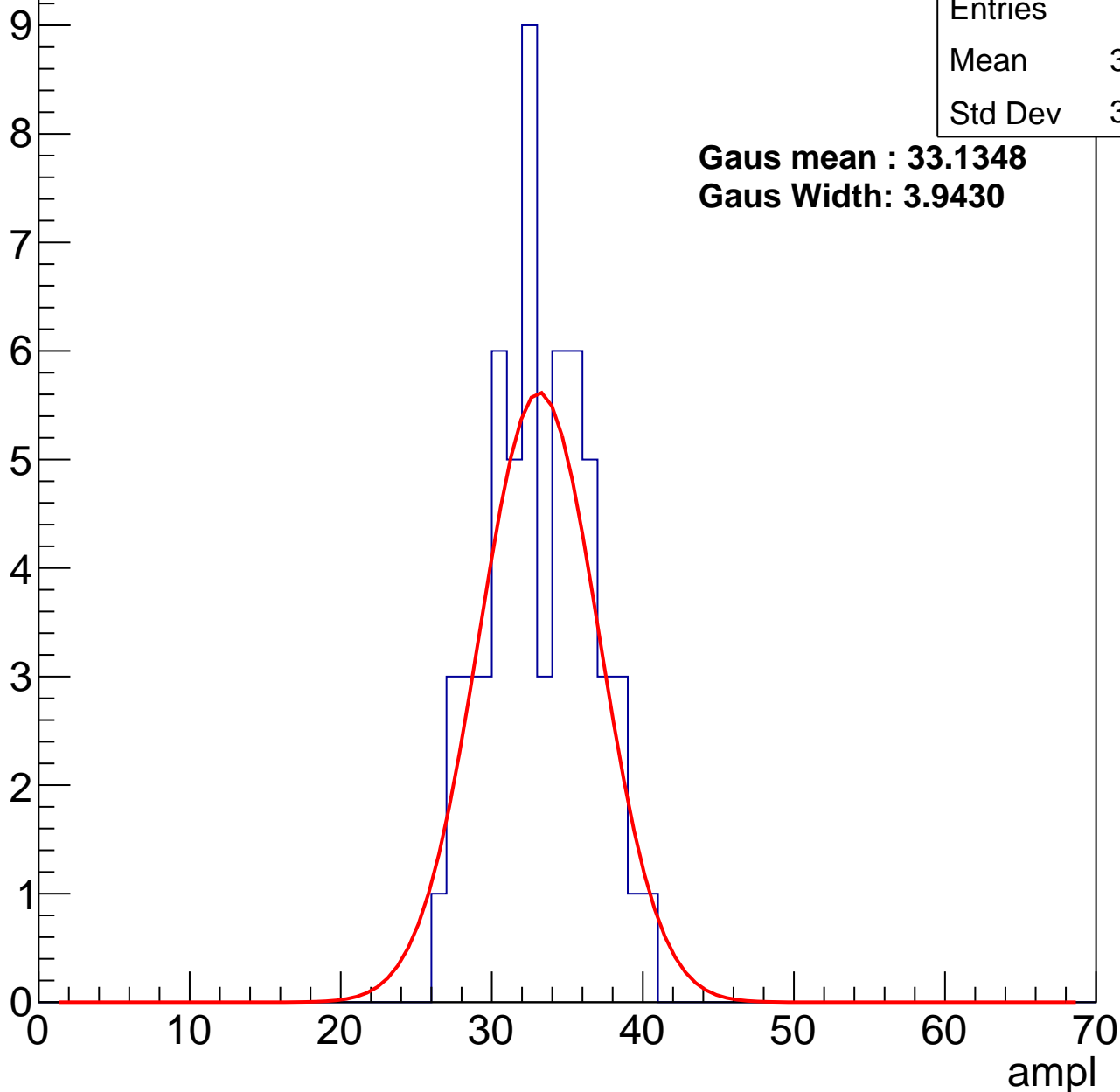
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	32.72
Std Dev	3.326

Gaus mean : 33.1348

Gaus Width: 3.9430



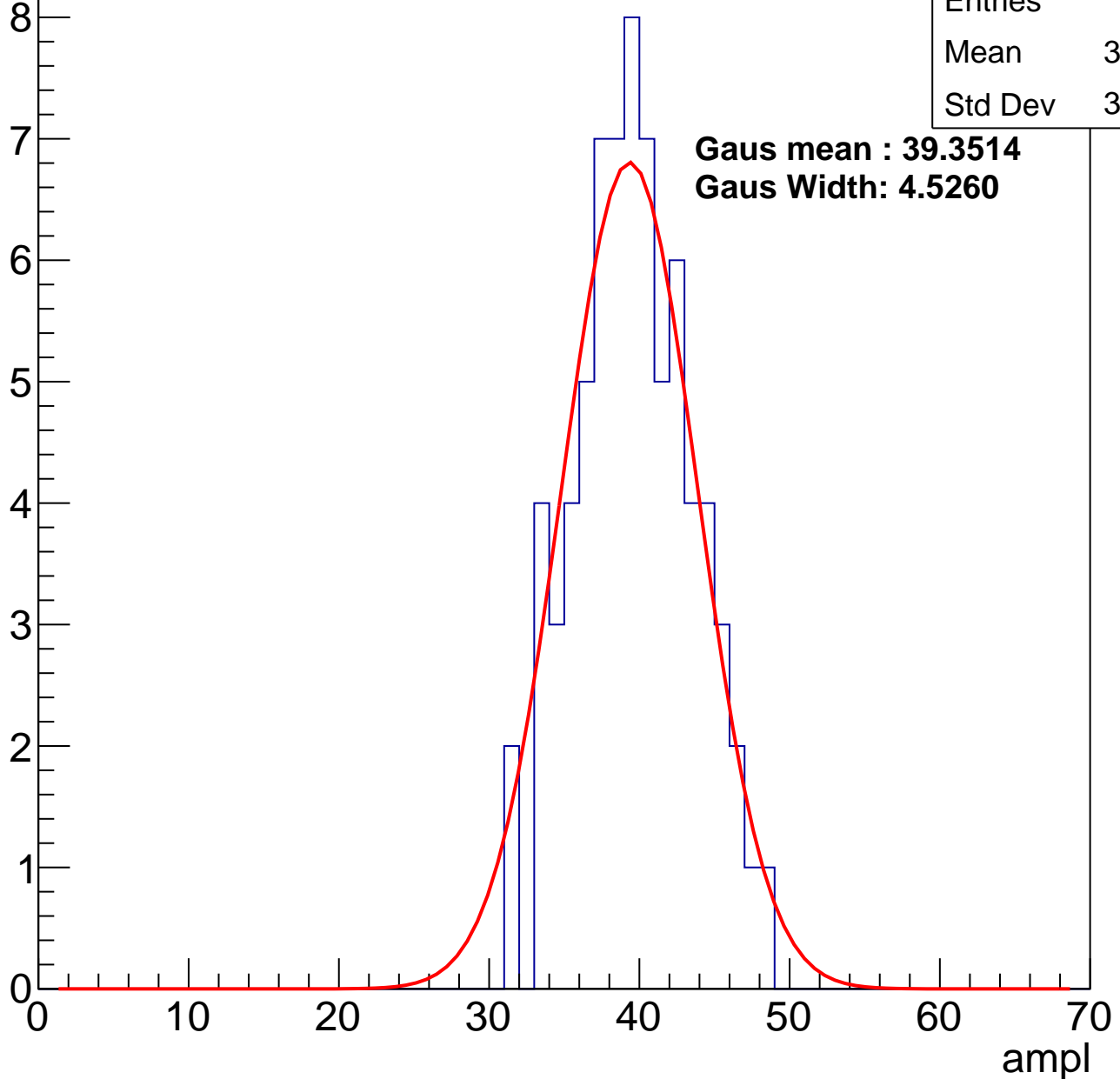
B0L001S, U22-ch30, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	39.18
Std Dev	3.855

Gaus mean : 39.3514
Gaus Width: 4.5260



B0L001S, U22-ch30, adc2

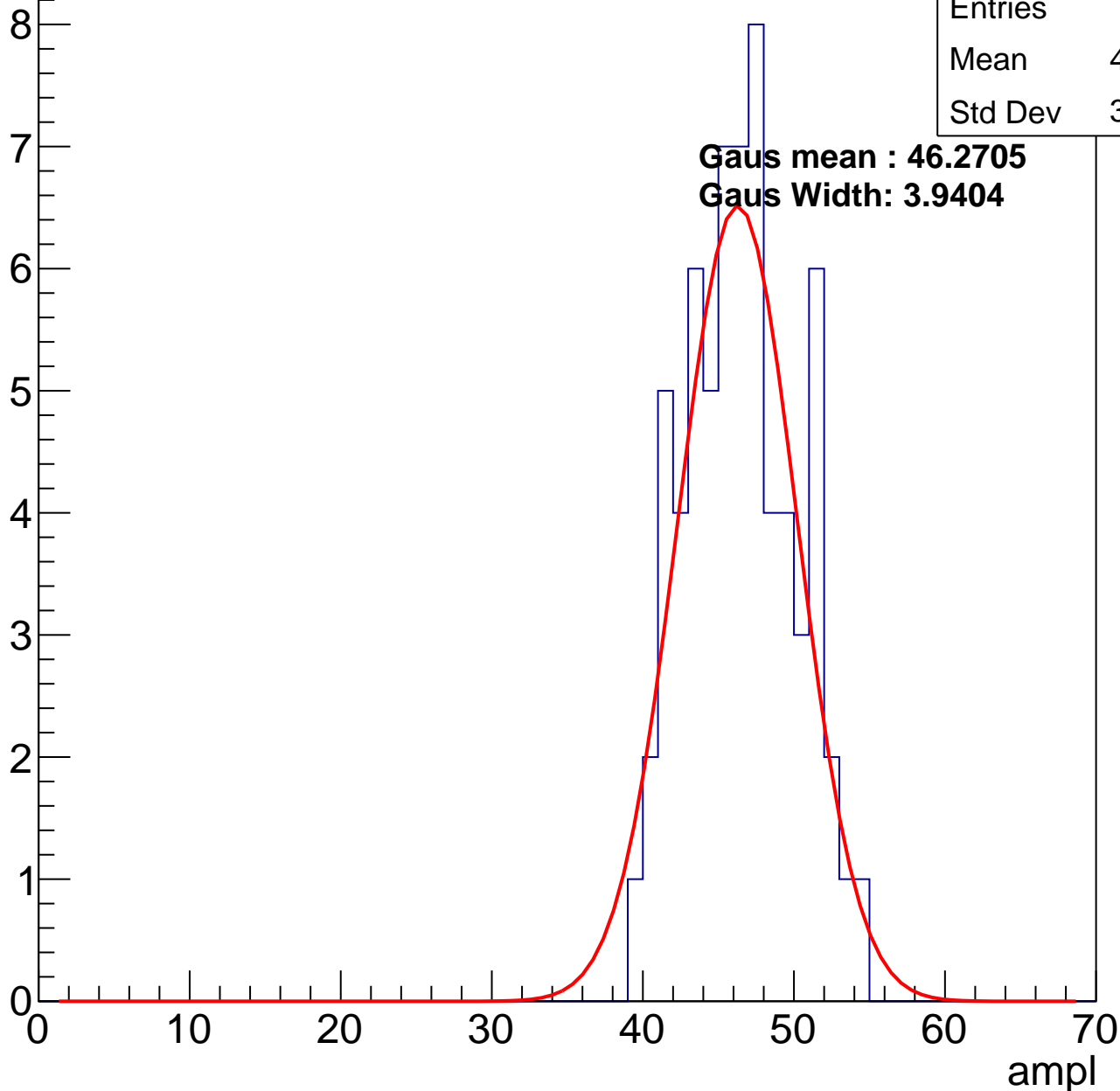
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	46.03
Std Dev	3.555

Gaus mean : 46.2705

Gaus Width: 3.9404

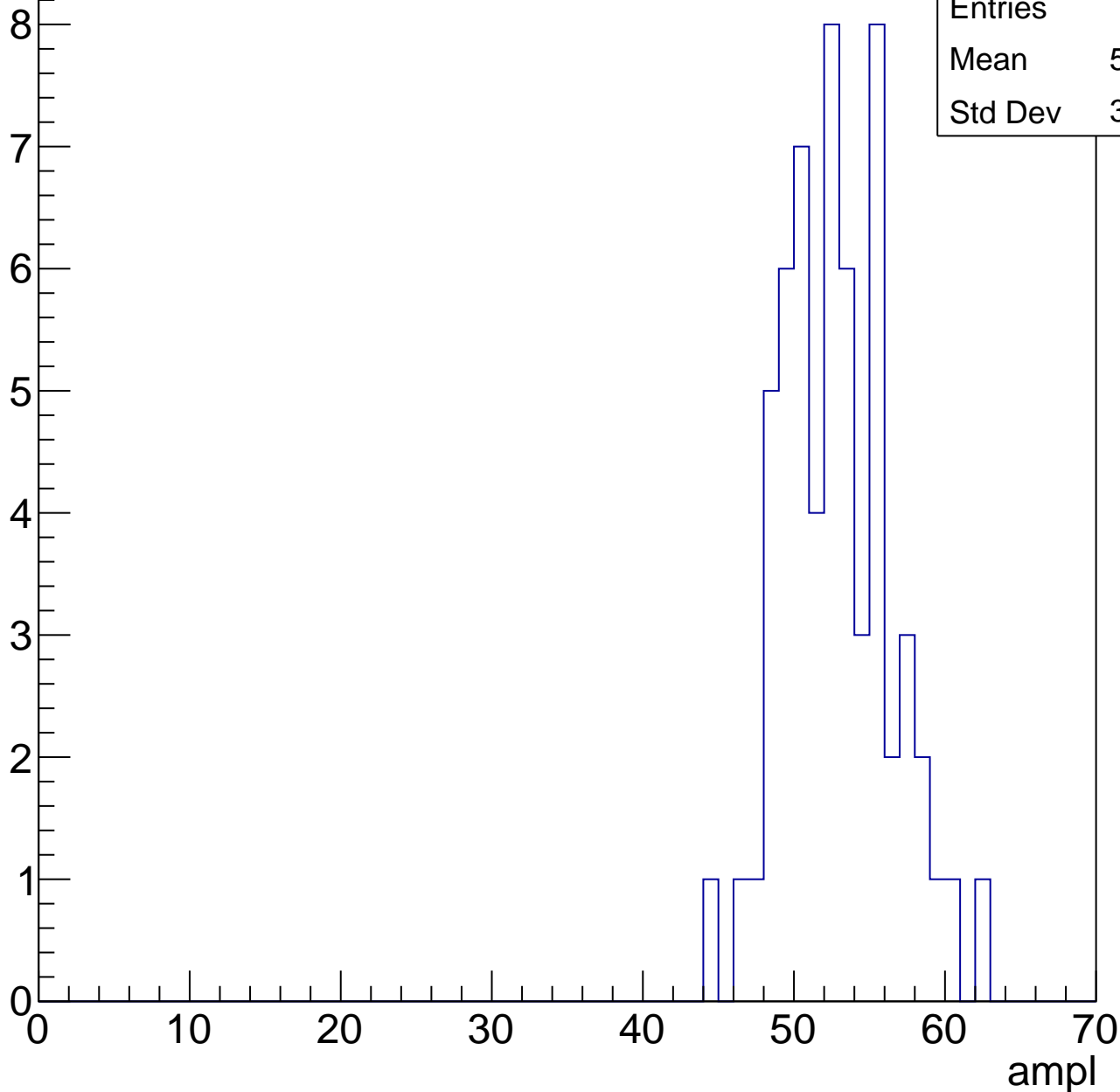


B0L001S, U22-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

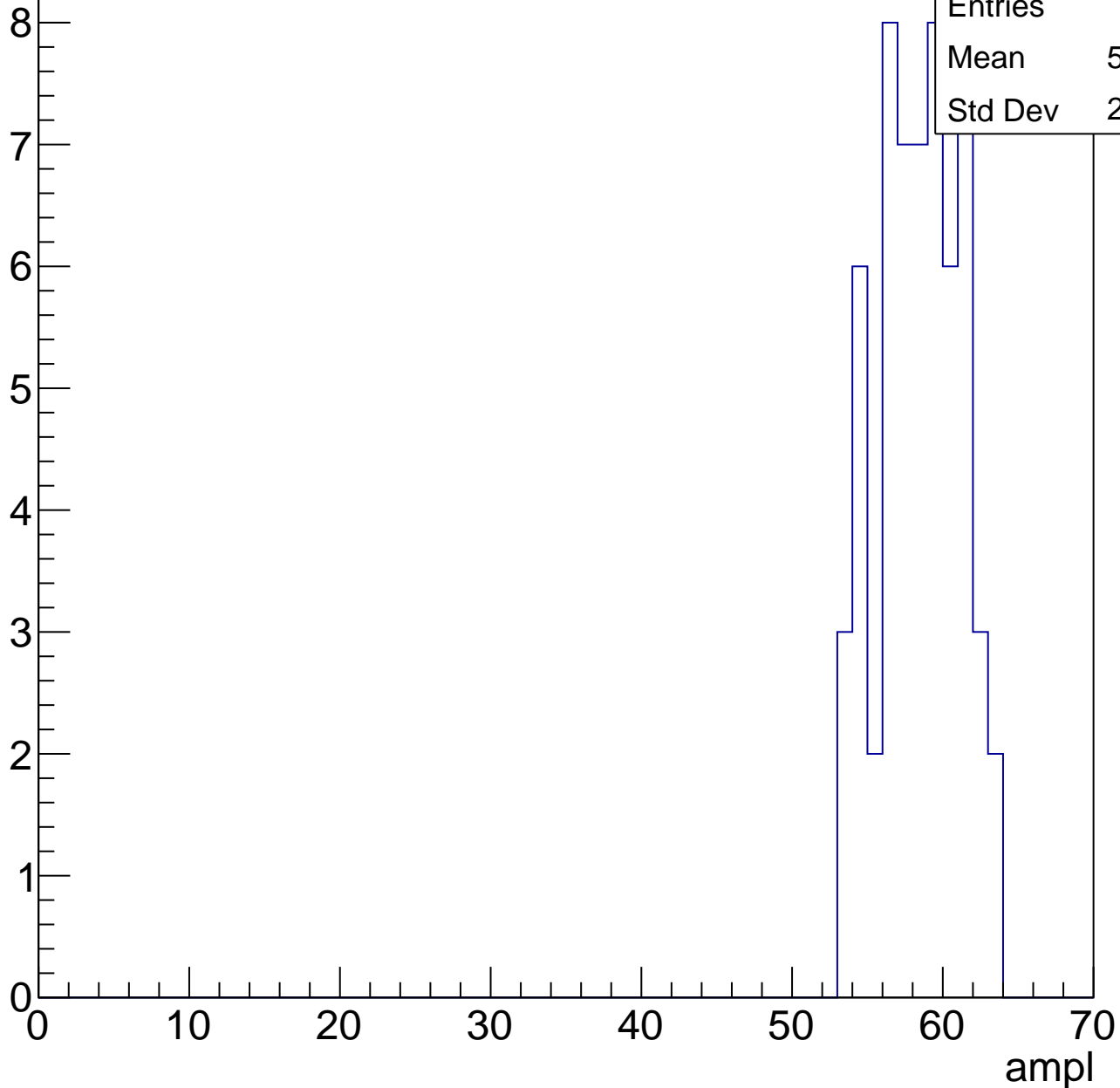
Entries	60
Mean	52.35
Std Dev	3.572



B0L001S, U22-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

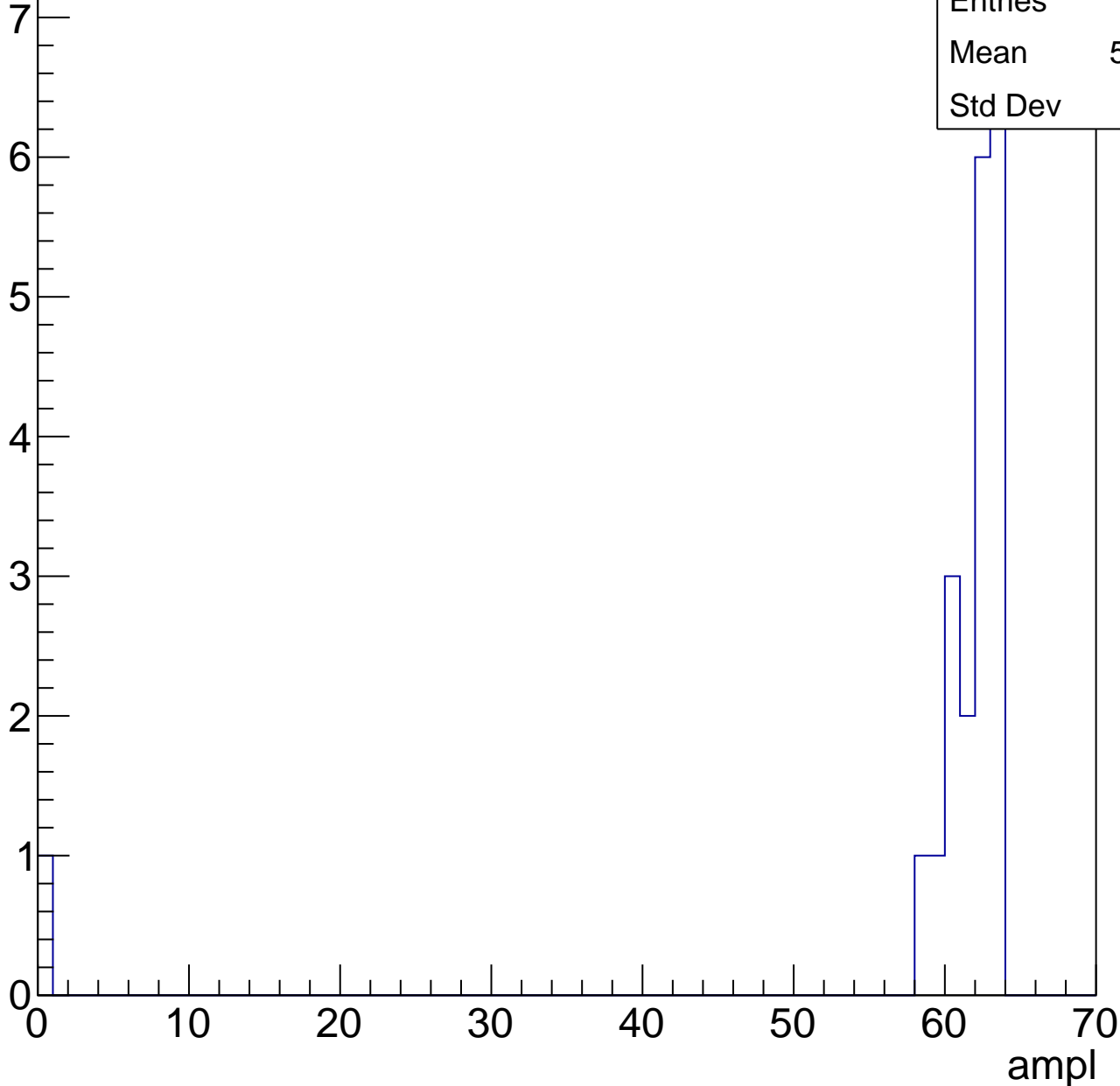


B0L001S, U22-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.67
Std Dev	13.2



B0L001S, U22-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

B0L001S, U22-ch31, adc0

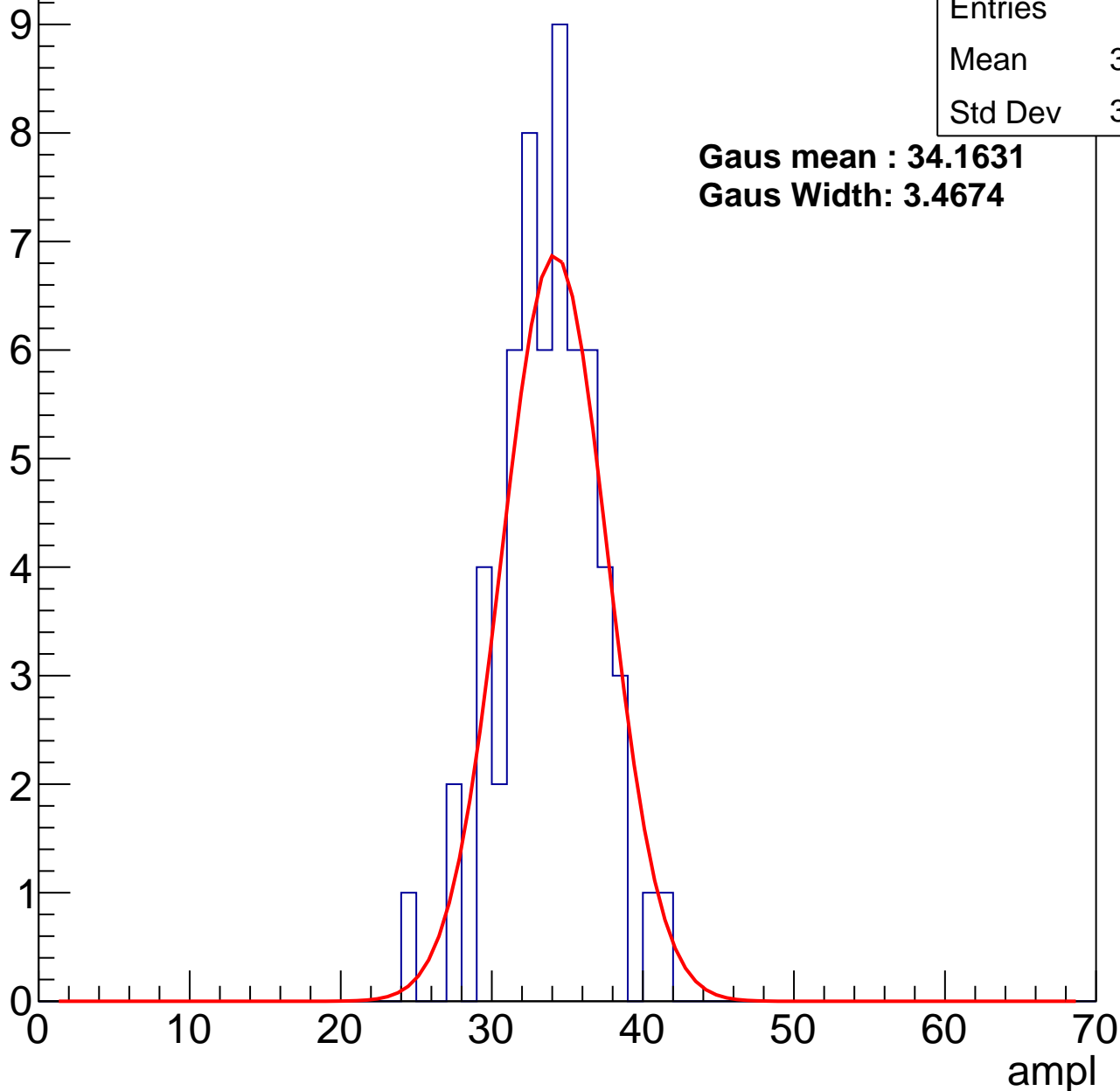
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	33.37
Std Dev	3.183

Gaus mean : 34.1631

Gaus Width: 3.4674



B0L001S, U22-ch31, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	40.12
Std Dev	3.594

Gaus mean : 40.3500

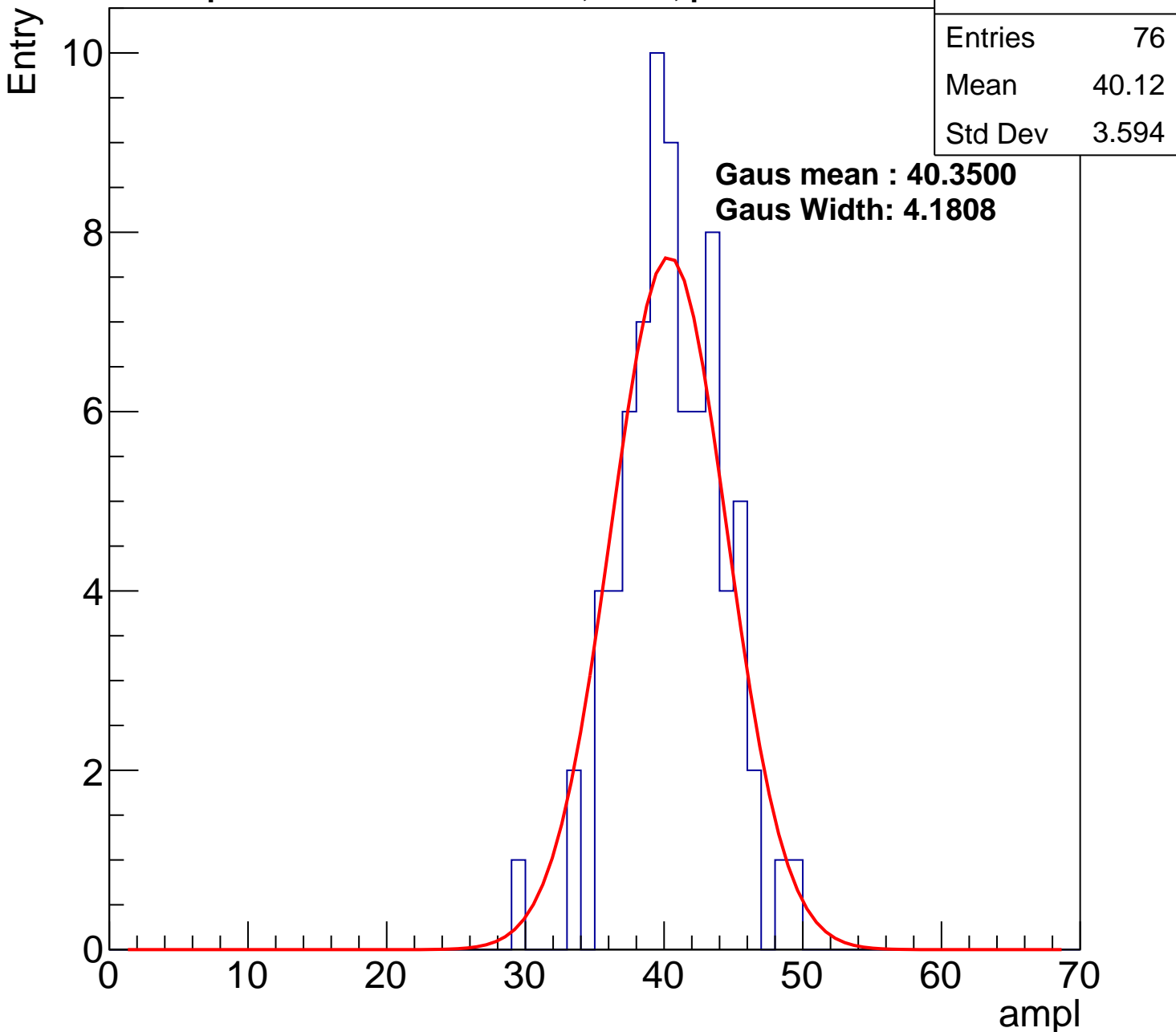
Gaus Width: 4.1808

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch31, adc2

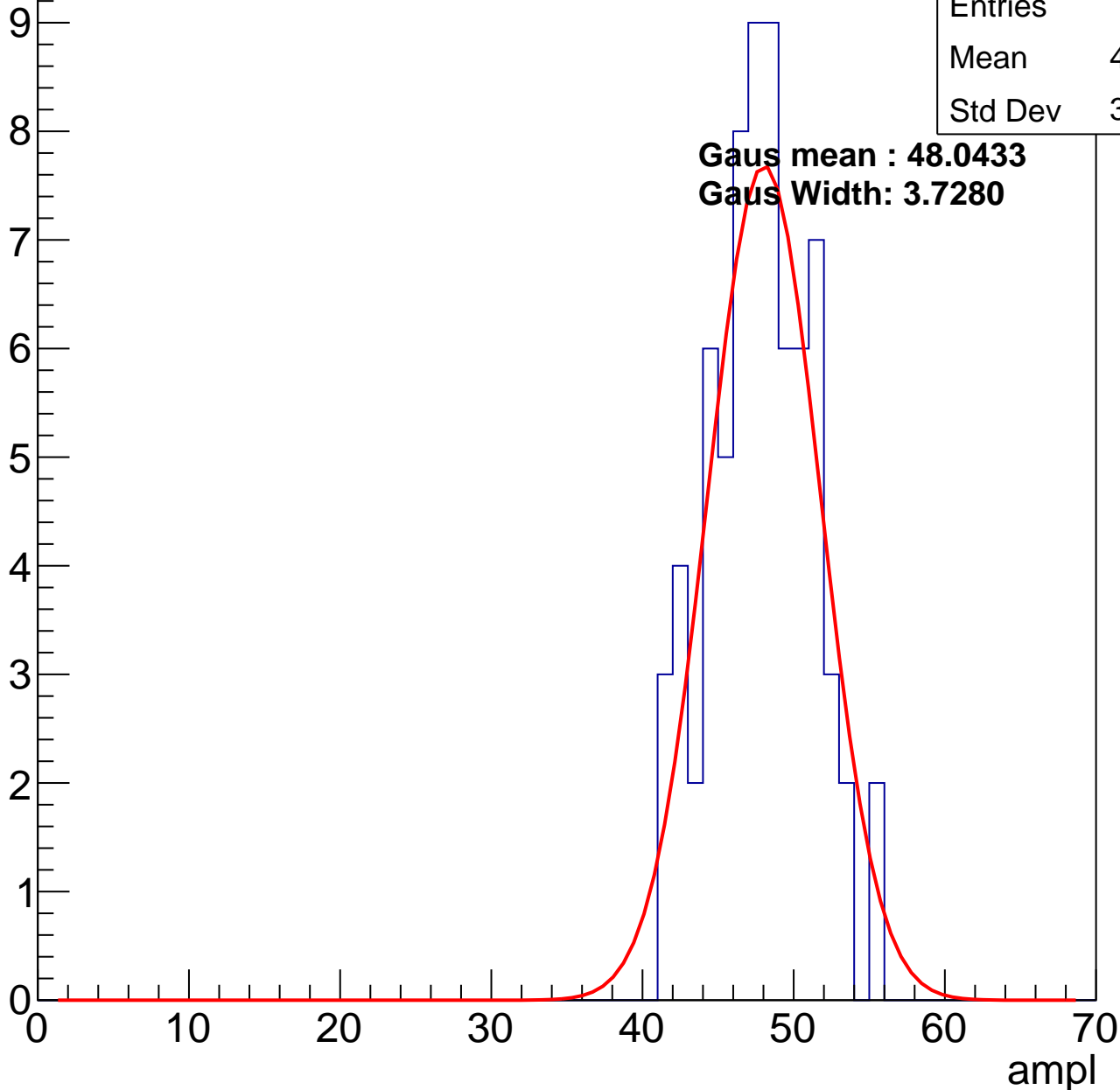
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	47.39
Std Dev	3.302

Gaus mean : 48.0433

Gaus Width: 3.7280

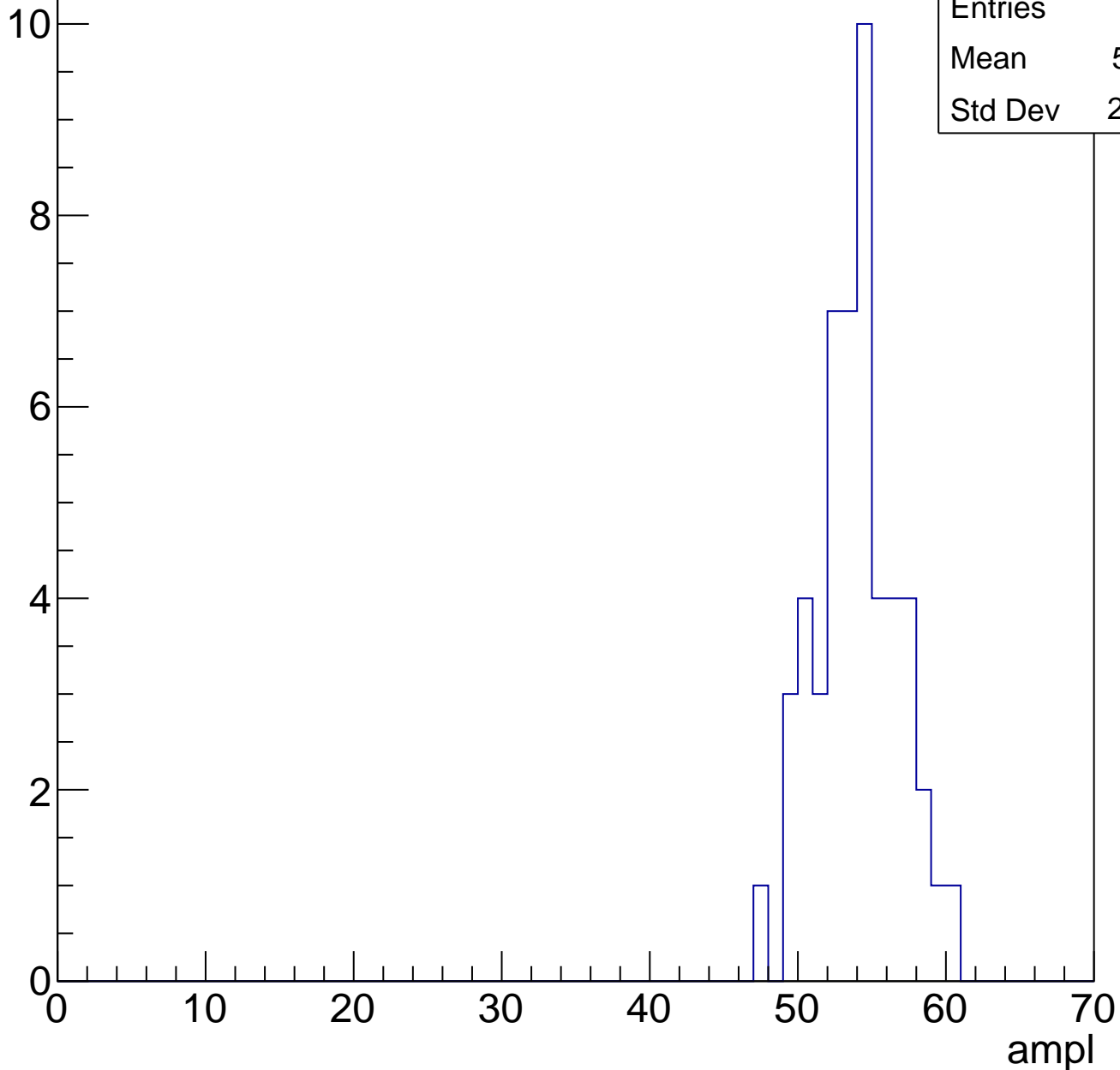


B0L001S, U22-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	51
Mean	53.51
Std Dev	2.754

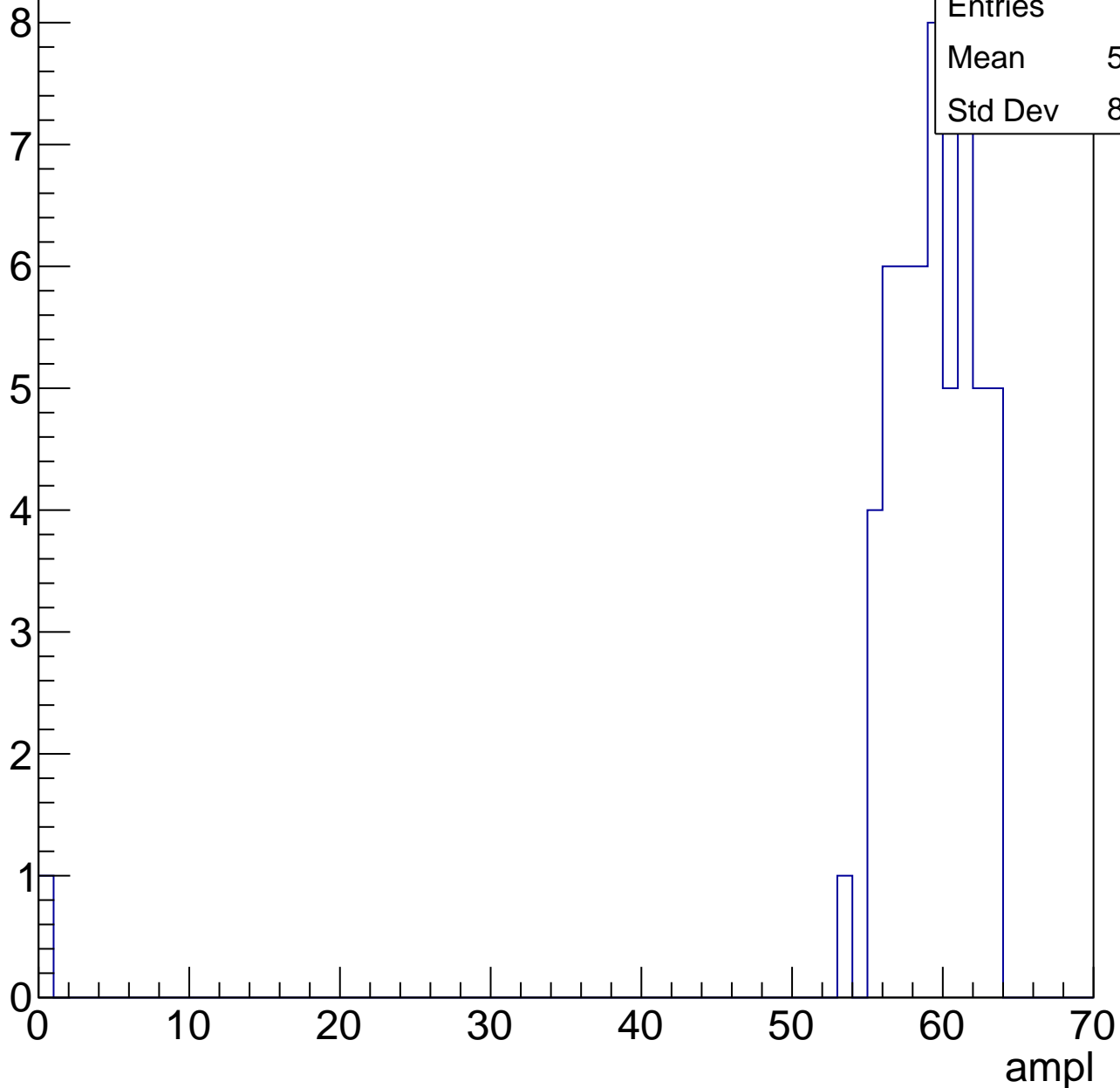
Entry



B0L001S, U22-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

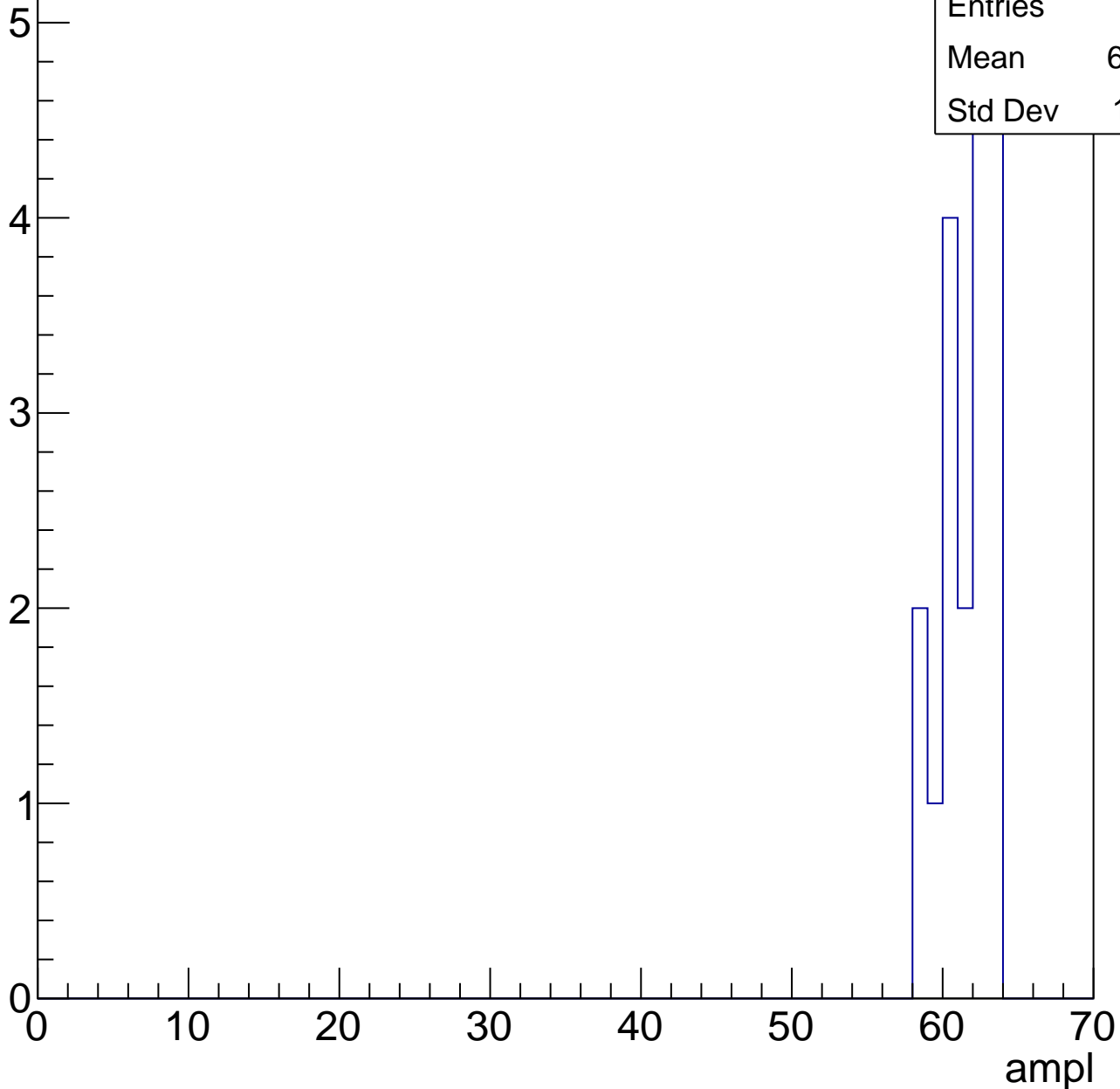


B0L001S, U22-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	61.16
Std Dev	1.631



B0L001S, U22-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U22-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch32, adc0

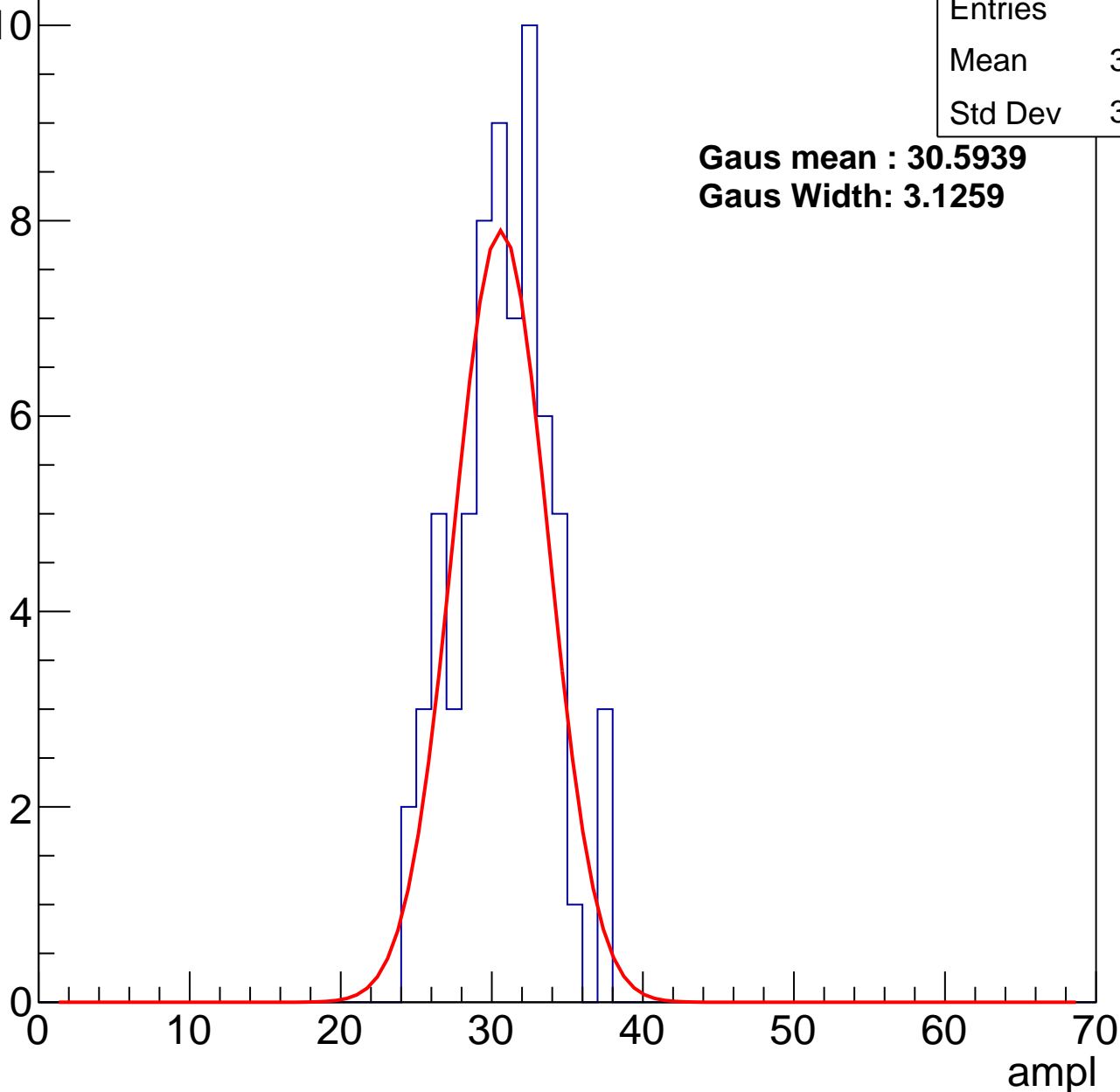
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.25
Std Dev	3.068

Gaus mean : 30.5939

Gaus Width: 3.1259



B0L001S, U22-ch32, adc1

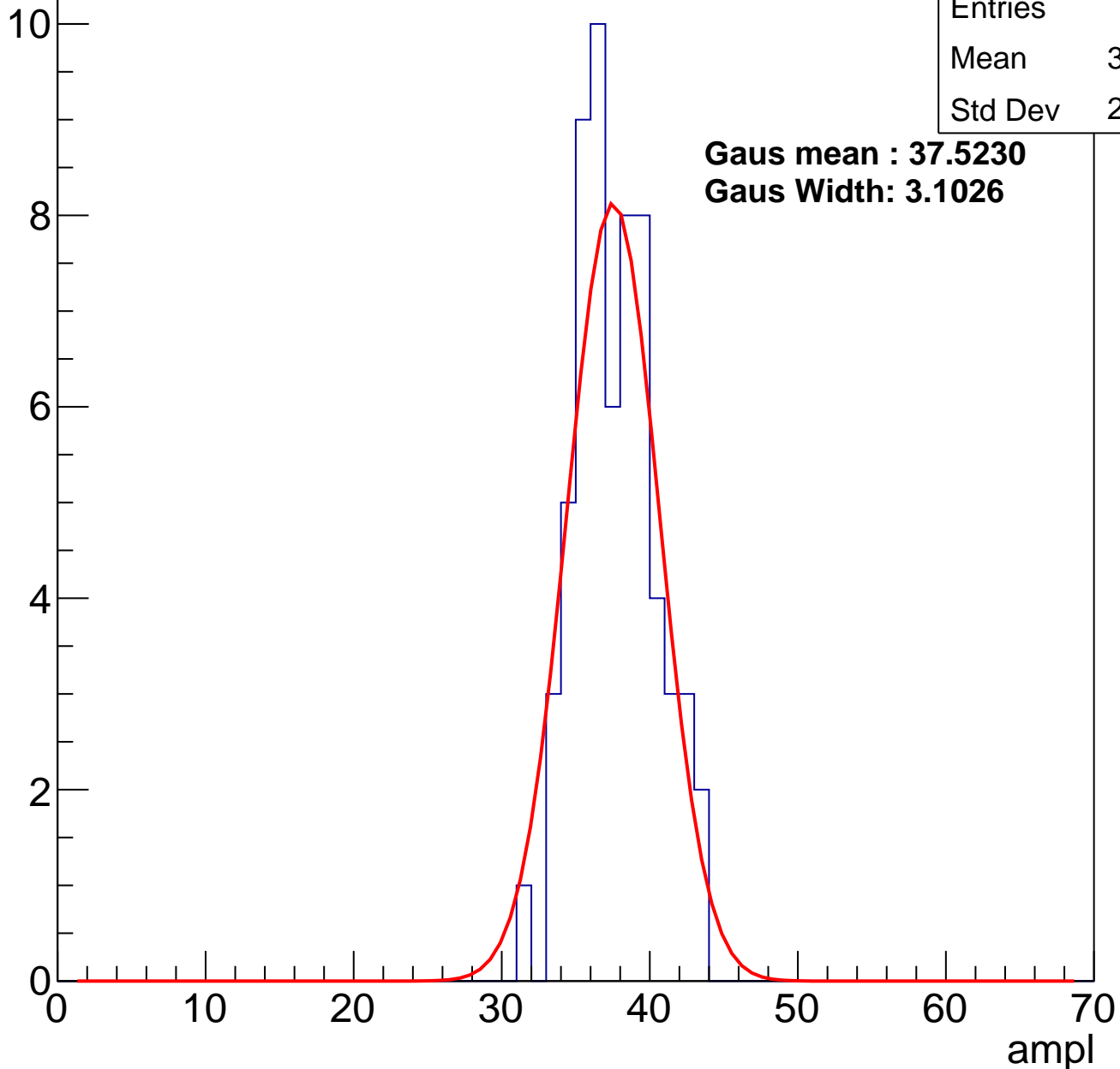
calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	37.23
Std Dev	2.673

Gaus mean : 37.5230

Gaus Width: 3.1026

Entry



B0L001S, U22-ch32, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	43.83
Std Dev	3.432

Gaus mean : 44.4215

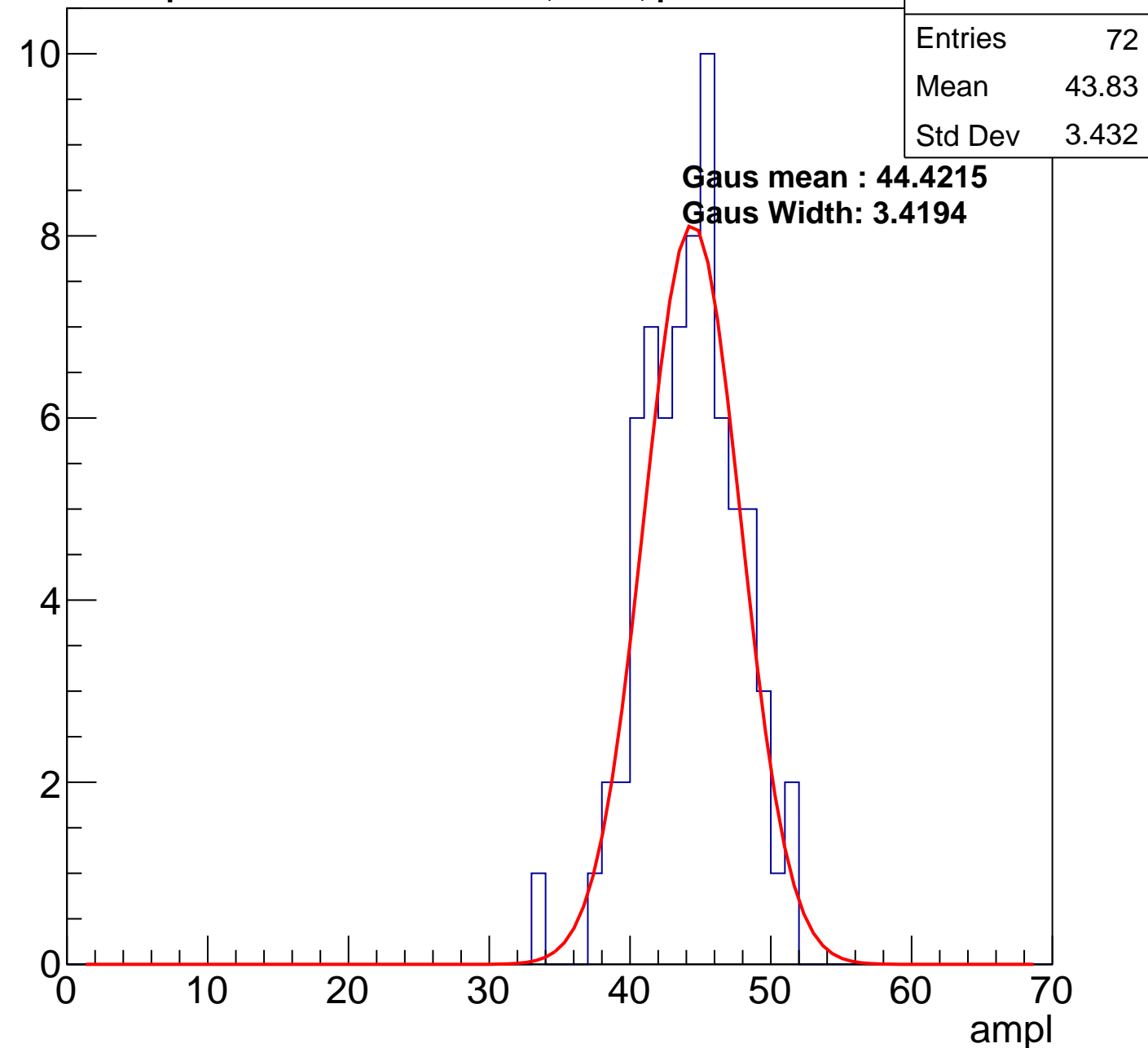
Gaus Width: 3.4194

Entry

10
8
6
4
2
0

ampl

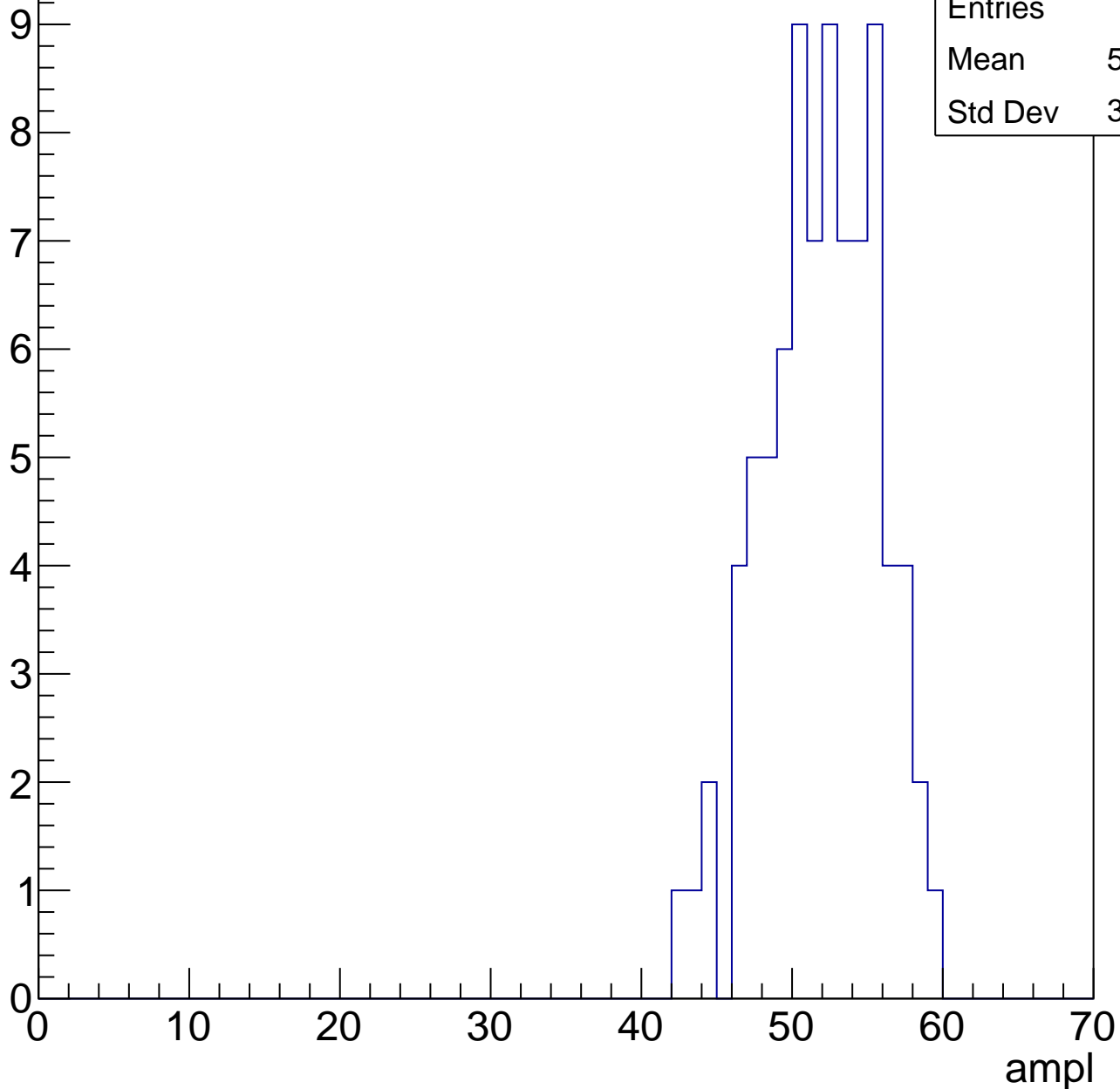
0 10 20 30 40 50 60 70



B0L001S, U22-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



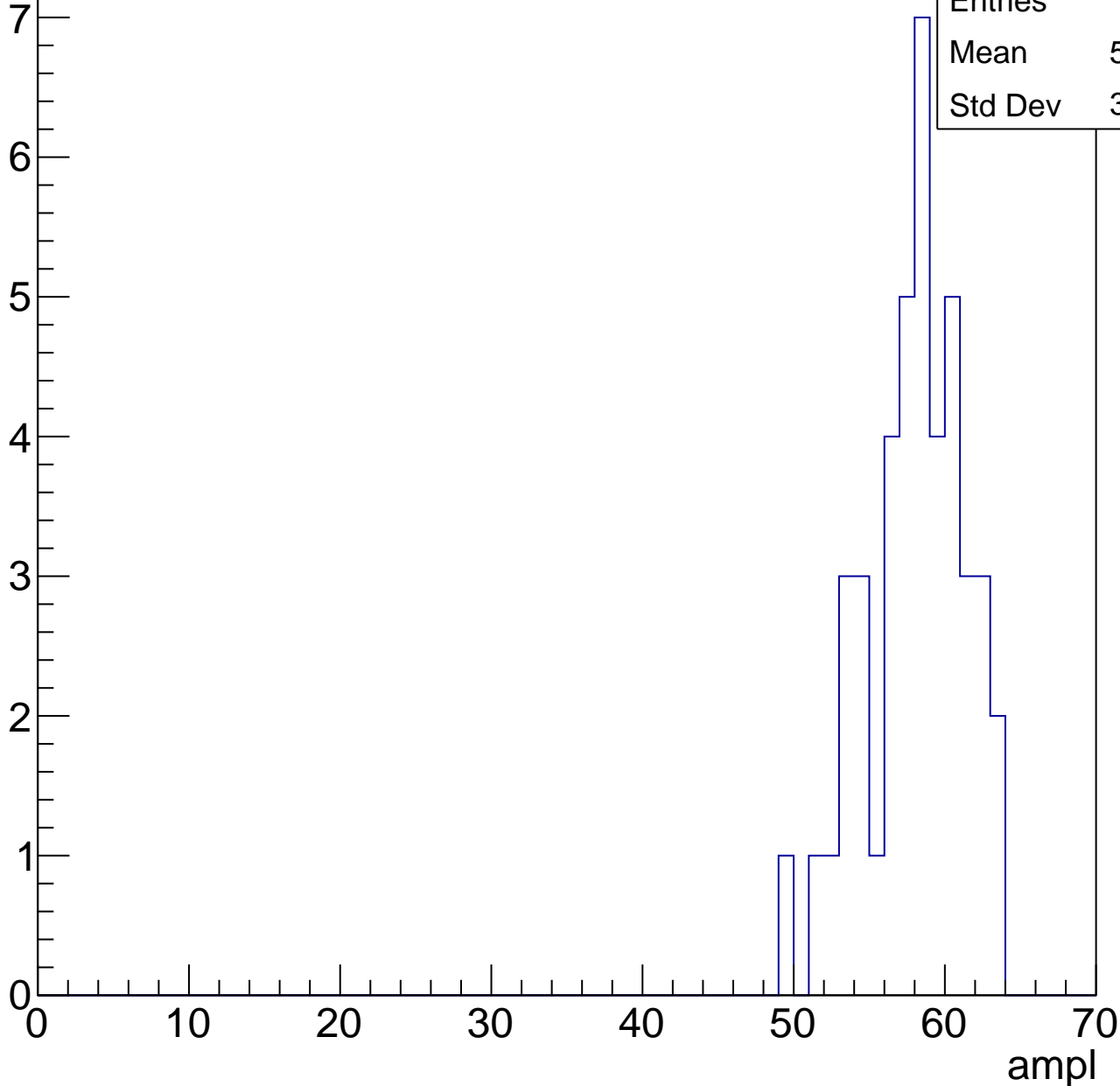
Entries	83
Mean	51.47
Std Dev	3.694

B0L001S, U22-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	57.53
Std Dev	3.266

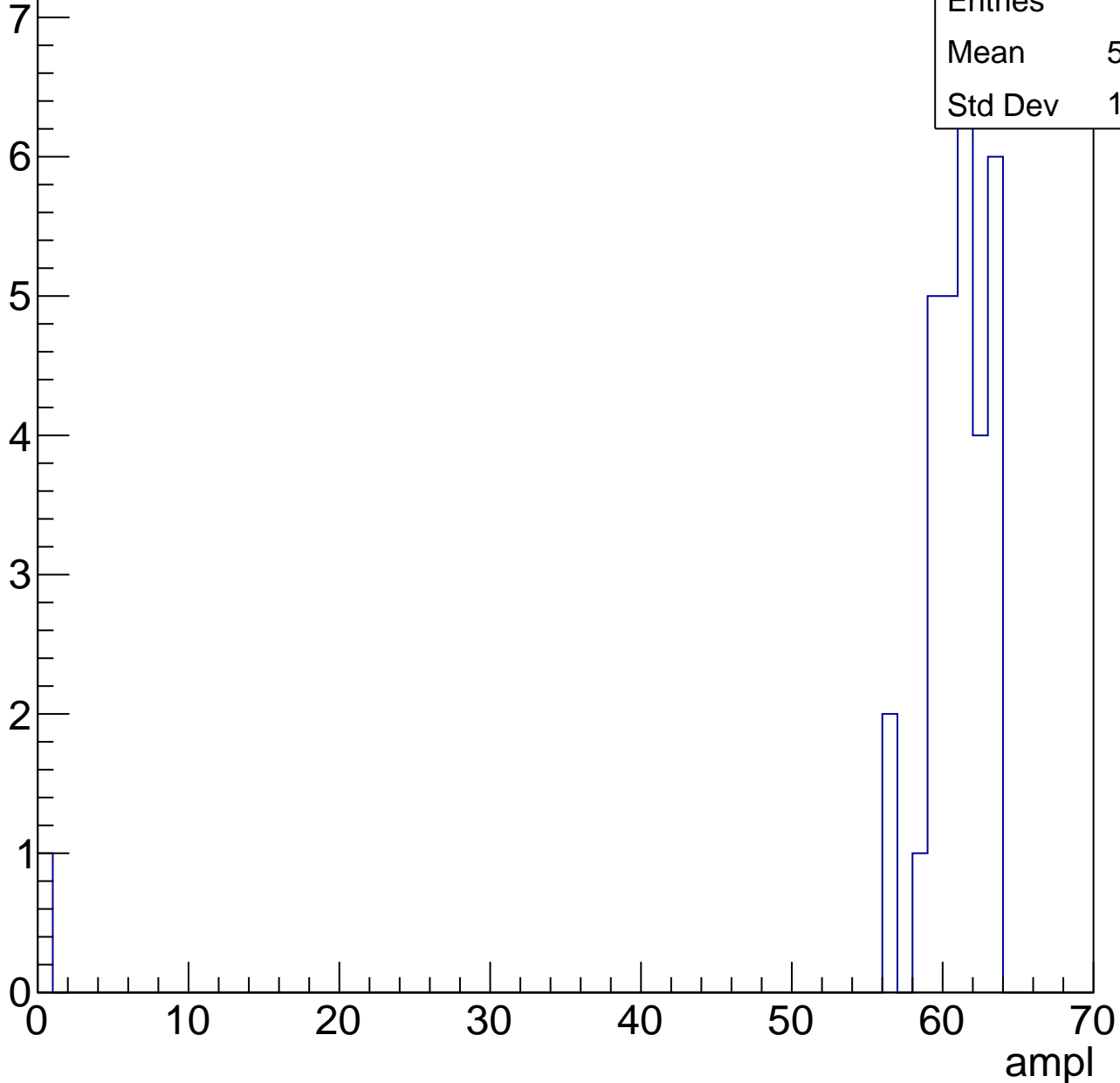


B0L001S, U22-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

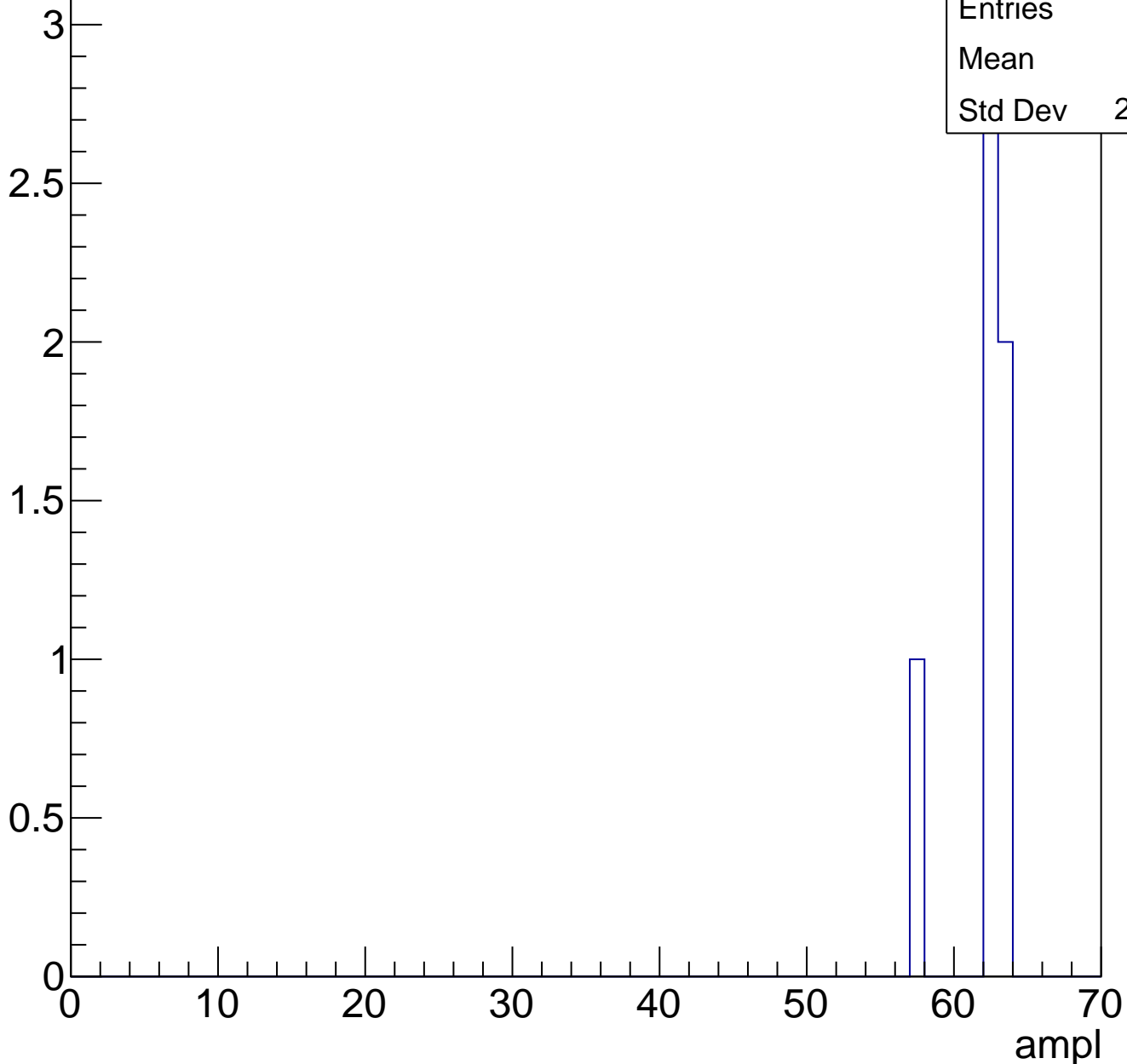
Entries	31
Mean	58.65
Std Dev	10.87



B0L001S, U22-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U22-ch33, adc0

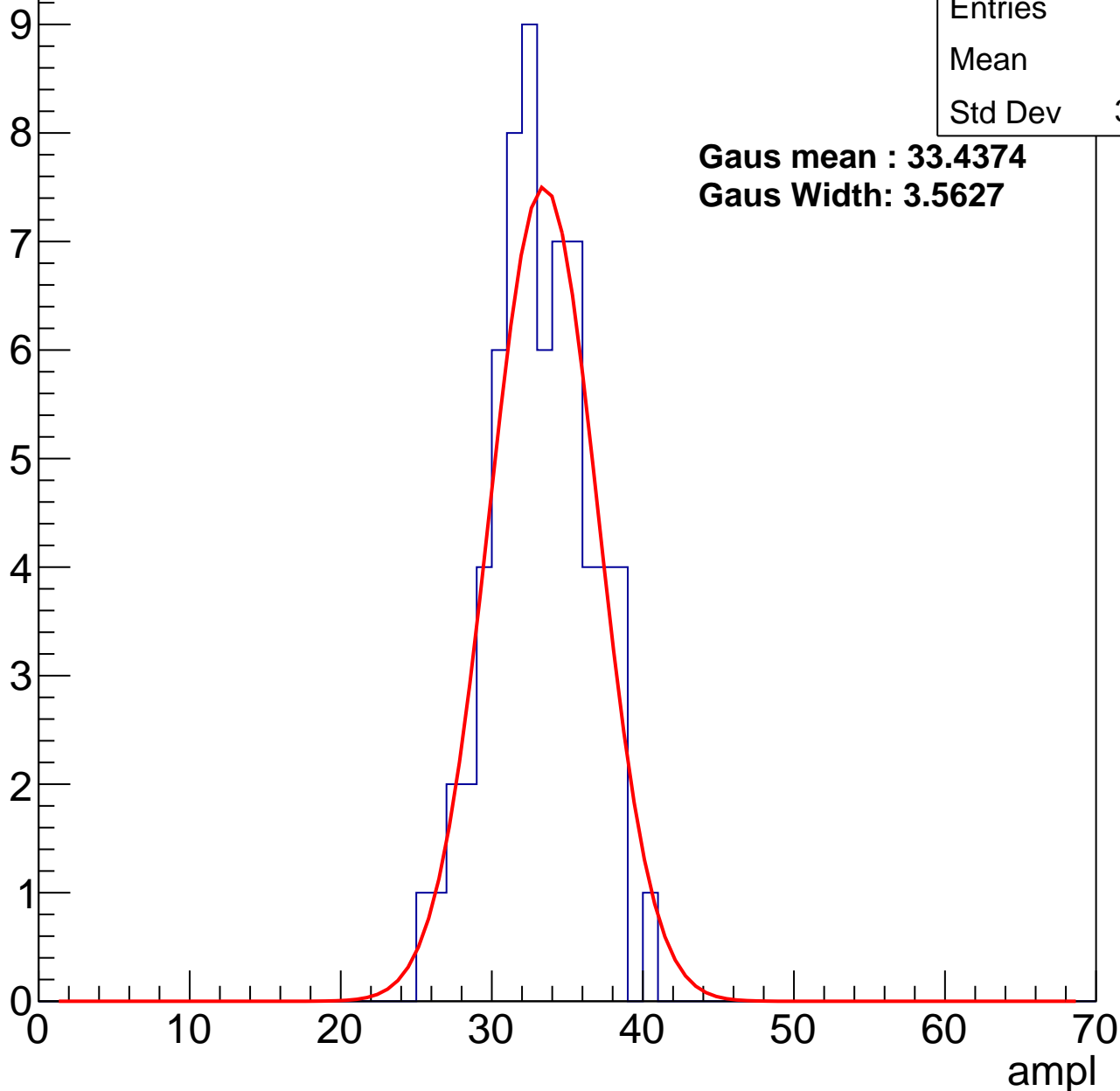
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	32.7
Std Dev	3.191

Gaus mean : 33.4374

Gaus Width: 3.5627



B0L001S, U22-ch33, adc1

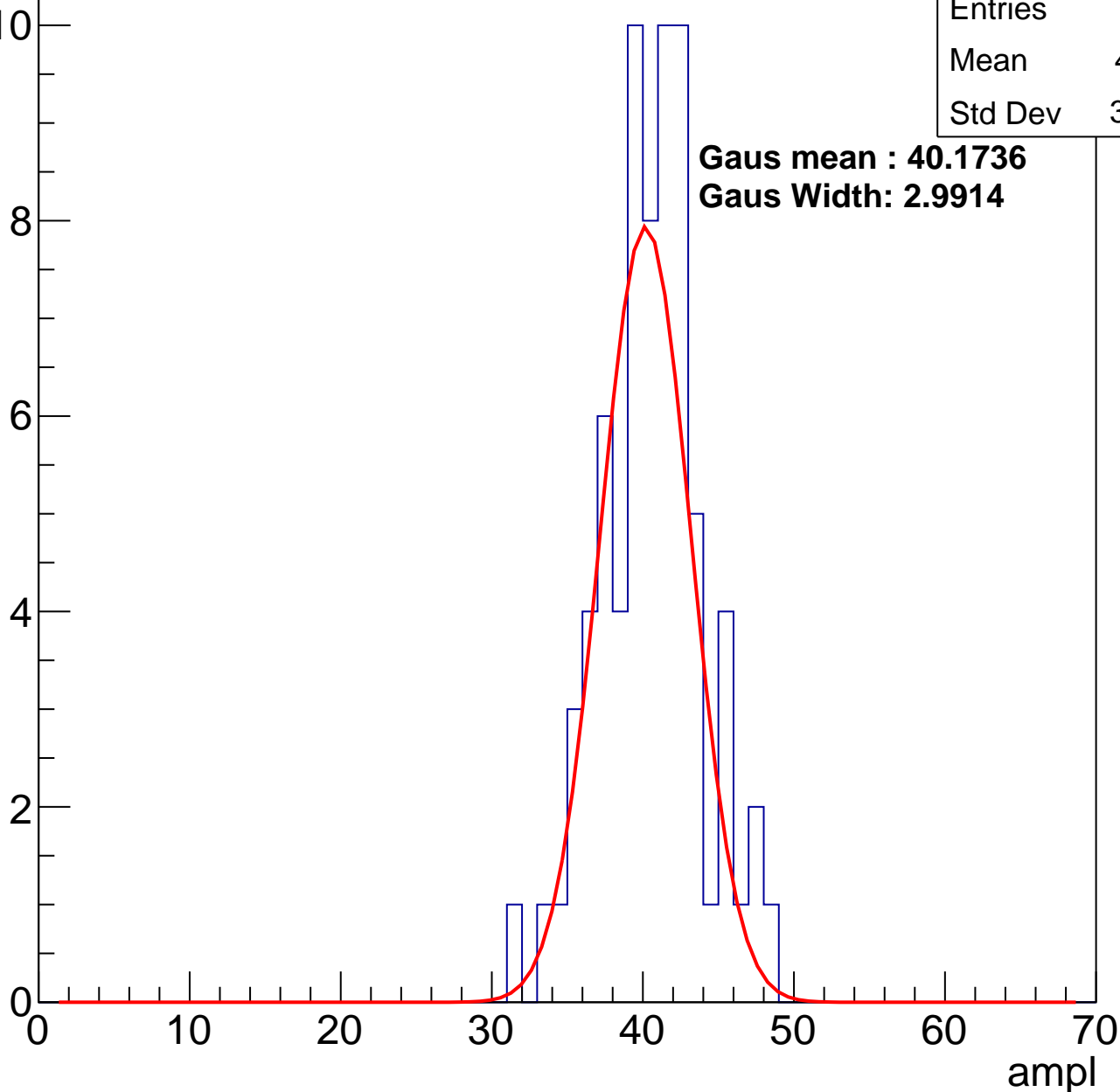
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	40.11
Std Dev	3.319

Gaus mean : 40.1736

Gaus Width: 2.9914



B0L001S, U22-ch33, adc2

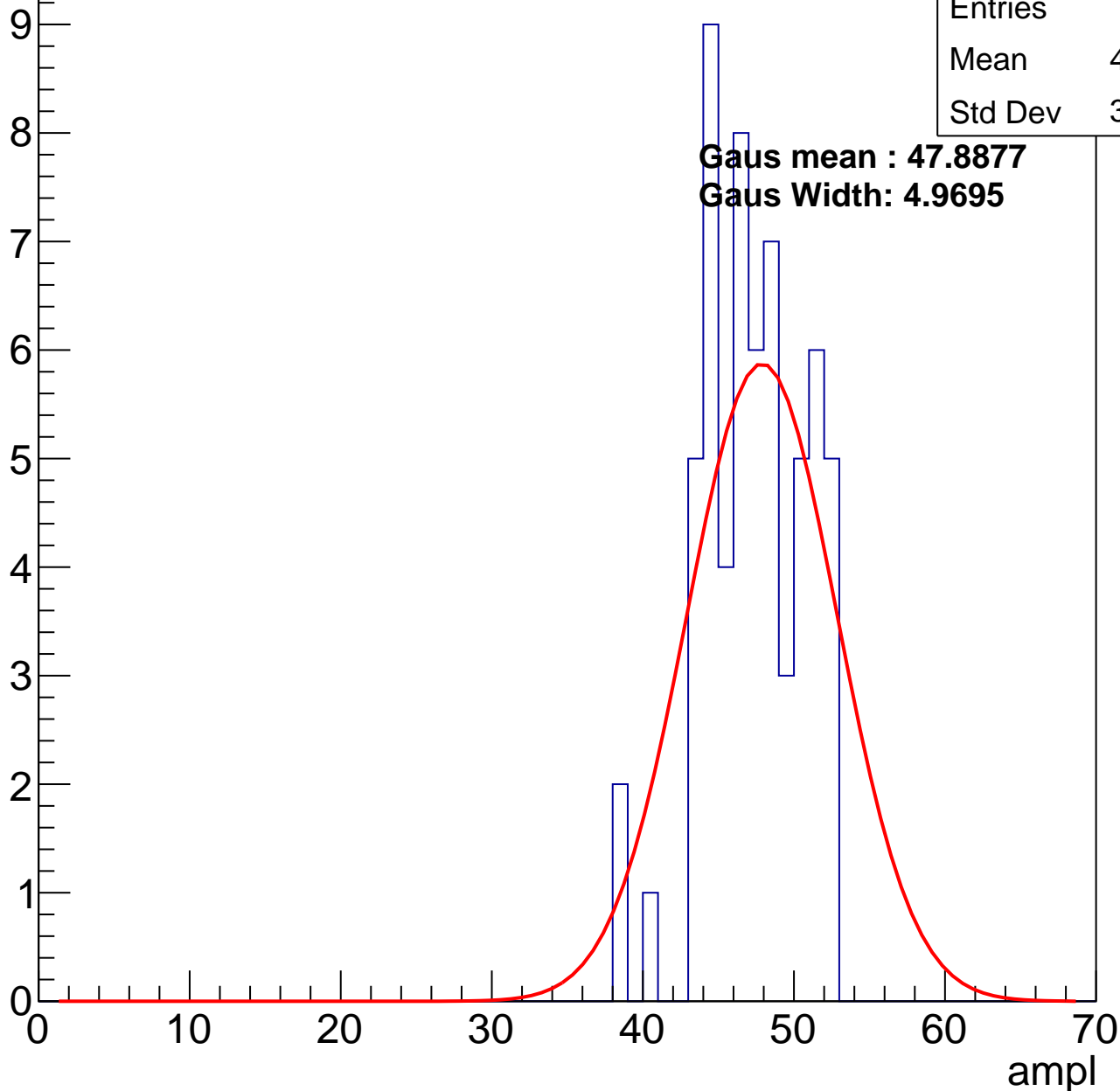
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	46.82
Std Dev	3.336

Gaus mean : 47.8877

Gaus Width: 4.9695

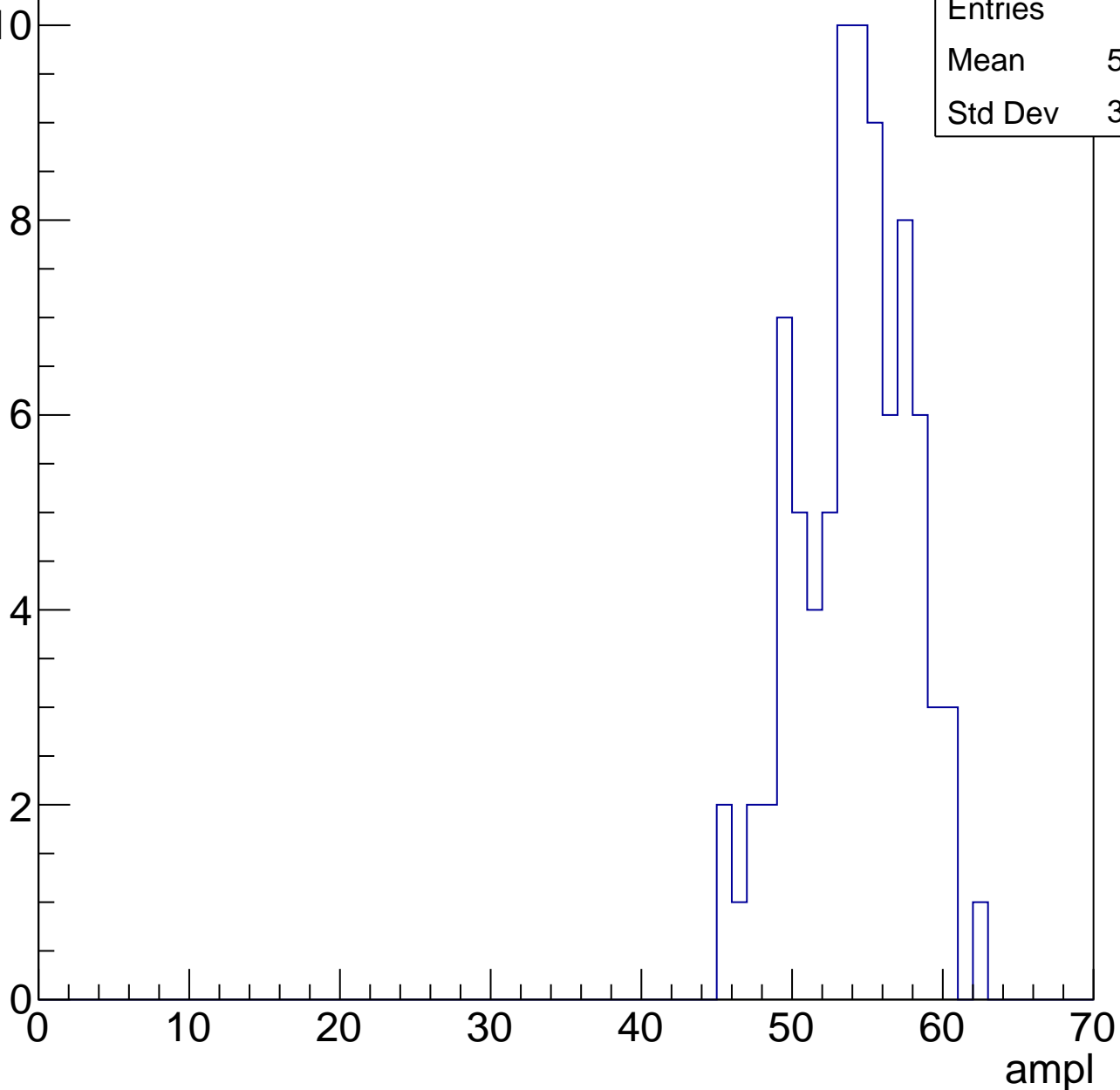


B0L001S, U22-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

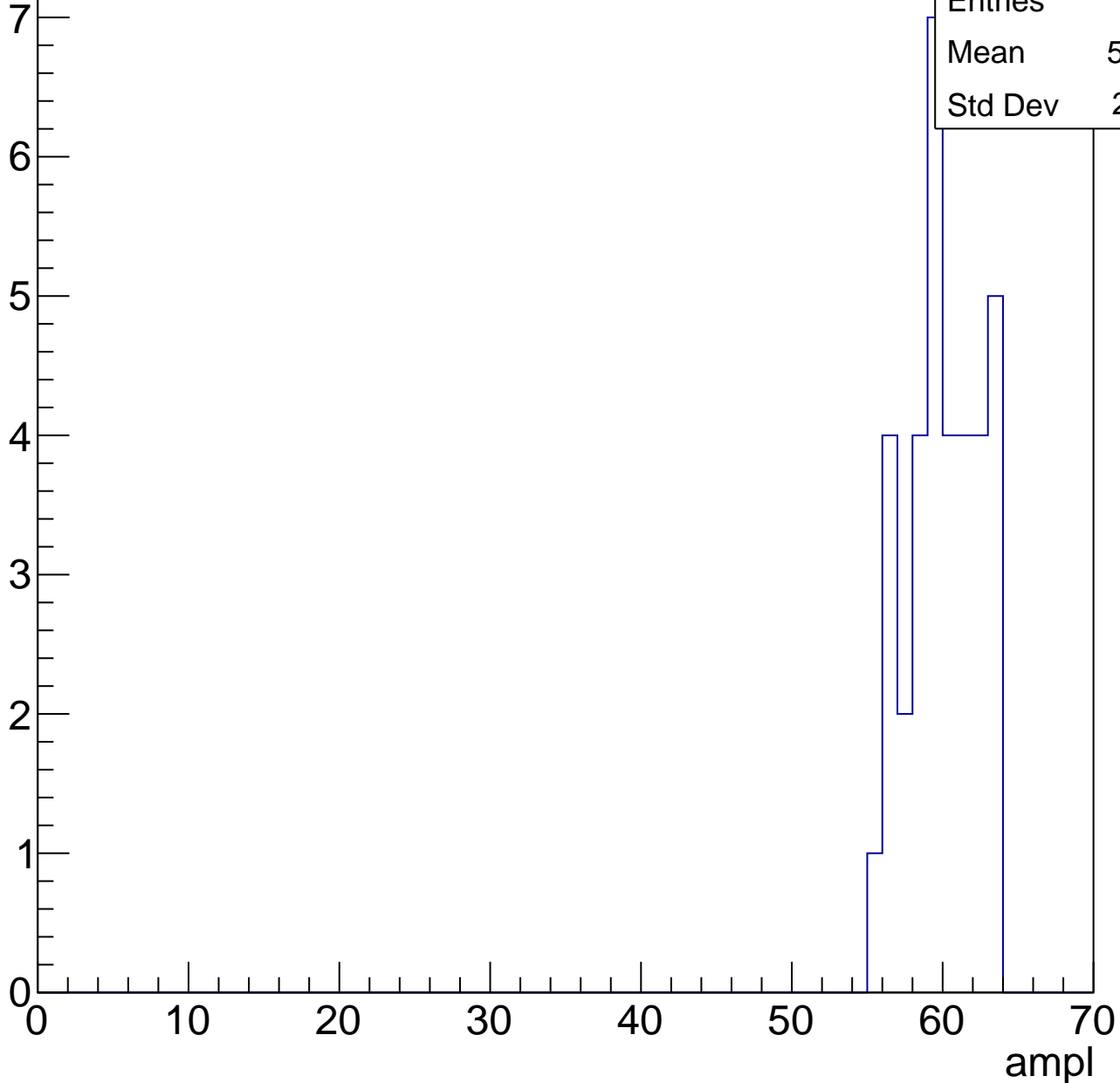
Entries	84
Mean	53.65
Std Dev	3.708



B0L001S, U22-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

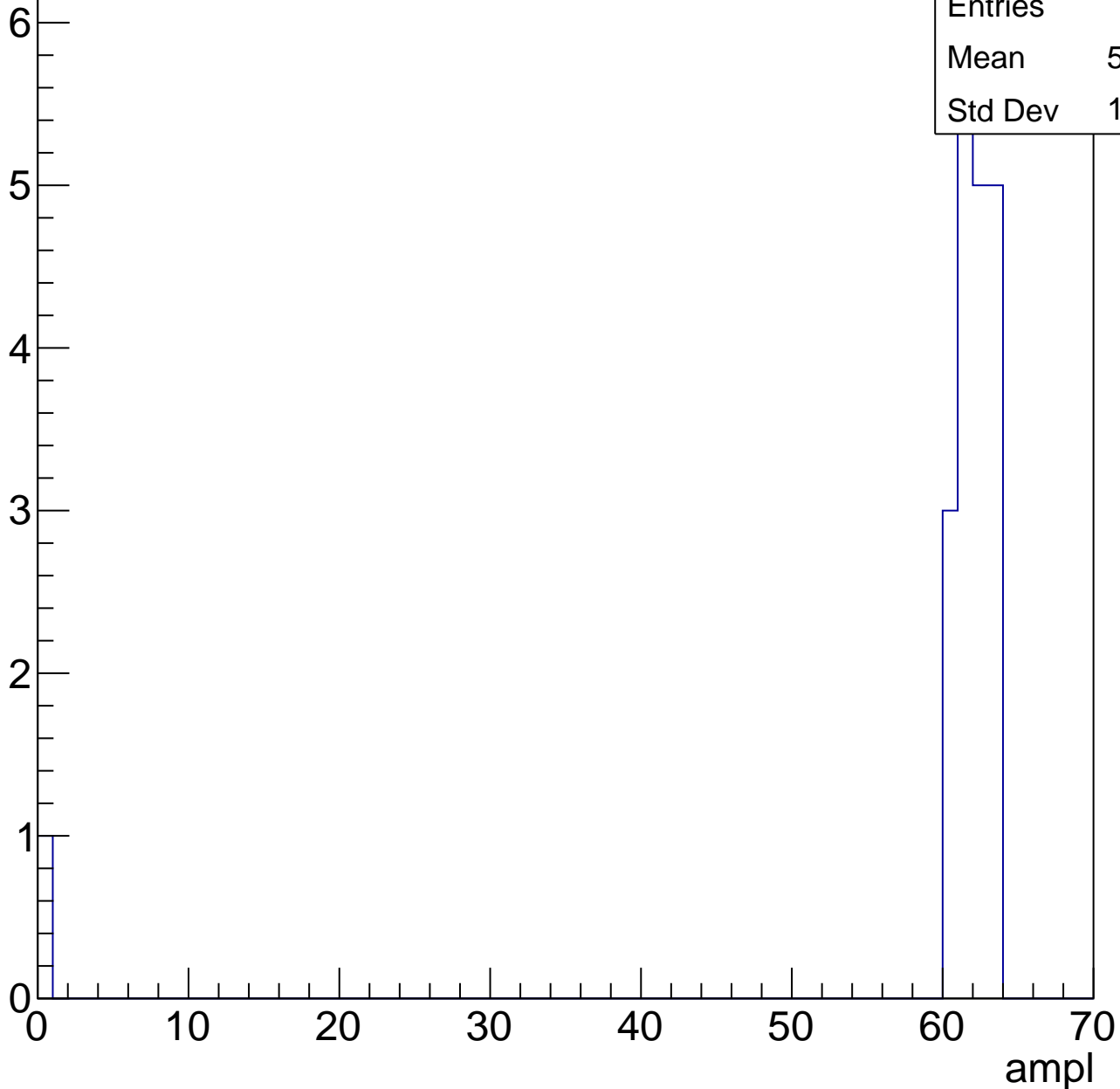


B0L001S, U22-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	20
Mean	58.55
Std Dev	13.47



B0L001S, U22-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch34, adc0

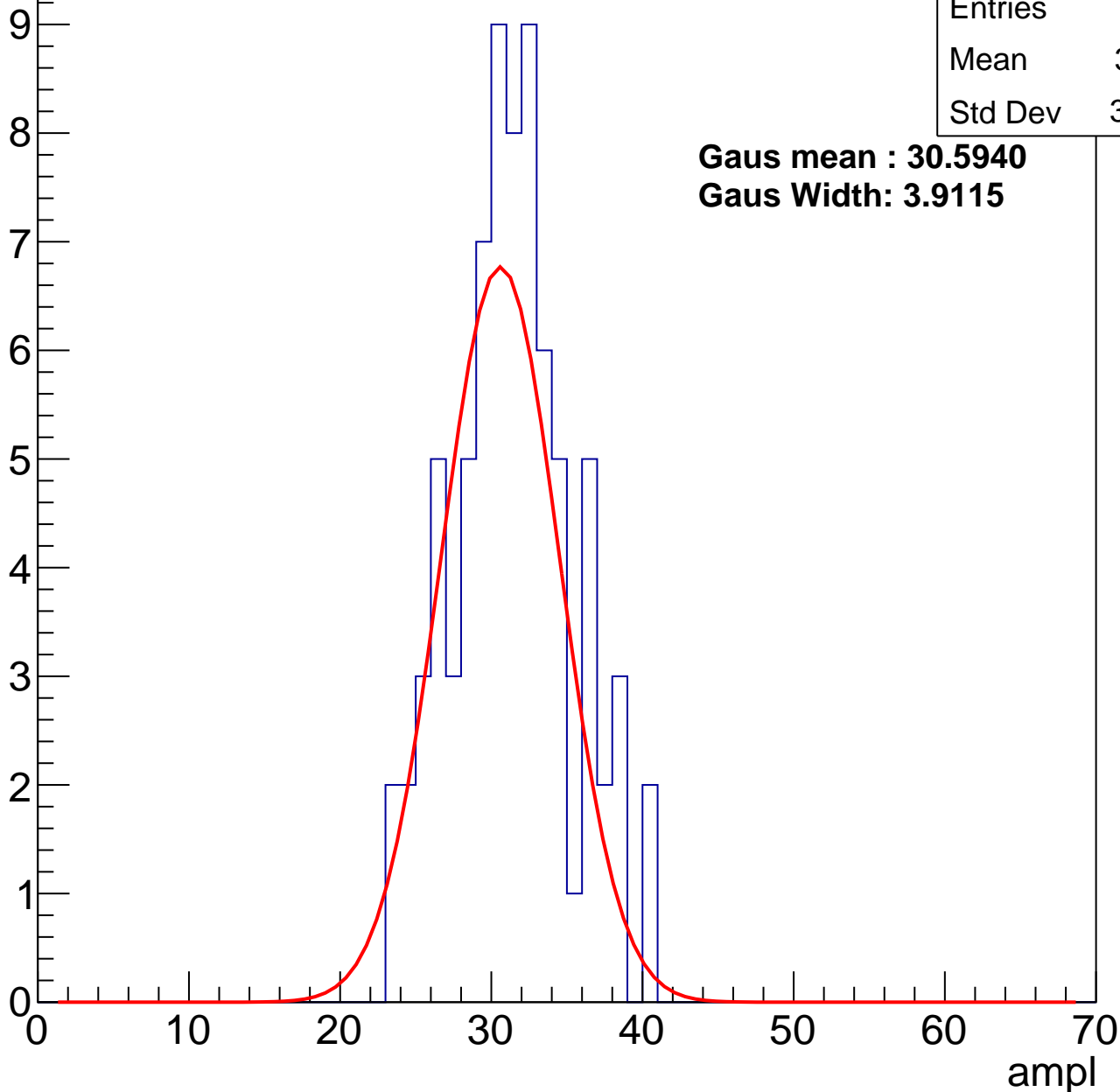
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	30.91
Std Dev	3.929

Gaus mean : 30.5940

Gaus Width: 3.9115



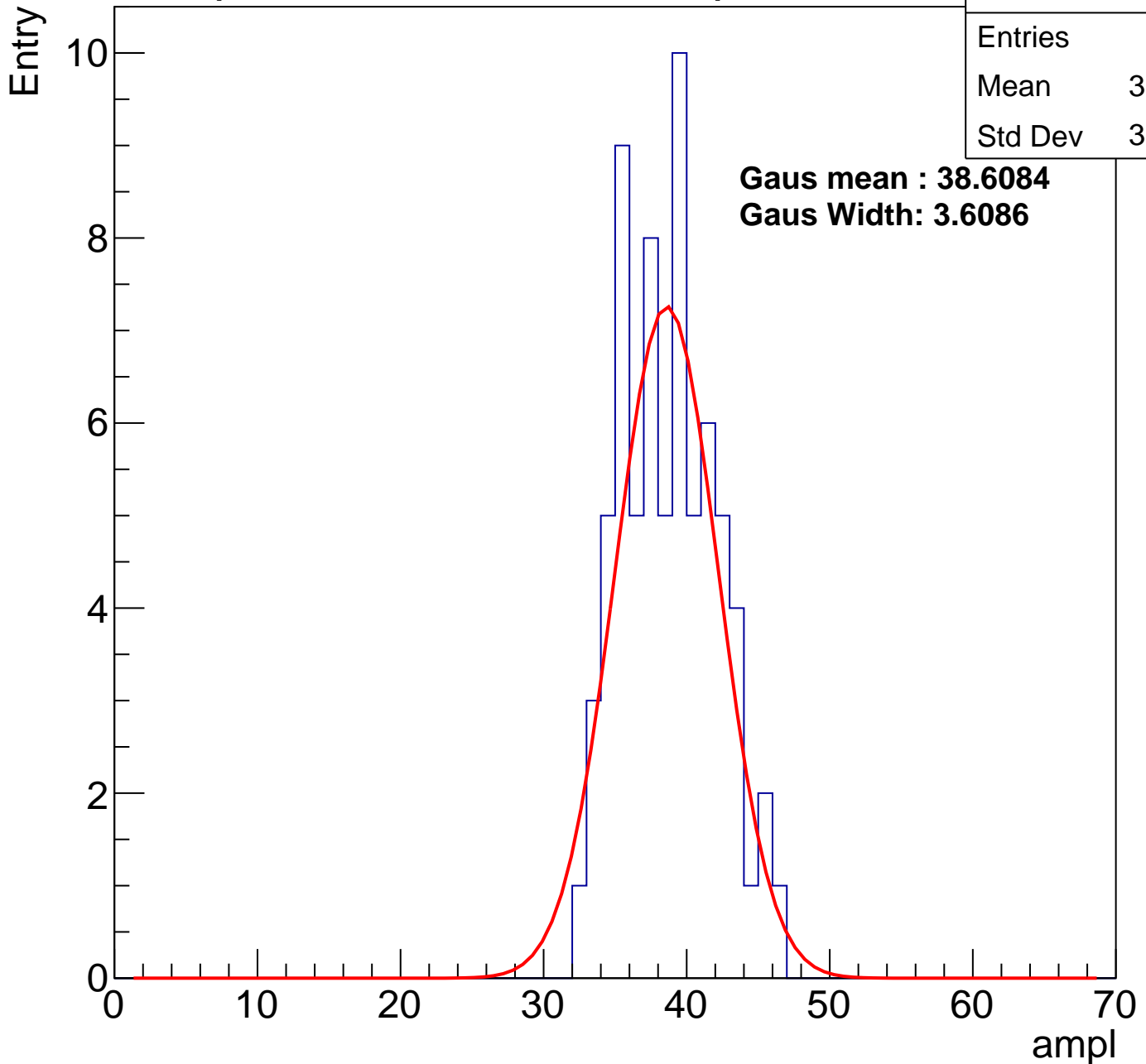
B0L001S, U22-ch34, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	38.29
Std Dev	3.287

Gaus mean : 38.6084

Gaus Width: 3.6086



B0L001S, U22-ch34, adc2

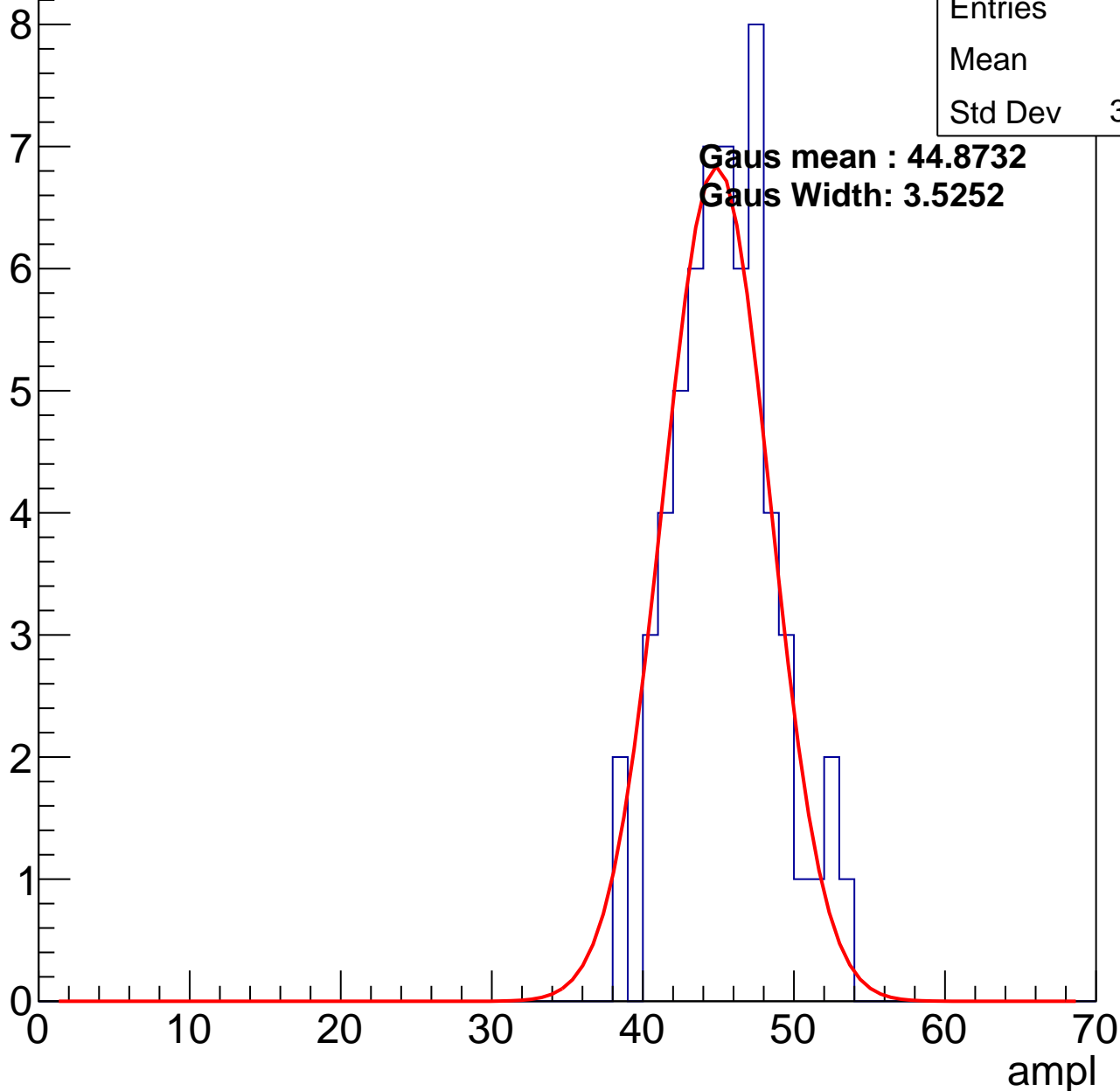
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45
Std Dev	3.312

Gaus mean : 44.8732

Gaus Width: 3.5252

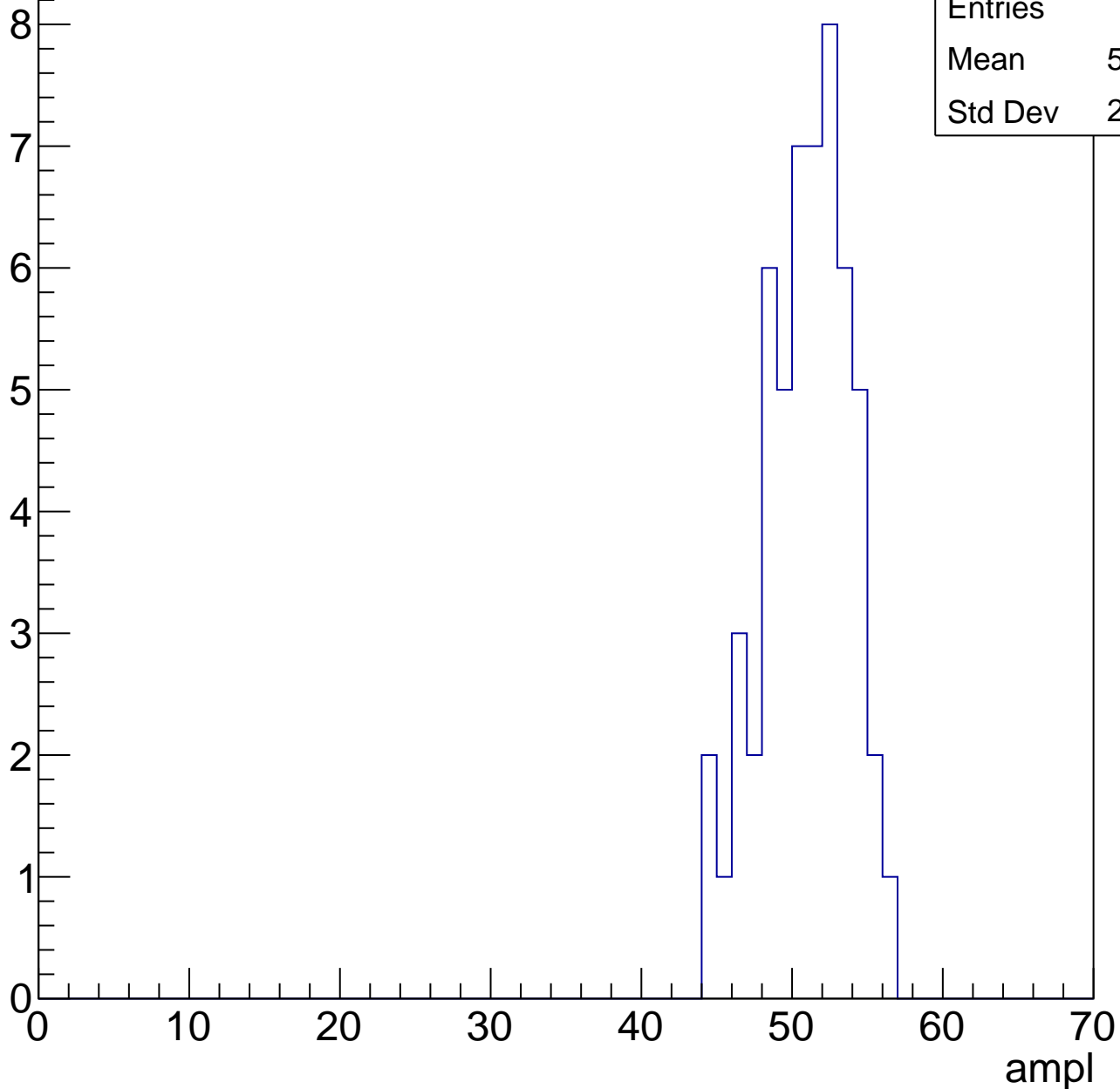


B0L001S, U22-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.45
Std Dev	2.827

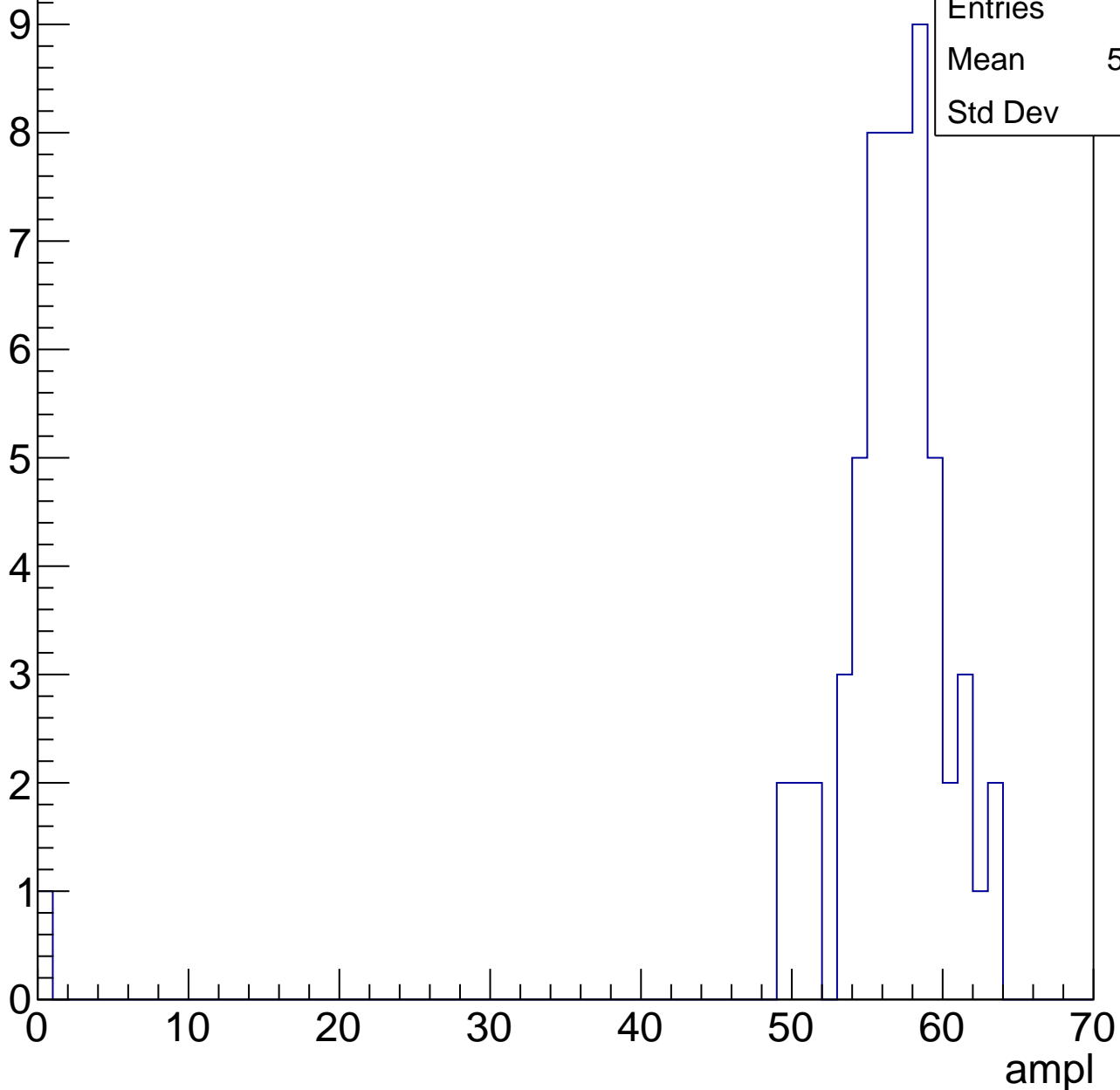


B0L001S, U22-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

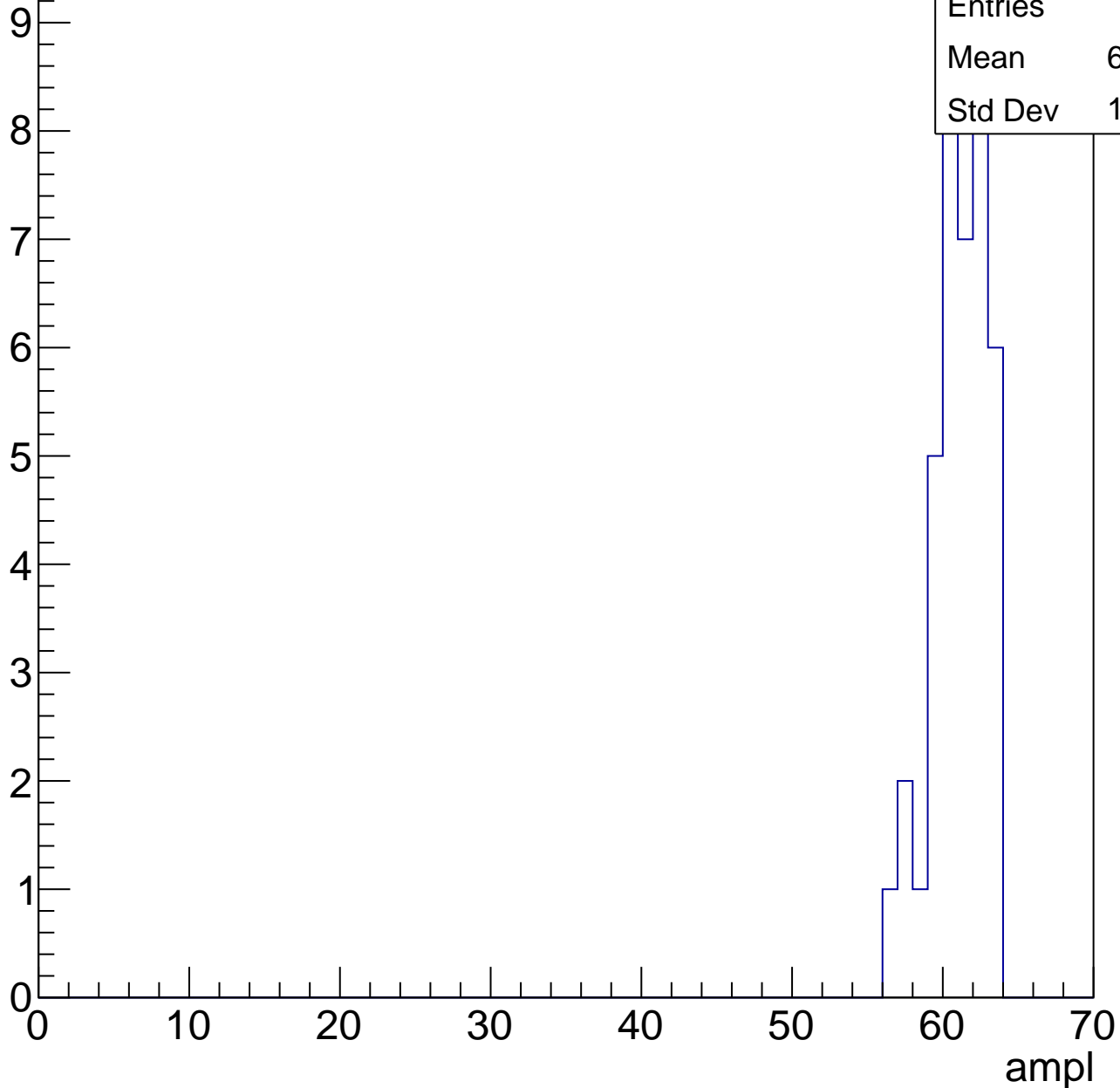
Entries	61
Mean	55.43
Std Dev	7.81



B0L001S, U22-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch35, adc0

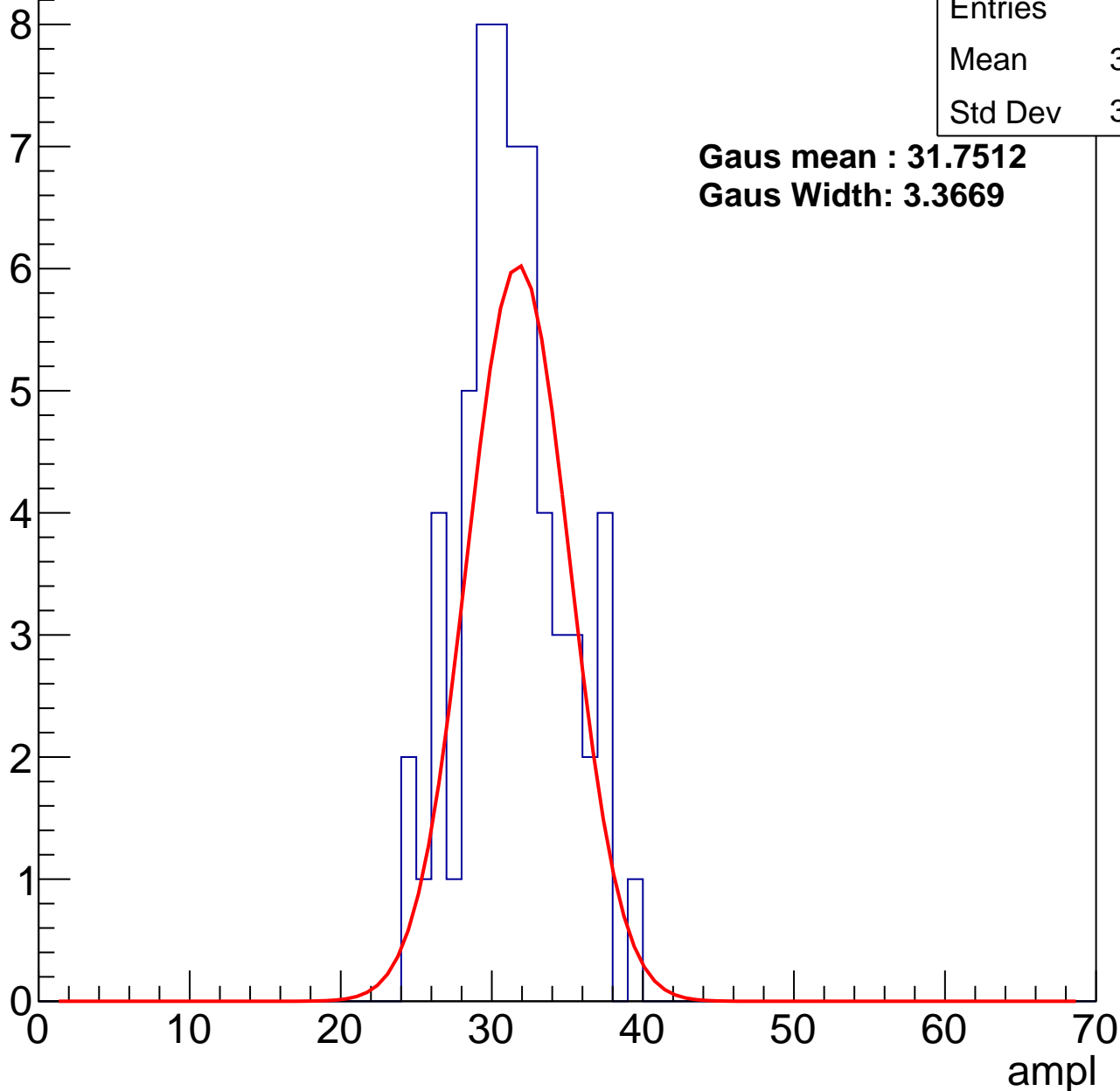
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.92
Std Dev	3.393

Gaus mean : 31.7512

Gaus Width: 3.3669



B0L001S, U22-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	36.89
Std Dev	3.142

Gaus mean : 37.1766

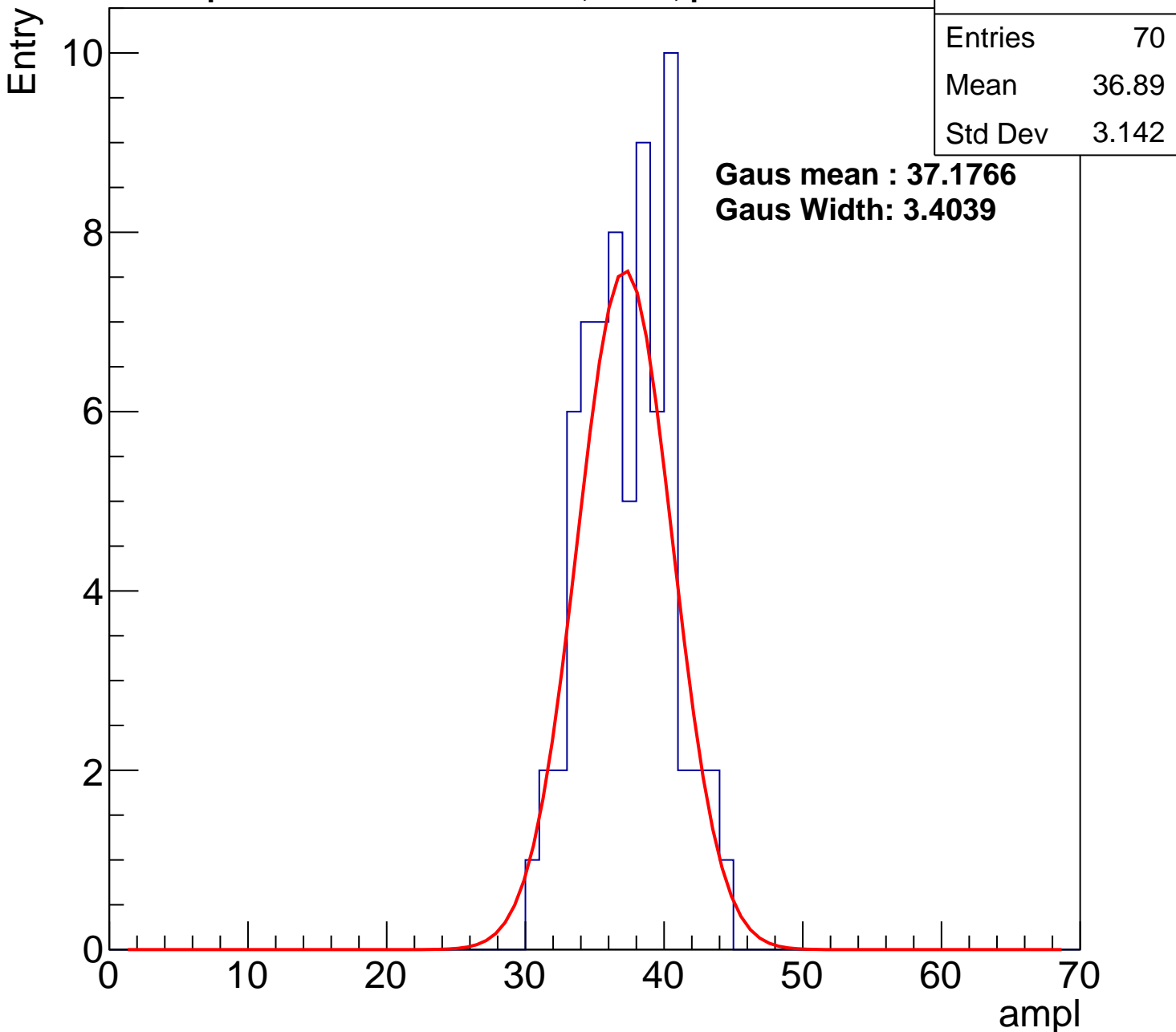
Gaus Width: 3.4039

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch35, adc2

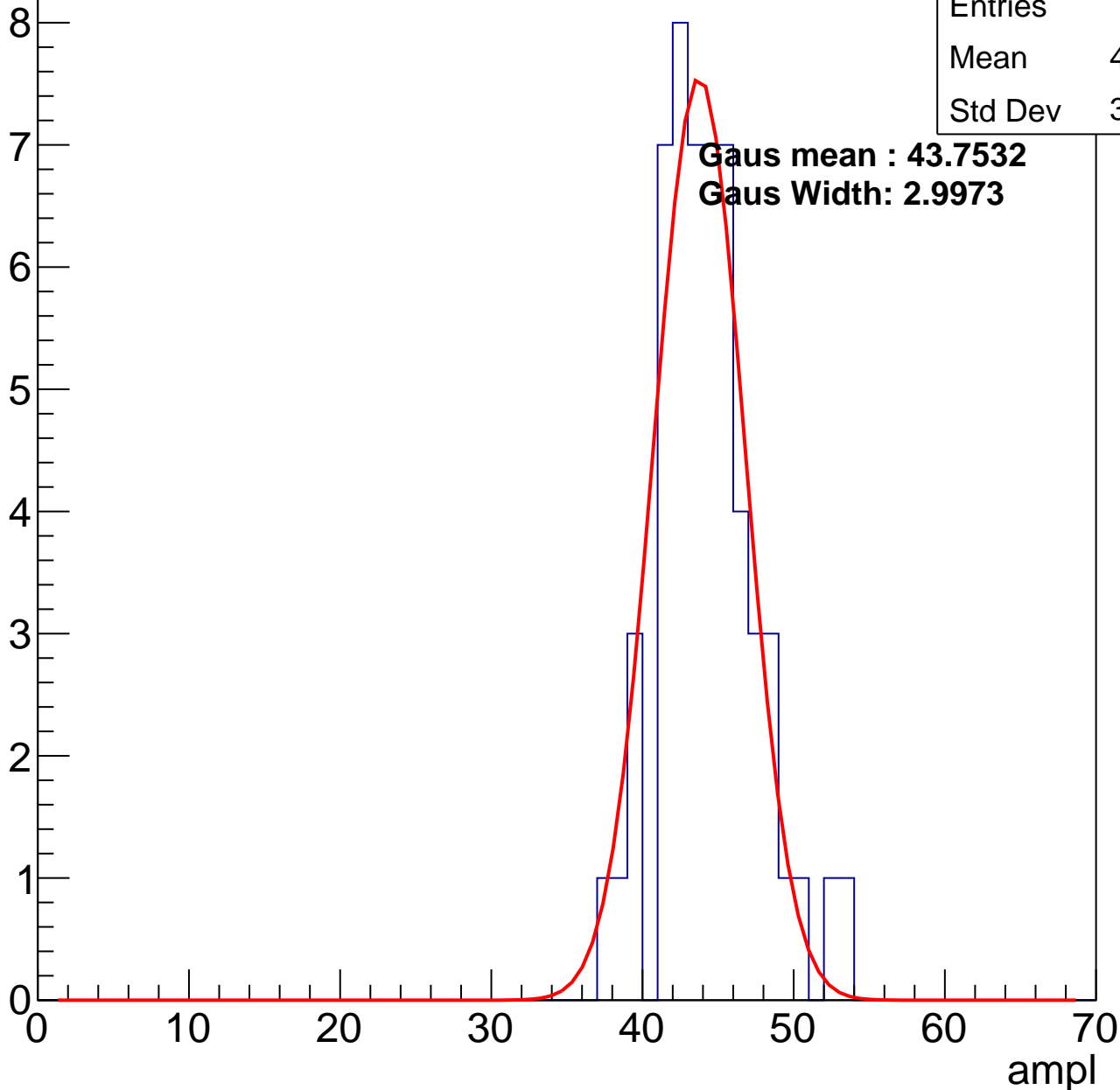
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.85
Std Dev	3.205

Gaus mean : 43.7532

Gaus Width: 2.9973

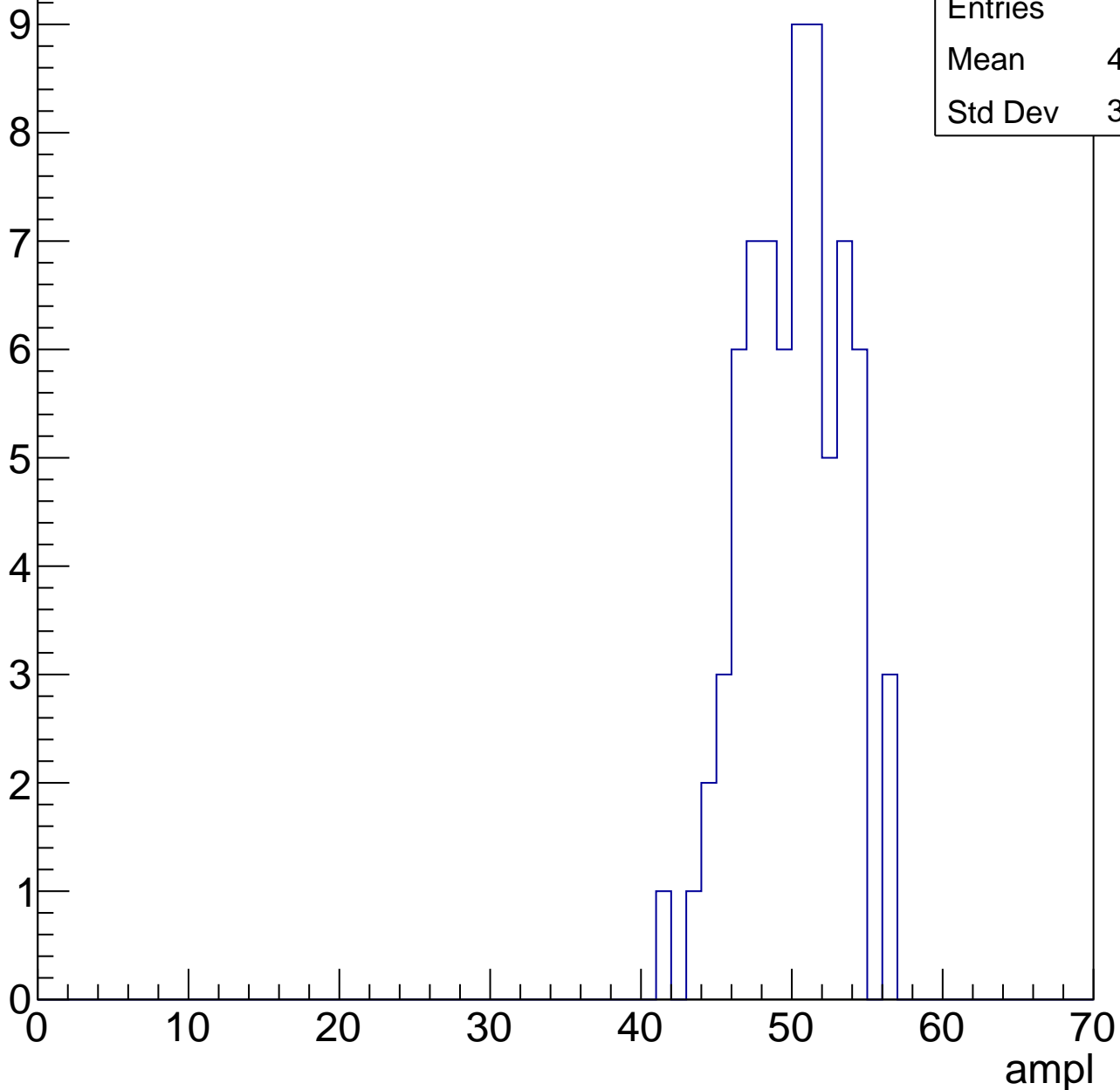


B0L001S, U22-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	49.64
Std Dev	3.242

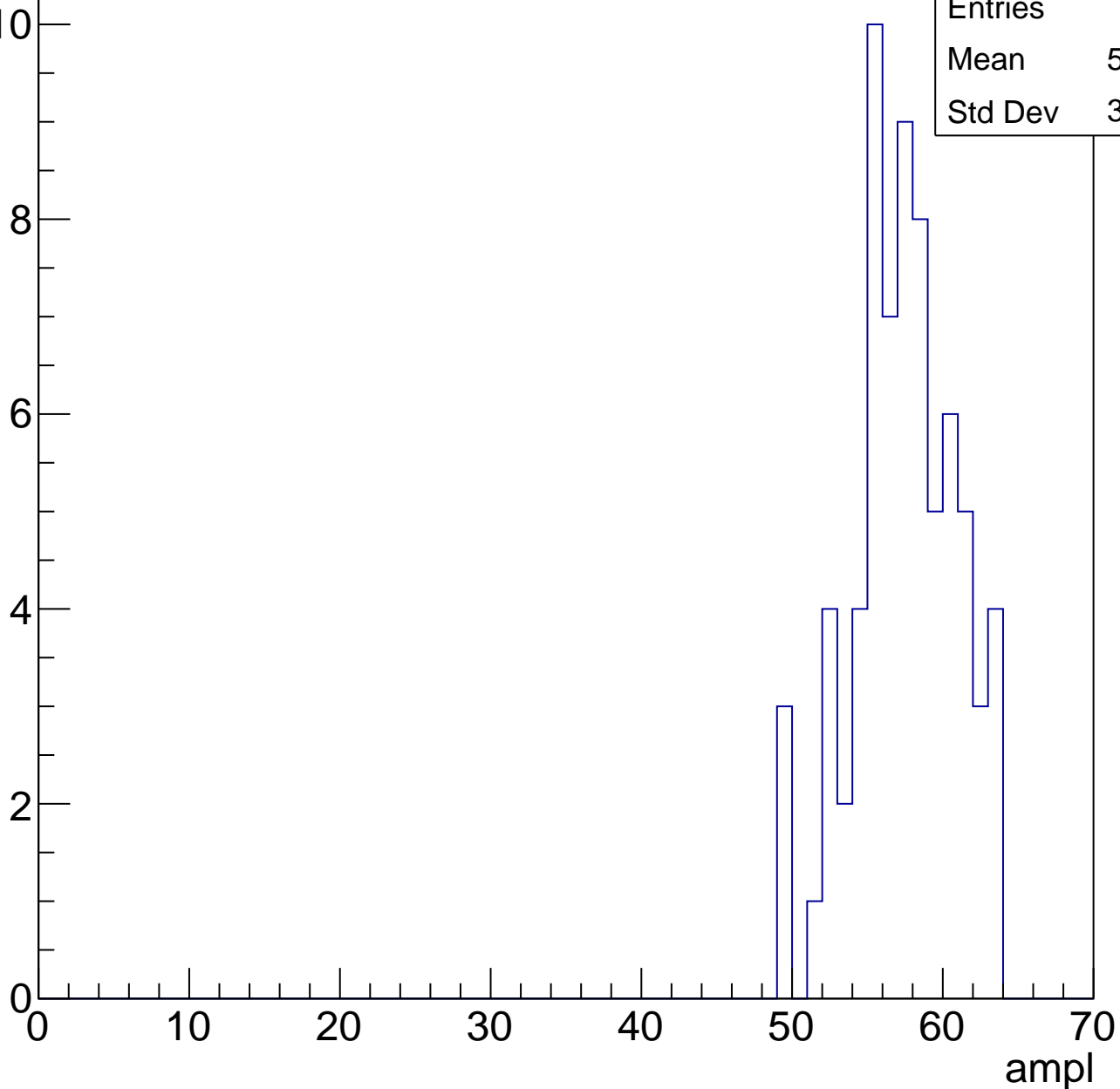


B0L001S, U22-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.97
Std Dev	3.407

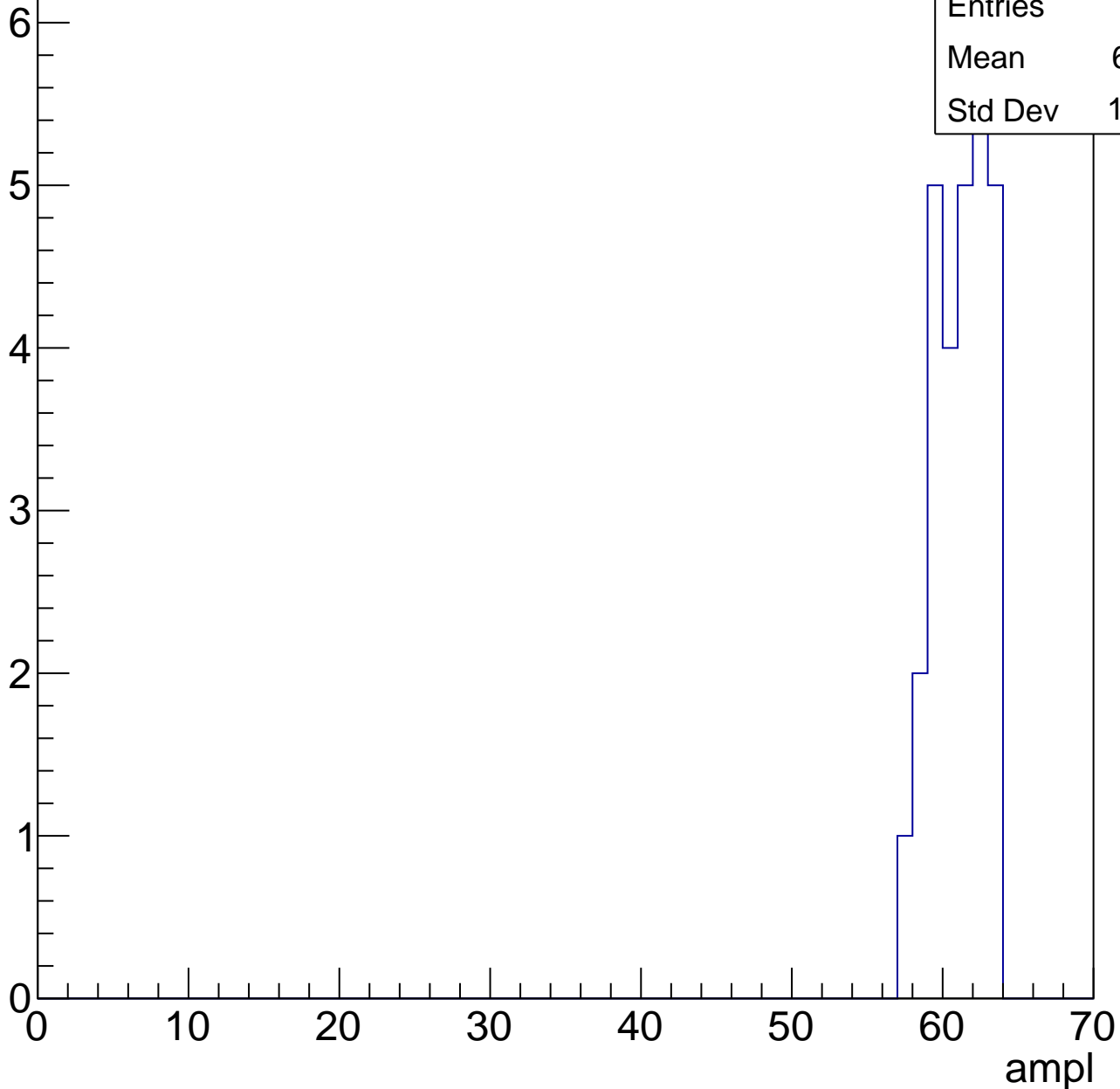


B0L001S, U22-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	60.71
Std Dev	1.708



B0L001S, U22-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch36, adc0

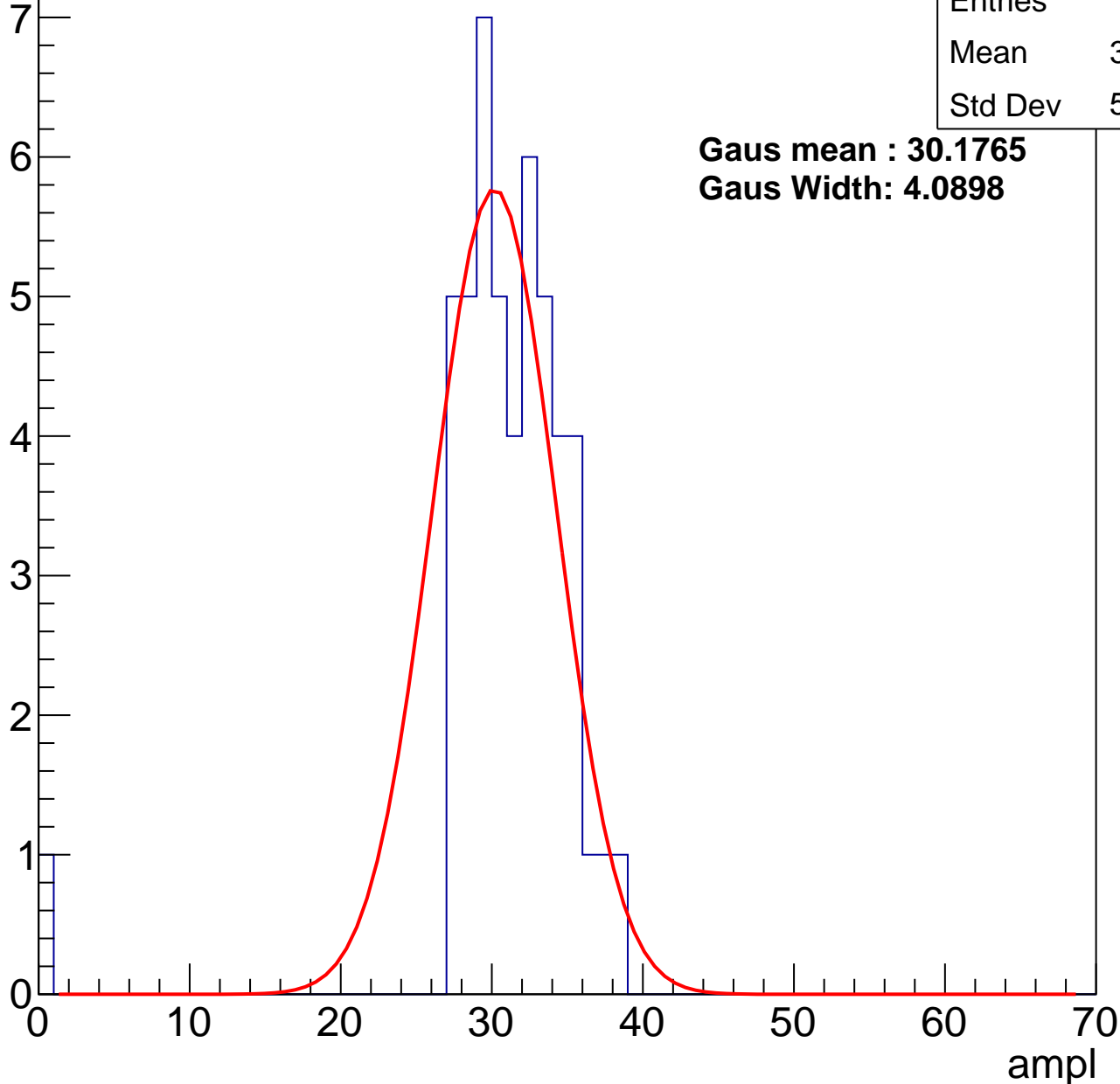
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	30.53
Std Dev	5.238

Gaus mean : 30.1765

Gaus Width: 4.0898



B0L001S, U22-ch36, adc1

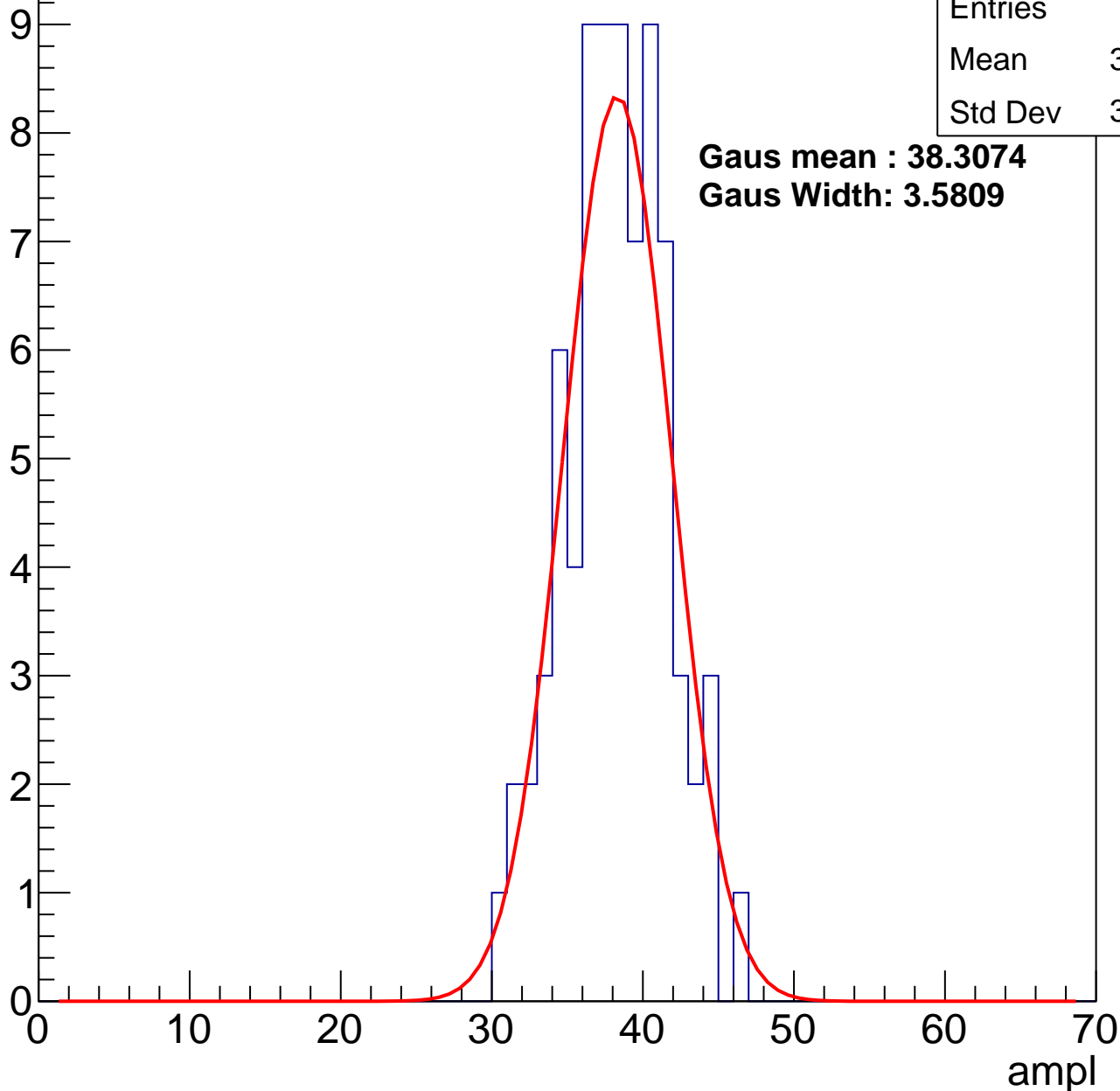
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.77
Std Dev	3.338

Gaus mean : 38.3074

Gaus Width: 3.5809



B0L001S, U22-ch36, adc2

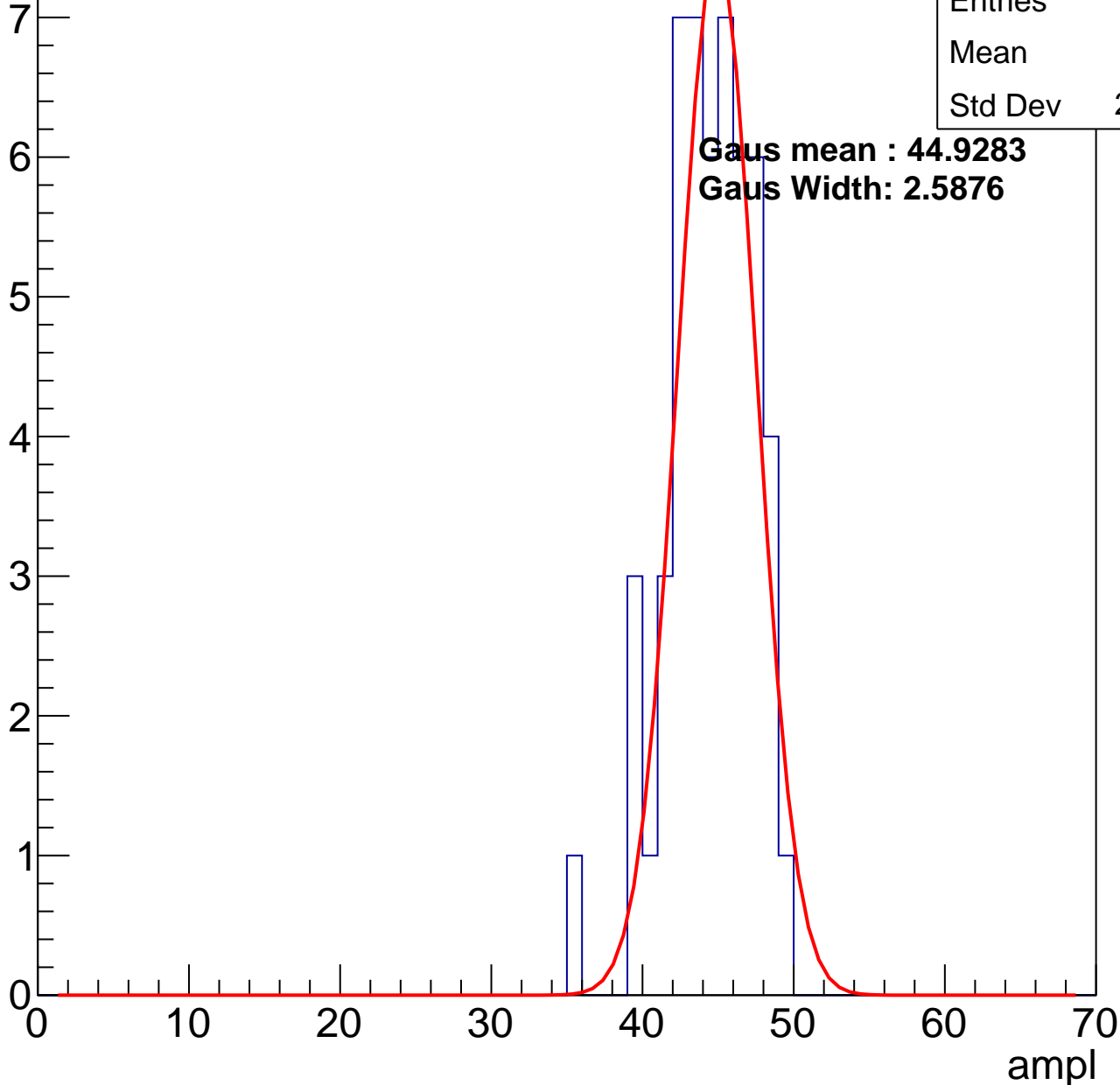
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44
Std Dev	2.801

Gaus mean : 44.9283

Gaus Width: 2.5876



B0L001S, U22-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

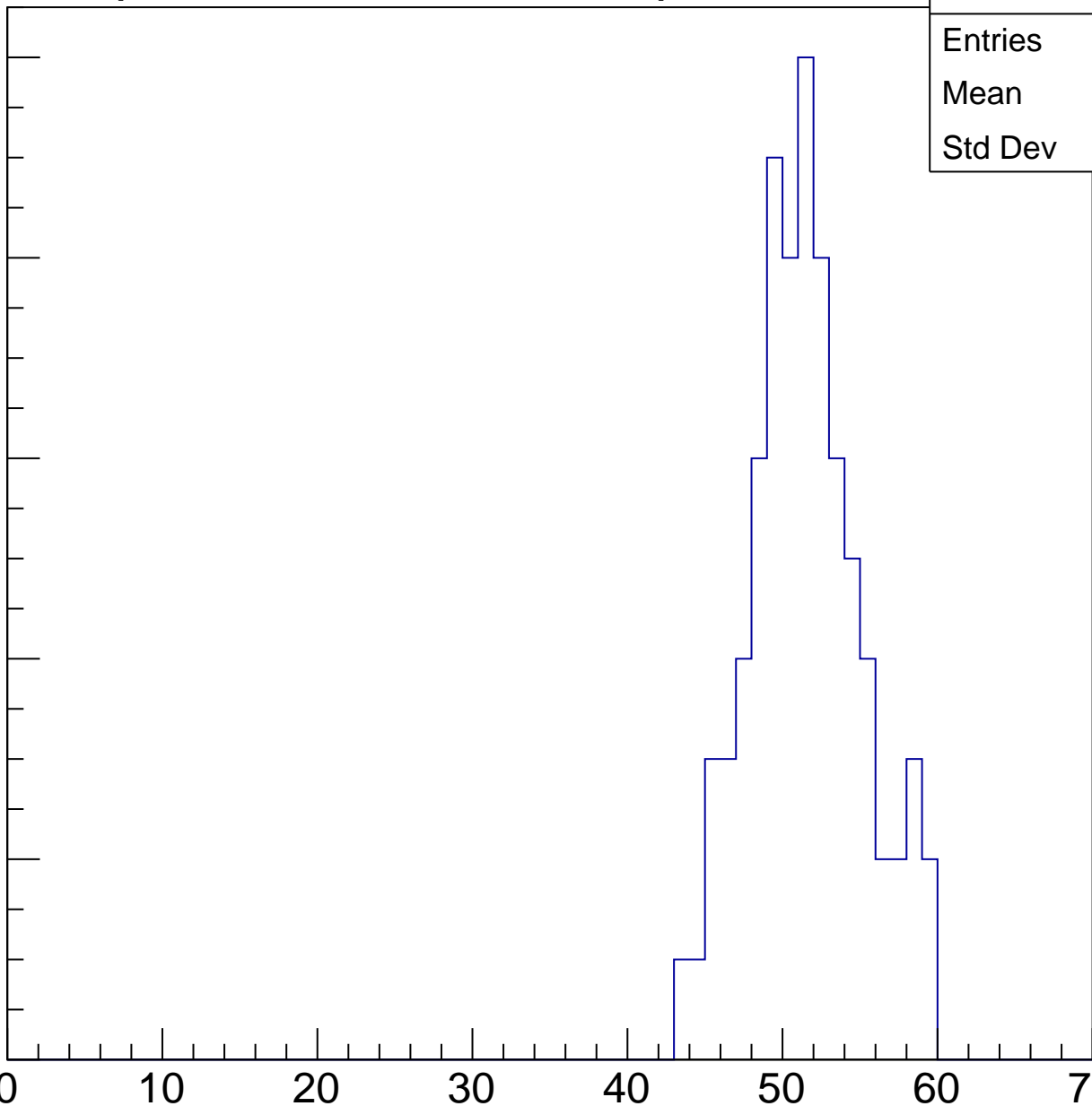
Entries	77
Mean	51.03
Std Dev	3.596

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

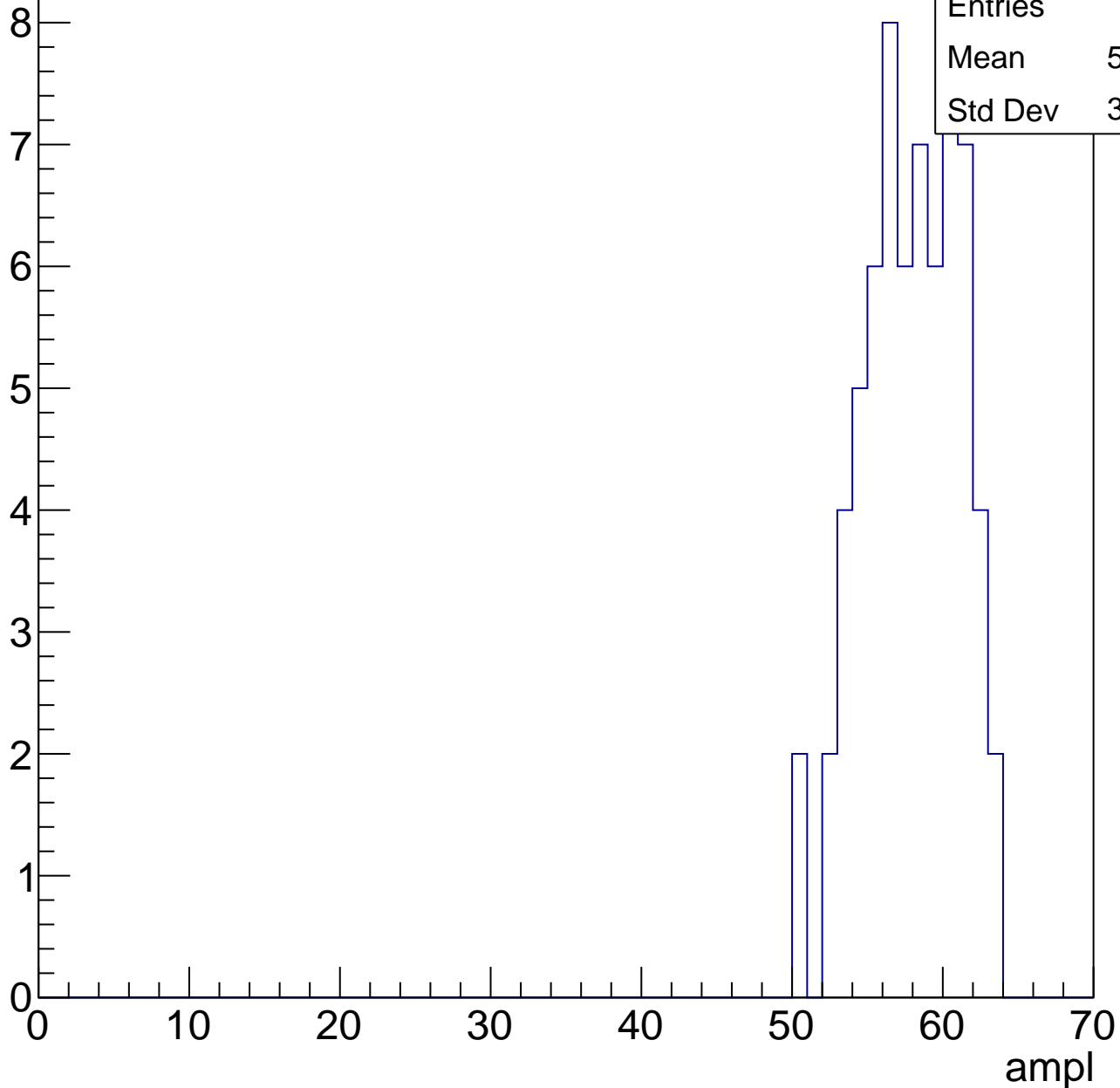


B0L001S, U22-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	57.42
Std Dev	3.149

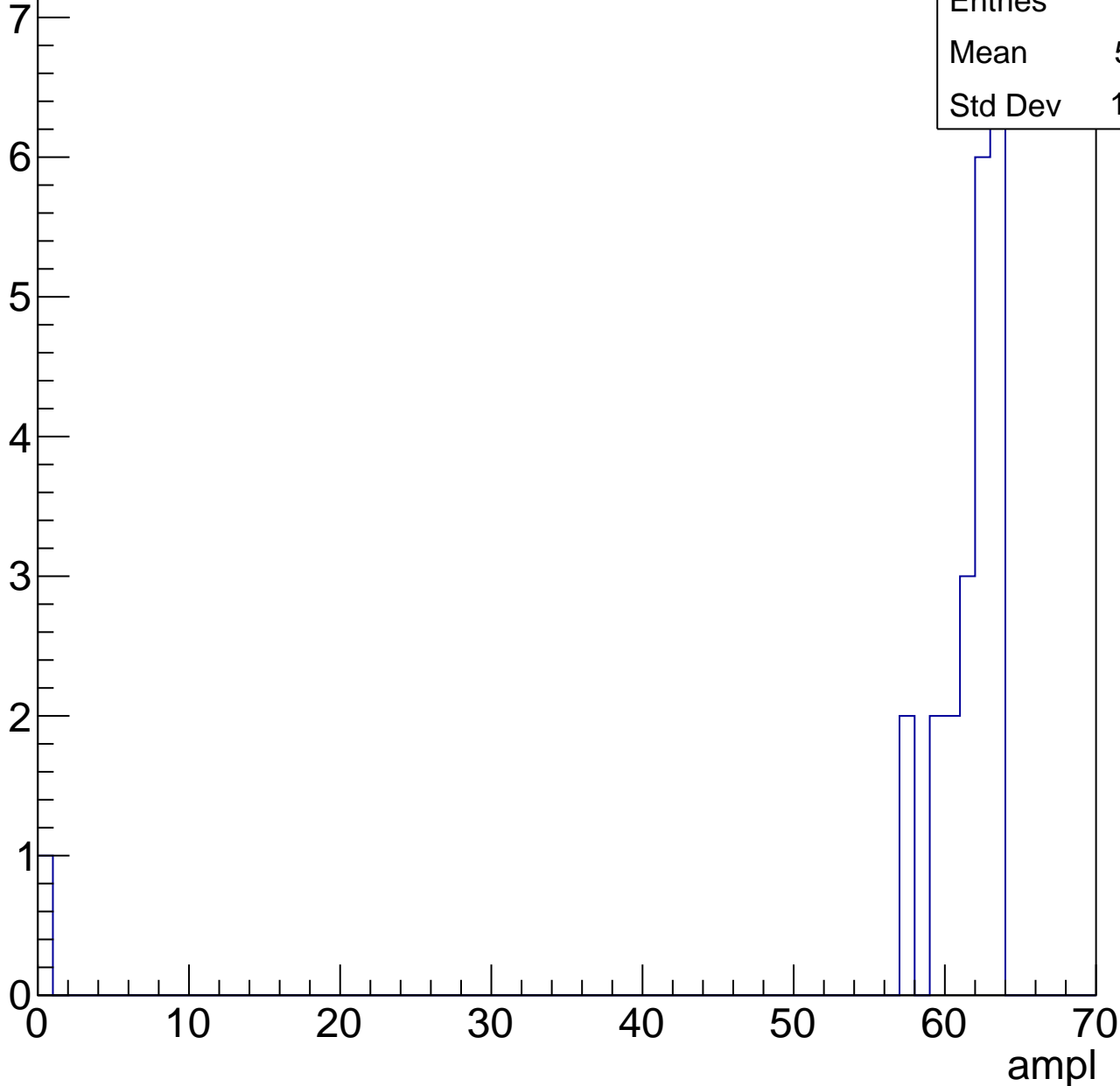


B0L001S, U22-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	58.61
Std Dev	12.62



B0L001S, U22-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch36, adc7

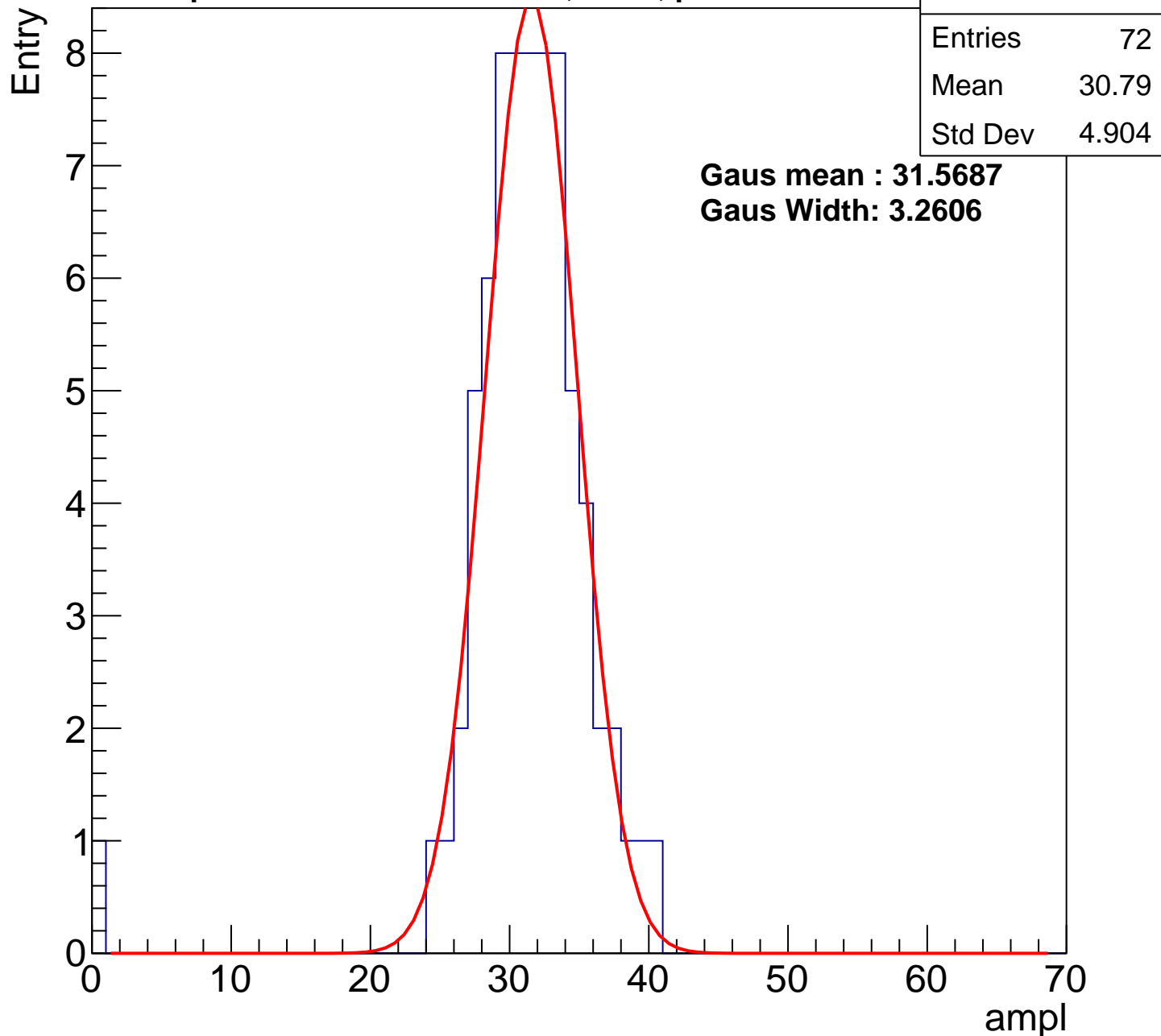
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch37, adc0

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U22-ch37, adc1

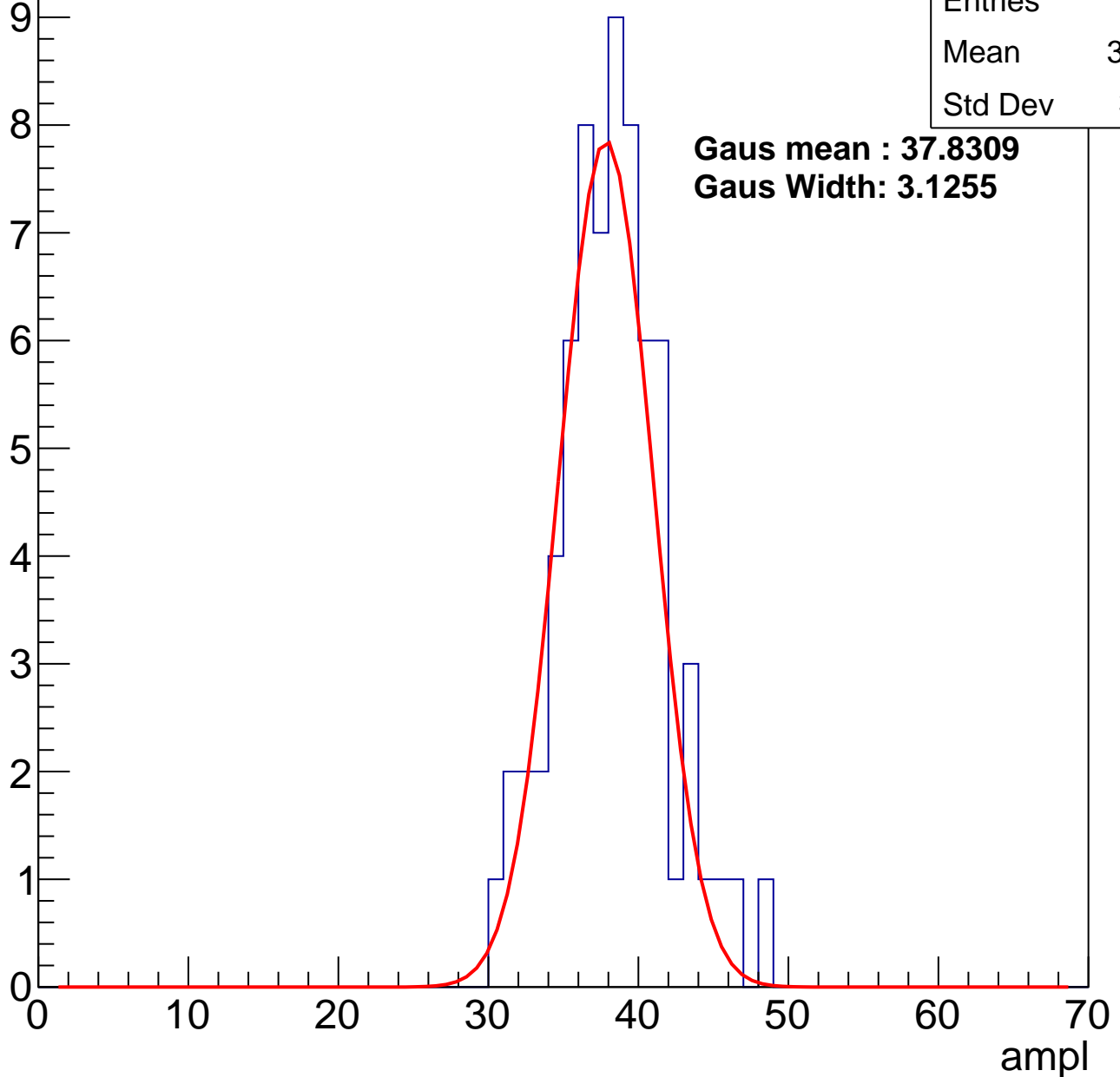
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	37.81
Std Dev	3.54

Gaus mean : 37.8309

Gaus Width: 3.1255



B0L001S, U22-ch37, adc2

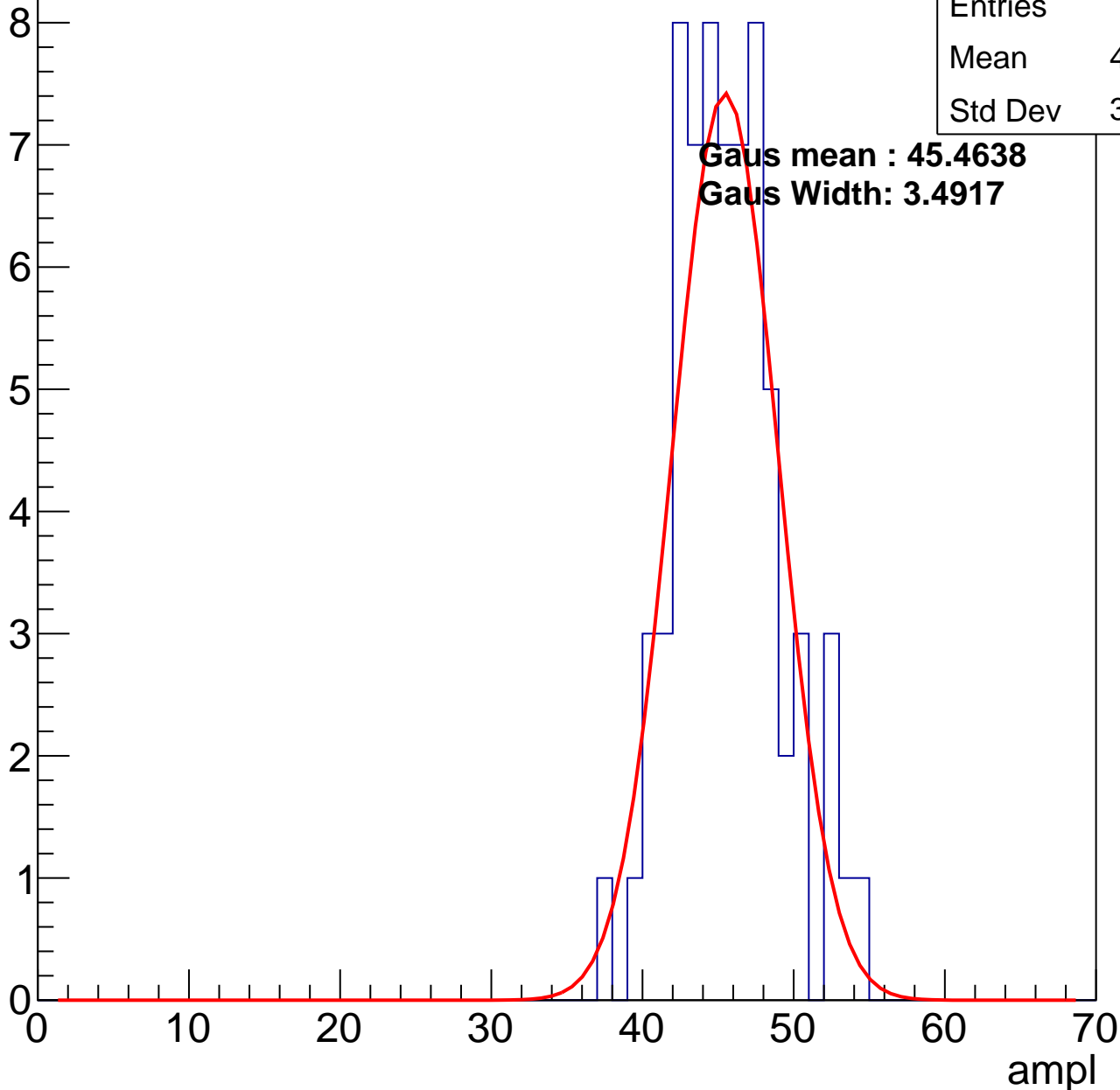
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45.18
Std Dev	3.455

Gaus mean : 45.4638

Gaus Width: 3.4917

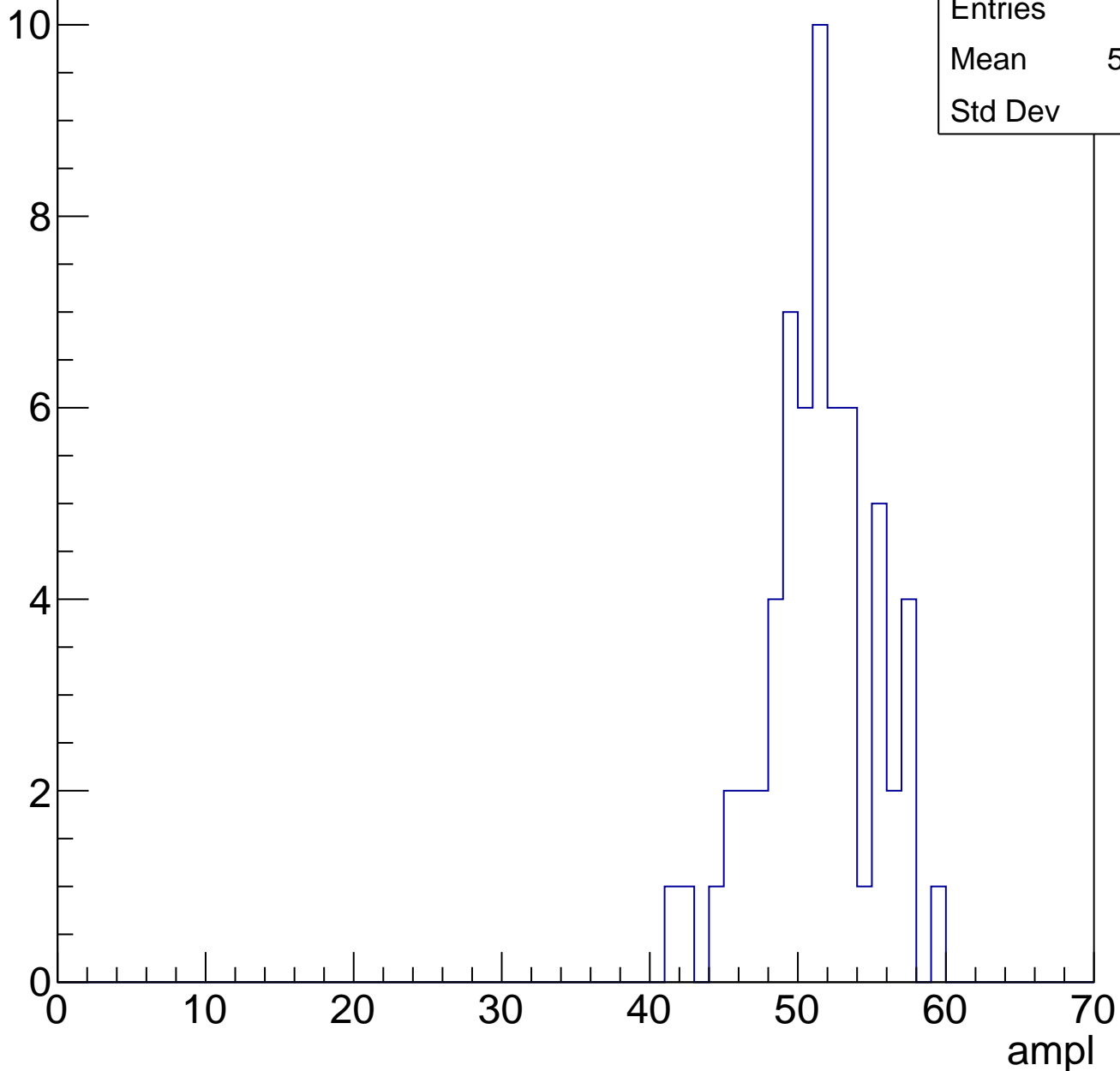


B0L001S, U22-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.92
Std Dev	3.69

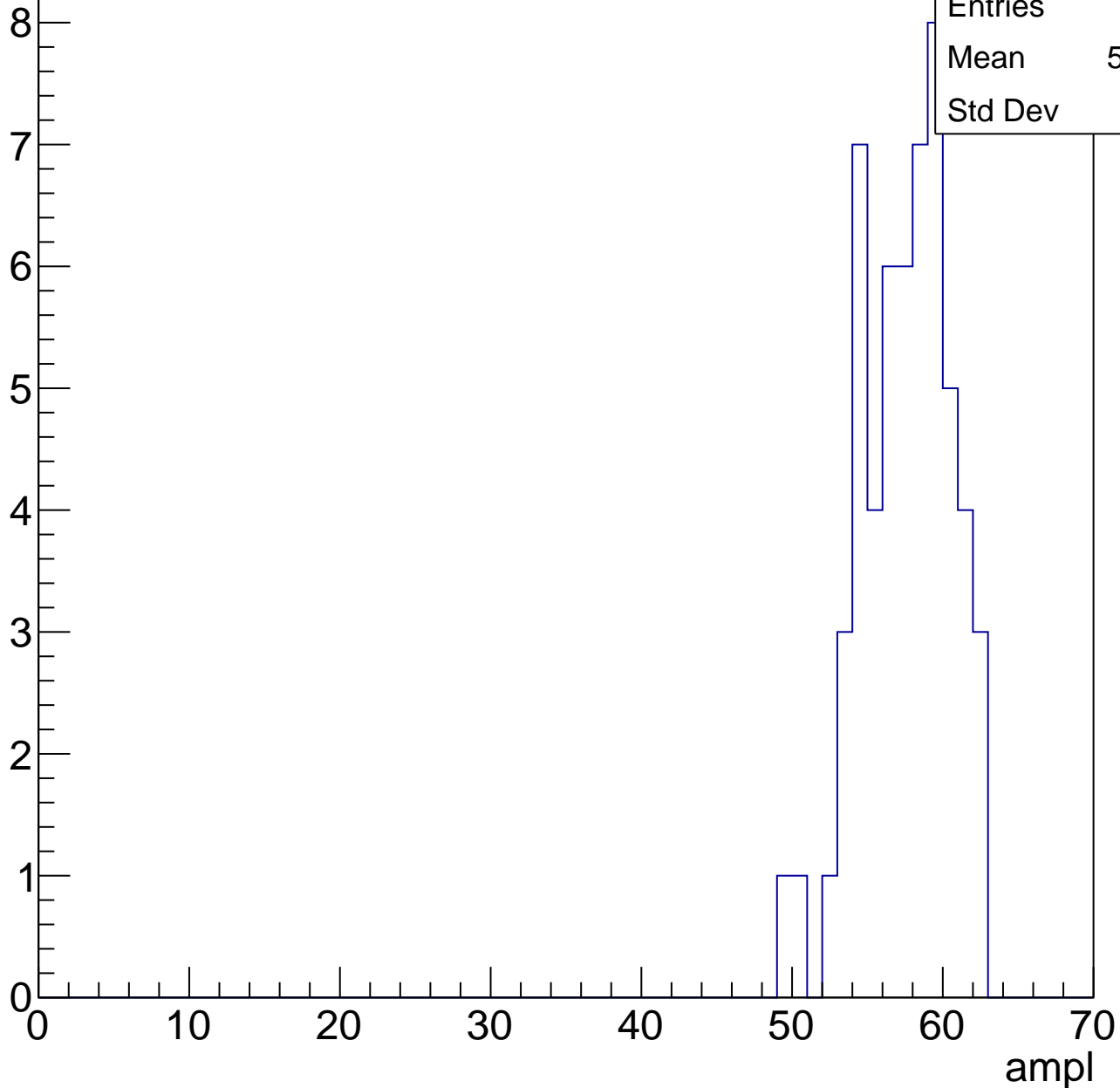


B0L001S, U22-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.04
Std Dev	2.97

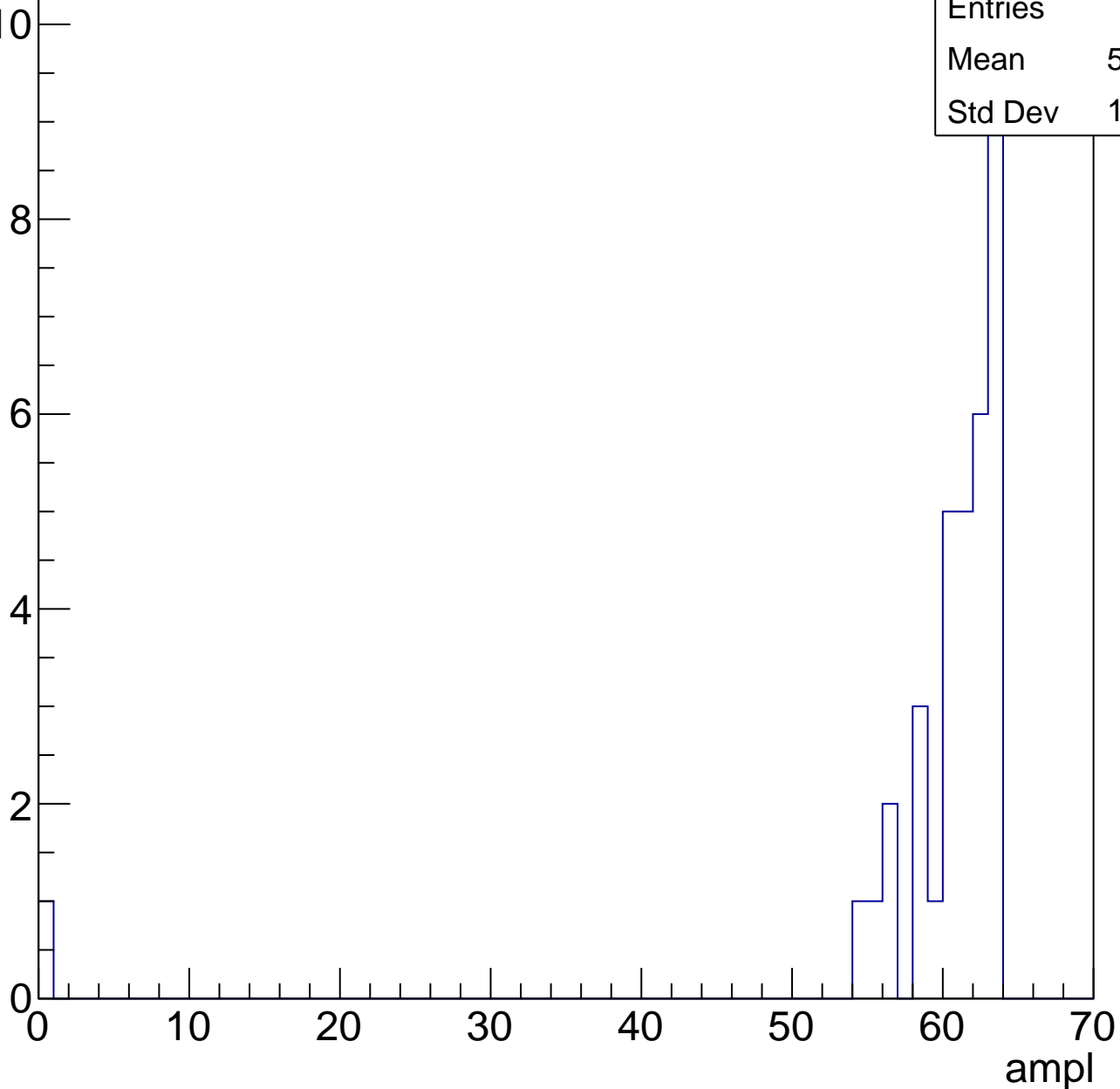


B0L001S, U22-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.89
Std Dev	10.39



B0L001S, U22-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch38, adc0

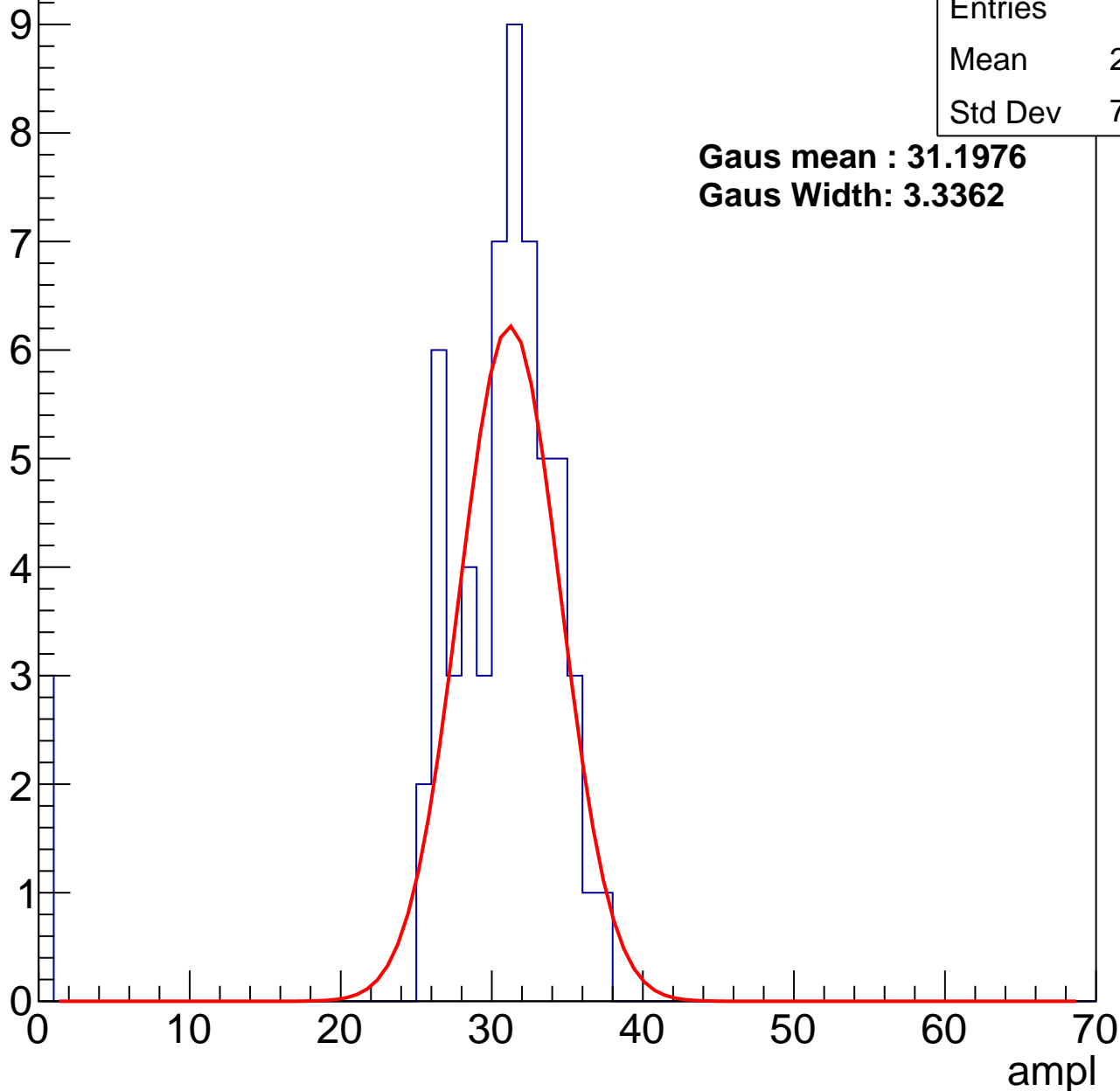
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	29.02
Std Dev	7.315

Gaus mean : 31.1976

Gaus Width: 3.3362



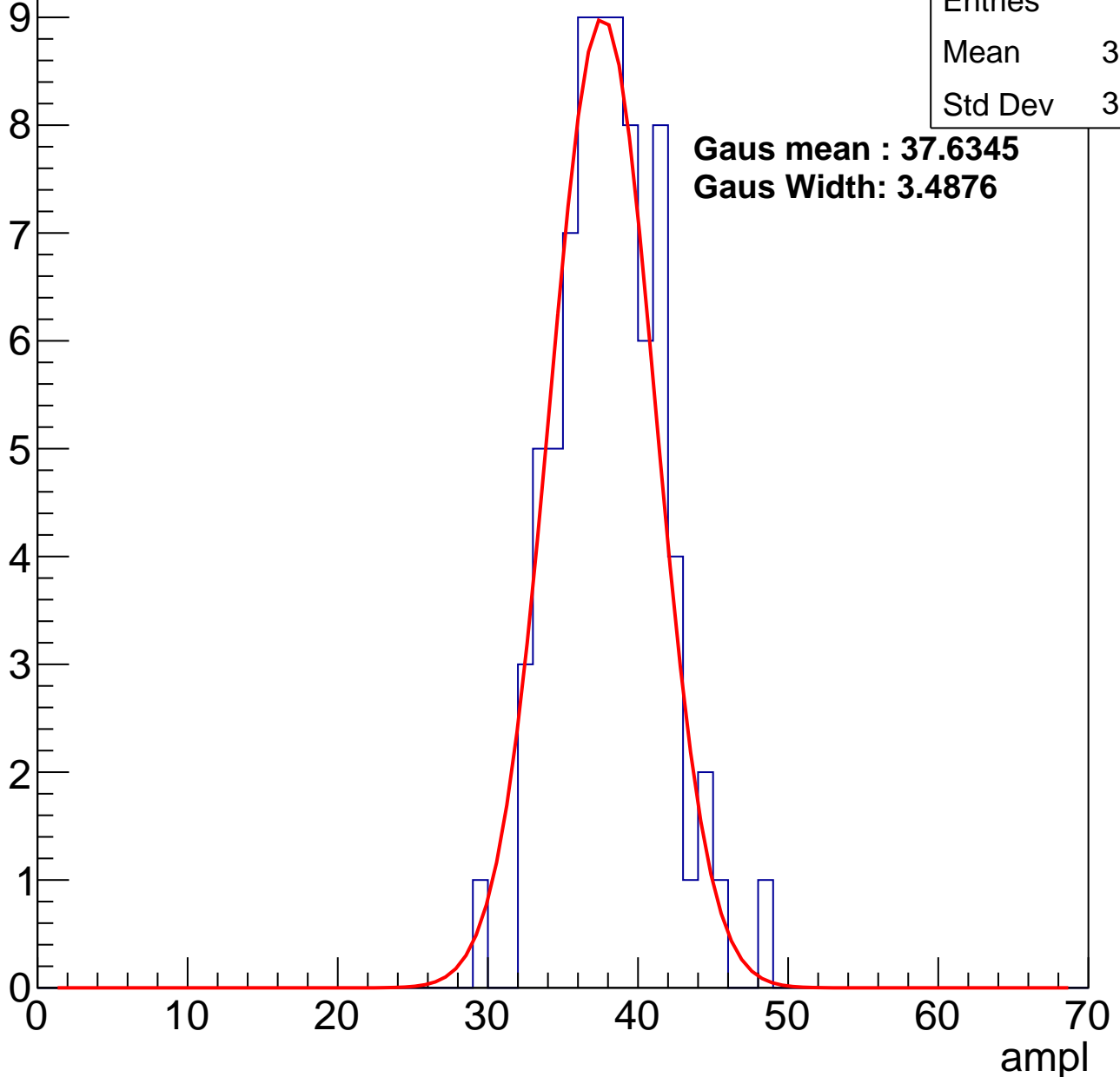
B0L001S, U22-ch38, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	37.67
Std Dev	3.389

Gaus mean : 37.6345
Gaus Width: 3.4876



B0L001S, U22-ch38, adc2

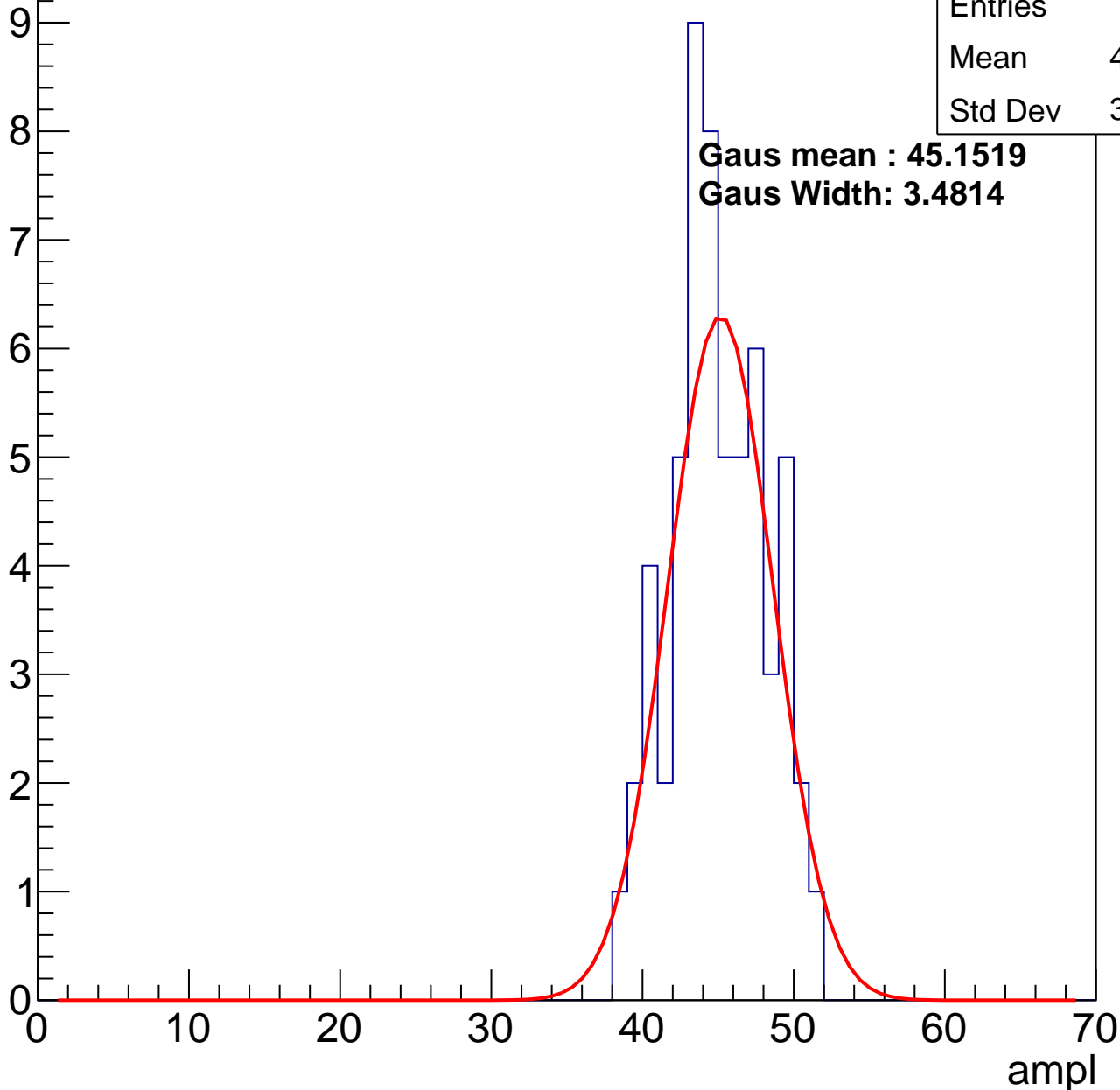
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.55
Std Dev	3.086

Gaus mean : 45.1519

Gaus Width: 3.4814

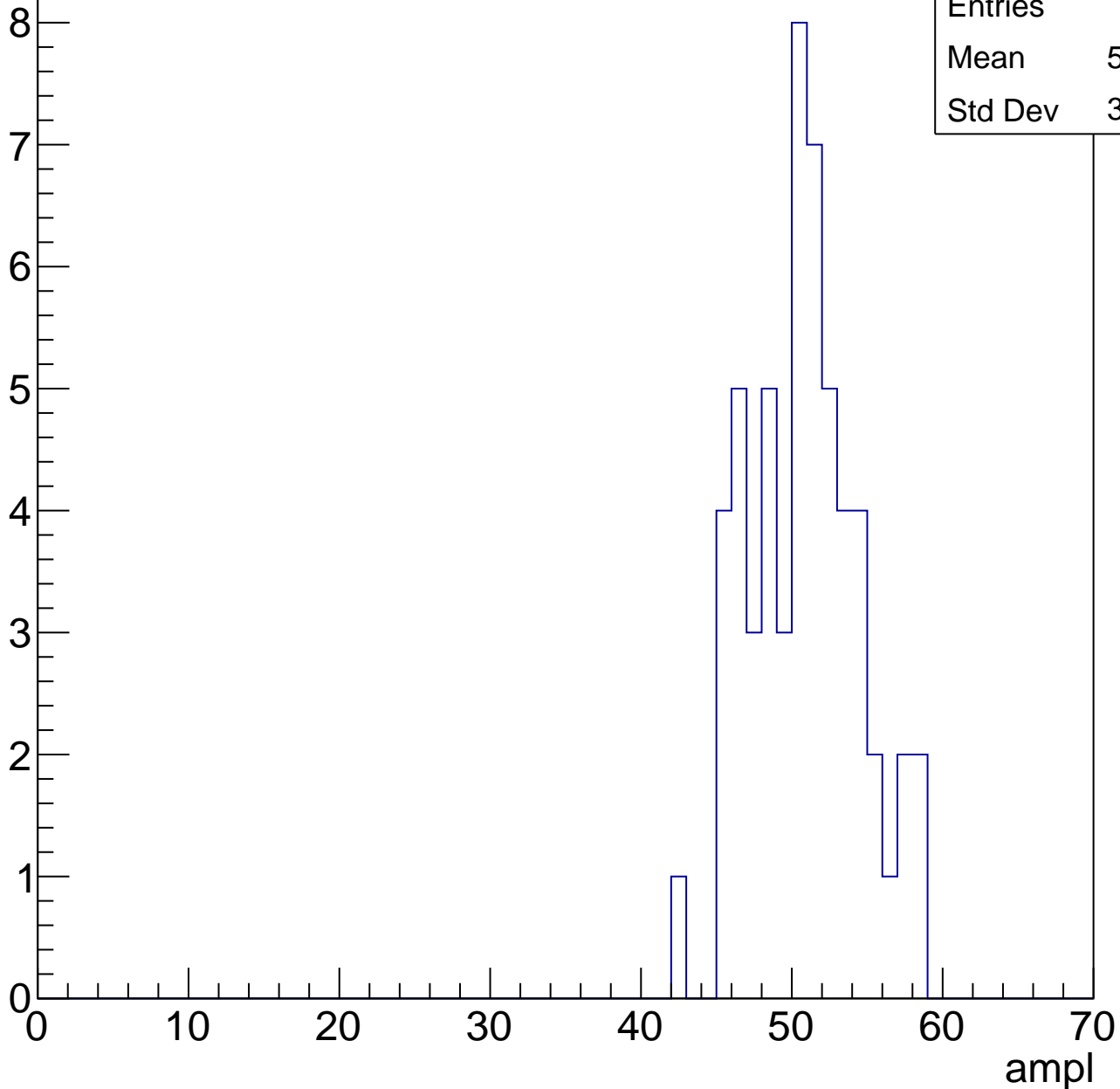


B0L001S, U22-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	50.38
Std Dev	3.598

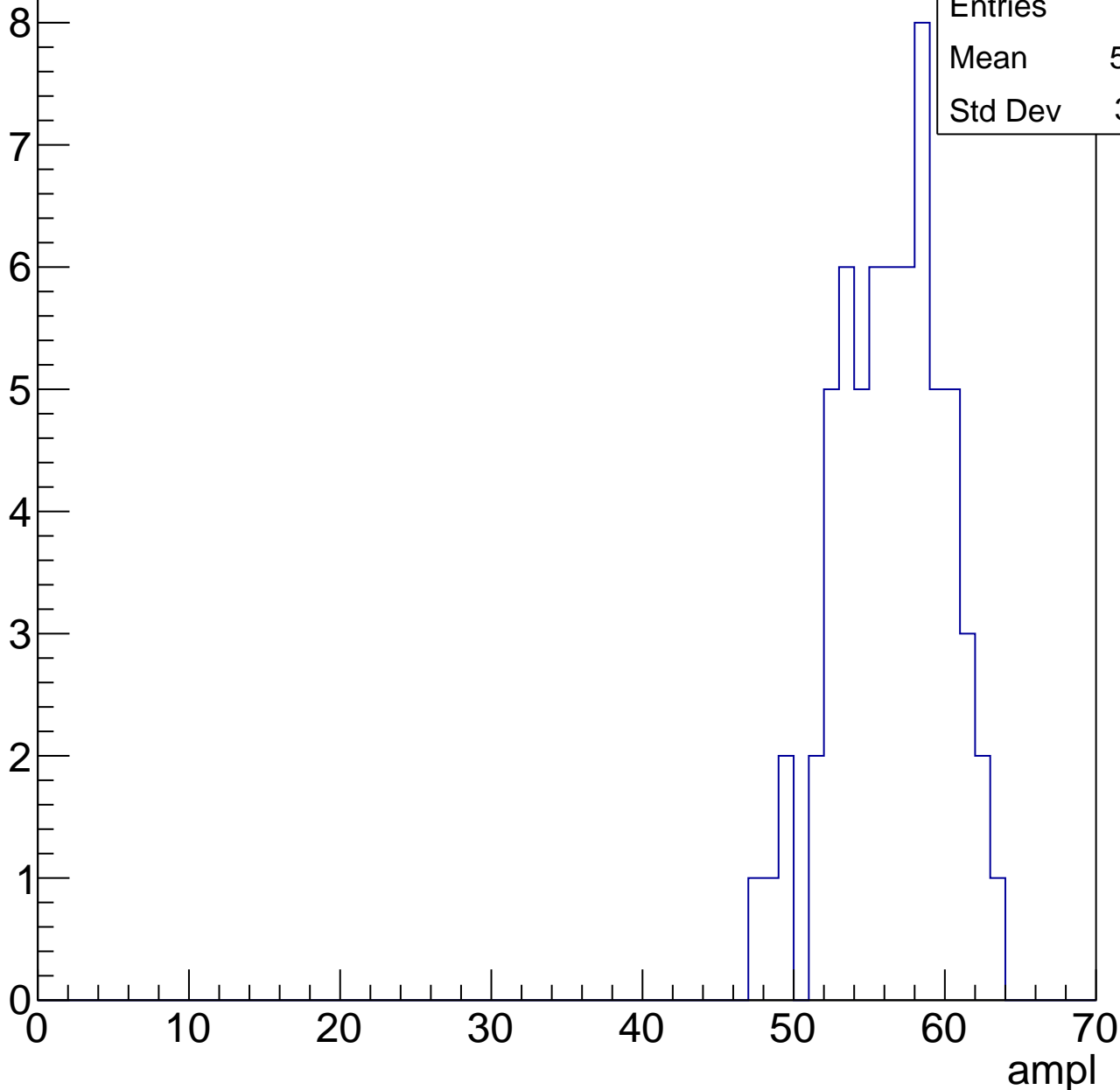


B0L001S, U22-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	55.94
Std Dev	3.561

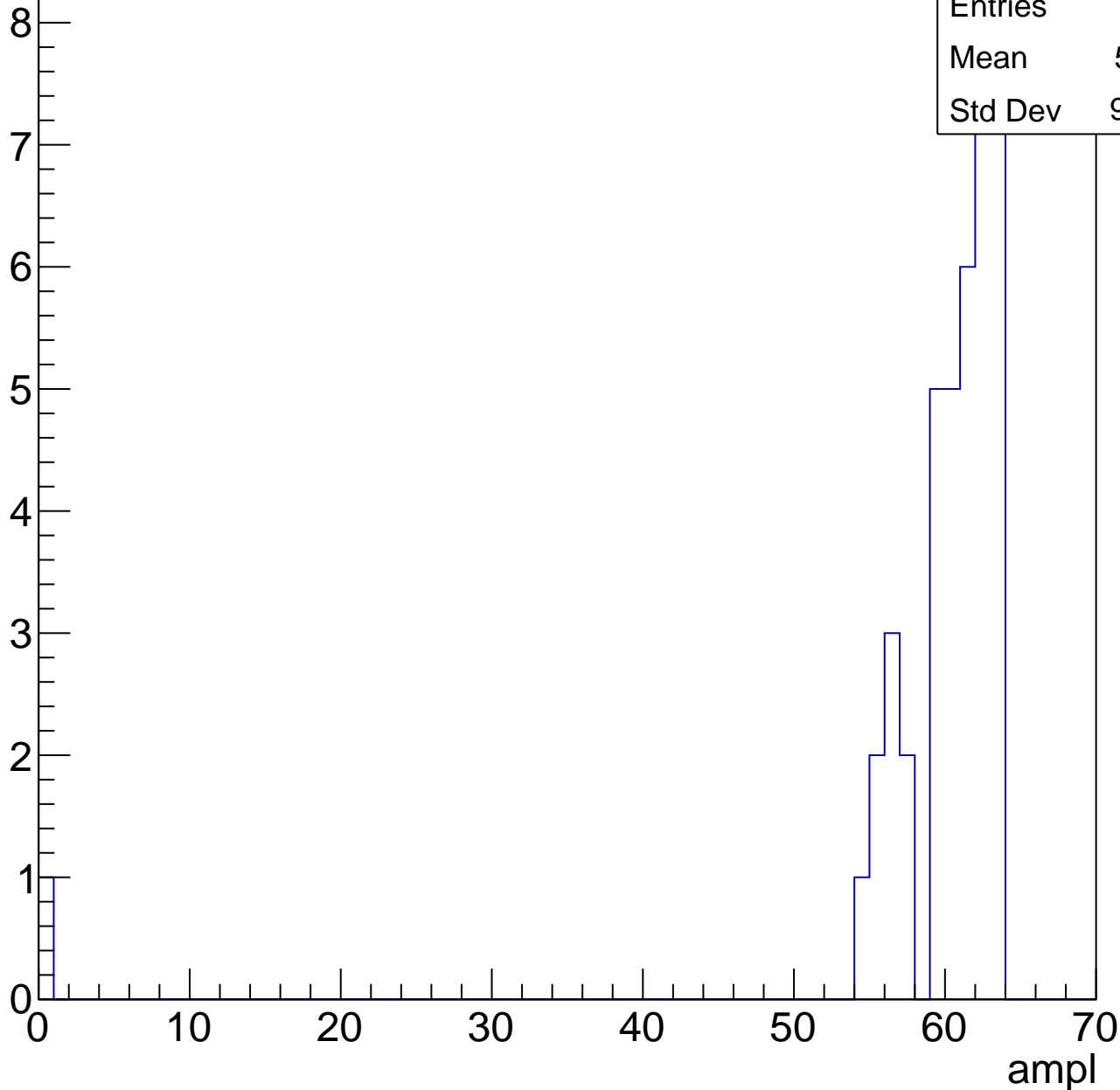


B0L001S, U22-ch38, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.71
Std Dev	9.625



B0L001S, U22-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch39, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	30.93
Std Dev	5.083

Gaus mean : 31.6155

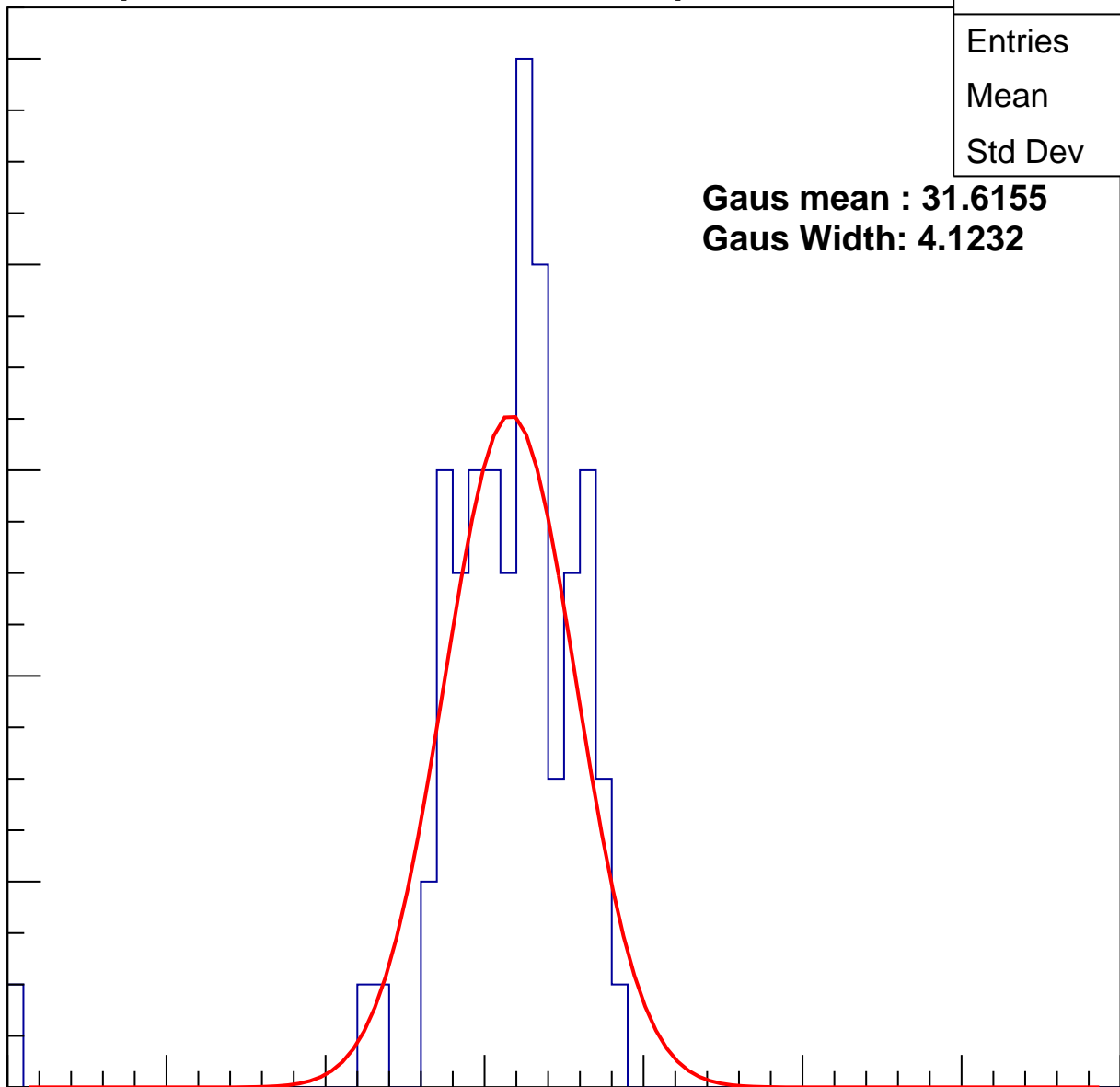
Gaus Width: 4.1232

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch39, adc1

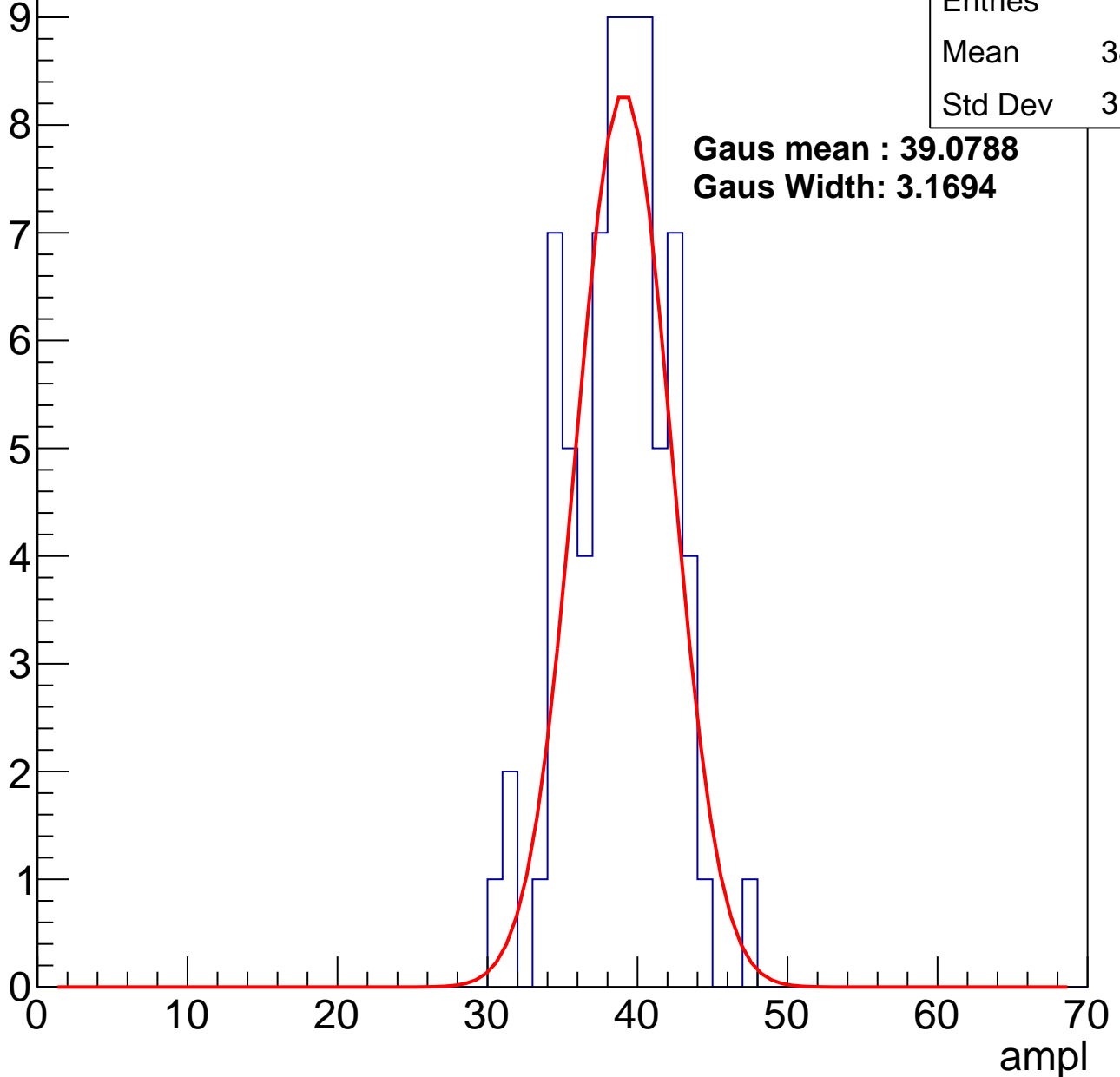
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.28
Std Dev	3.292

Gaus mean : 39.0788

Gaus Width: 3.1694



B0L001S, U22-ch39, adc2

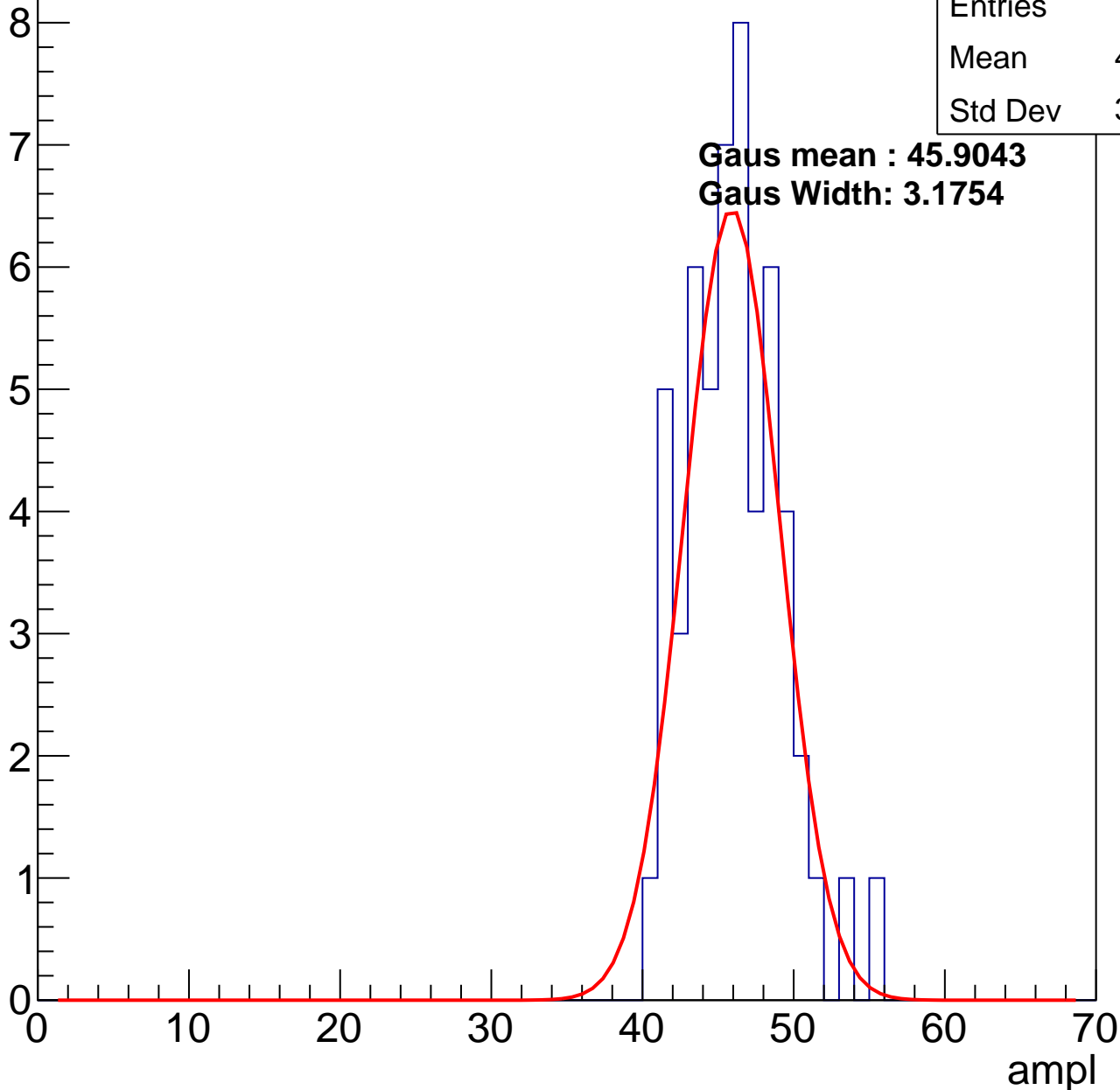
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	45.61
Std Dev	3.141

Gaus mean : 45.9043

Gaus Width: 3.1754

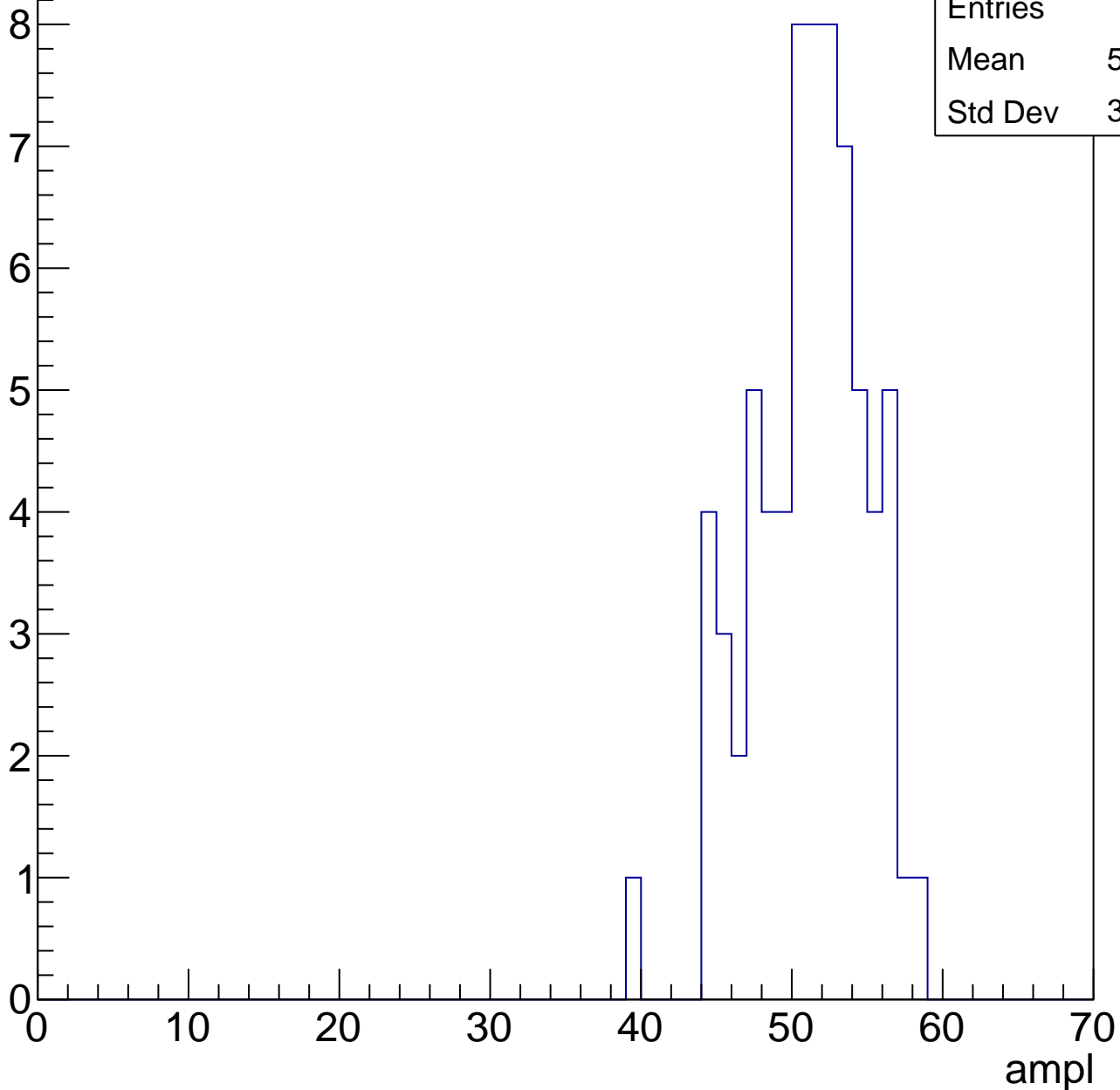


B0L001S, U22-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.64
Std Dev	3.749

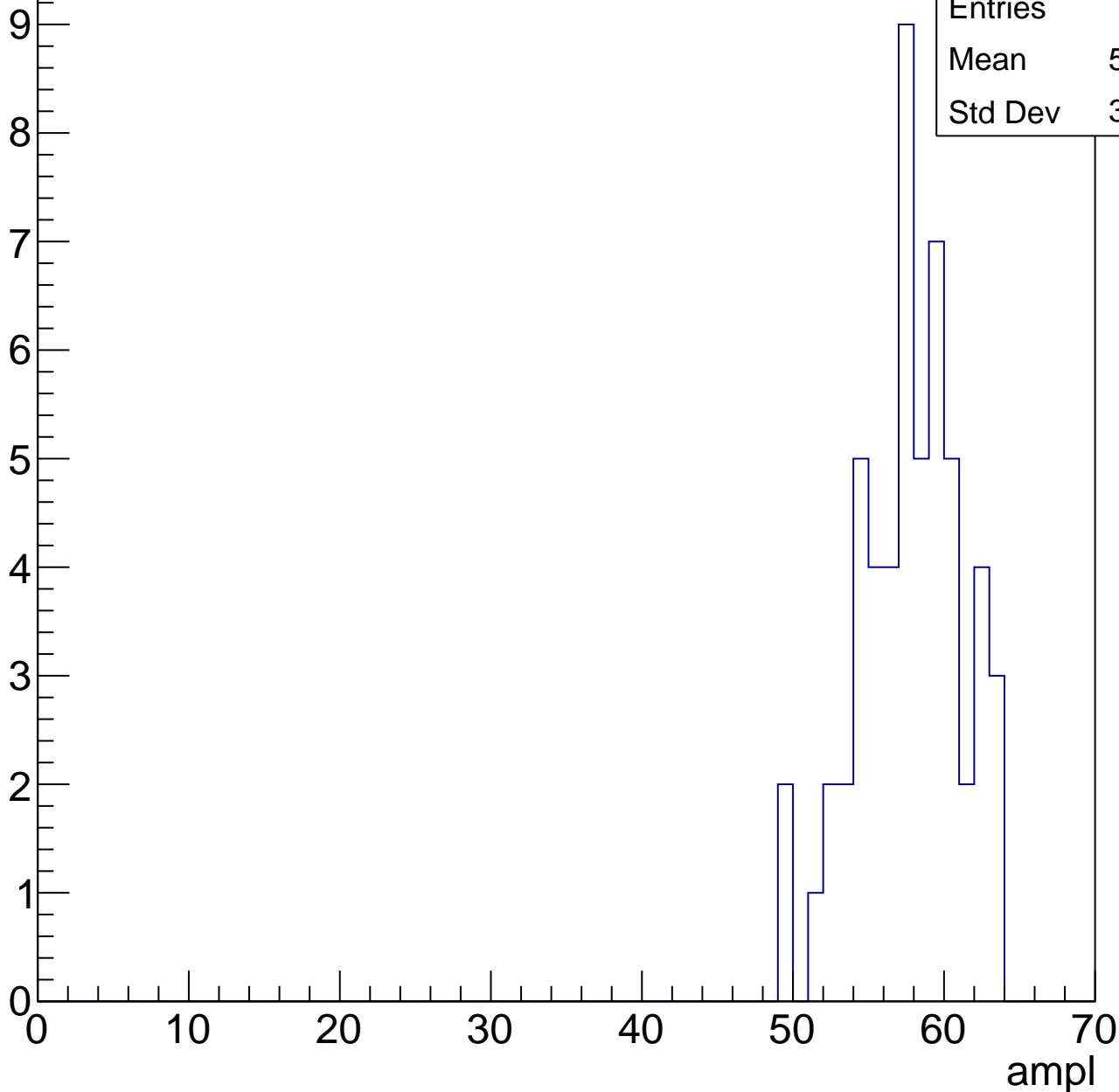


B0L001S, U22-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.24
Std Dev	3.373

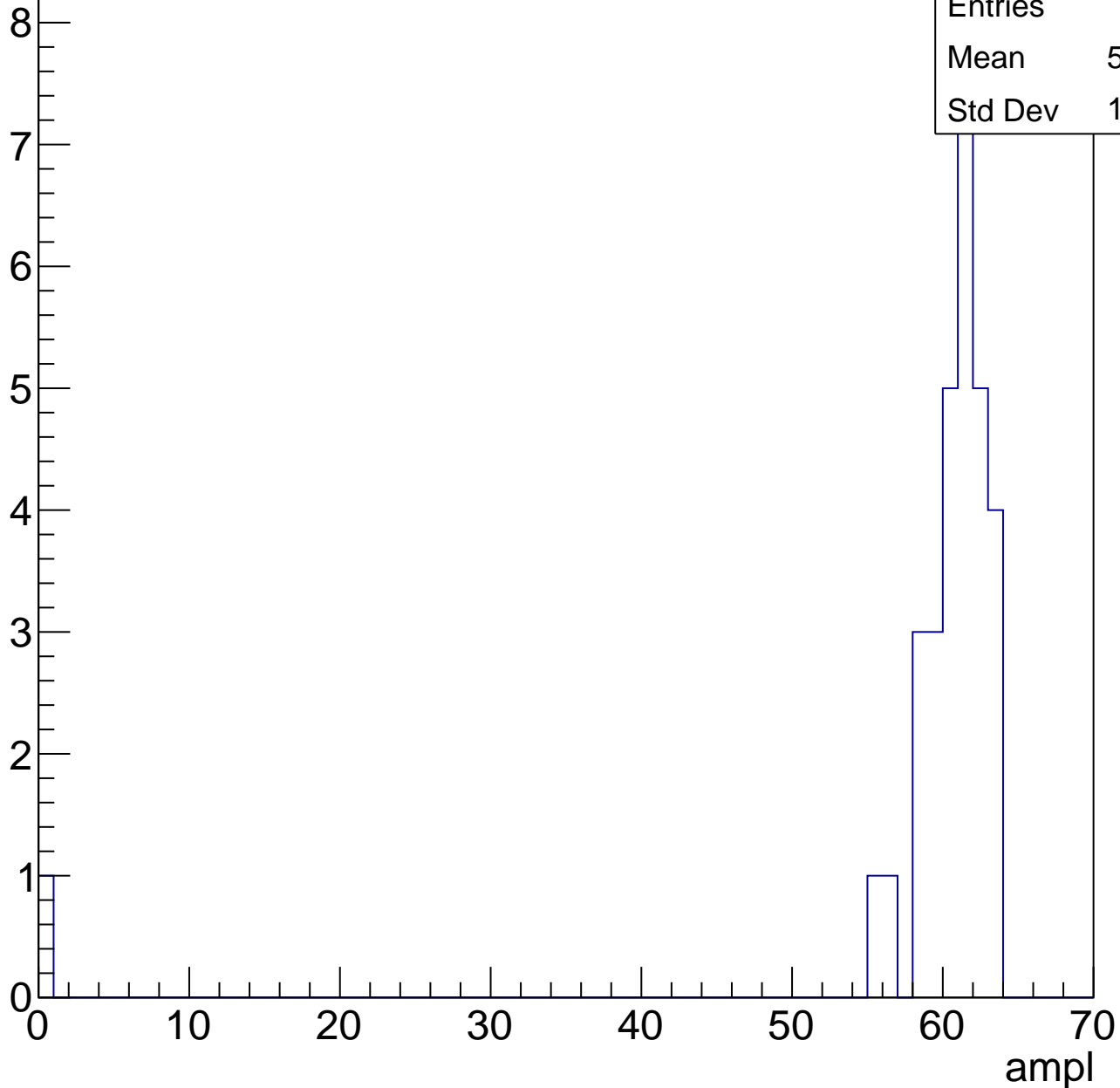


B0L001S, U22-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

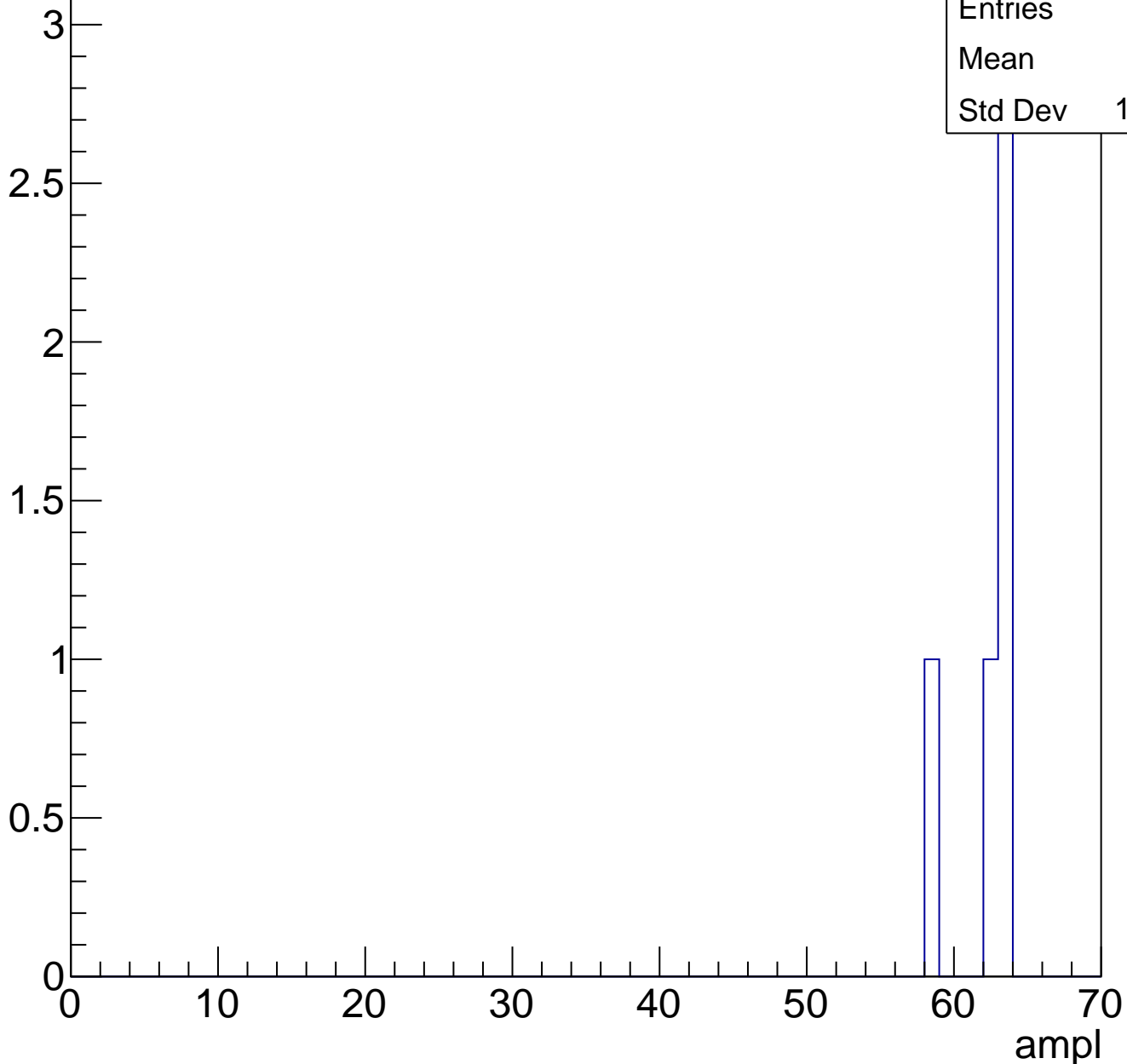
Entries	31
Mean	58.45
Std Dev	10.84



B0L001S, U22-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	5
Mean	61.8
Std Dev	1.939

B0L001S, U22-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch40, adc0

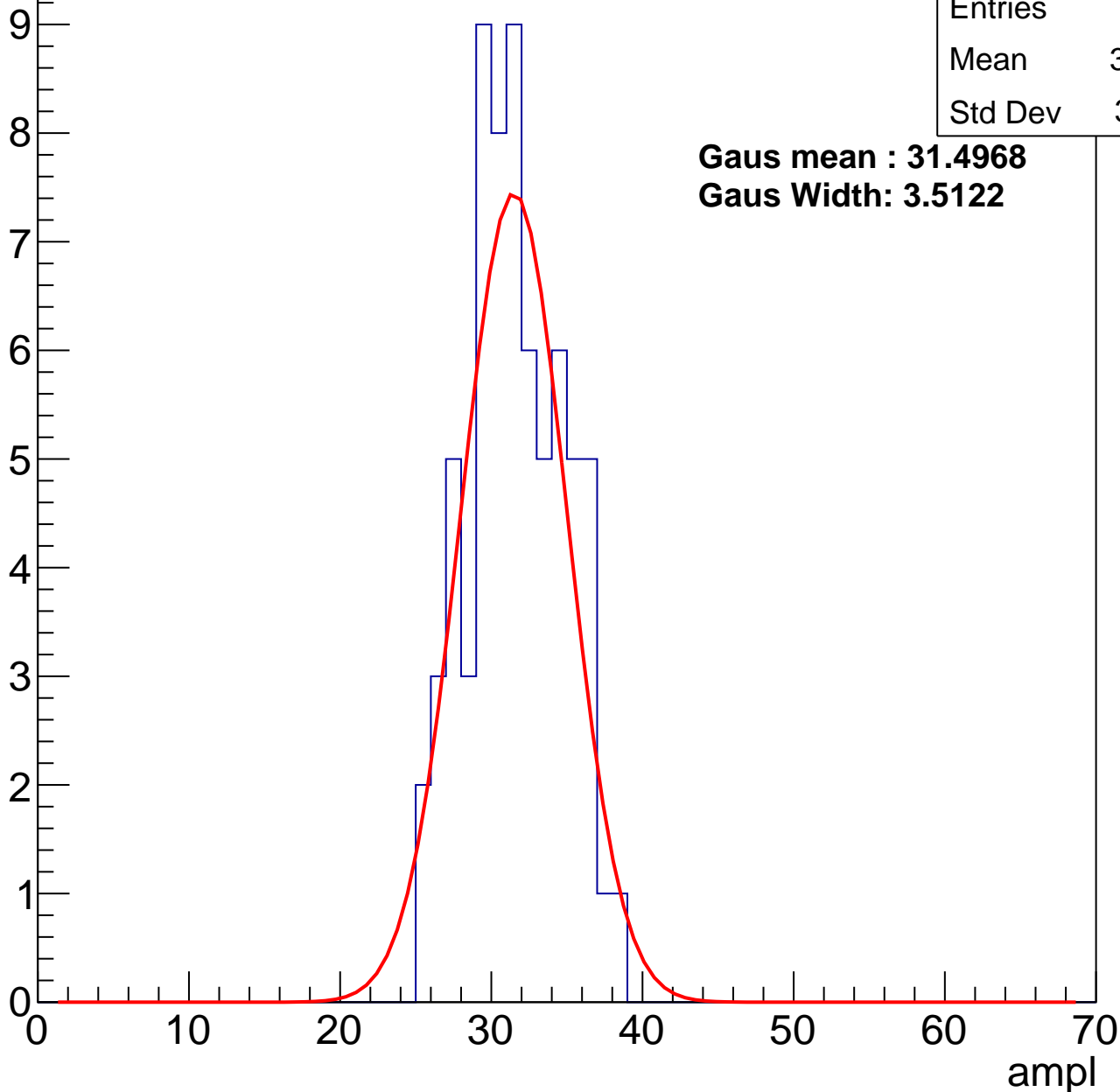
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	31.15
Std Dev	3.131

Gaus mean : 31.4968

Gaus Width: 3.5122



B0L001S, U22-ch40, adc1

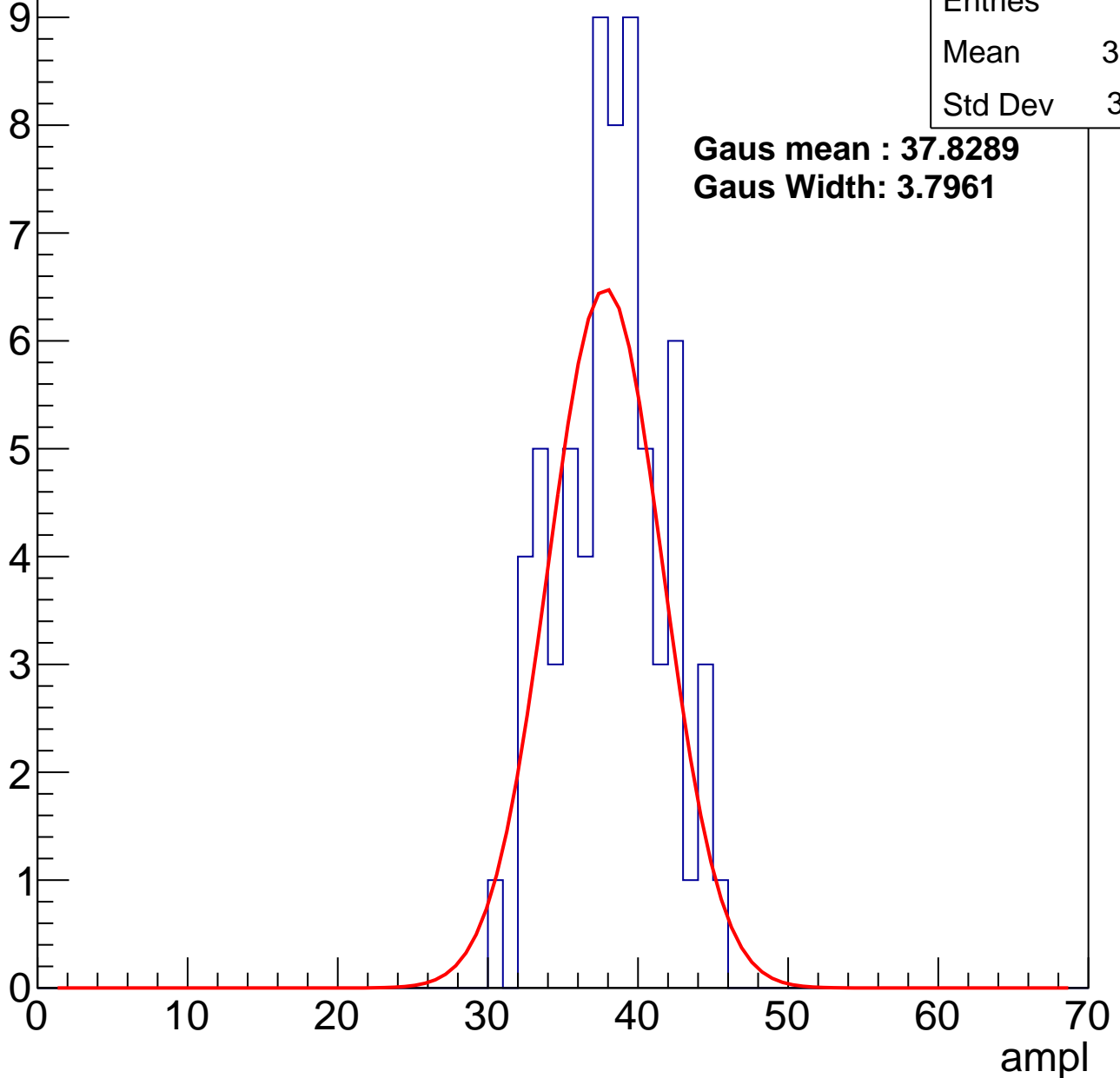
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.72
Std Dev	3.411

Gaus mean : 37.8289

Gaus Width: 3.7961



B0L001S, U22-ch40, adc2

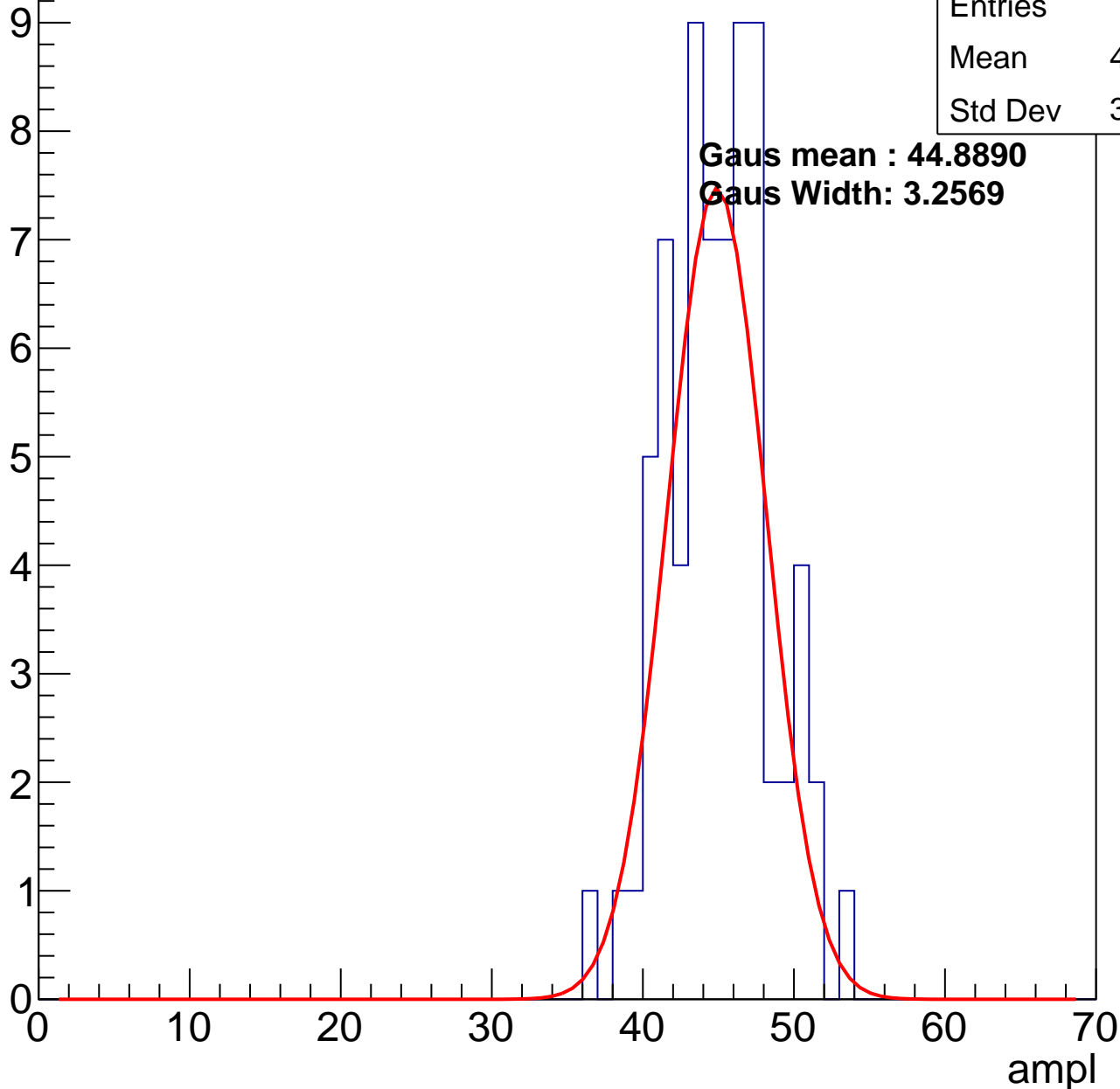
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	44.56
Std Dev	3.356

Gaus mean : 44.8890

Gaus Width: 3.2569

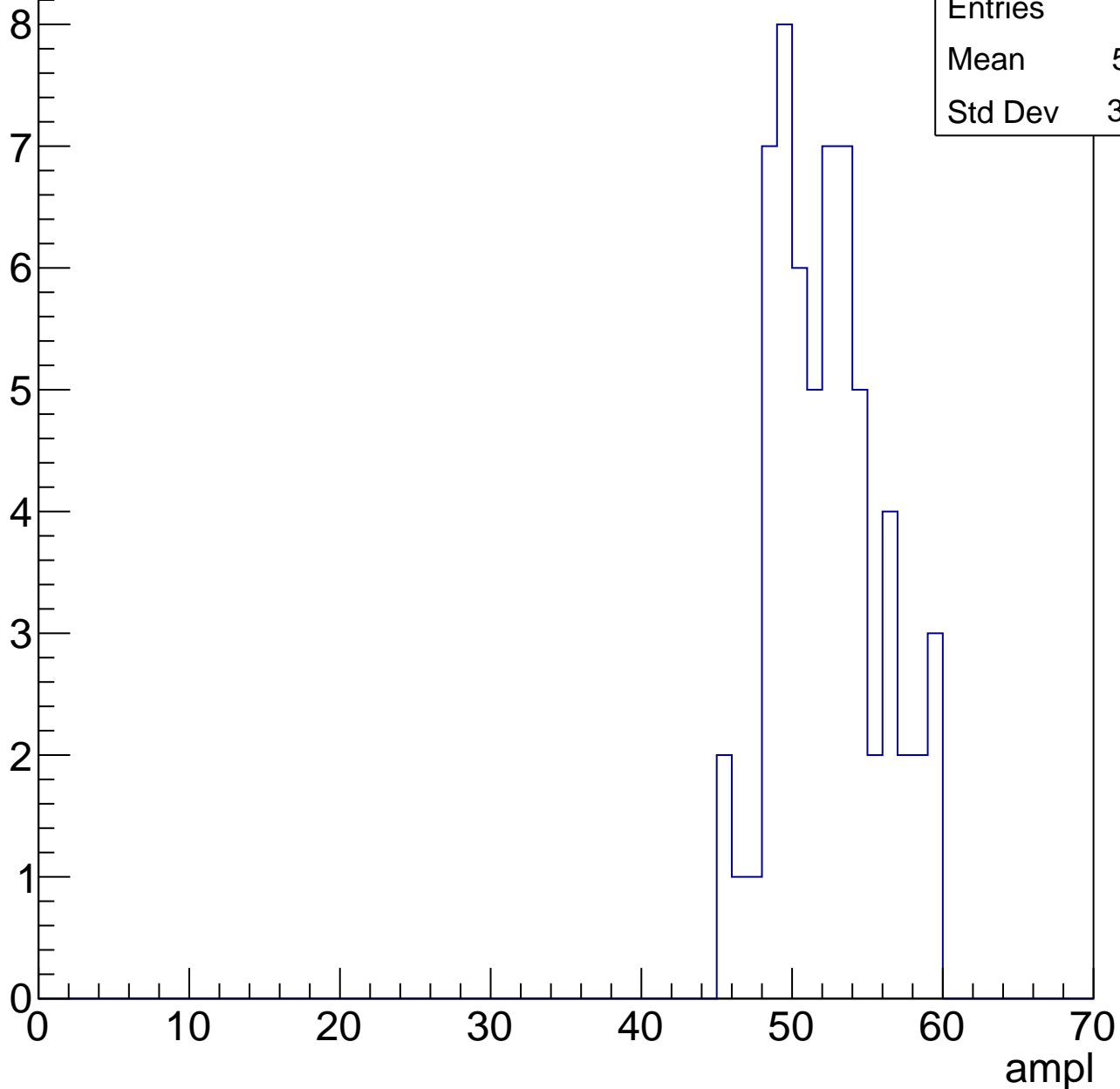


B0L001S, U22-ch40, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.81
Std Dev	3.473



B0L001S, U22-ch40, adc4

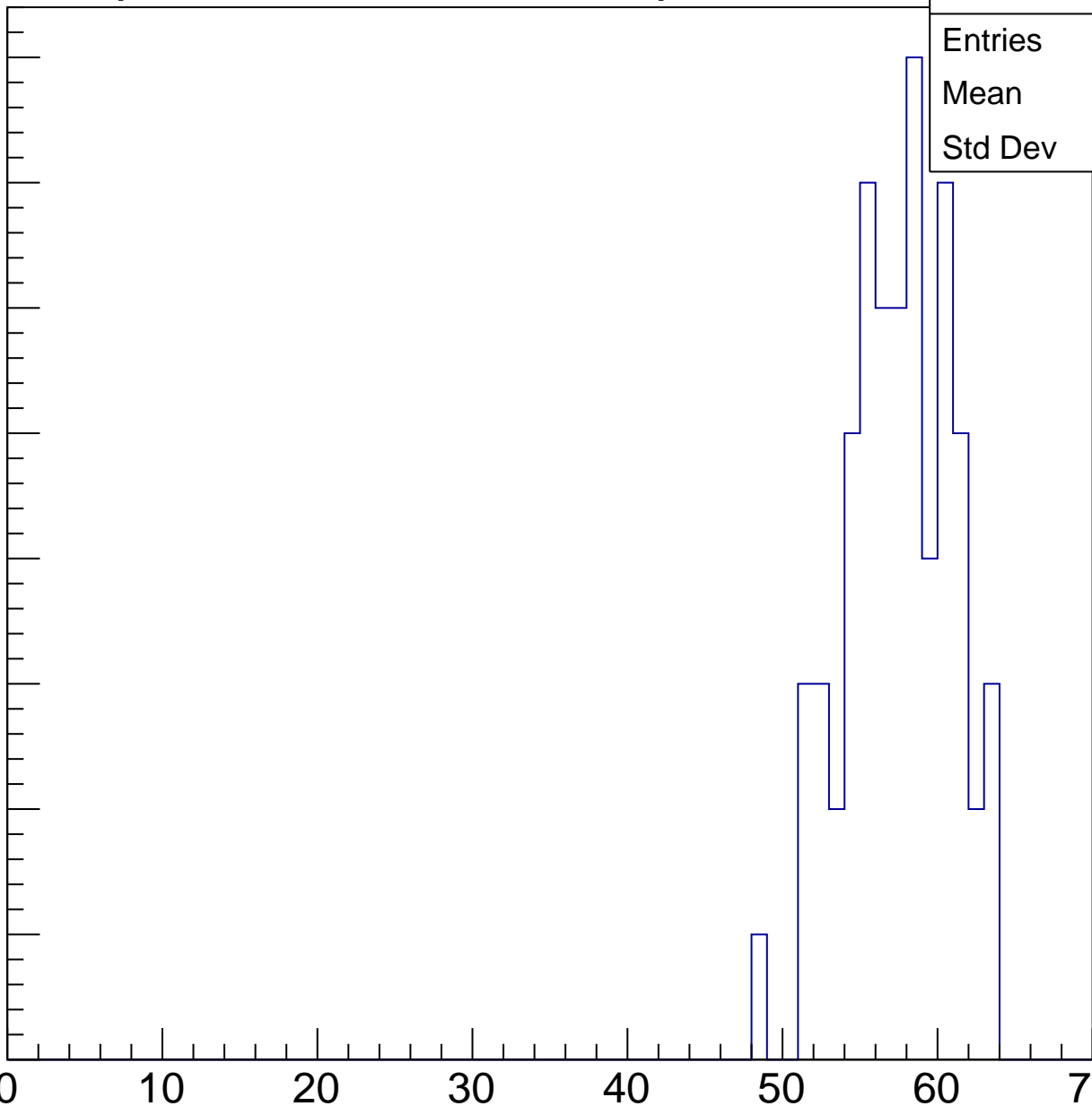
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	62
Mean	57
Std Dev	3.36

ampl

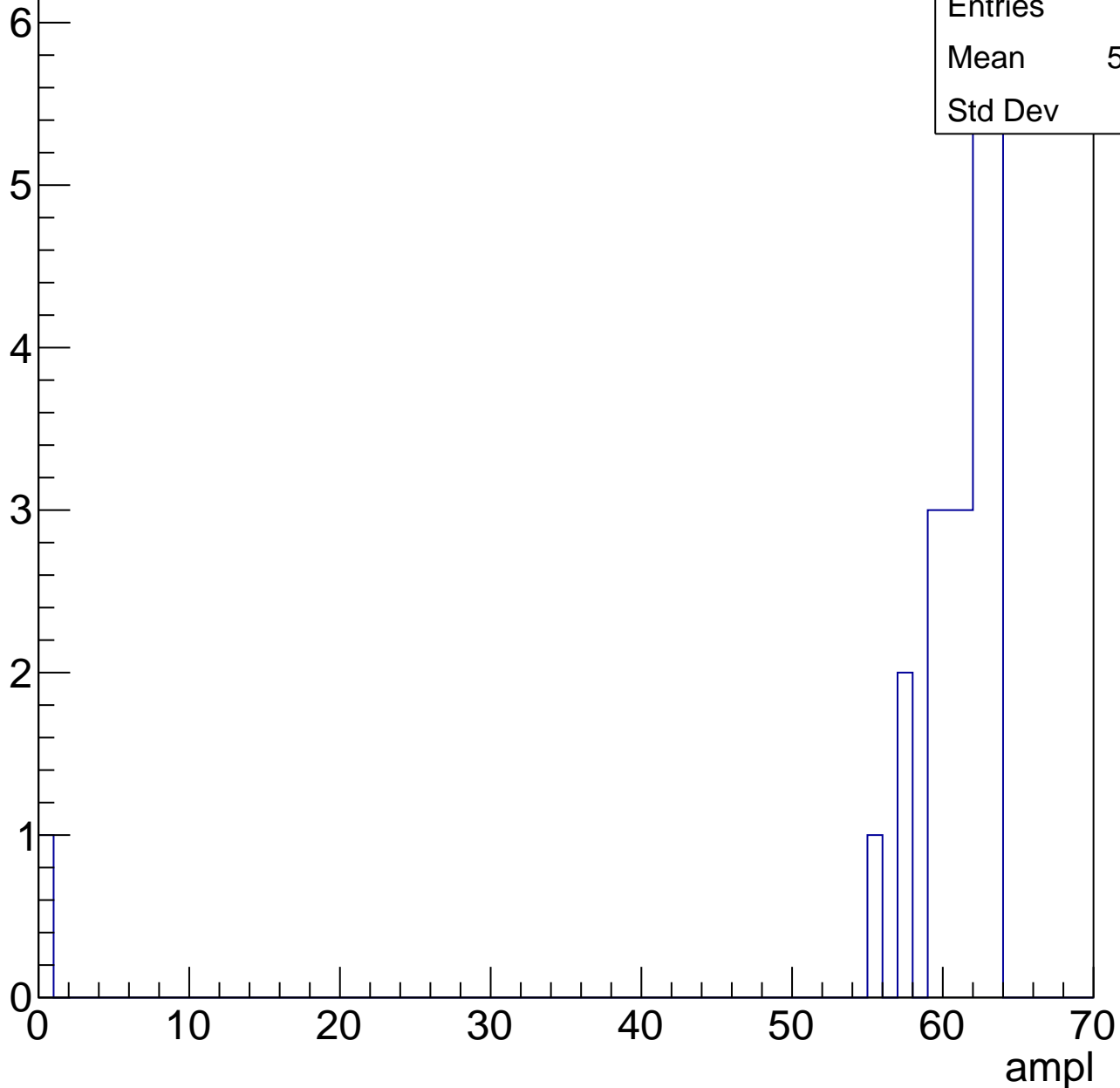


B0L001S, U22-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.36
Std Dev	12.1



B0L001S, U22-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L001S, U22-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch41, adc0

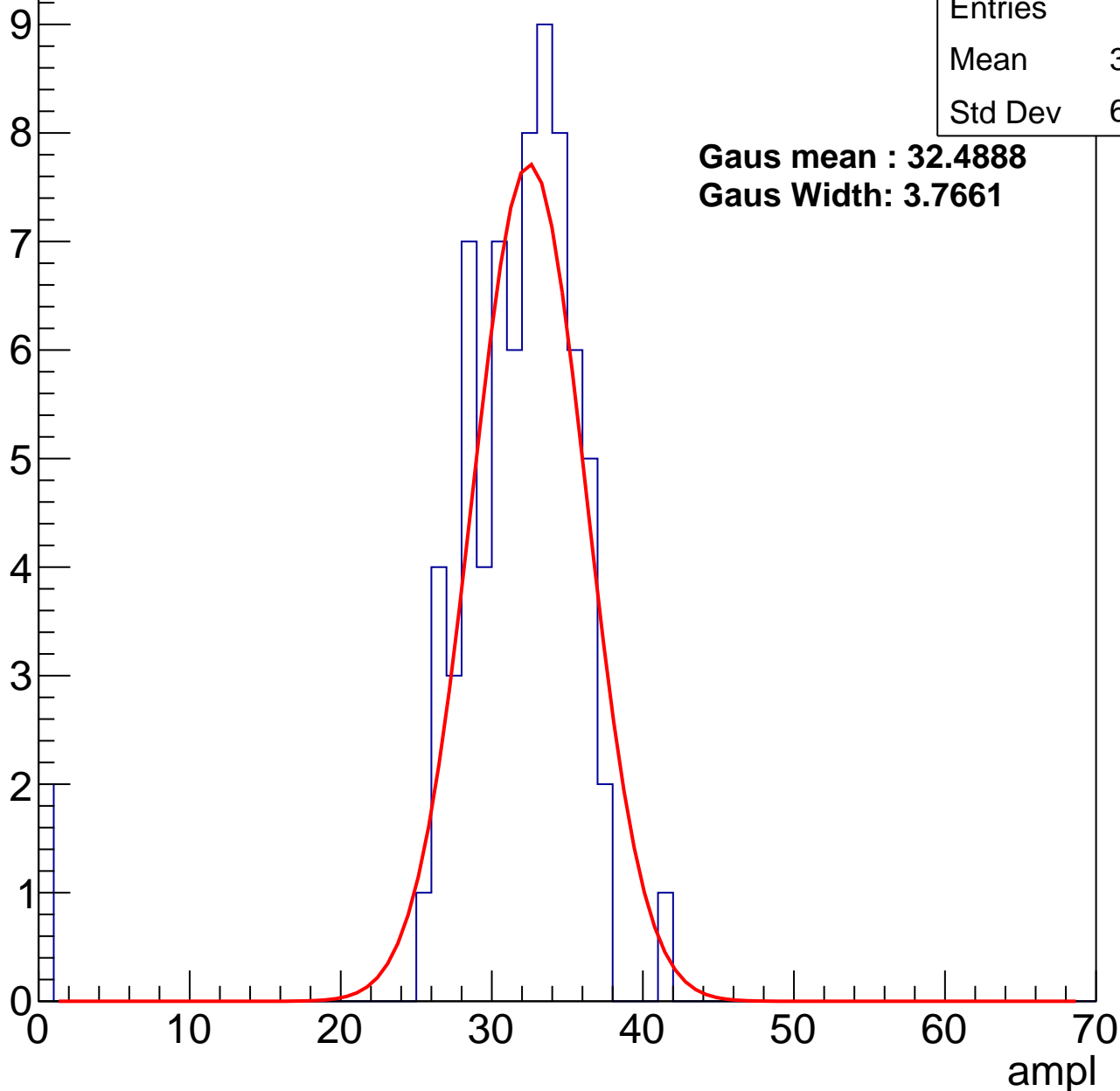
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.79
Std Dev	6.079

Gaus mean : 32.4888

Gaus Width: 3.7661



B0L001S, U22-ch41, adc1

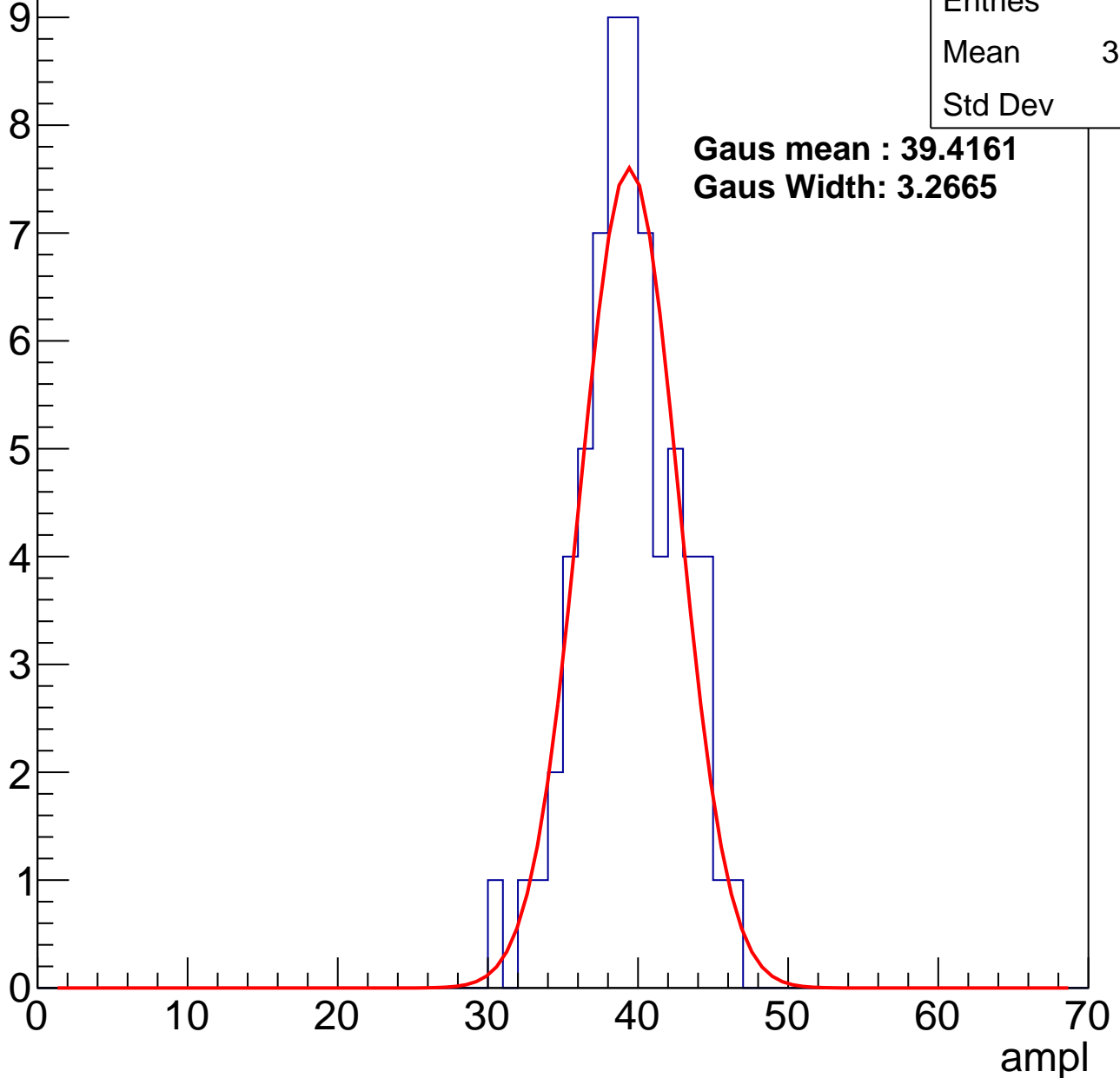
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	38.89
Std Dev	3.23

Gaus mean : 39.4161

Gaus Width: 3.2665



B0L001S, U22-ch41, adc2

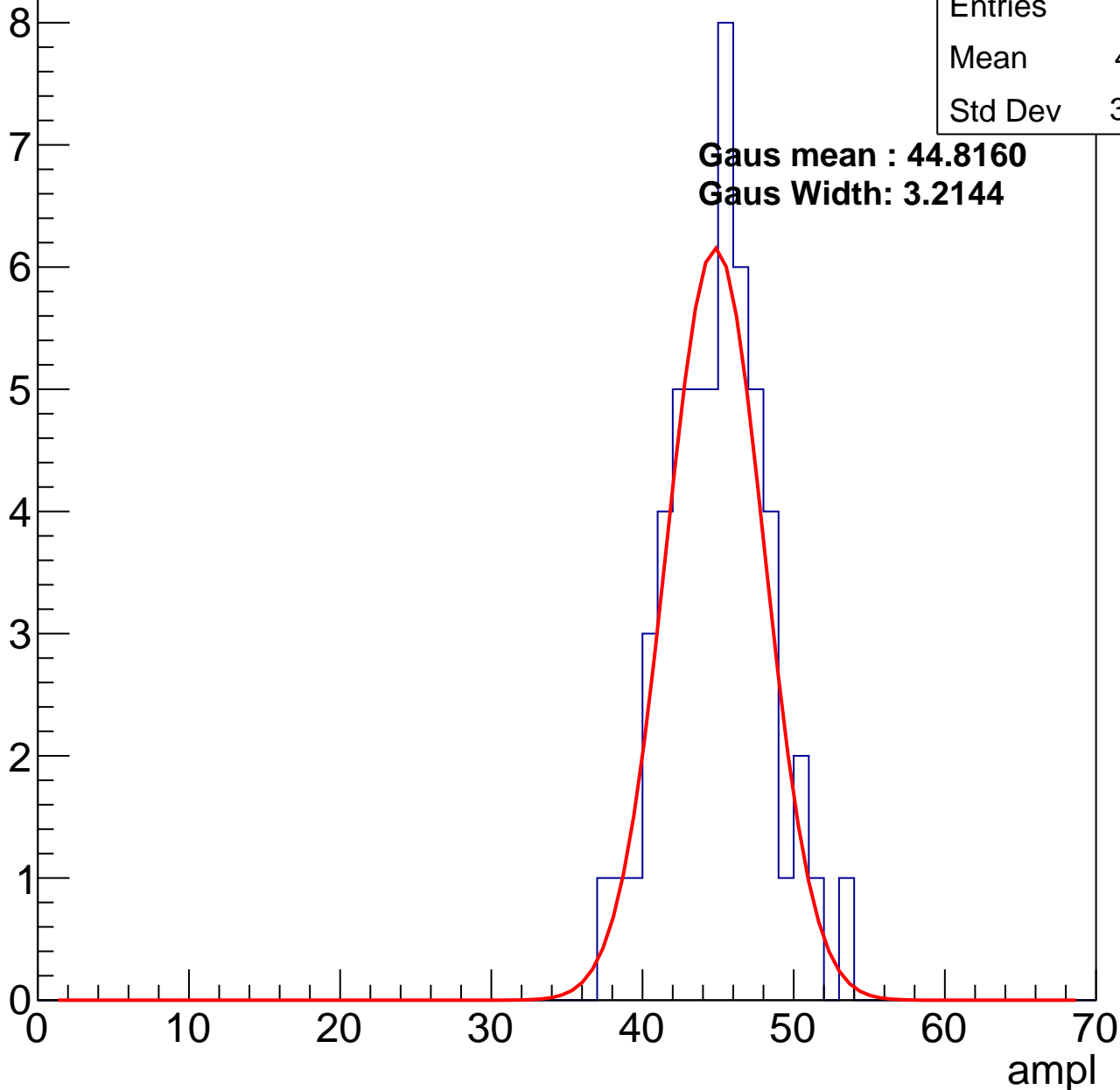
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	44.51
Std Dev	3.289

Gaus mean : 44.8160

Gaus Width: 3.2144

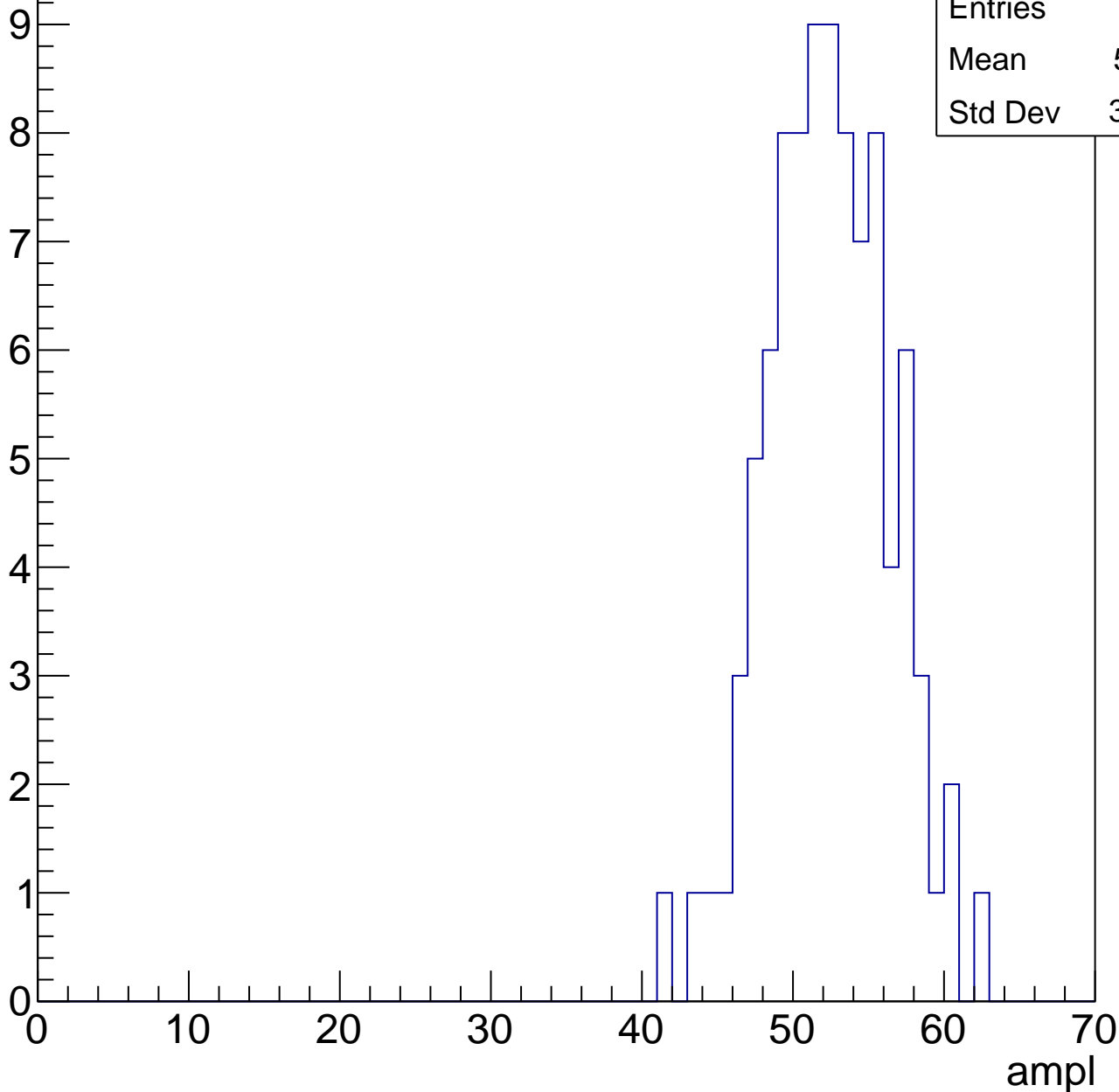


B0L001S, U22-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	92
Mean	51.91
Std Dev	3.988

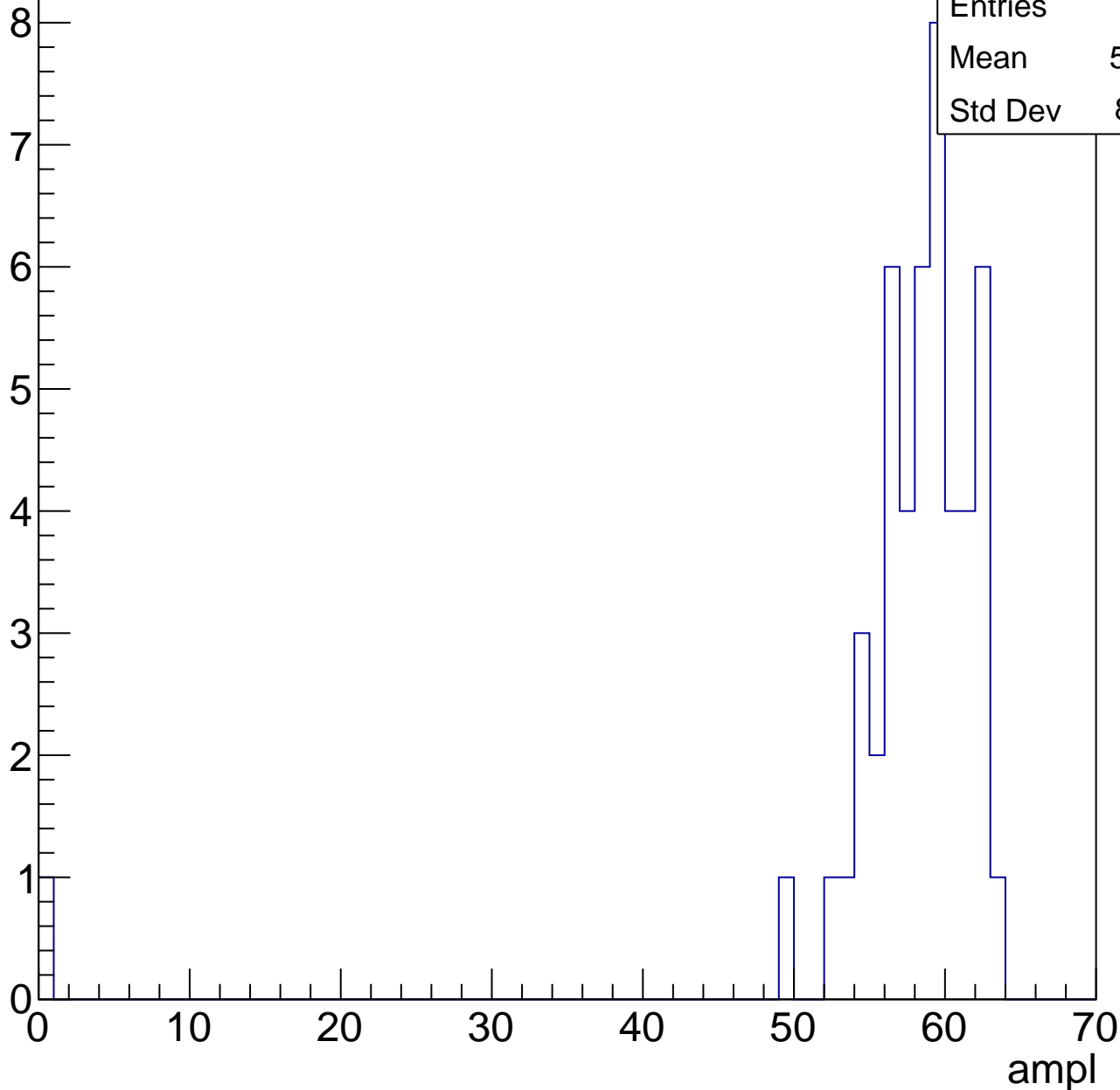


B0L001S, U22-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.85
Std Dev	8.801

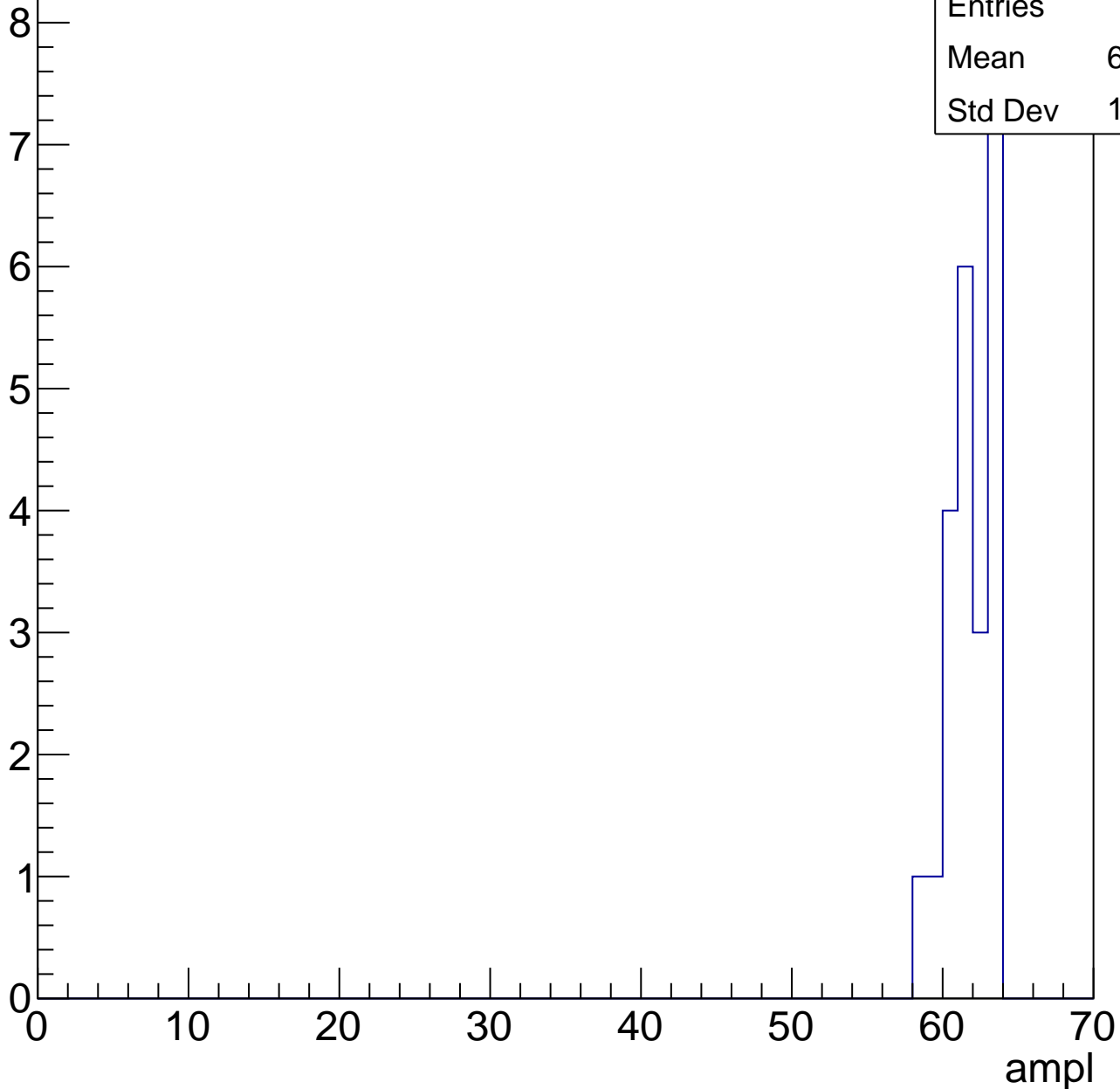


B0L001S, U22-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	61.43
Std Dev	1.439



B0L001S, U22-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	63
Std Dev	0

B0L001S, U22-ch41, adc7

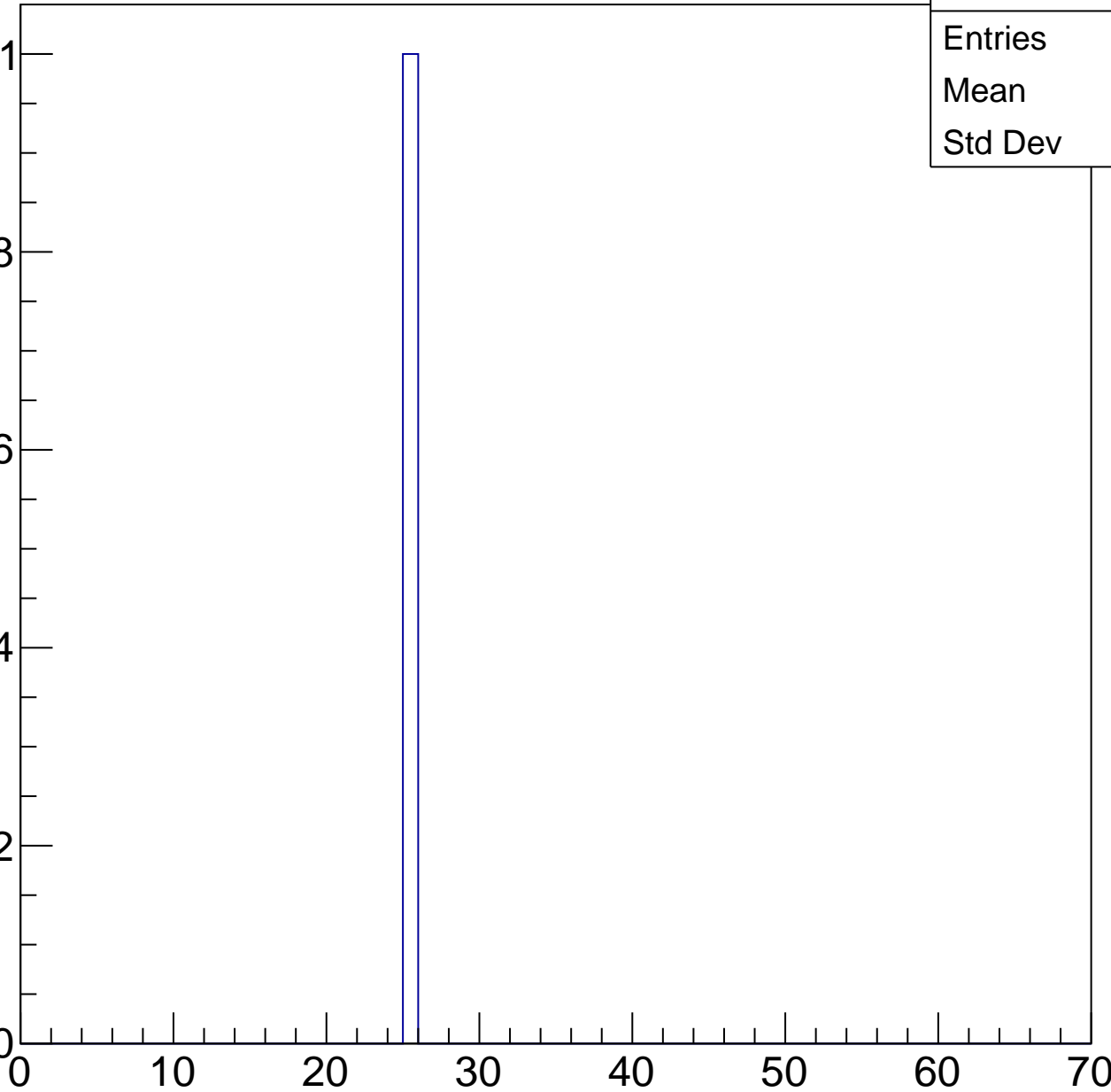
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B0L001S, U22-ch42, adc0

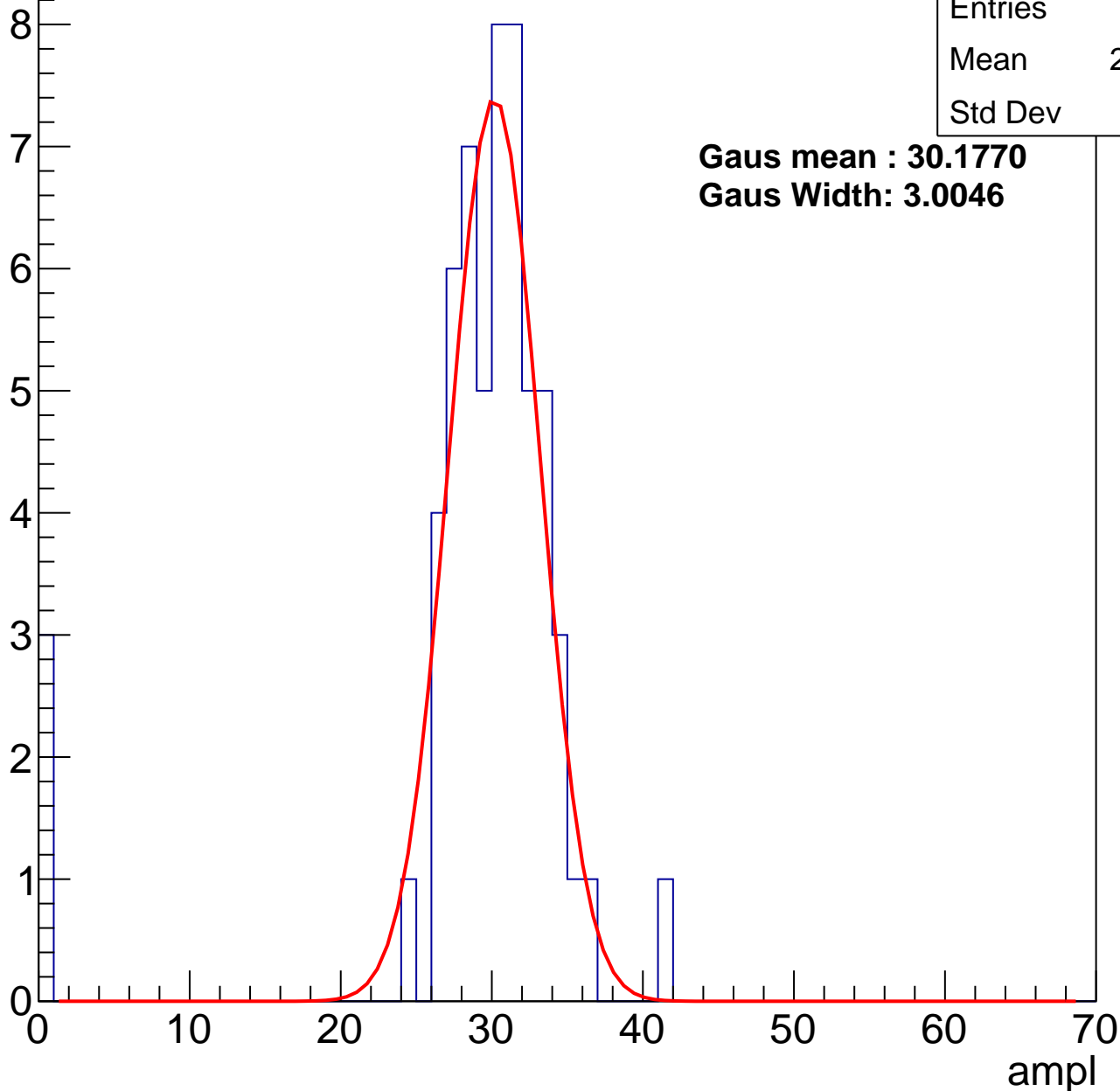
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	28.59
Std Dev	7.28

Gaus mean : 30.1770

Gaus Width: 3.0046



B0L001S, U22-ch42, adc1

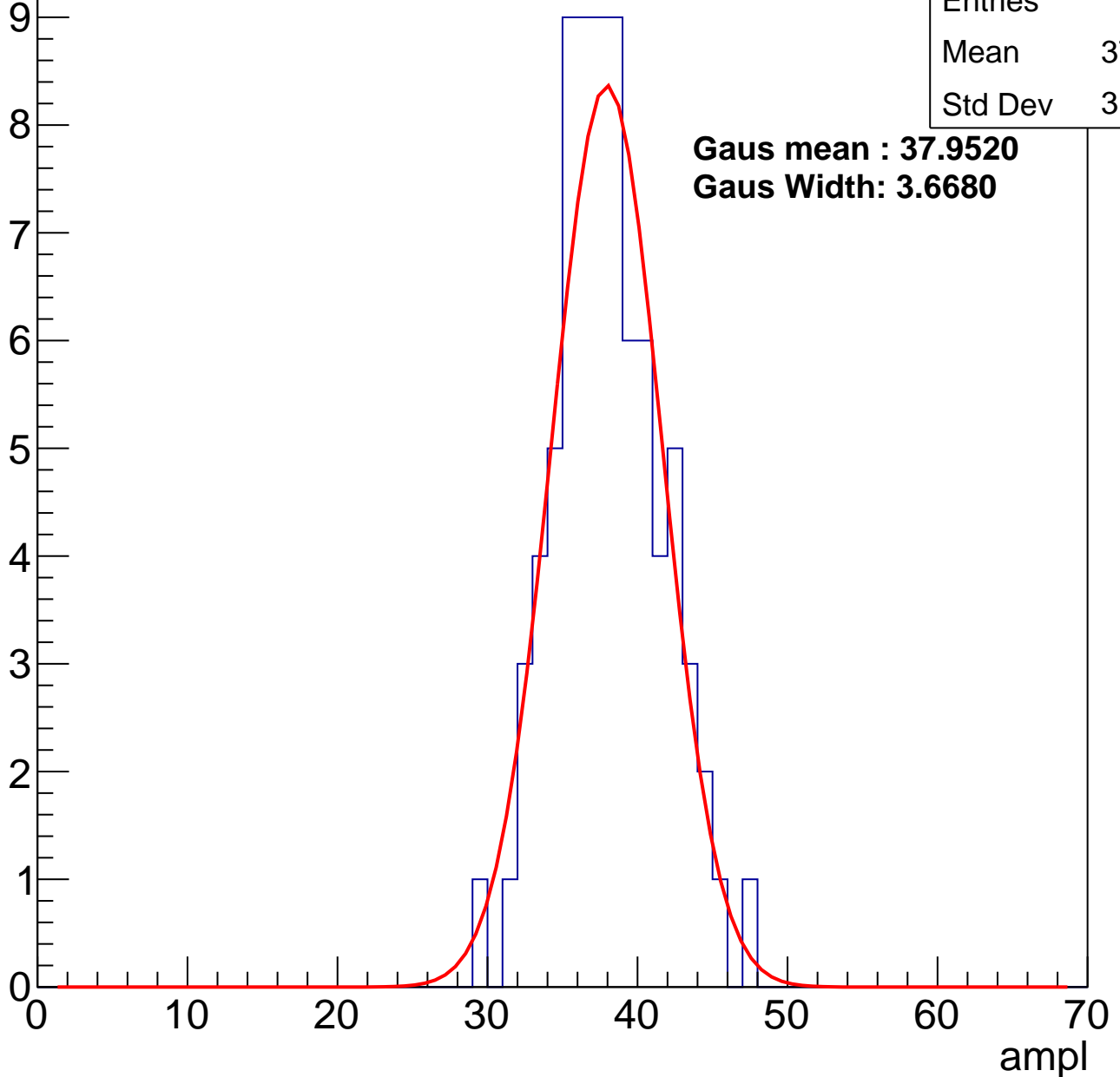
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.55
Std Dev	3.492

Gaus mean : 37.9520

Gaus Width: 3.6680



B0L001S, U22-ch42, adc2

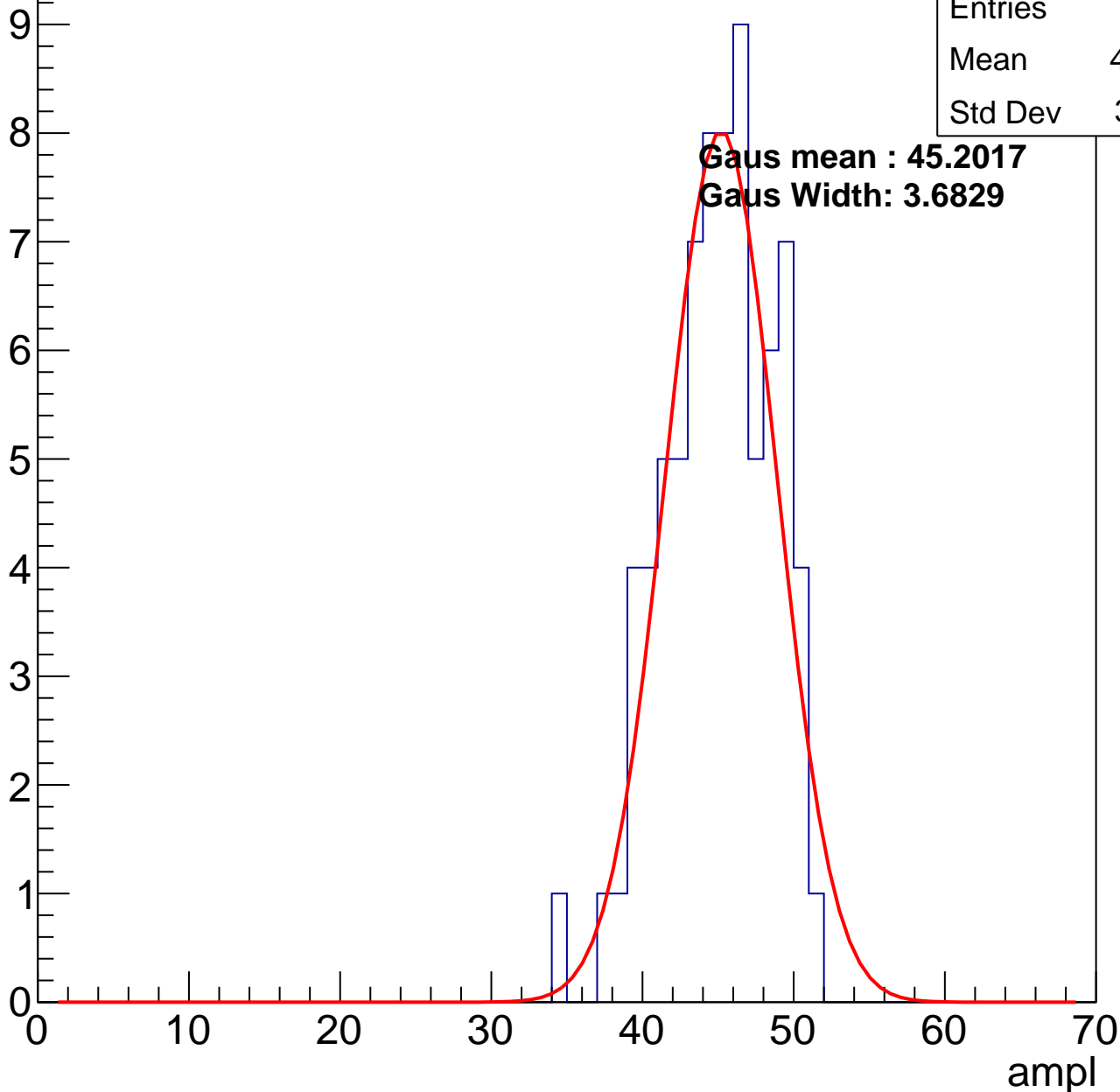
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	44.53
Std Dev	3.541

Gaus mean : 45.2017

Gaus Width: 3.6829

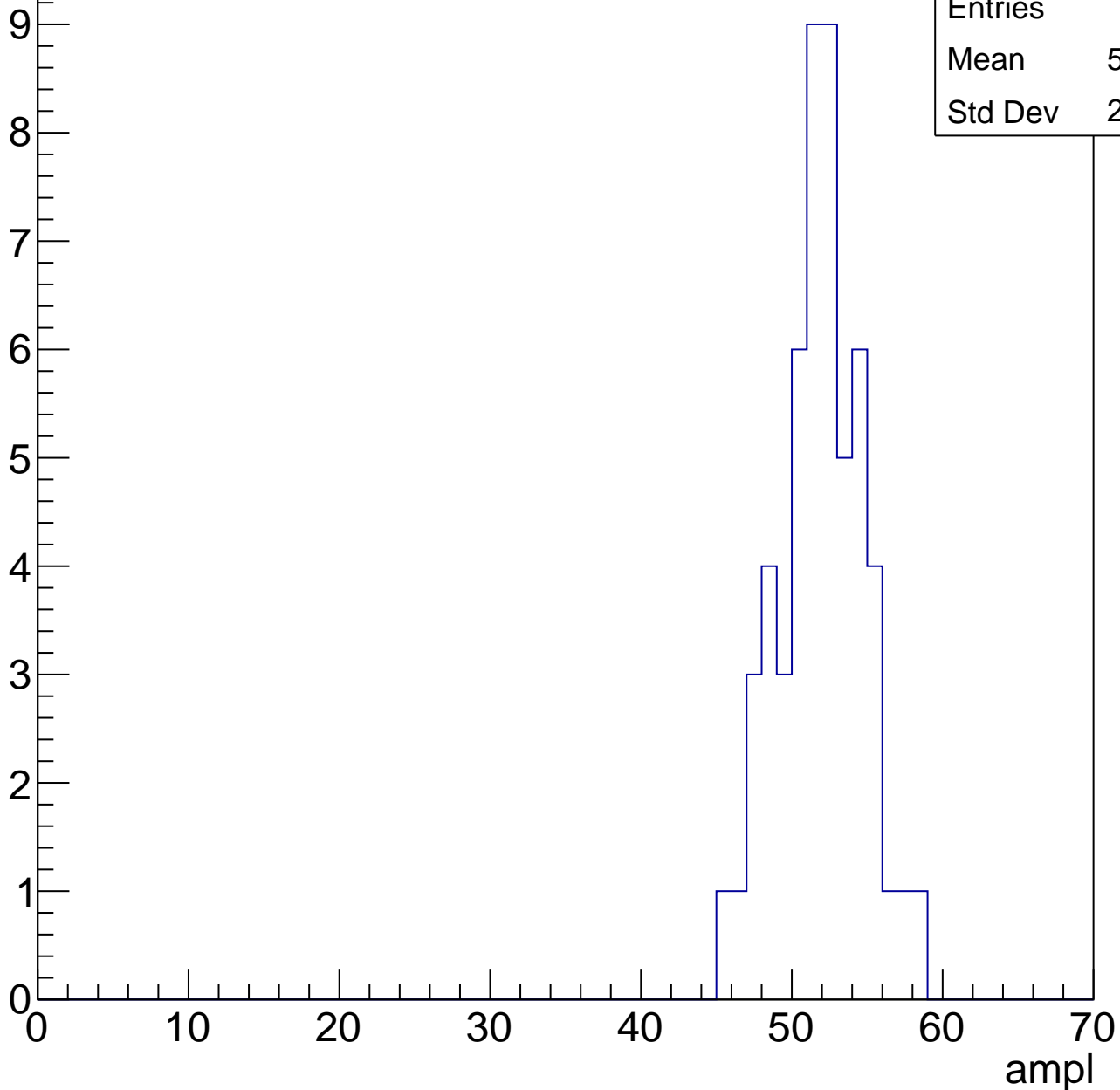


B0L001S, U22-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	51.44
Std Dev	2.753

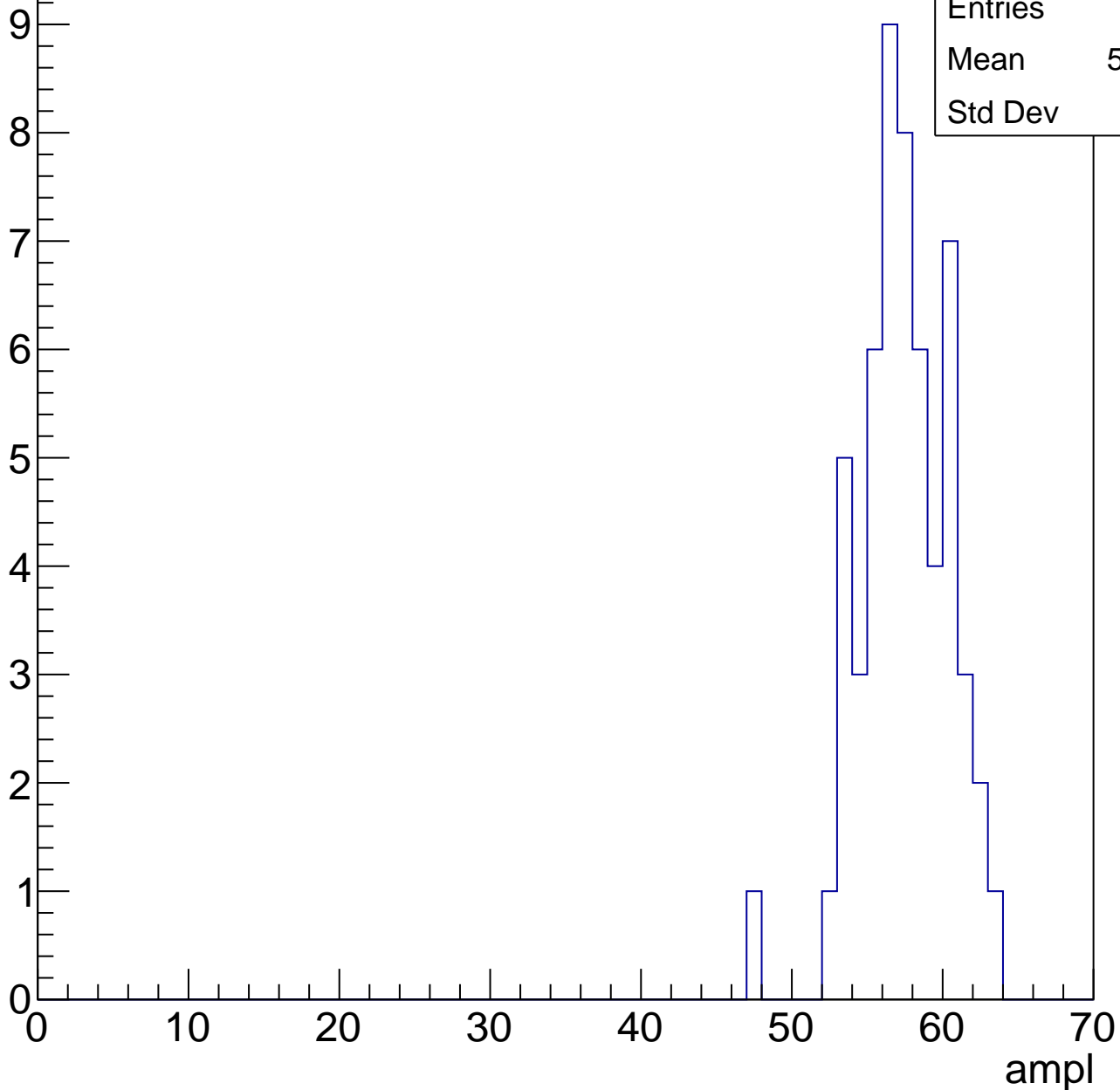


B0L001S, U22-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.96
Std Dev	2.94

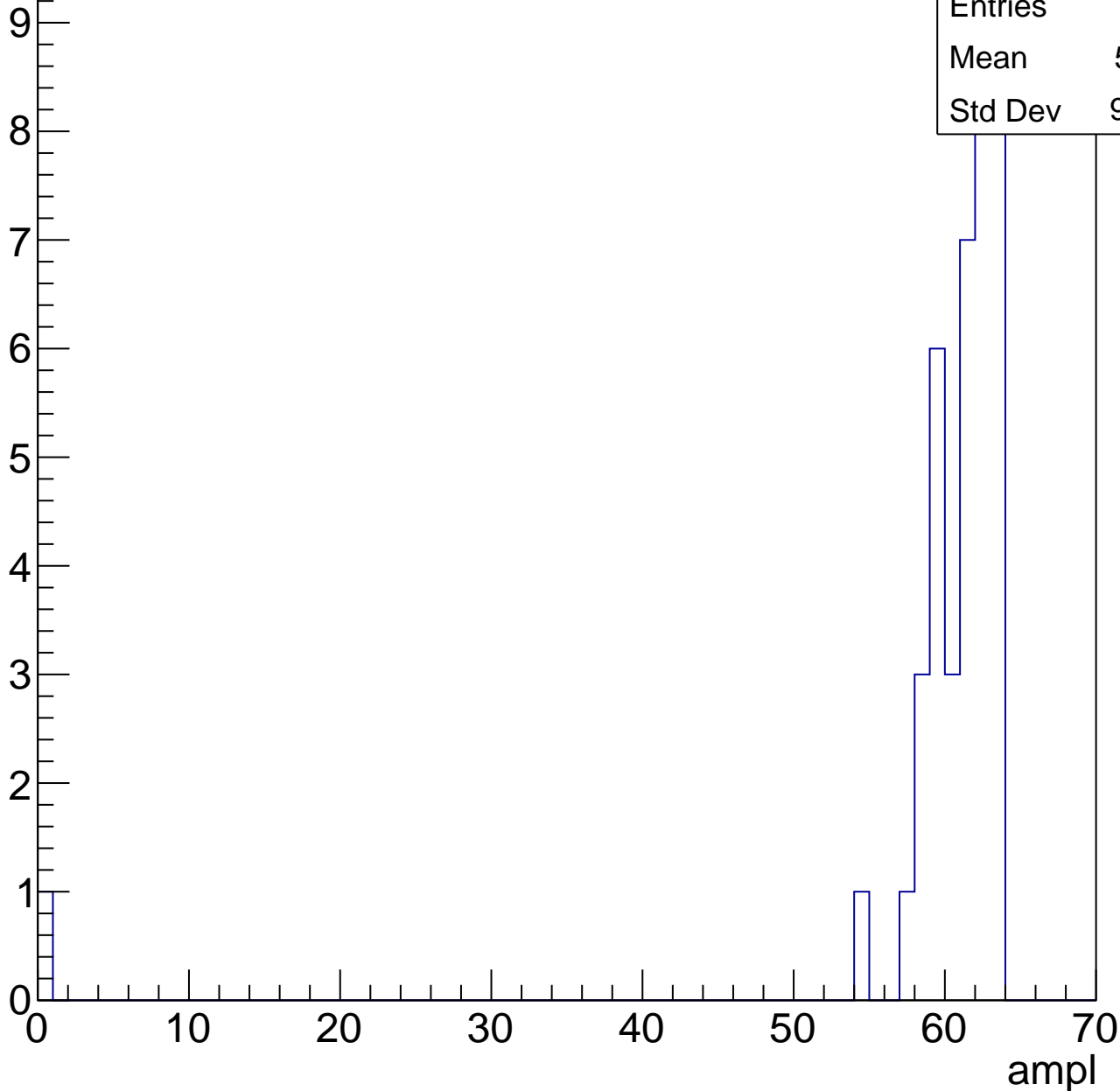


B0L001S, U22-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	59.21
Std Dev	9.817



B0L001S, U22-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch43, adc0

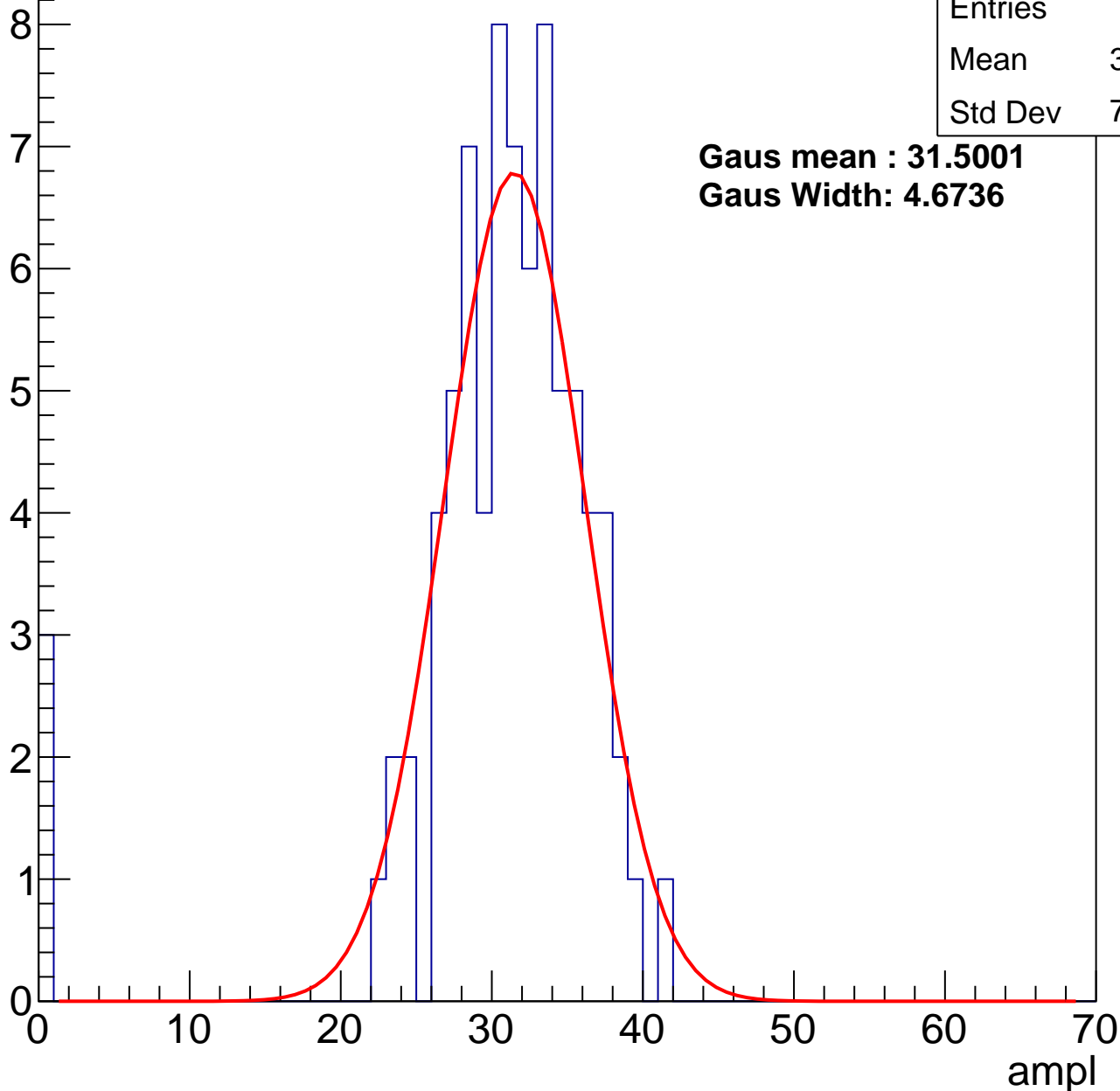
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.04
Std Dev	7.163

Gaus mean : 31.5001

Gaus Width: 4.6736



B0L001S, U22-ch43, adc1

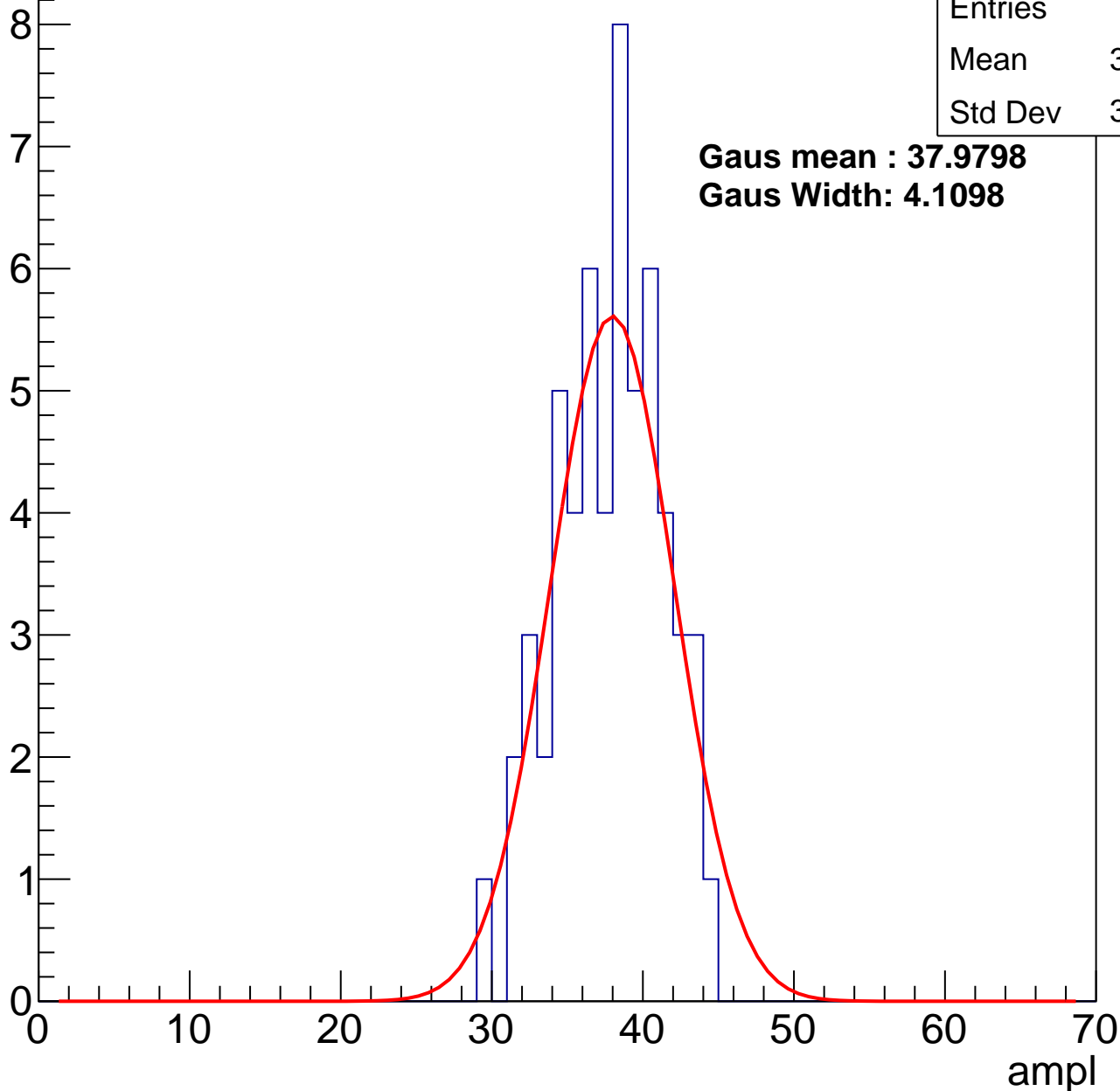
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	37.35
Std Dev	3.456

Gaus mean : 37.9798

Gaus Width: 4.1098



B0L001S, U22-ch43, adc2

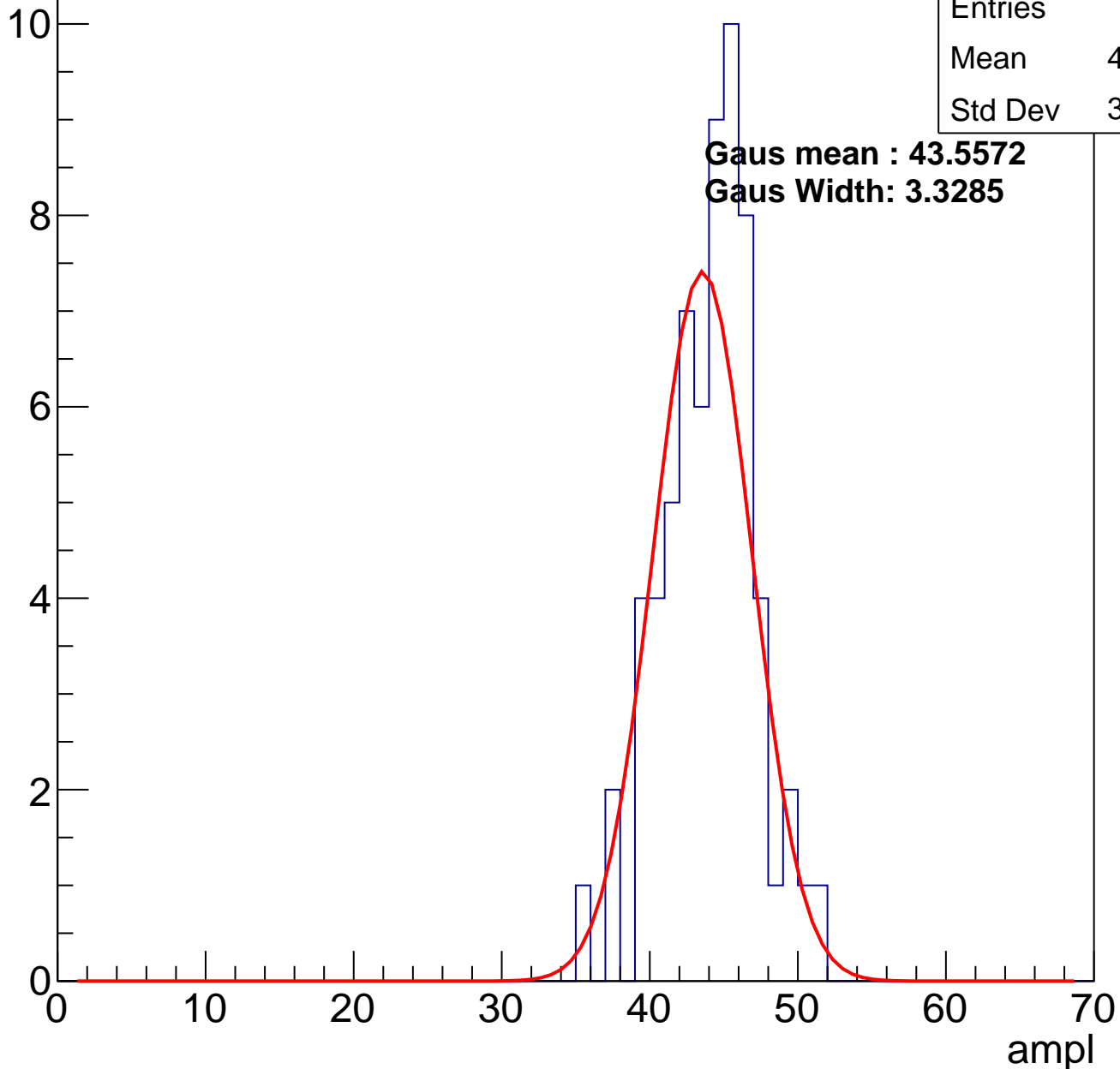
calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	43.55
Std Dev	3.133

Gaus mean : 43.5572

Gaus Width: 3.3285

Entry

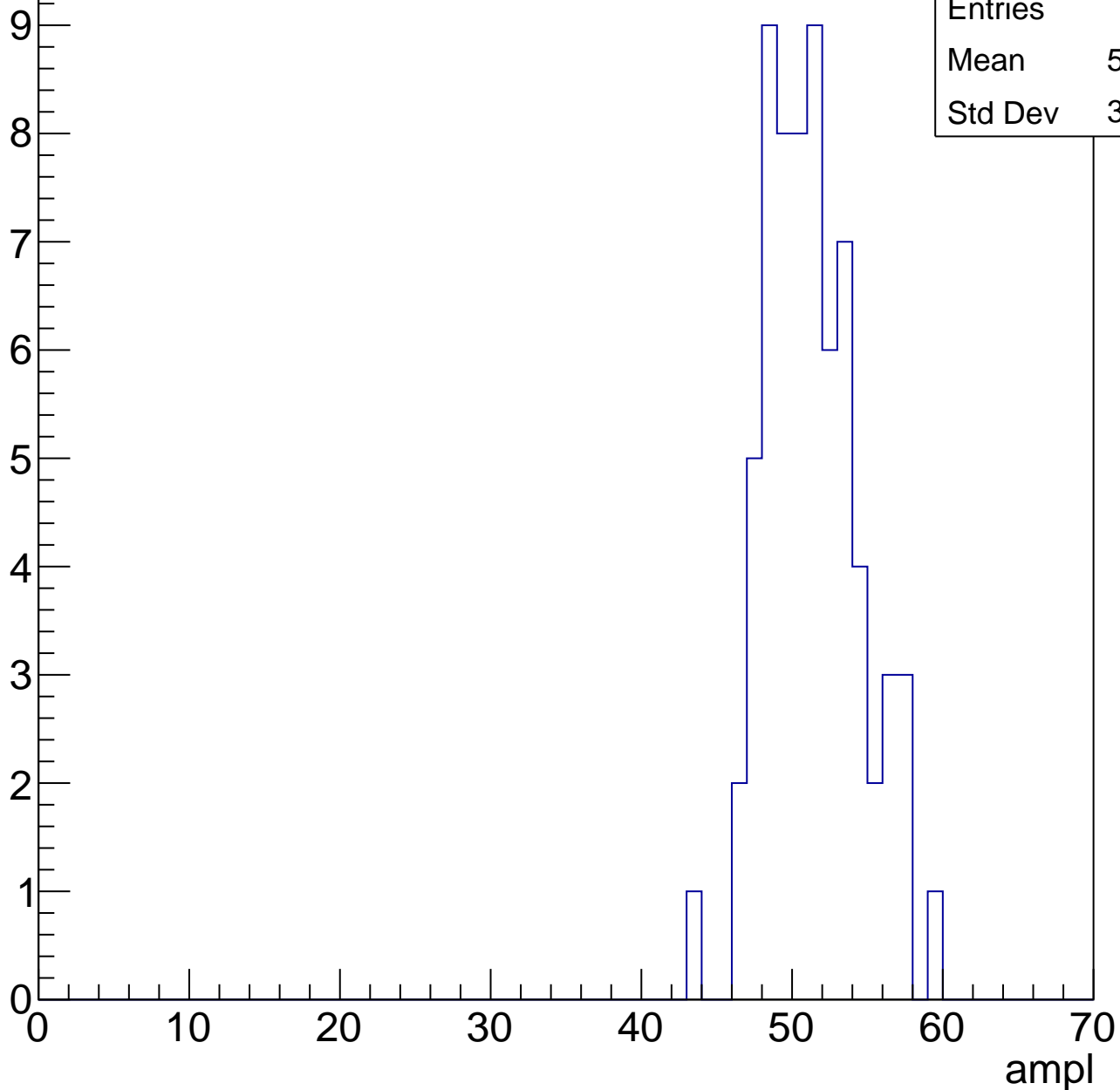


B0L001S, U22-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.88
Std Dev	3.123

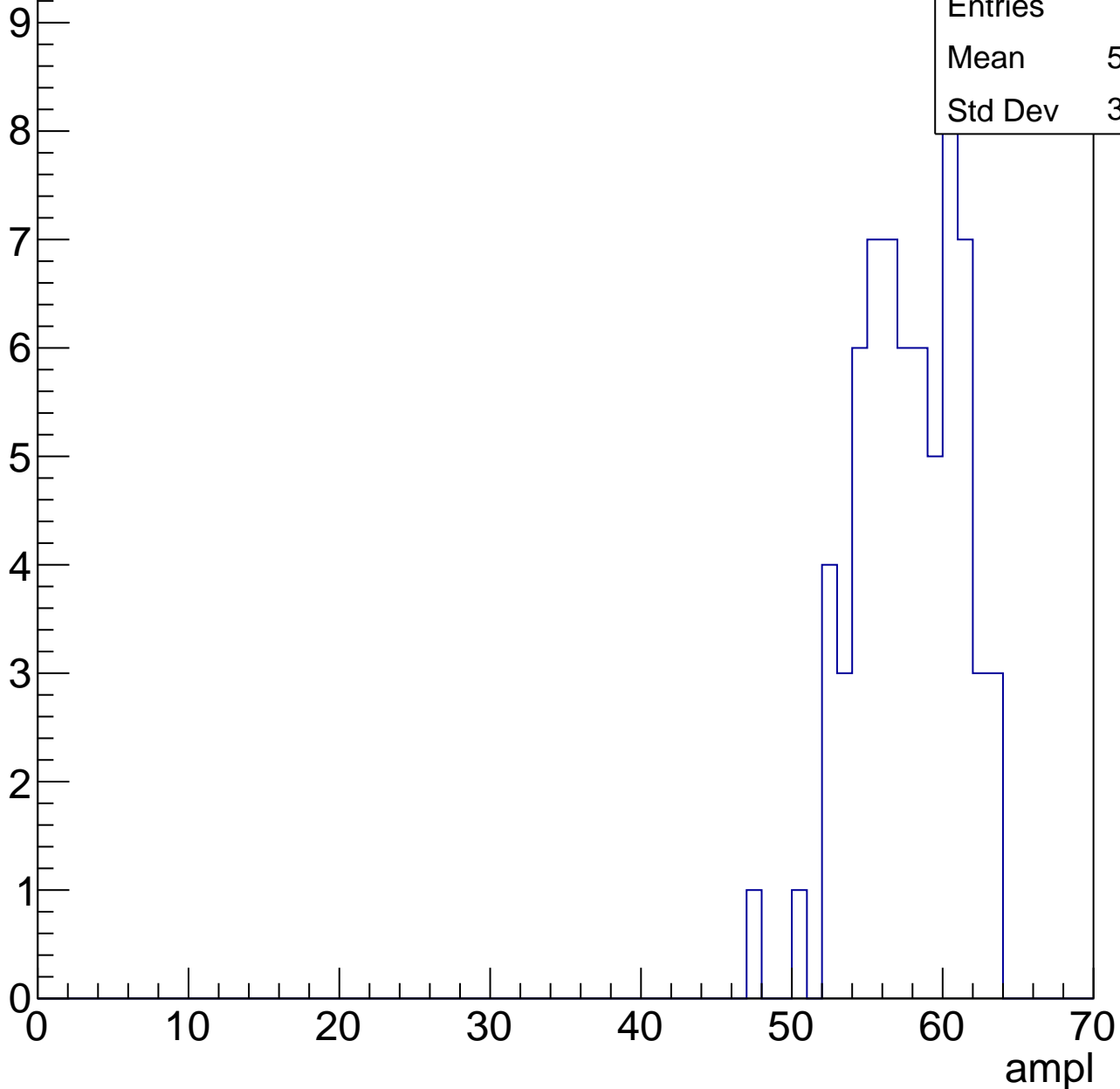


B0L001S, U22-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	57.24
Std Dev	3.396

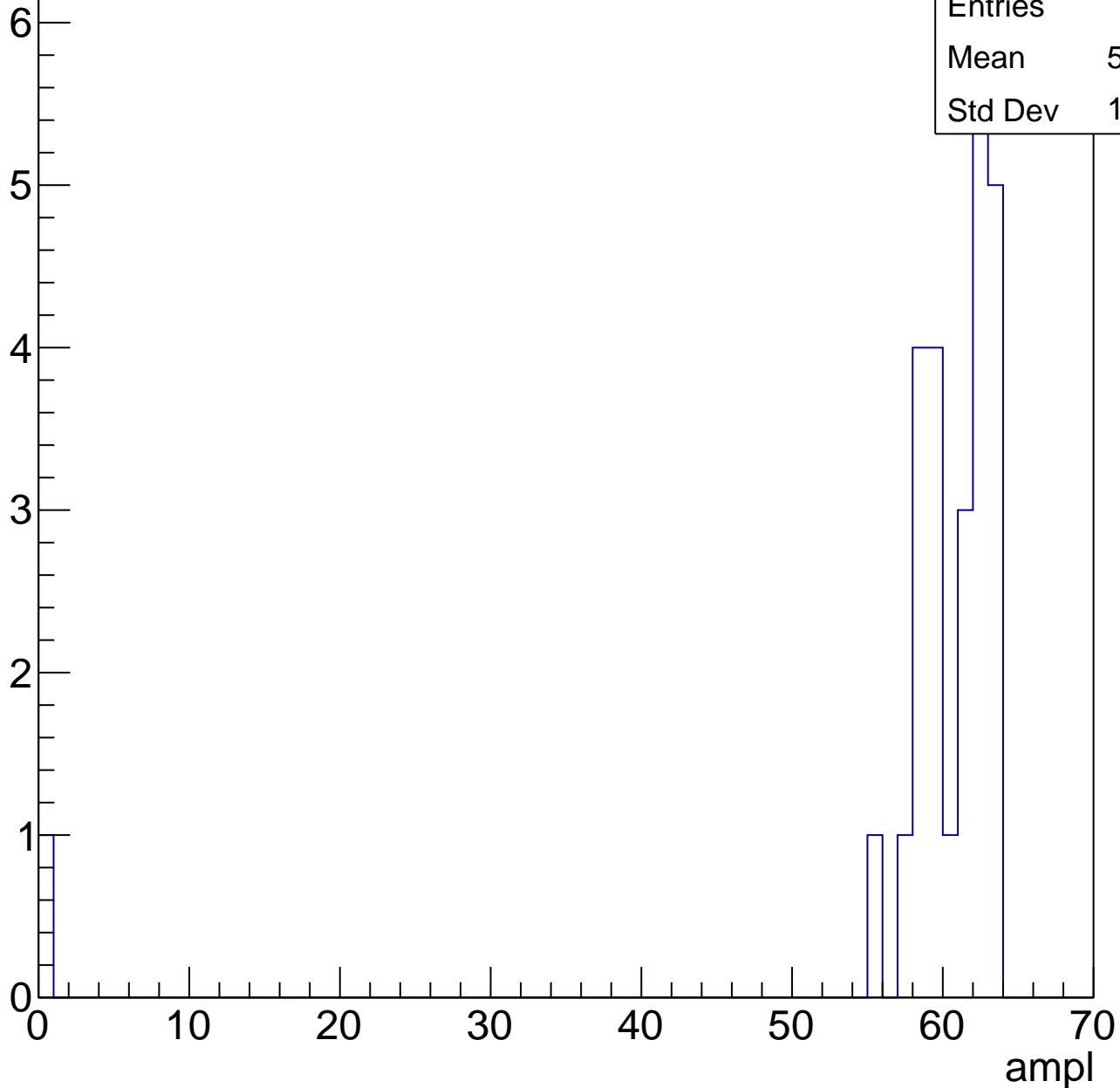


B0L001S, U22-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.08
Std Dev	11.82



B0L001S, U22-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

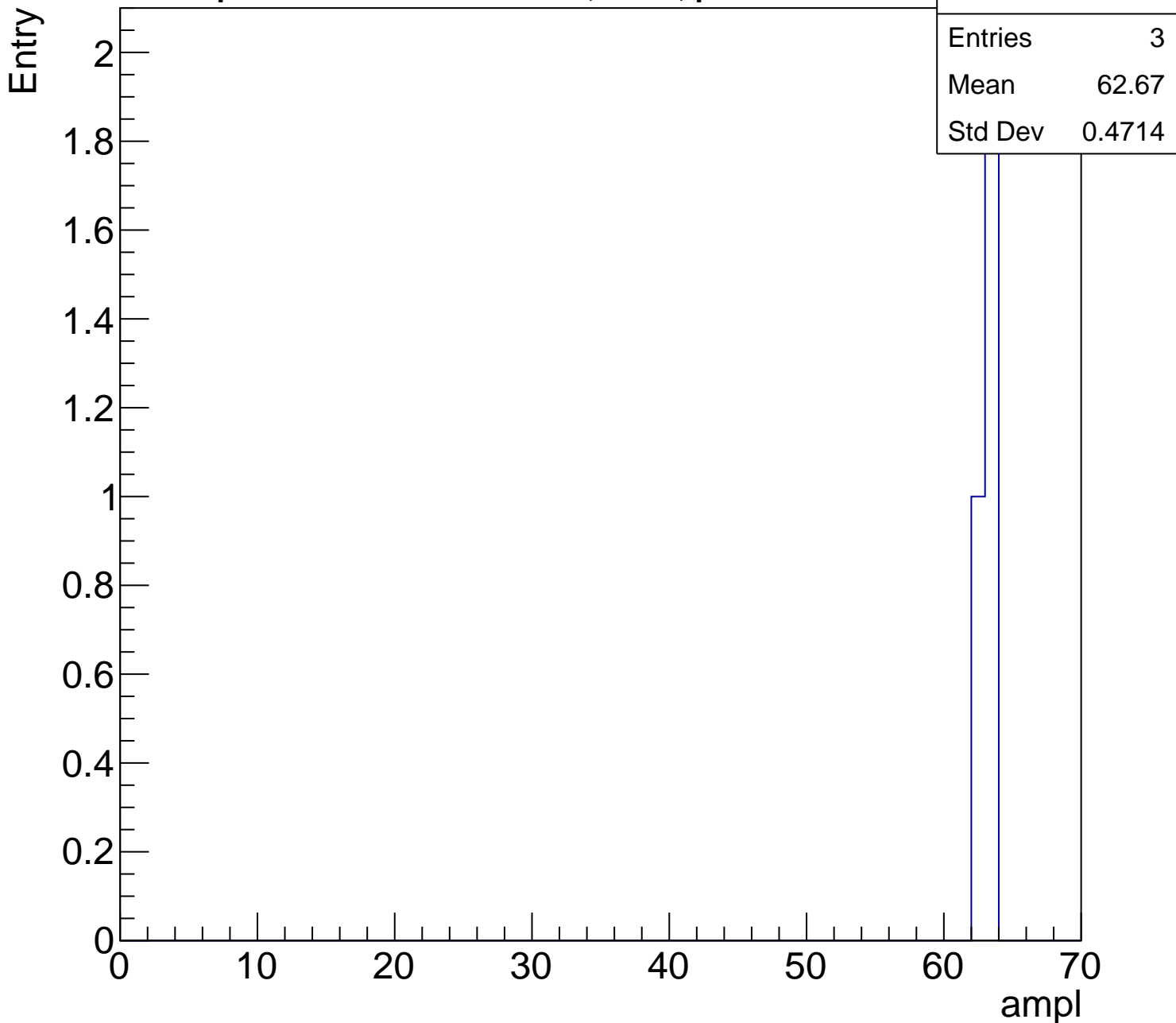
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch44, adc0

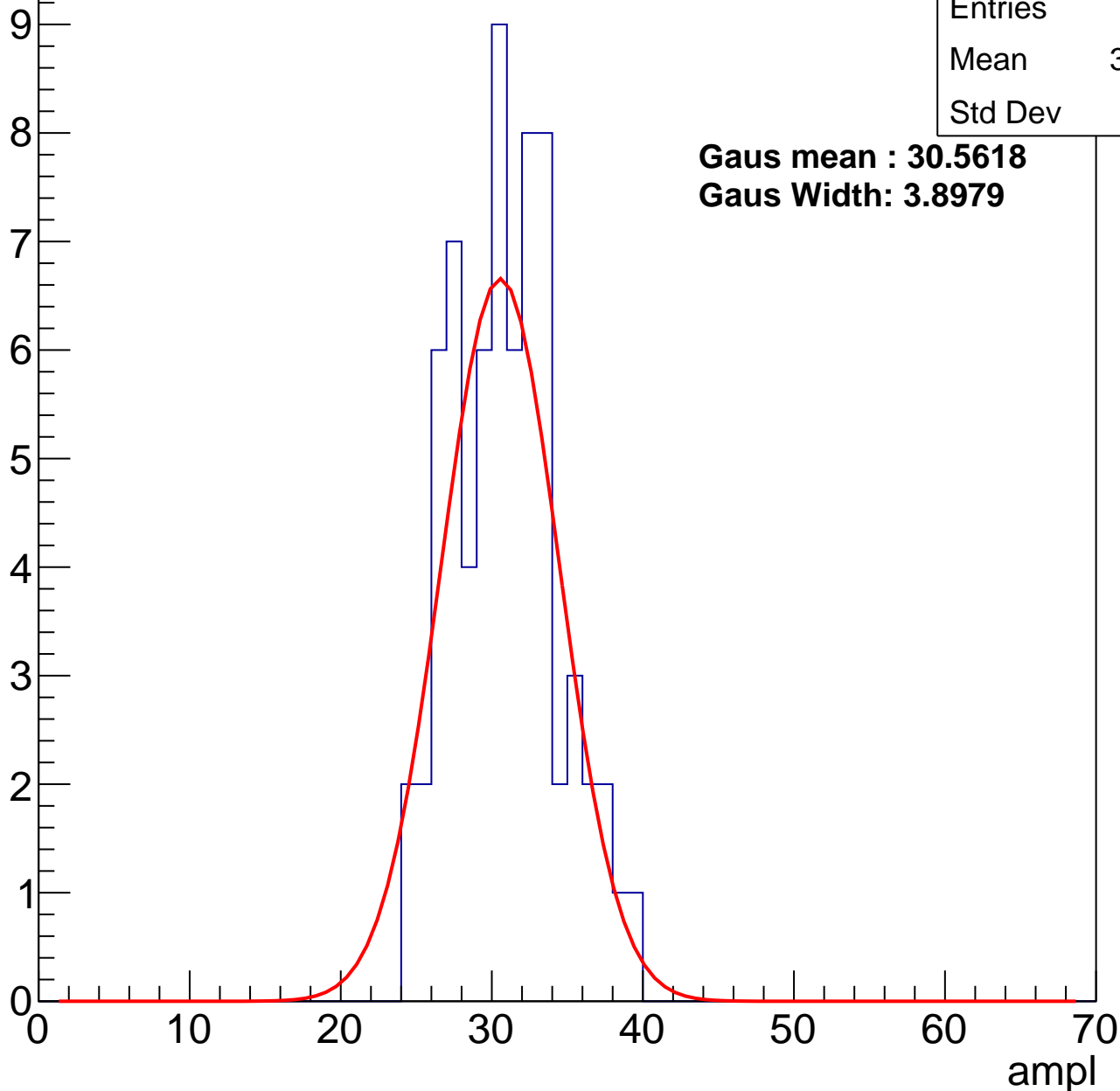
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.45
Std Dev	3.45

Gaus mean : 30.5618

Gaus Width: 3.8979



B0L001S, U22-ch44, adc1

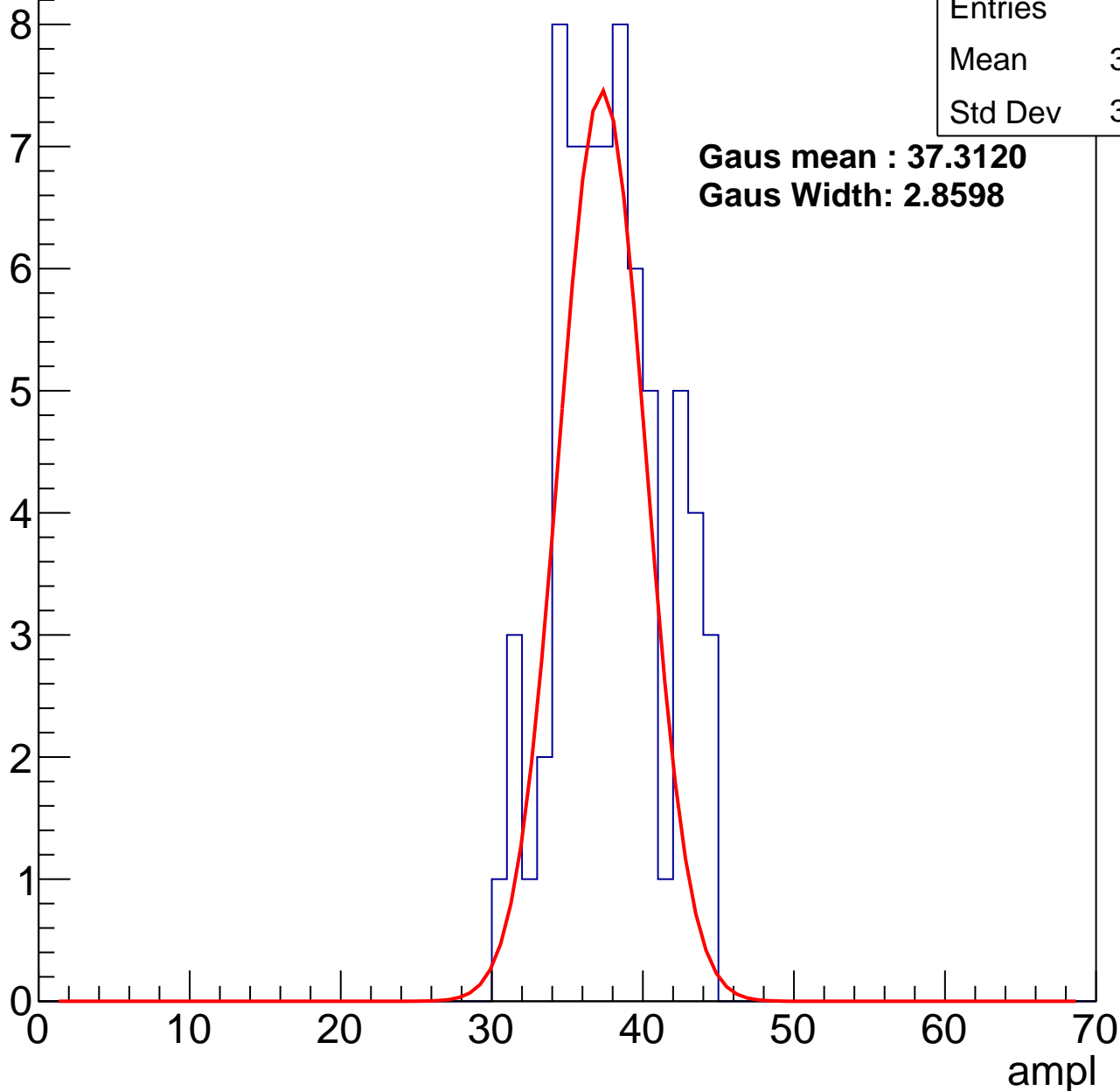
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.38
Std Dev	3.473

Gaus mean : 37.3120

Gaus Width: 2.8598



B0L001S, U22-ch44, adc2

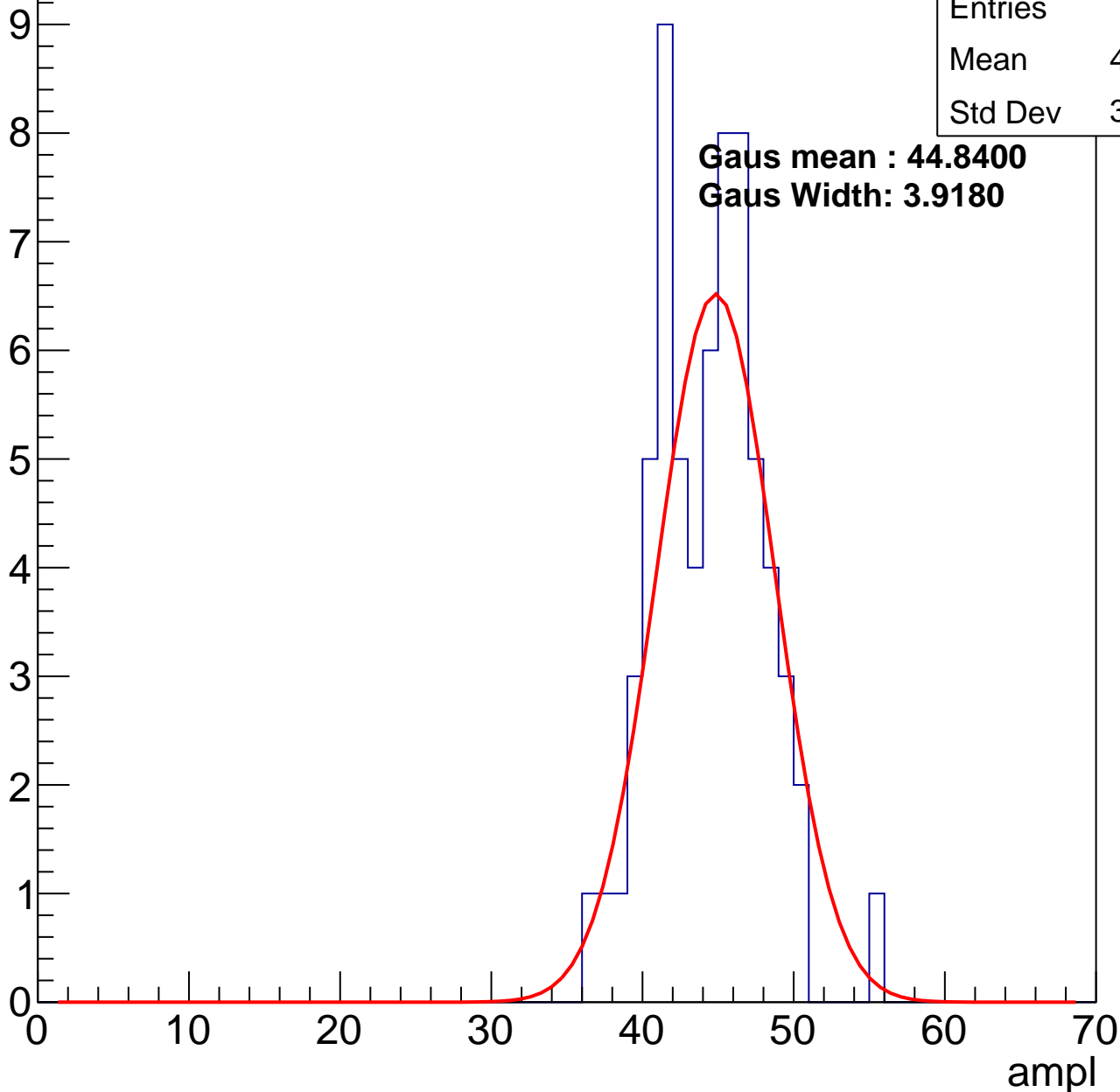
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.94
Std Dev	3.537

Gaus mean : 44.8400

Gaus Width: 3.9180

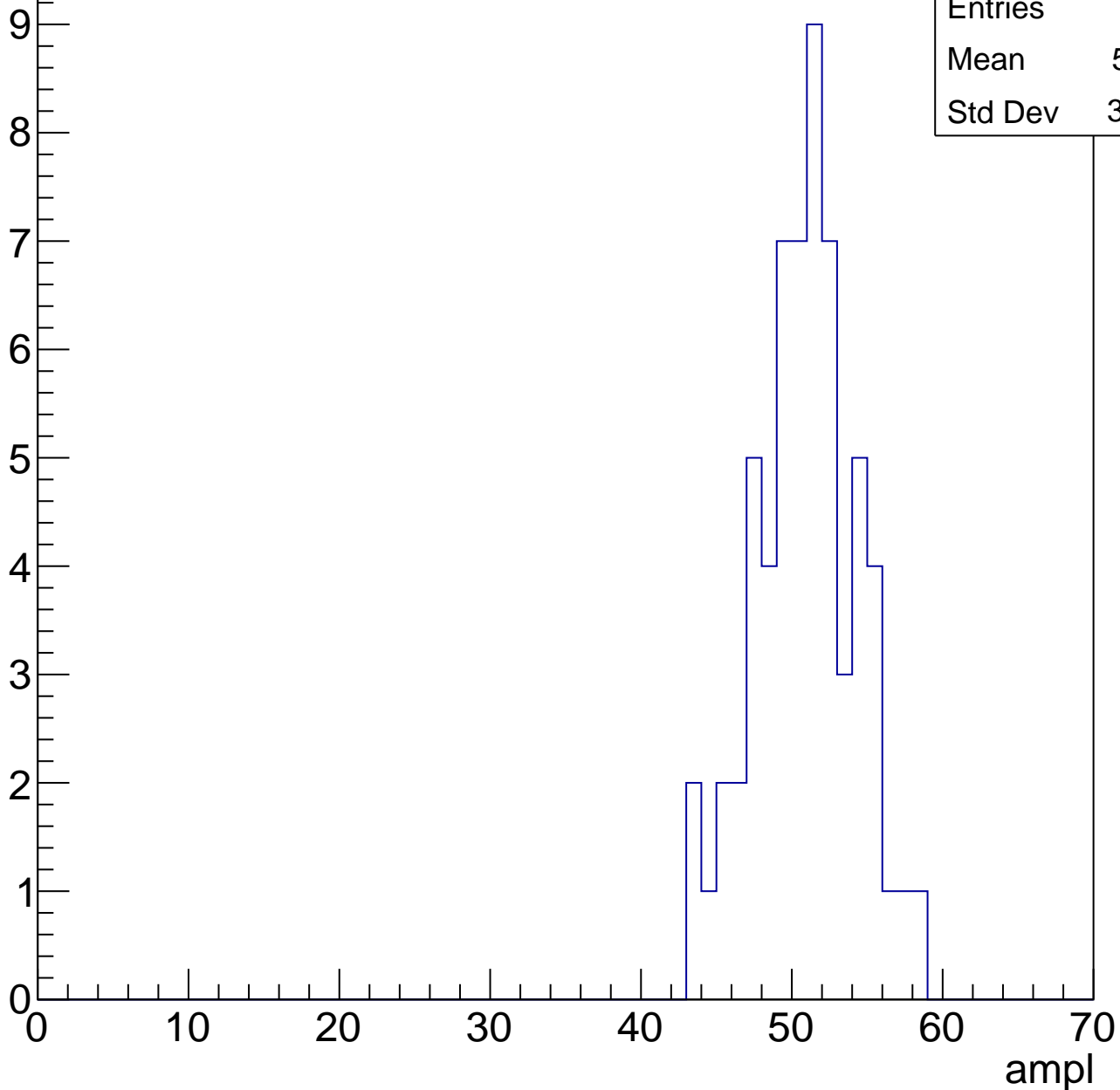


B0L001S, U22-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.41
Std Dev	3.306

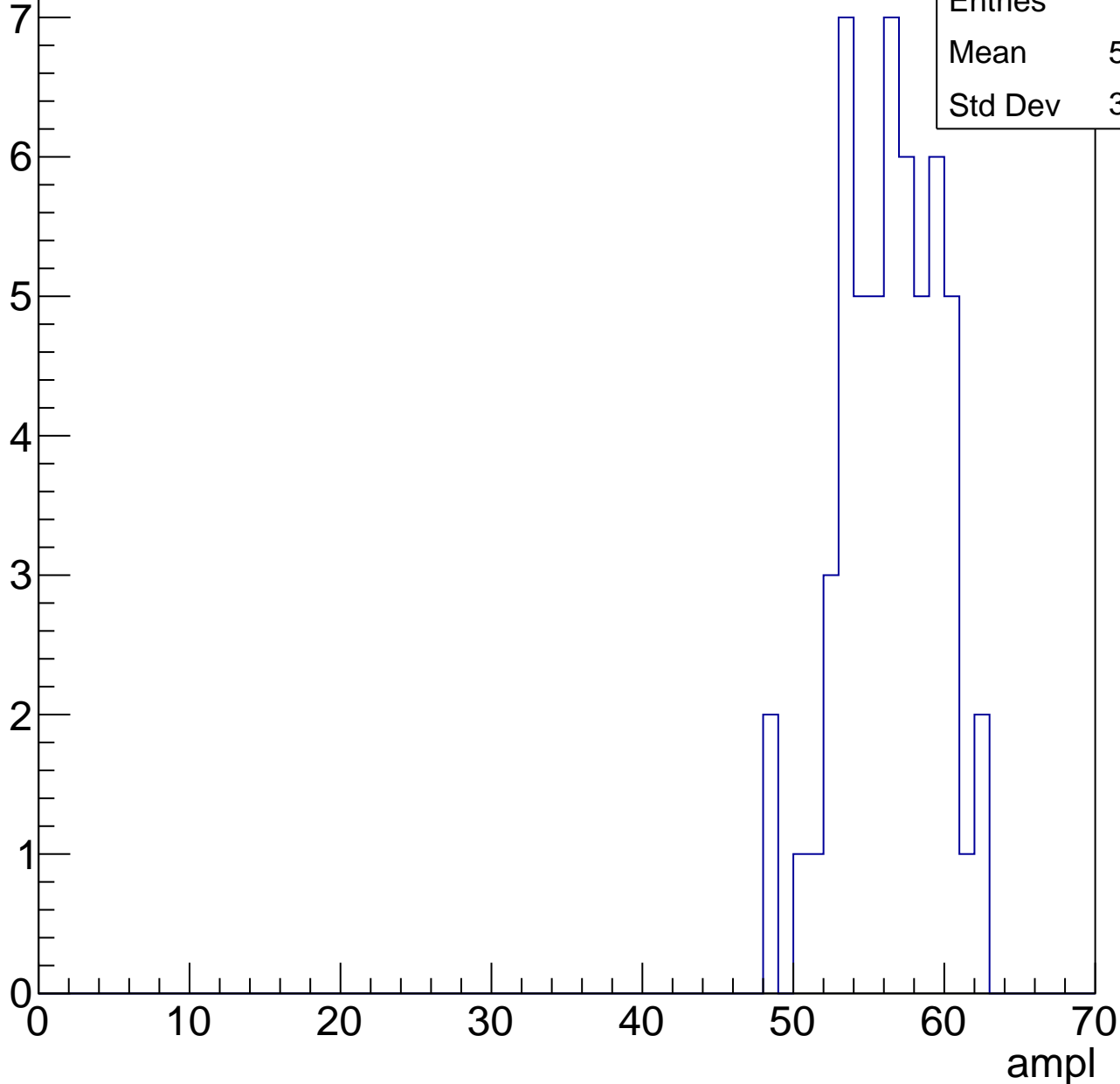


B0L001S, U22-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	55.93
Std Dev	3.223

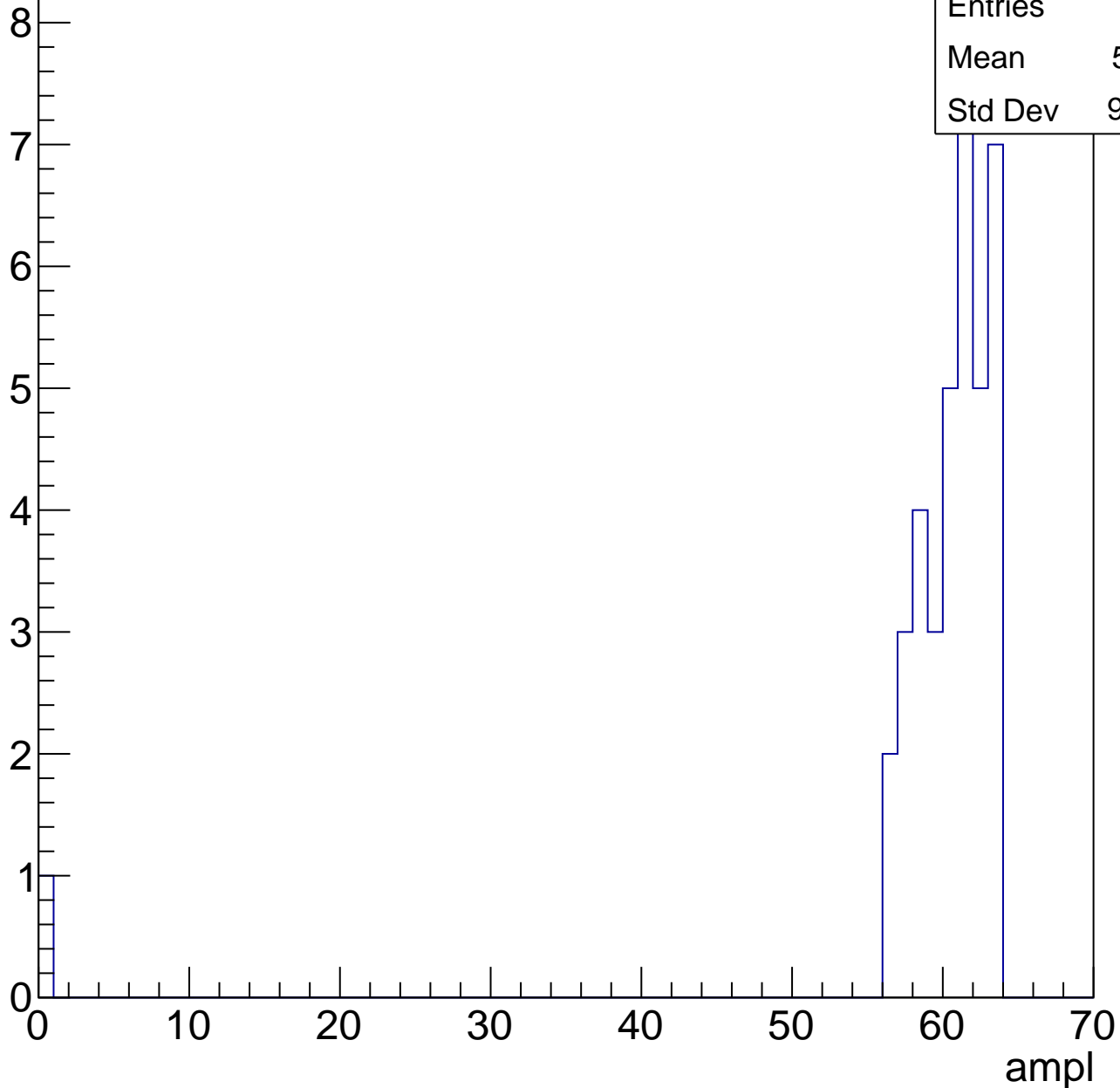


B0L001S, U22-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

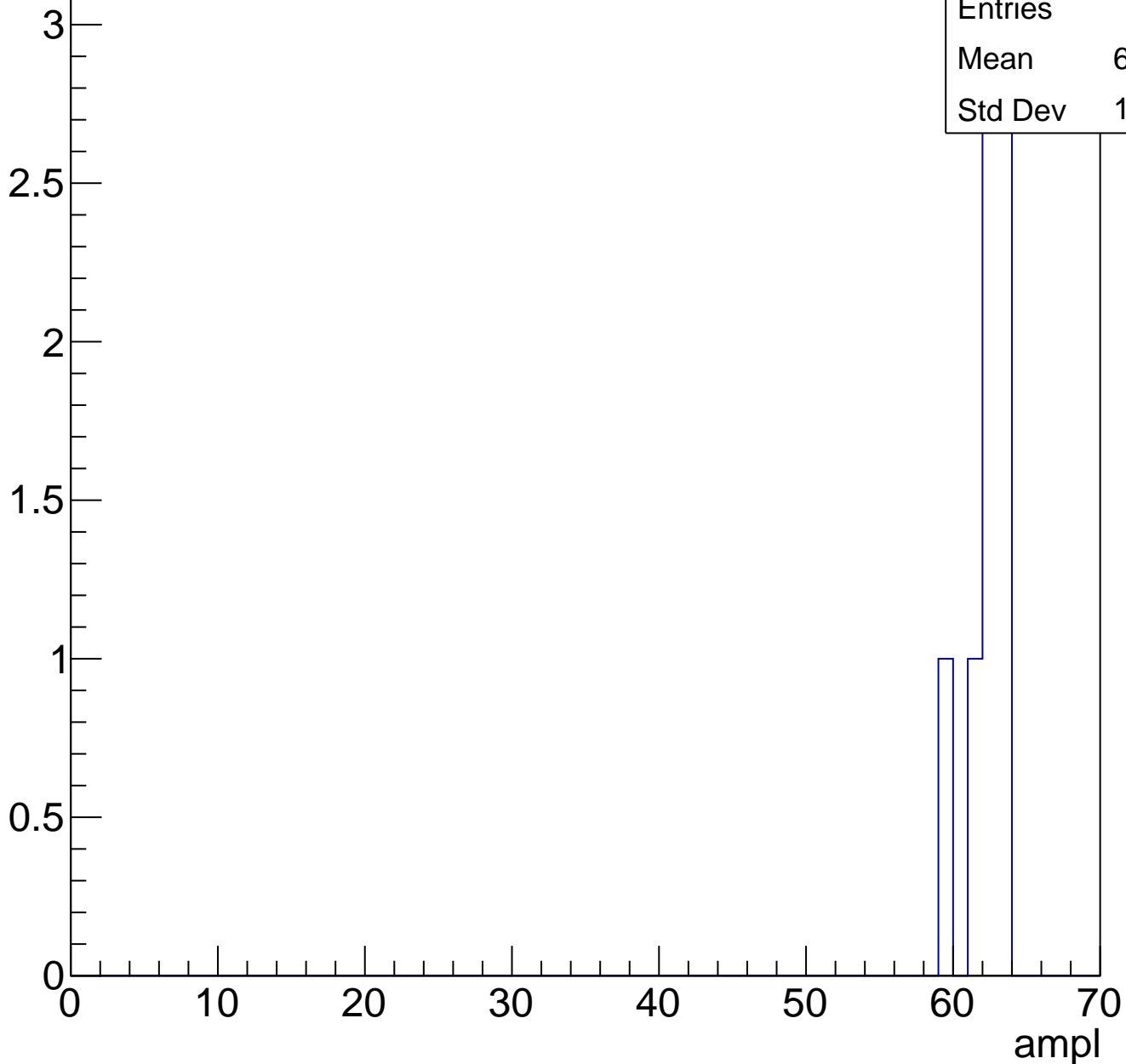
Entries	38
Mean	58.71
Std Dev	9.875



B0L001S, U22-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch45, adc0

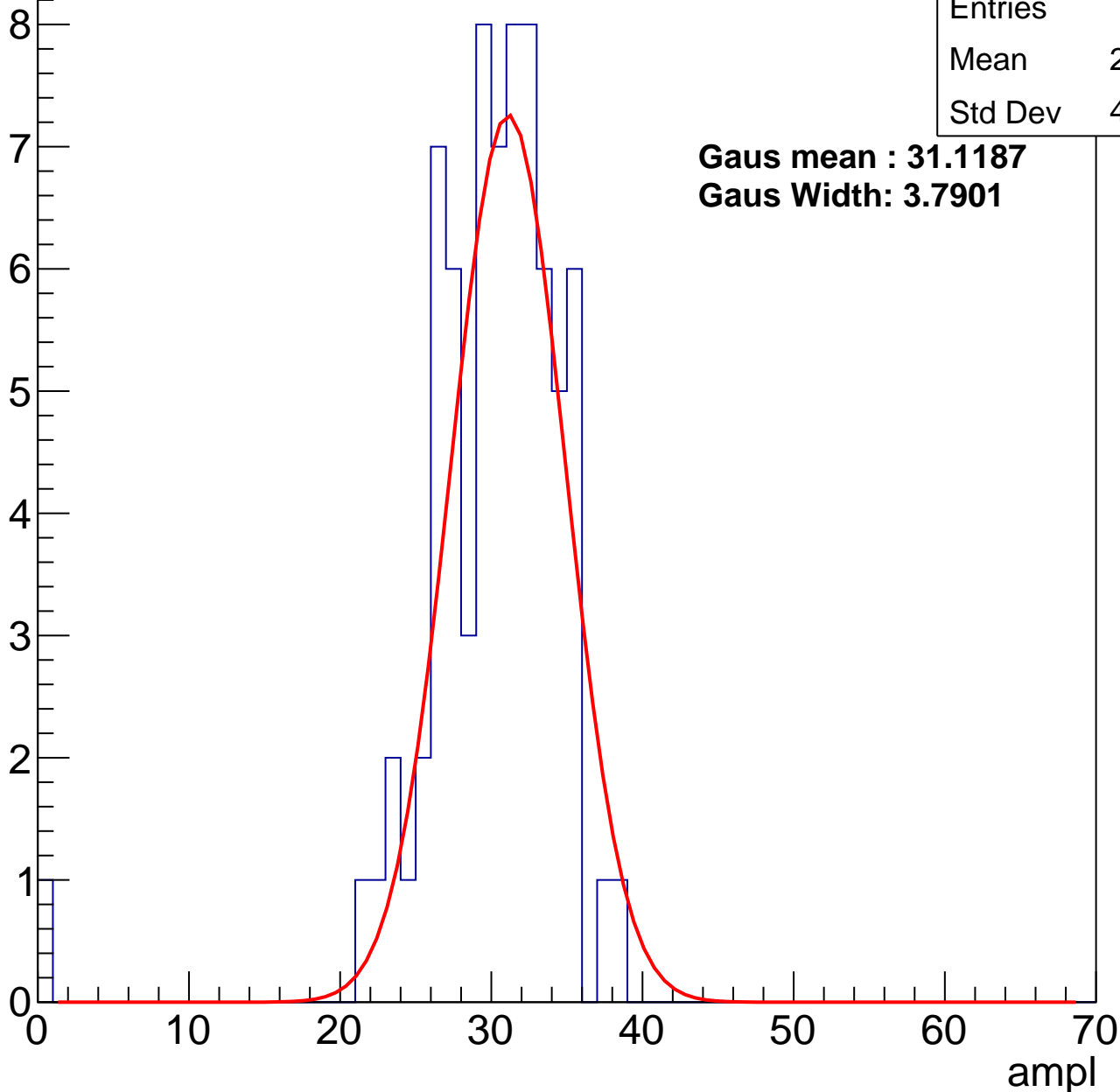
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	29.59
Std Dev	4.986

Gaus mean : 31.1187

Gaus Width: 3.7901



B0L001S, U22-ch45, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	37.8
Std Dev	3.696

Gaus mean : 38.4791

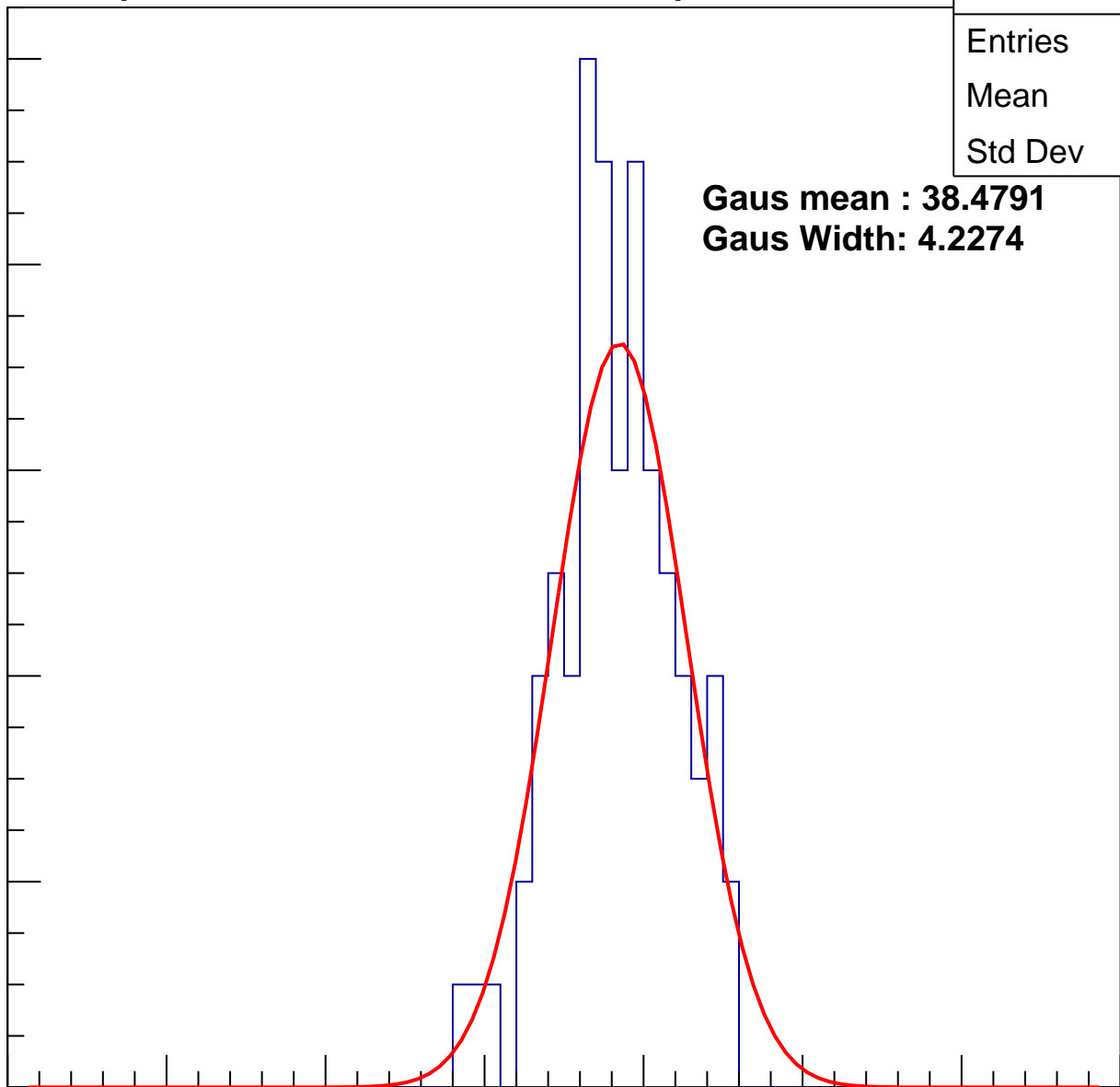
Gaus Width: 4.2274

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch45, adc2

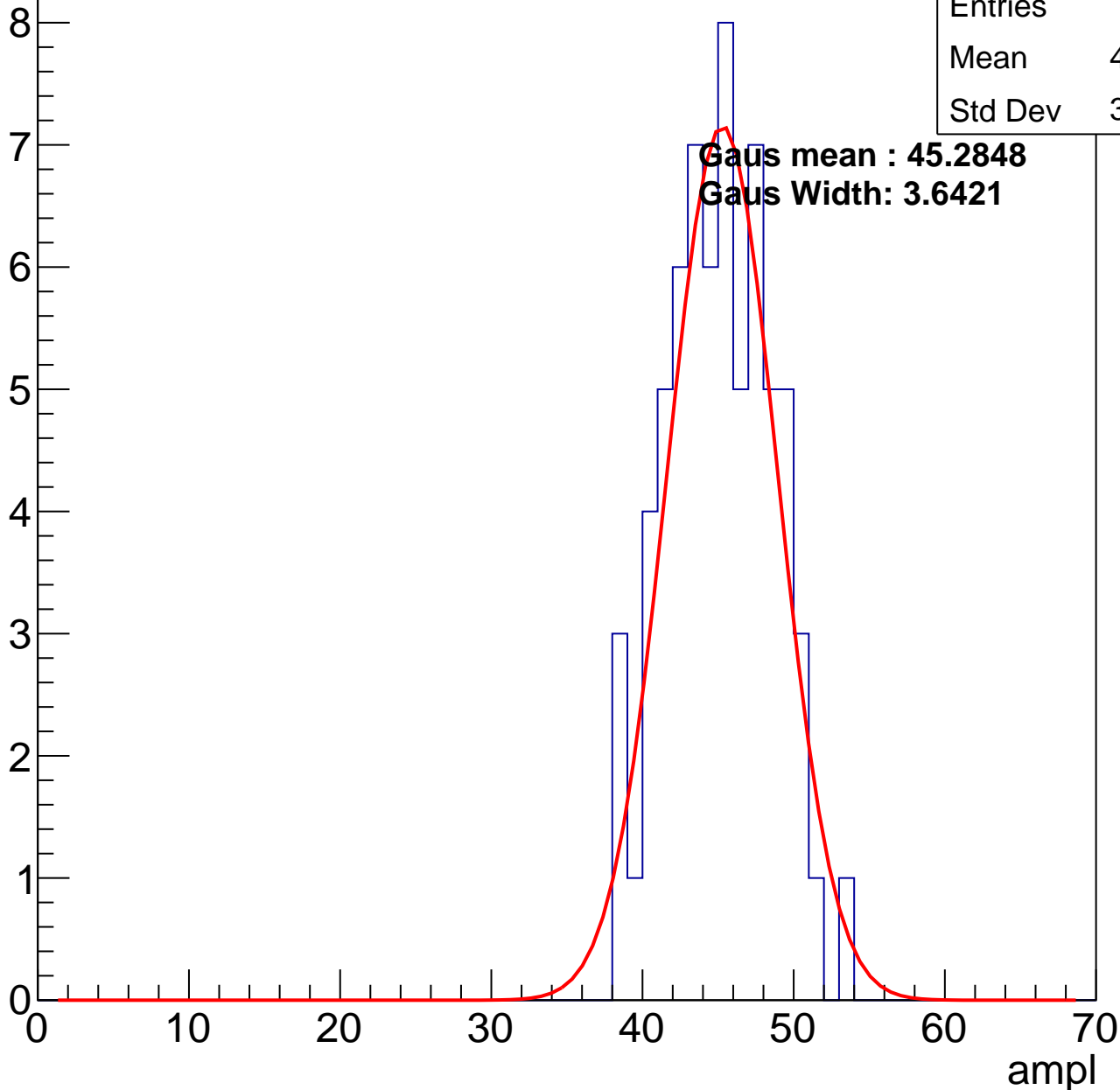
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.67
Std Dev	3.405

Gaus mean : 45.2848

Gaus Width: 3.6421

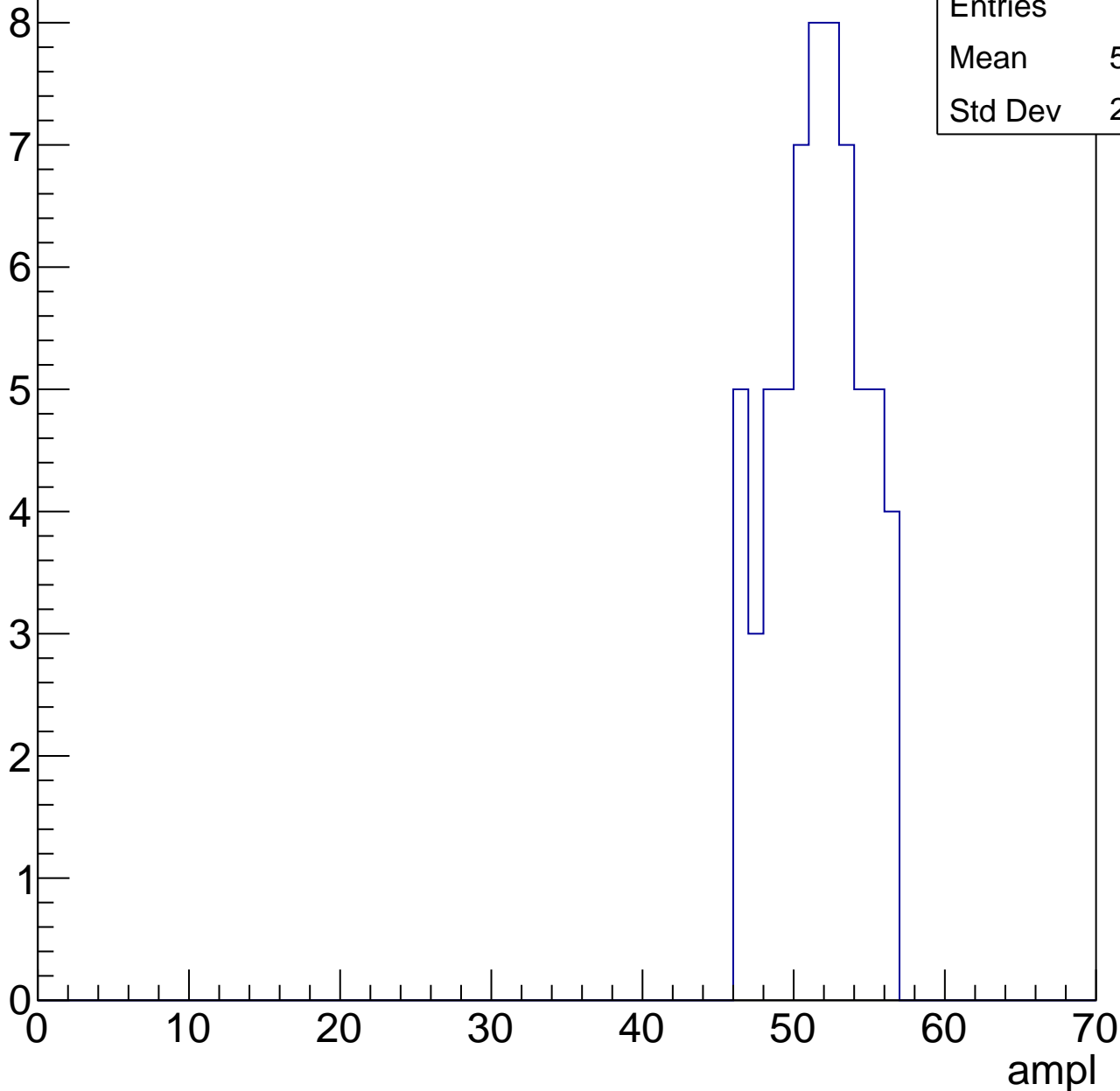


B0L001S, U22-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

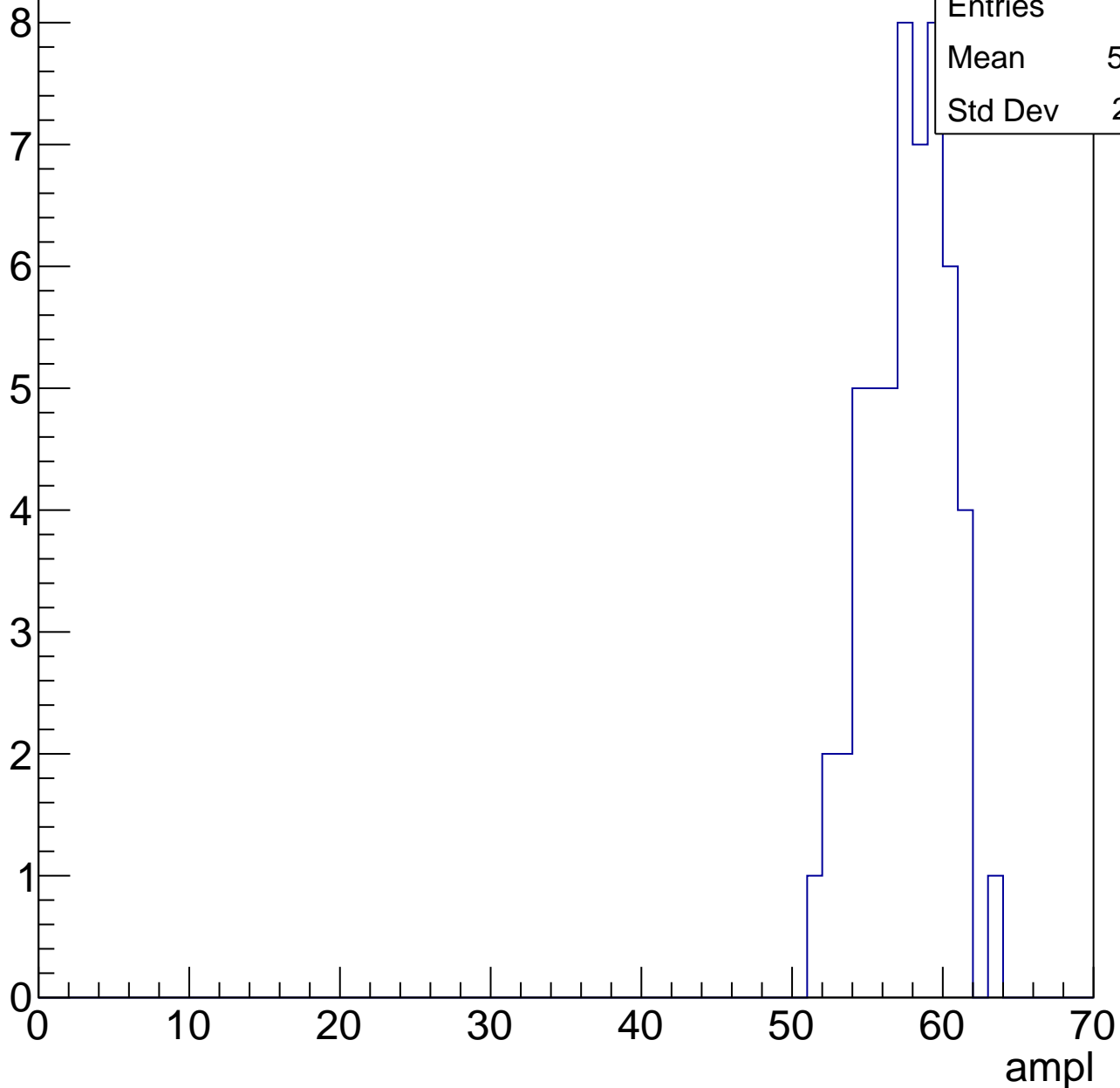
Entries	62
Mean	51.13
Std Dev	2.854



B0L001S, U22-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



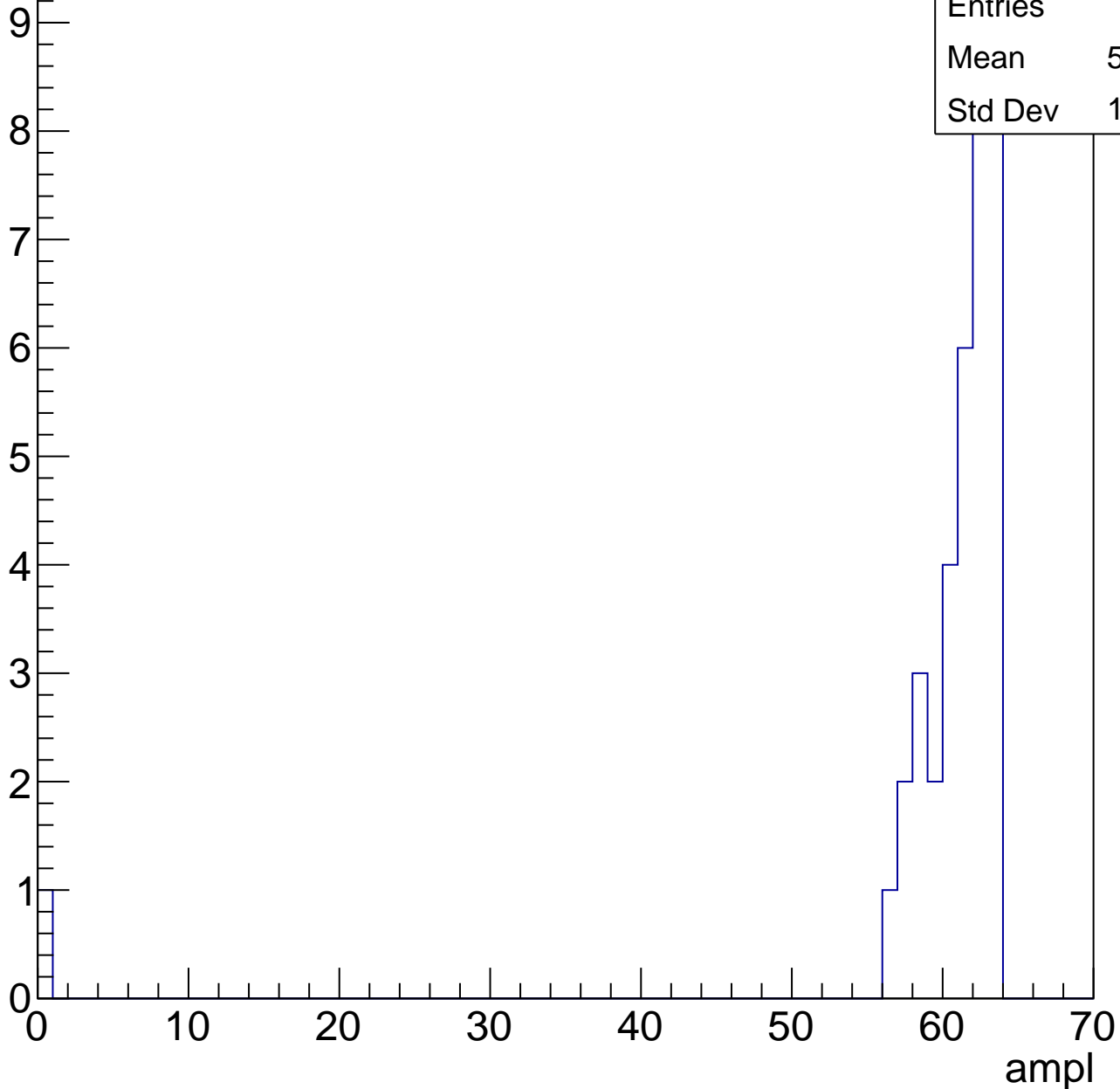
Entries	54
Mean	57.17
Std Dev	2.651

B0L001S, U22-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.17
Std Dev	10.19



B0L001S, U22-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch46, adc0

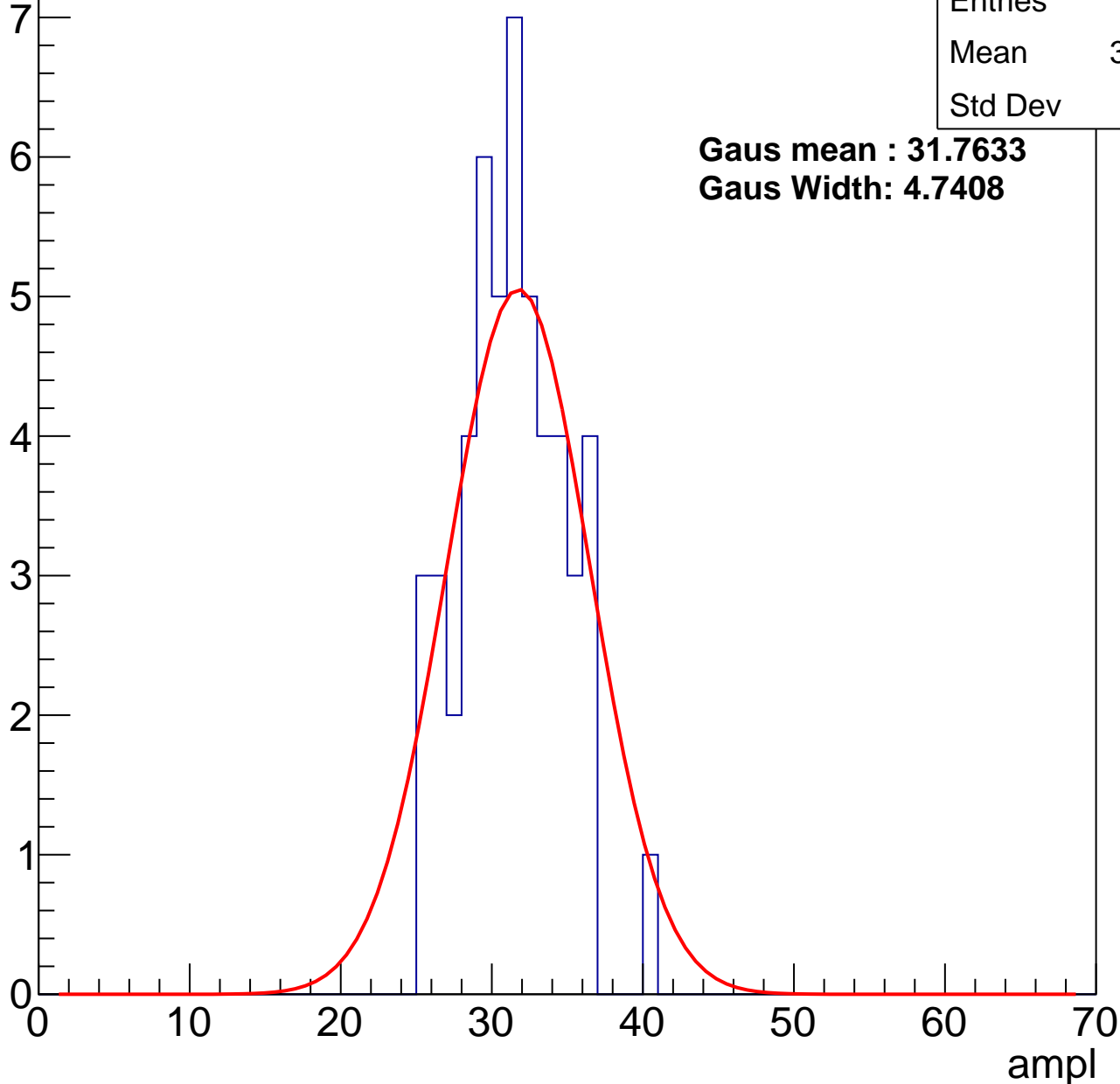
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30.92
Std Dev	3.33

Gaus mean : 31.7633

Gaus Width: 4.7408



B0L001S, U22-ch46, adc1

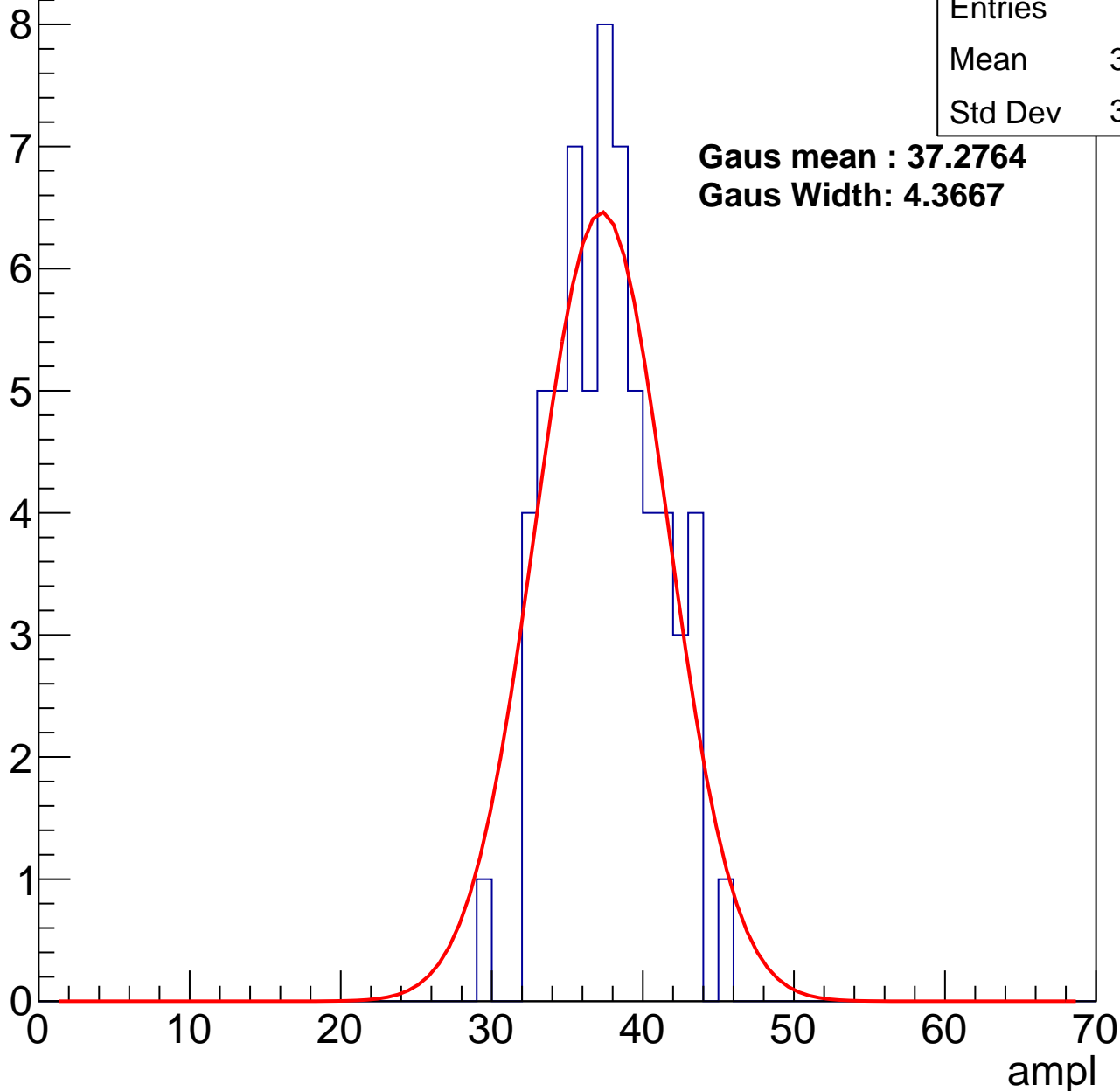
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.16
Std Dev	3.405

Gaus mean : 37.2764

Gaus Width: 4.3667



B0L001S, U22-ch46, adc2

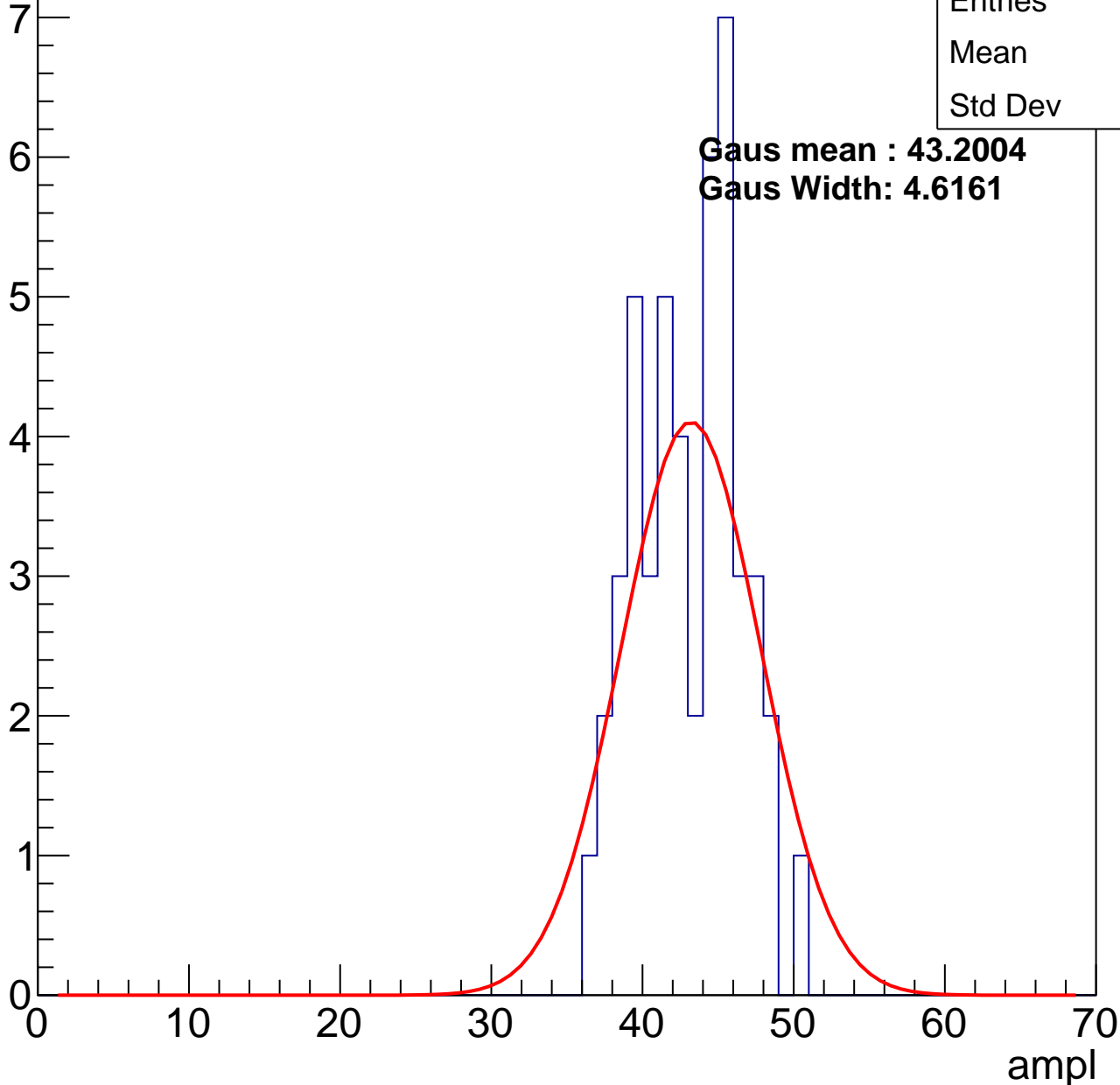
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	42.6
Std Dev	3.35

Gaus mean : 43.2004

Gaus Width: 4.6161



B0L001S, U22-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	92
Mean	49.03
Std Dev	3.891

Entry

10

8

6

4

2

0

0

10

20

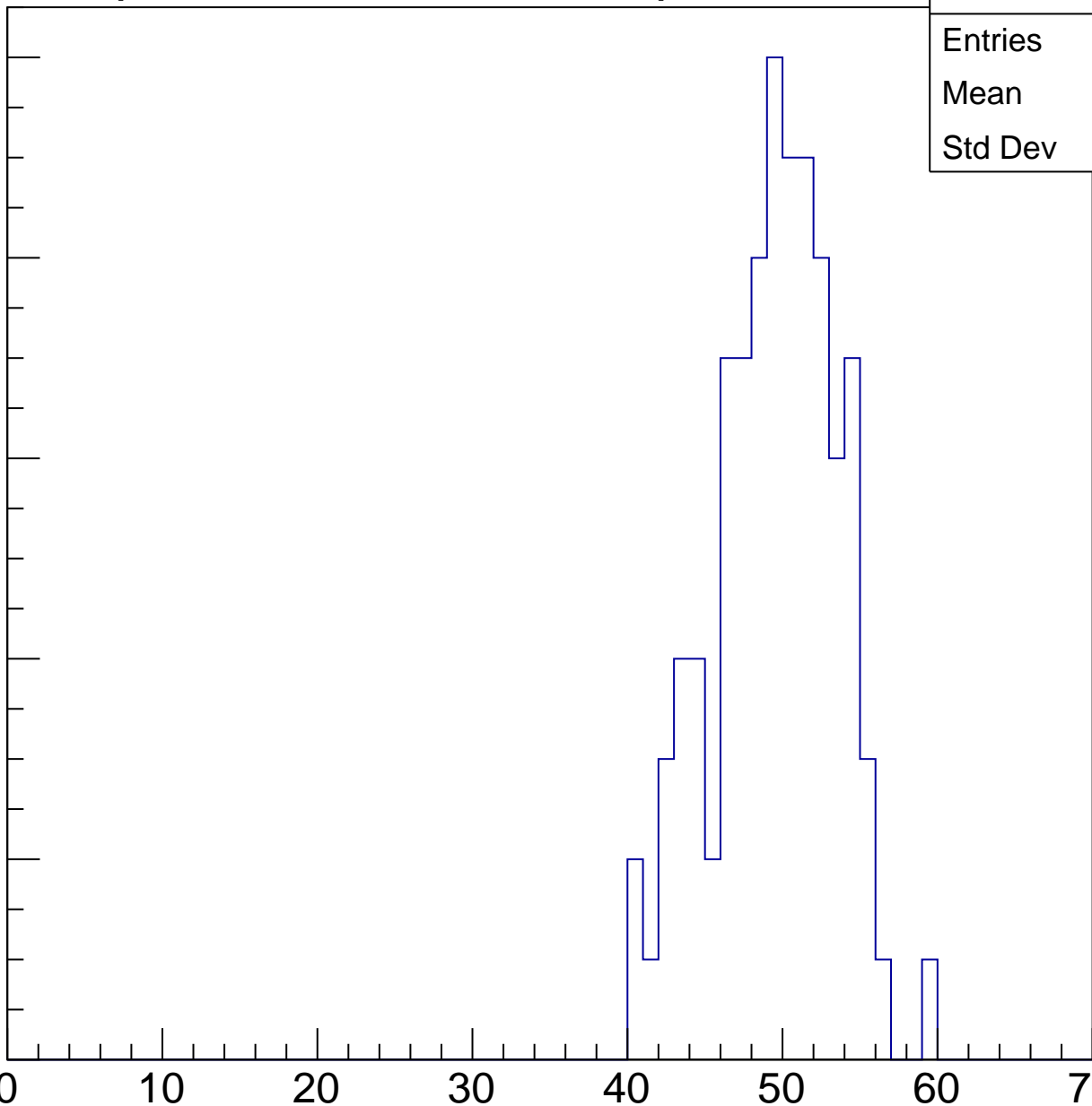
30

40

50

60

ampl

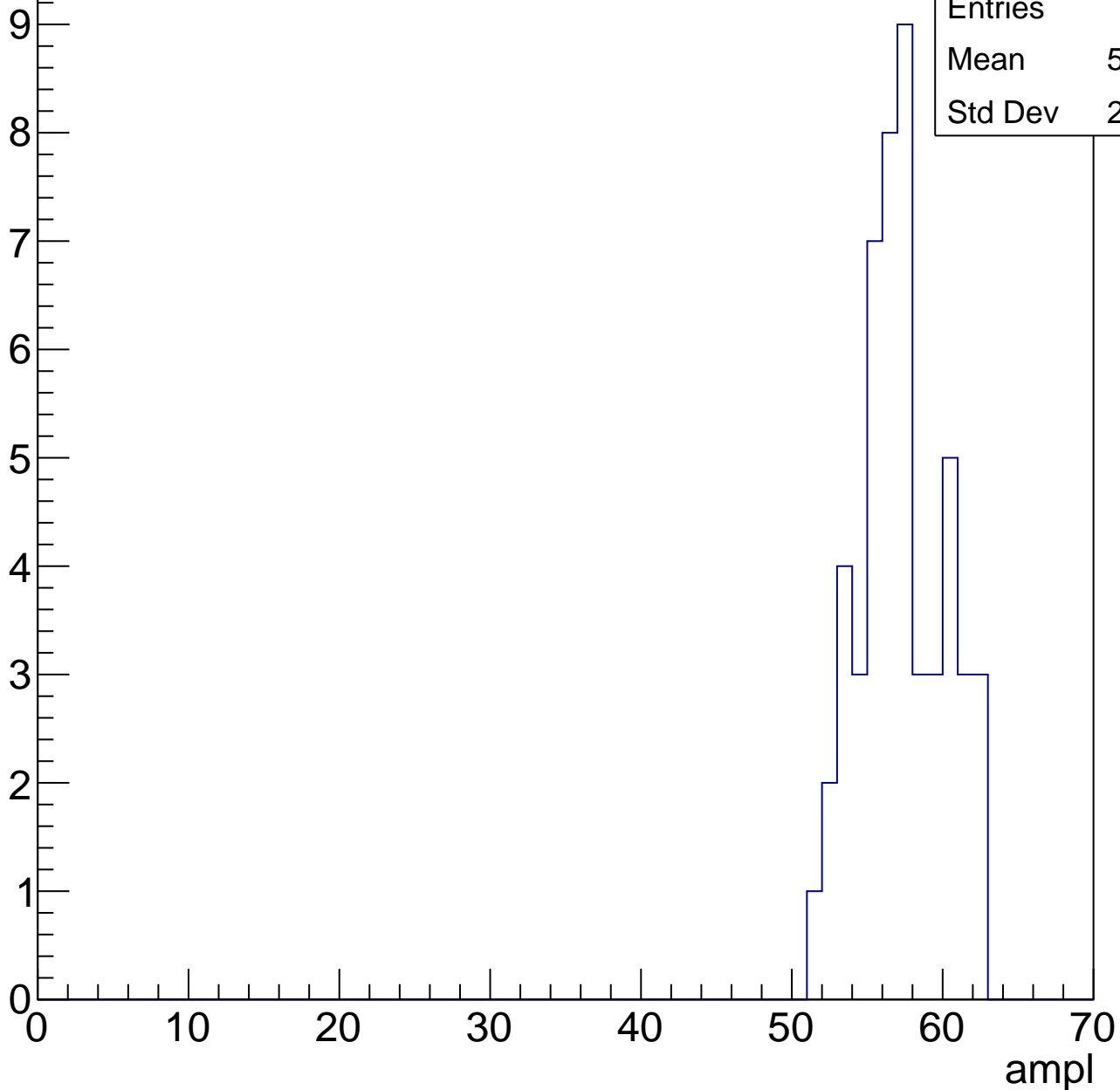


B0L001S, U22-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

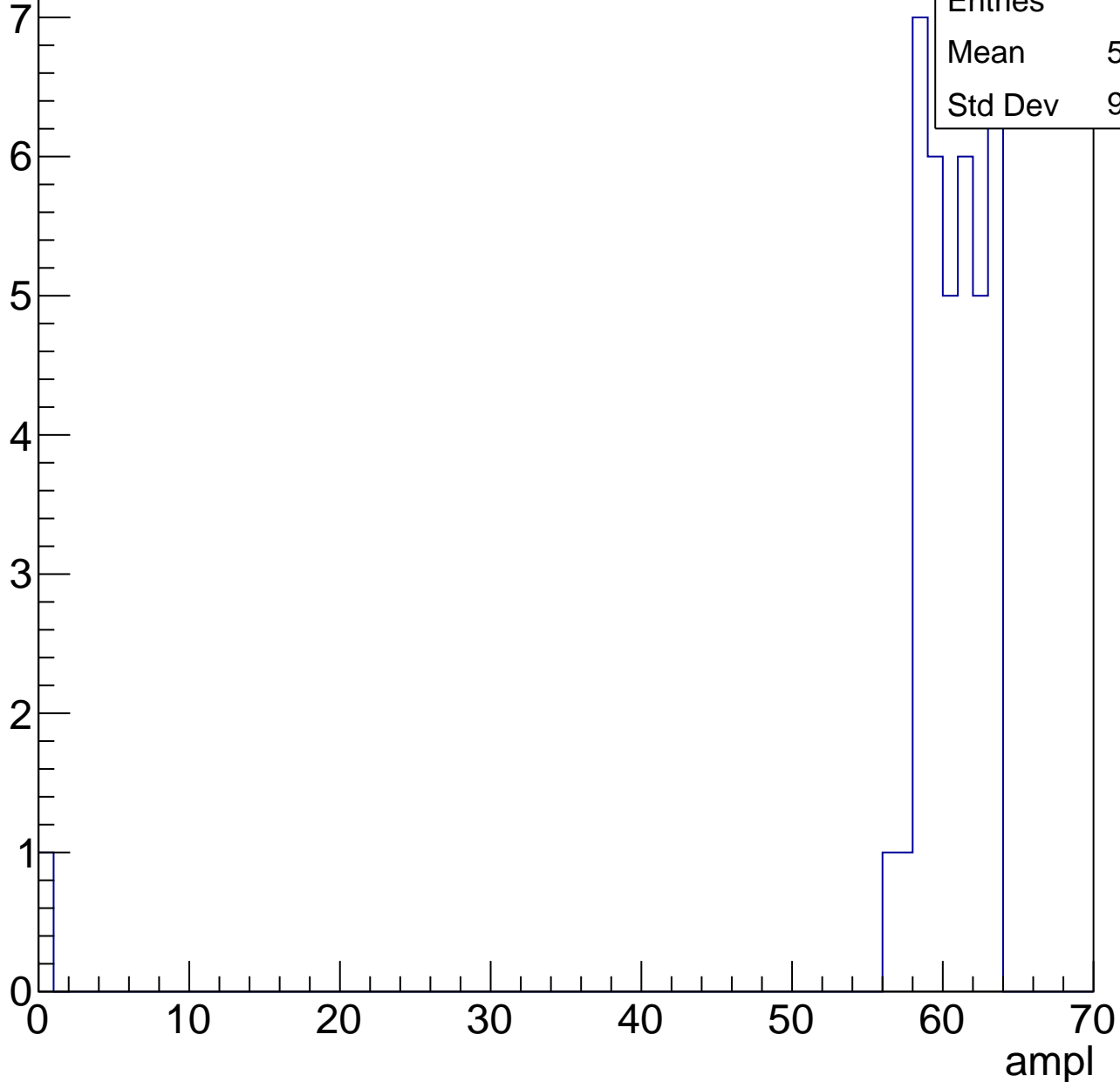
Entries	51
Mean	56.76
Std Dev	2.777



B0L001S, U22-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

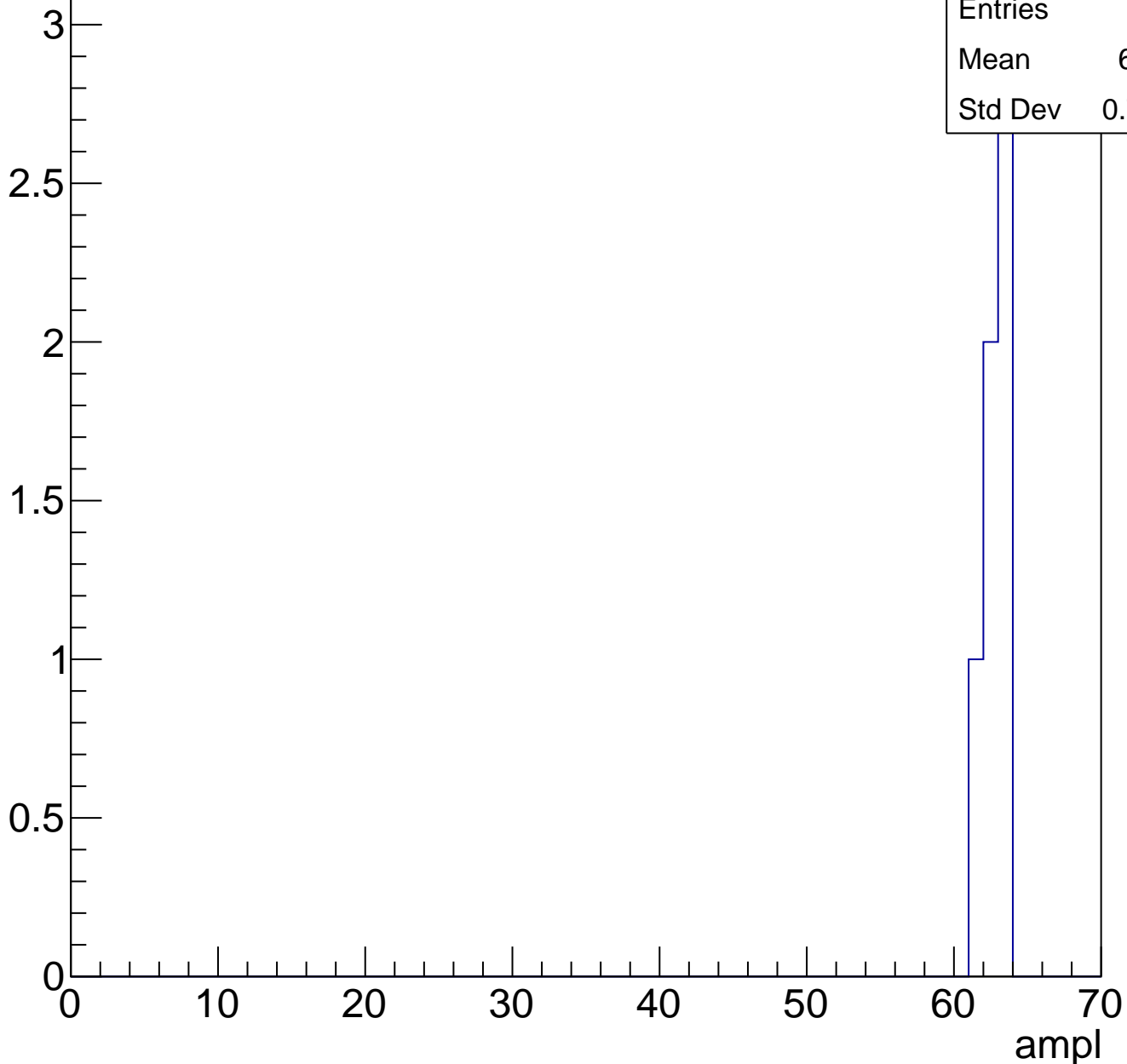


Entries	39
Mean	58.72
Std Dev	9.719

B0L001S, U22-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B0L001S, U22-ch47, adc0

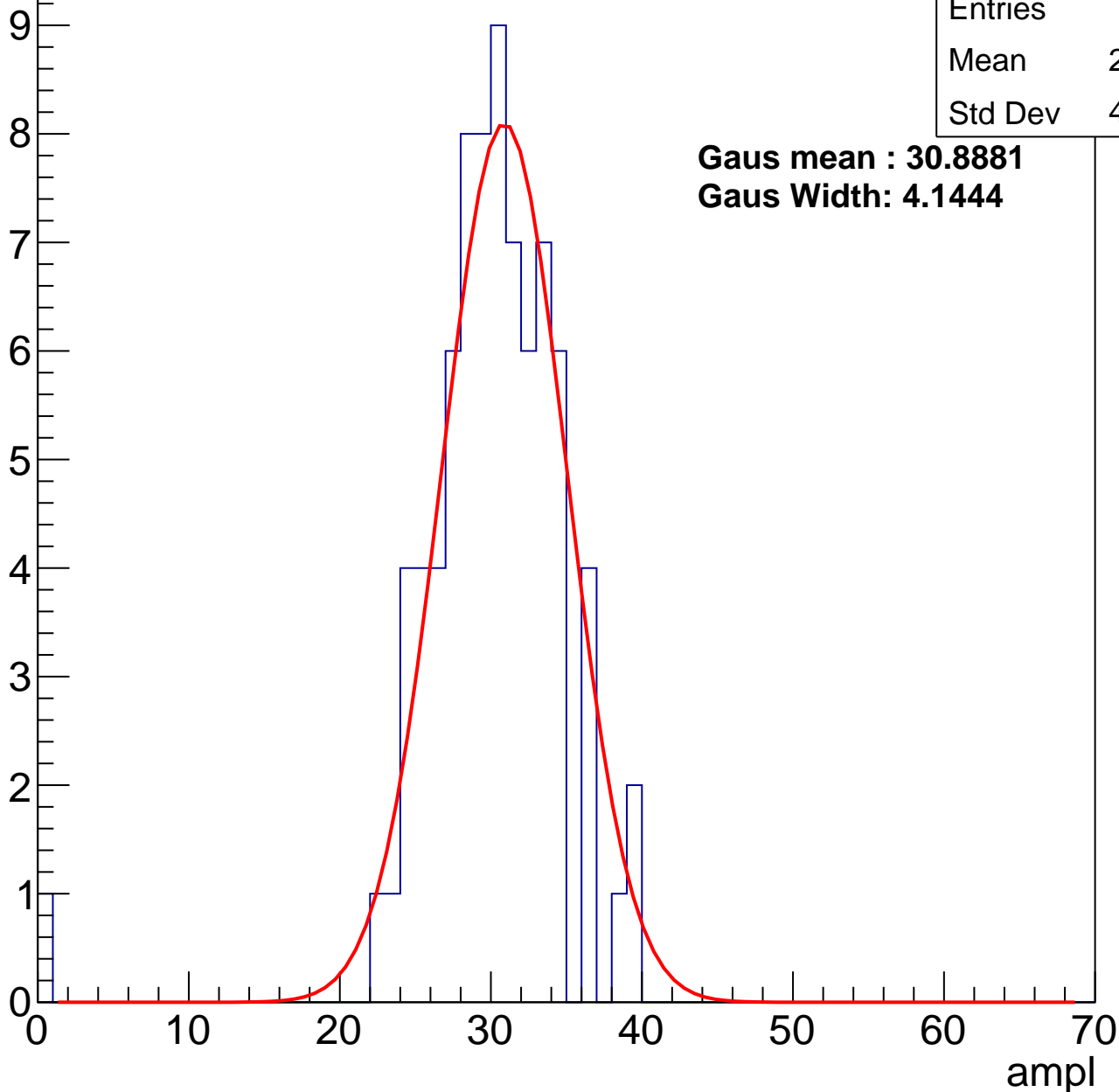
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	29.58
Std Dev	4.983

Gaus mean : 30.8881

Gaus Width: 4.1444



B0L001S, U22-ch47, adc1

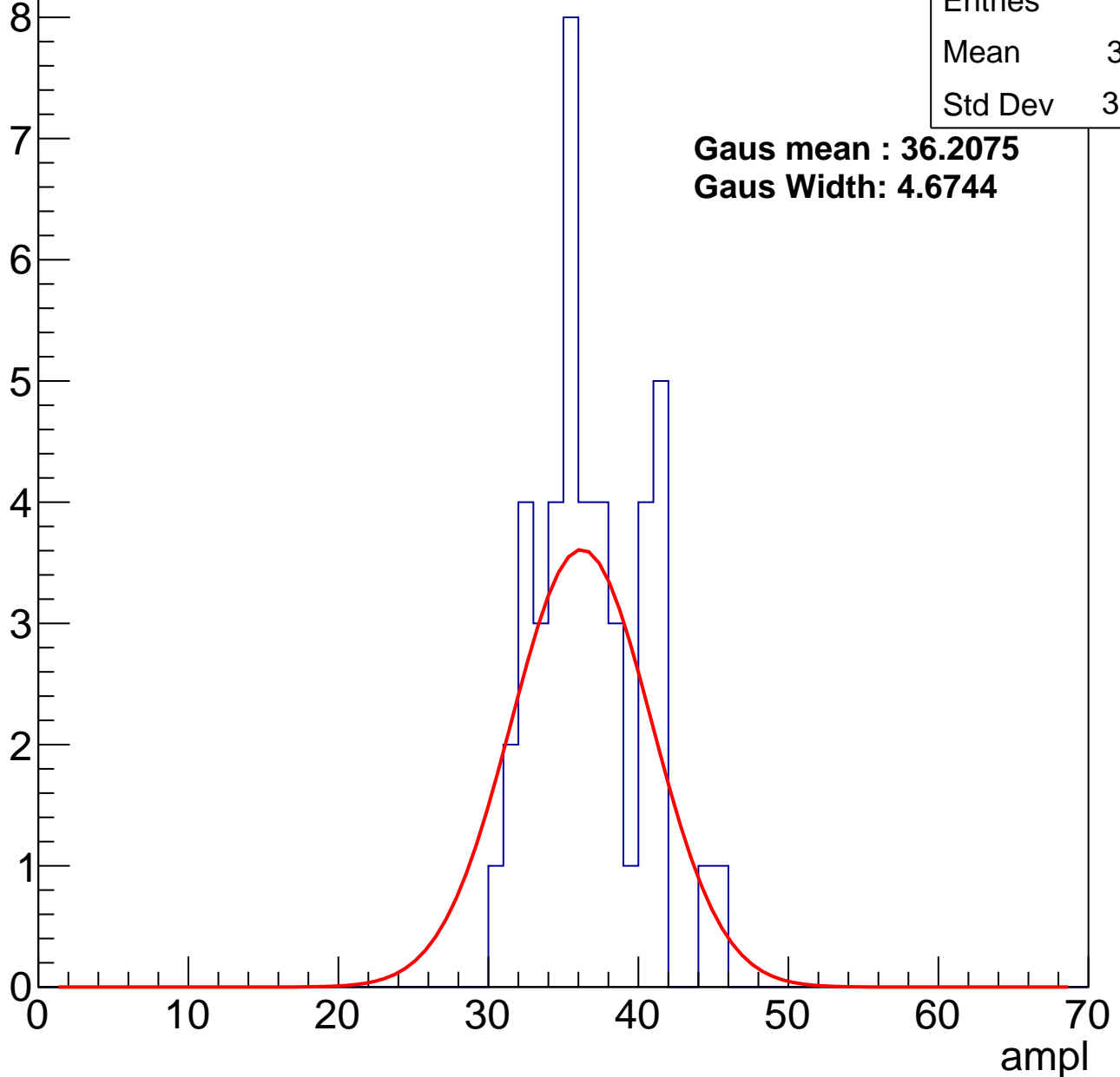
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	36.31
Std Dev	3.514

Gaus mean : 36.2075

Gaus Width: 4.6744



B0L001S, U22-ch47, adc2

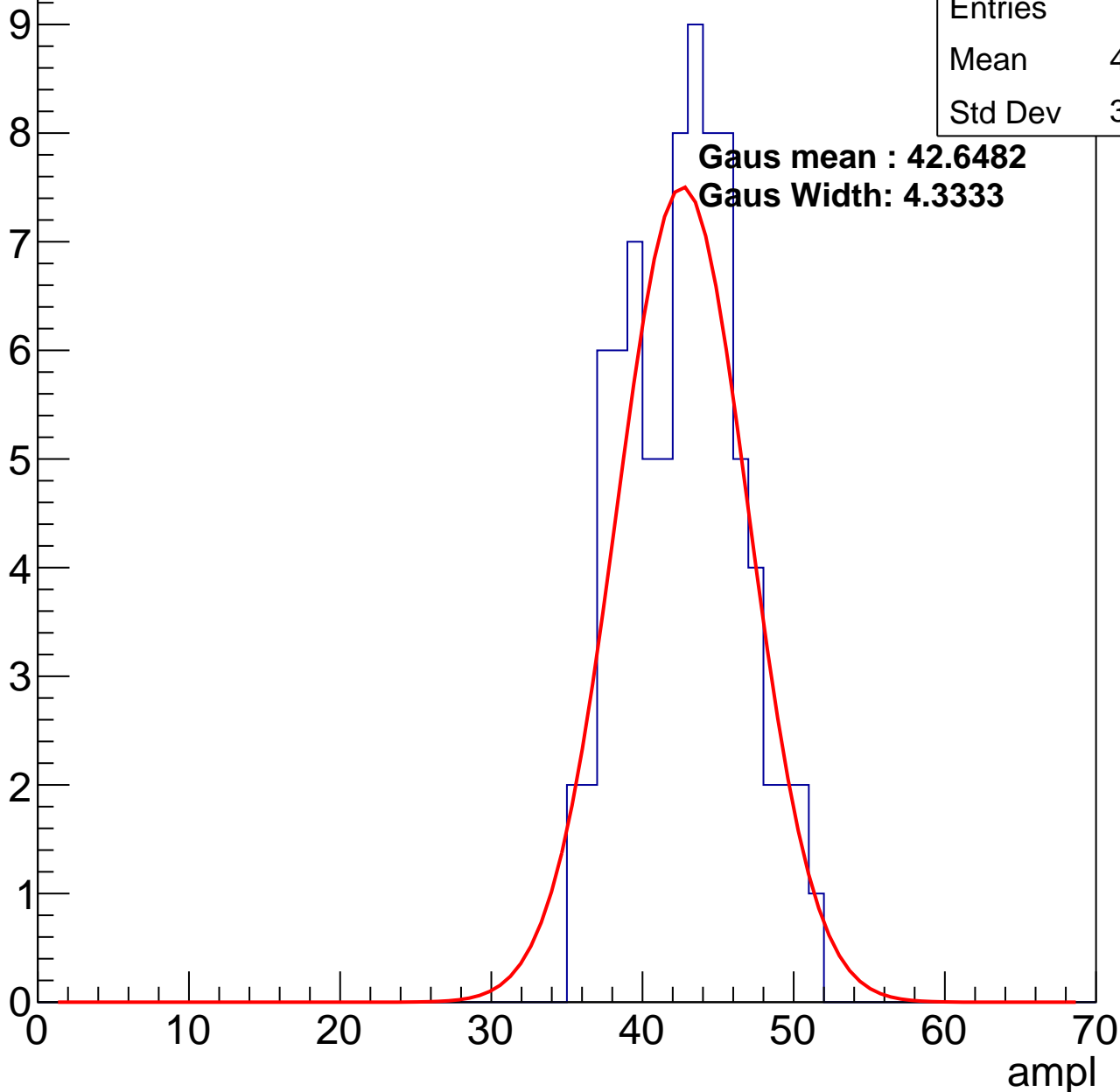
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	42.29
Std Dev	3.776

Gaus mean : 42.6482

Gaus Width: 4.3333

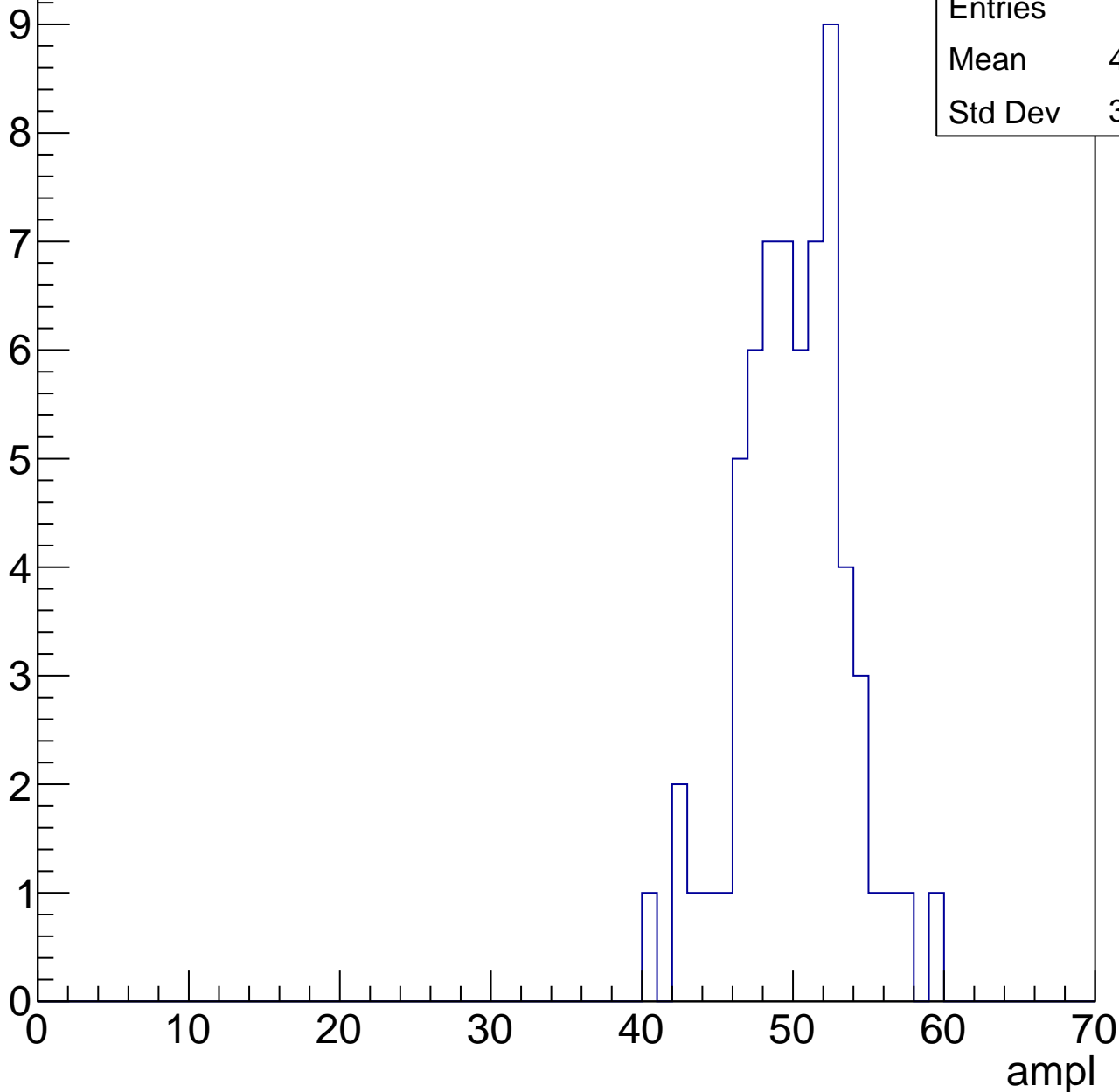


B0L001S, U22-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.58
Std Dev	3.565

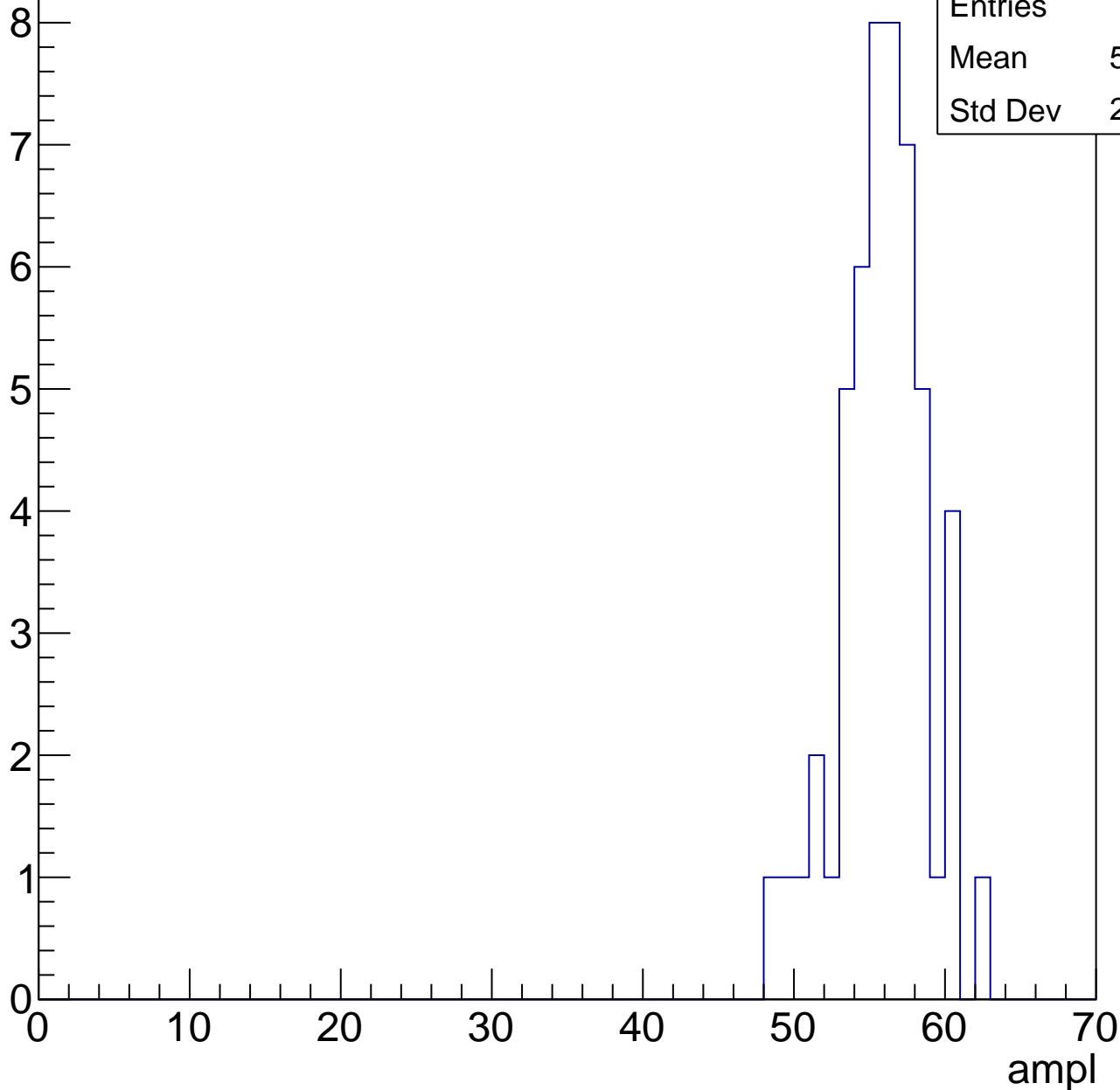


B0L001S, U22-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	55.45
Std Dev	2.844

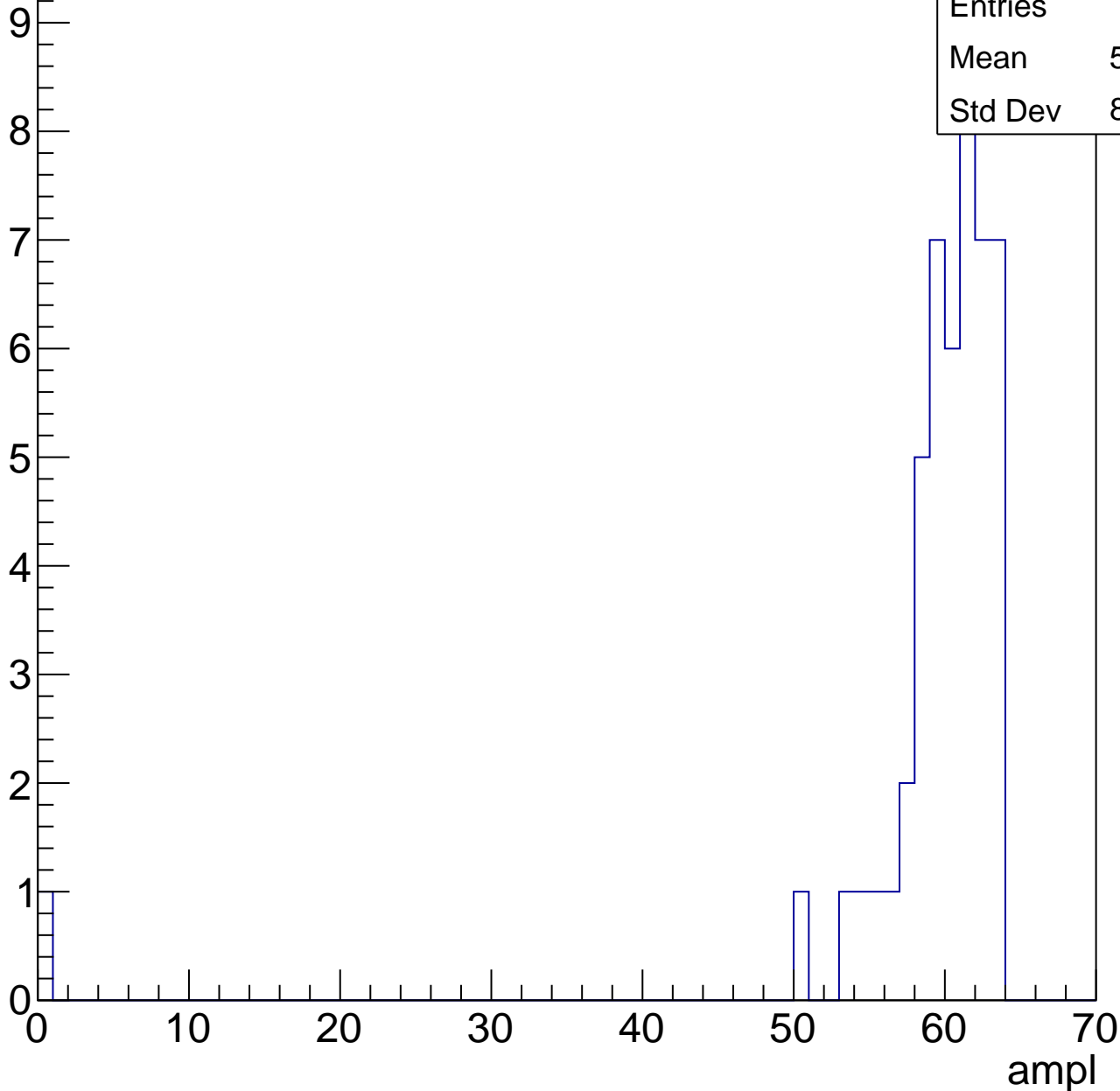


B0L001S, U22-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.55
Std Dev	8.885



B0L001S, U22-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch48, adc0

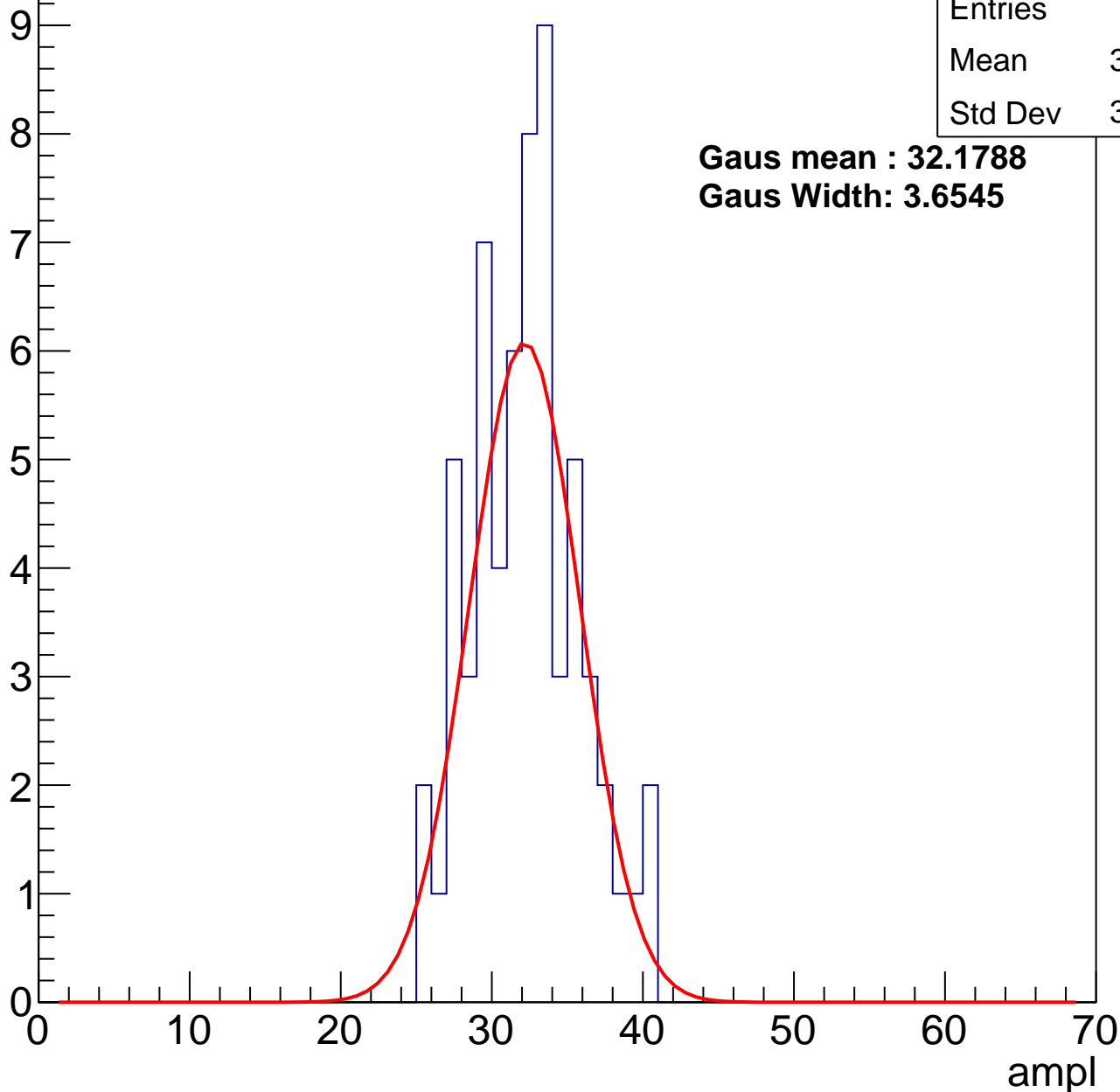
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.82
Std Dev	3.517

Gaus mean : 32.1788

Gaus Width: 3.6545



B0L001S, U22-ch48, adc1

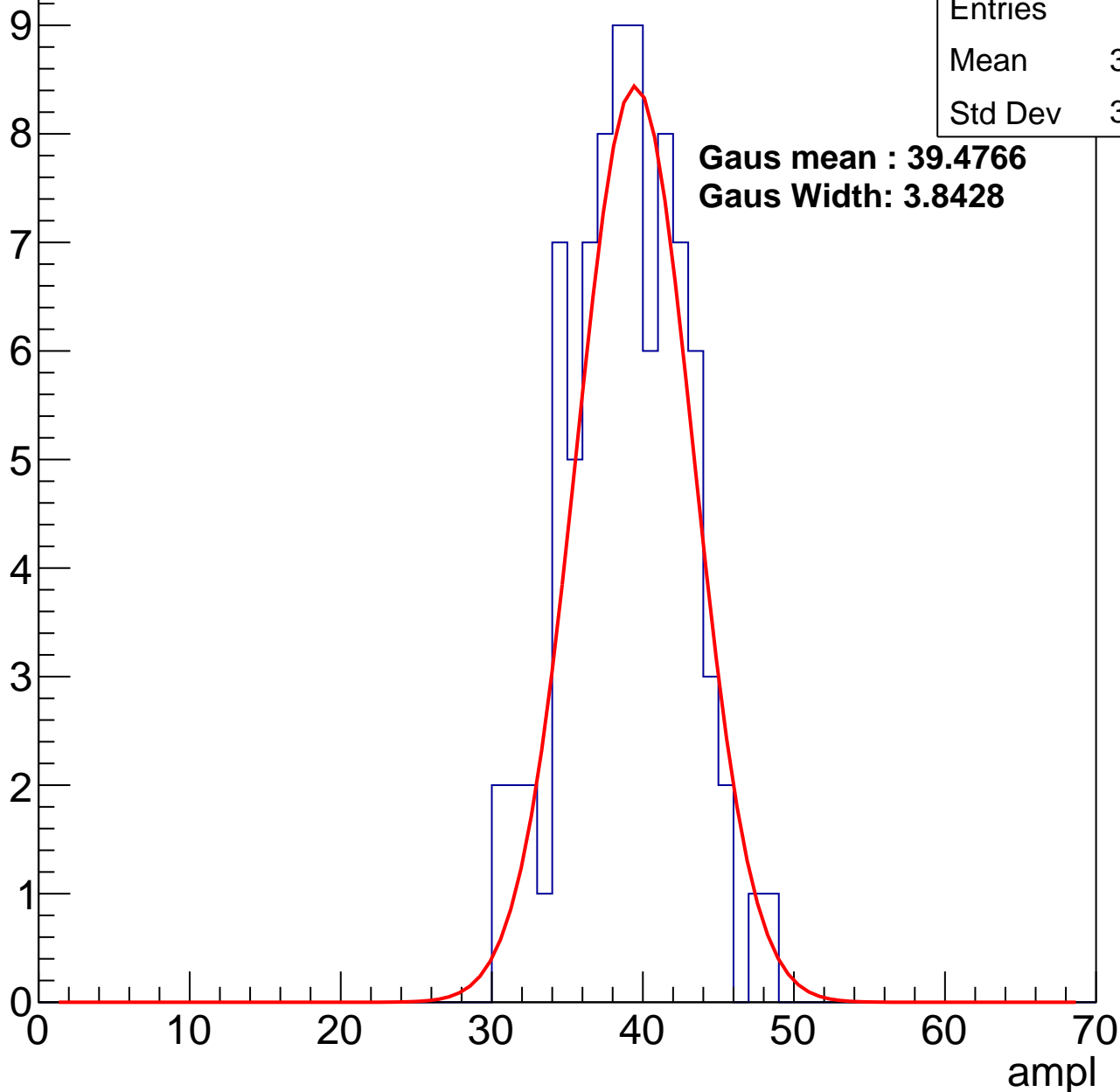
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	38.49
Std Dev	3.812

Gaus mean : 39.4766

Gaus Width: 3.8428



B0L001S, U22-ch48, adc2

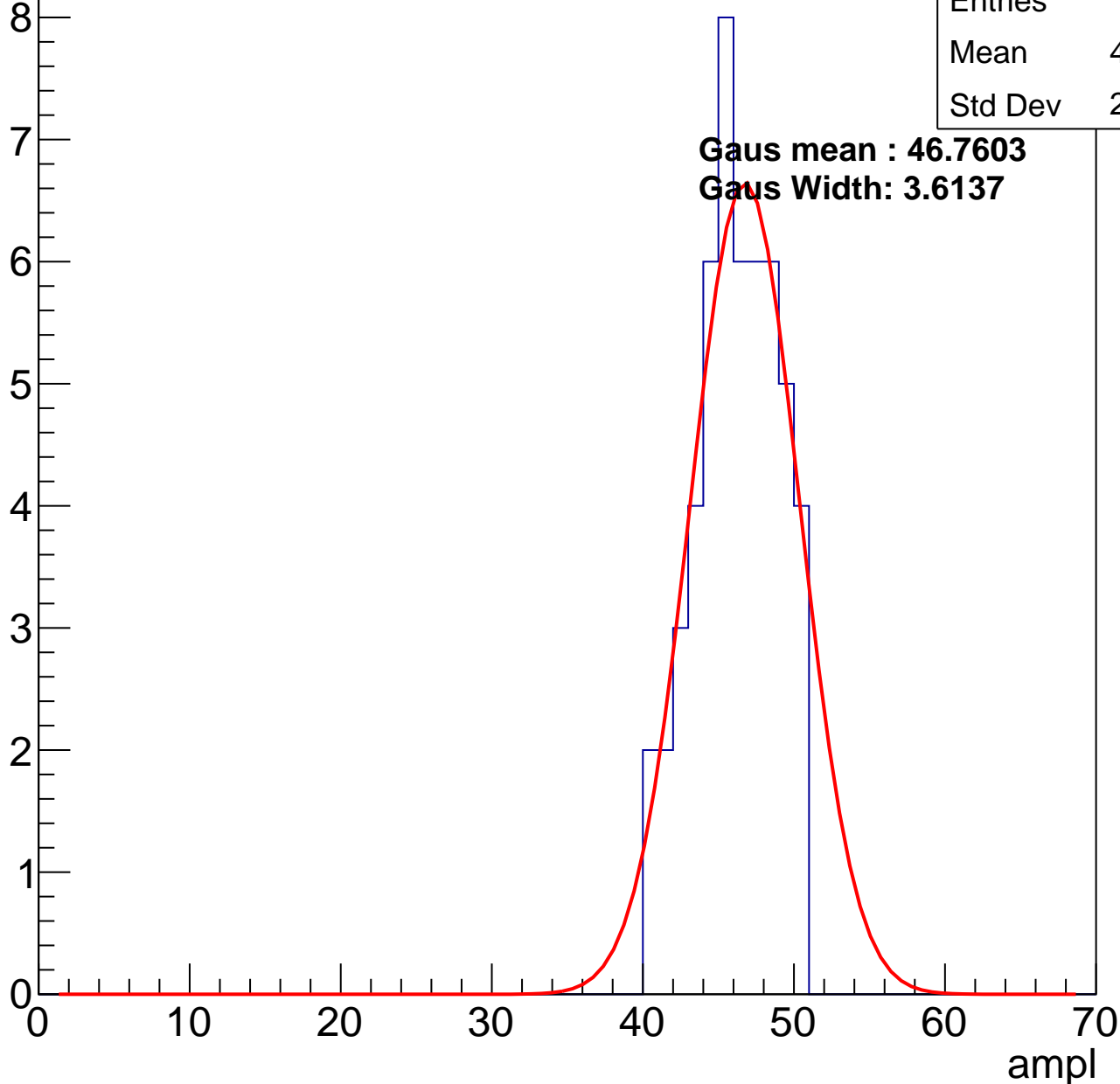
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	45.67
Std Dev	2.673

Gaus mean : 46.7603

Gaus Width: 3.6137

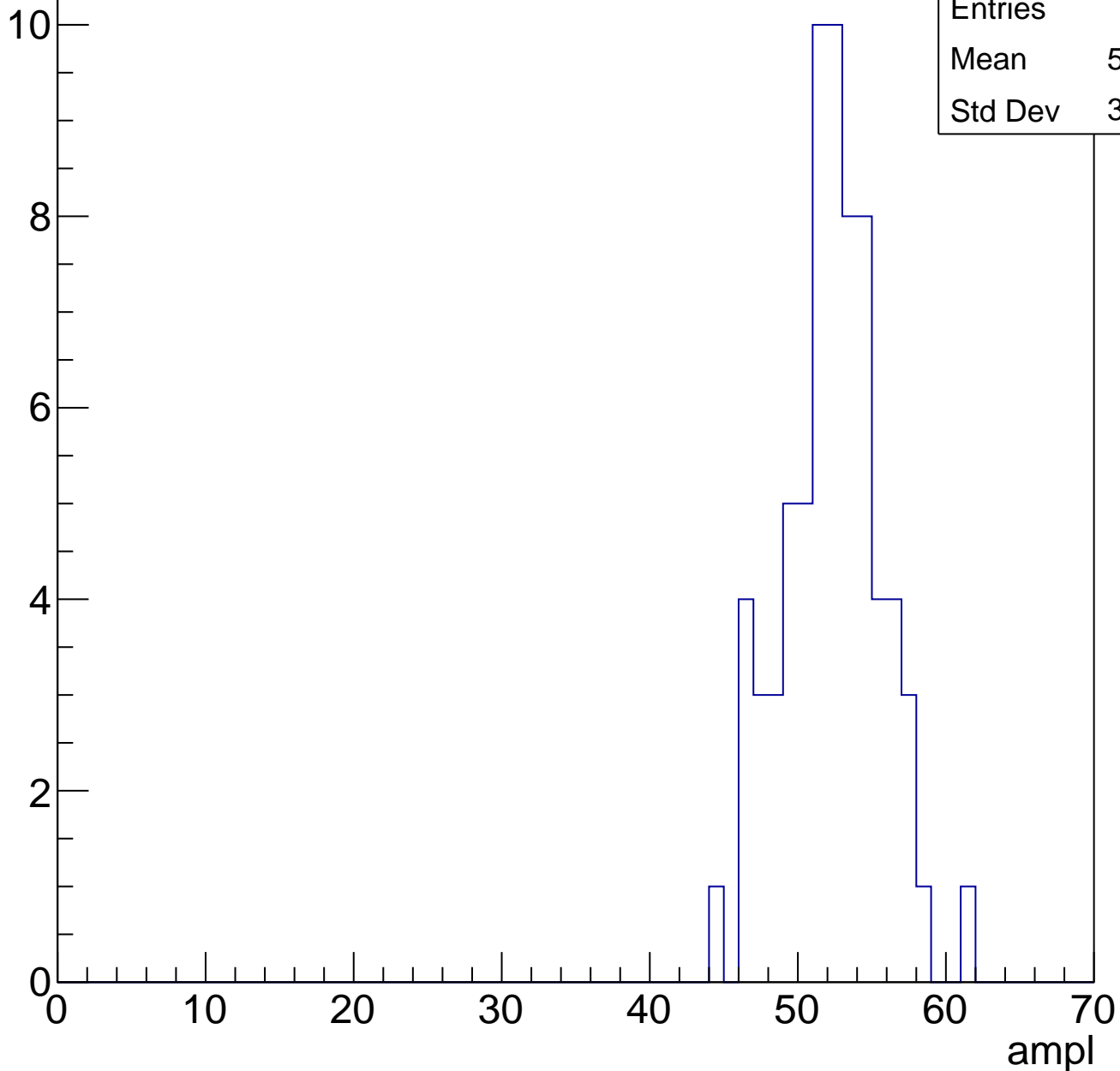


B0L001S, U22-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	51.83
Std Dev	3.242

Entry

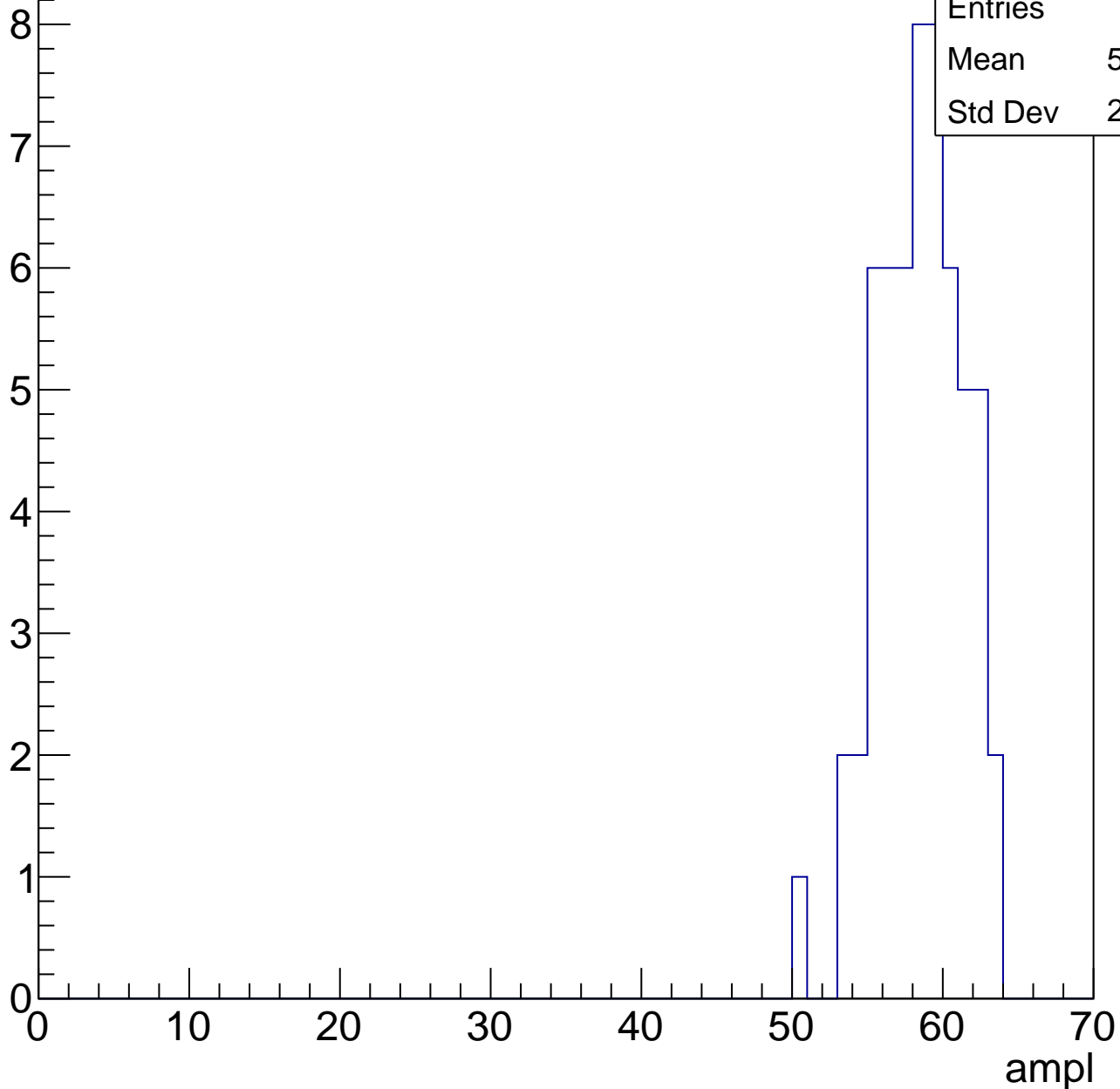


B0L001S, U22-ch48, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	58.05
Std Dev	2.768

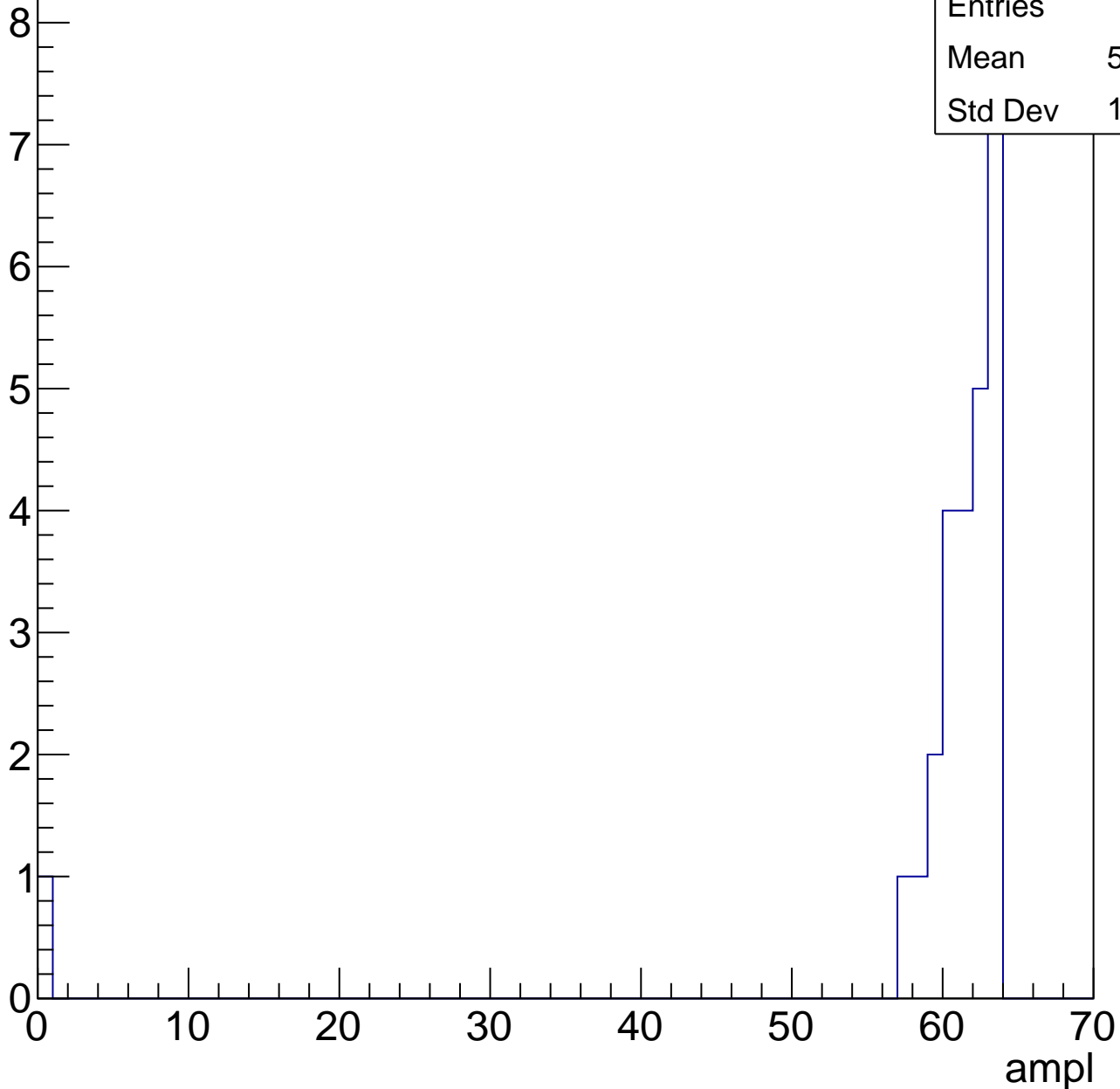


B0L001S, U22-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.88
Std Dev	11.89



B0L001S, U22-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch49, adc0

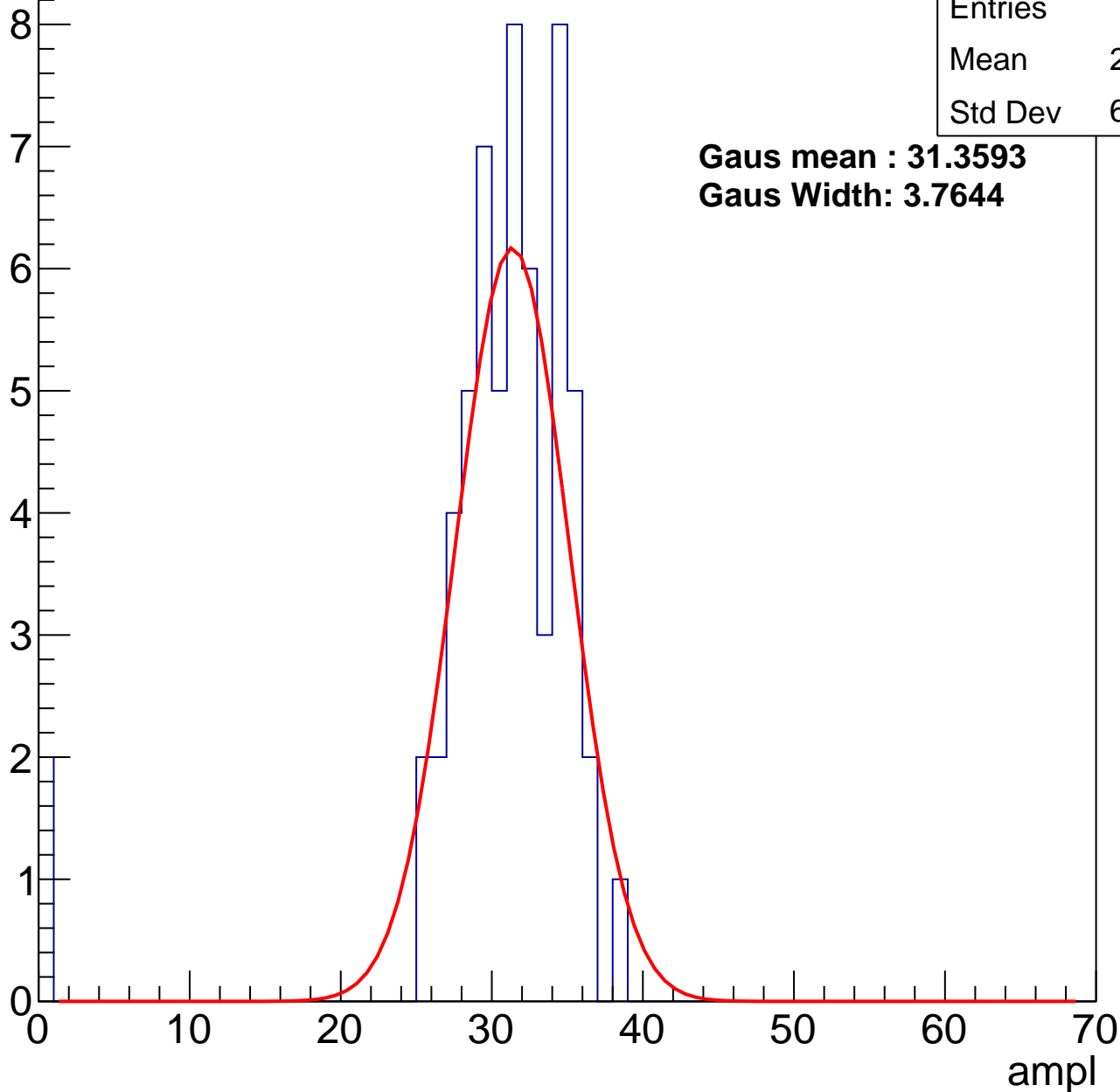
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.98
Std Dev	6.315

Gaus mean : 31.3593

Gaus Width: 3.7644



B0L001S, U22-ch49, adc1

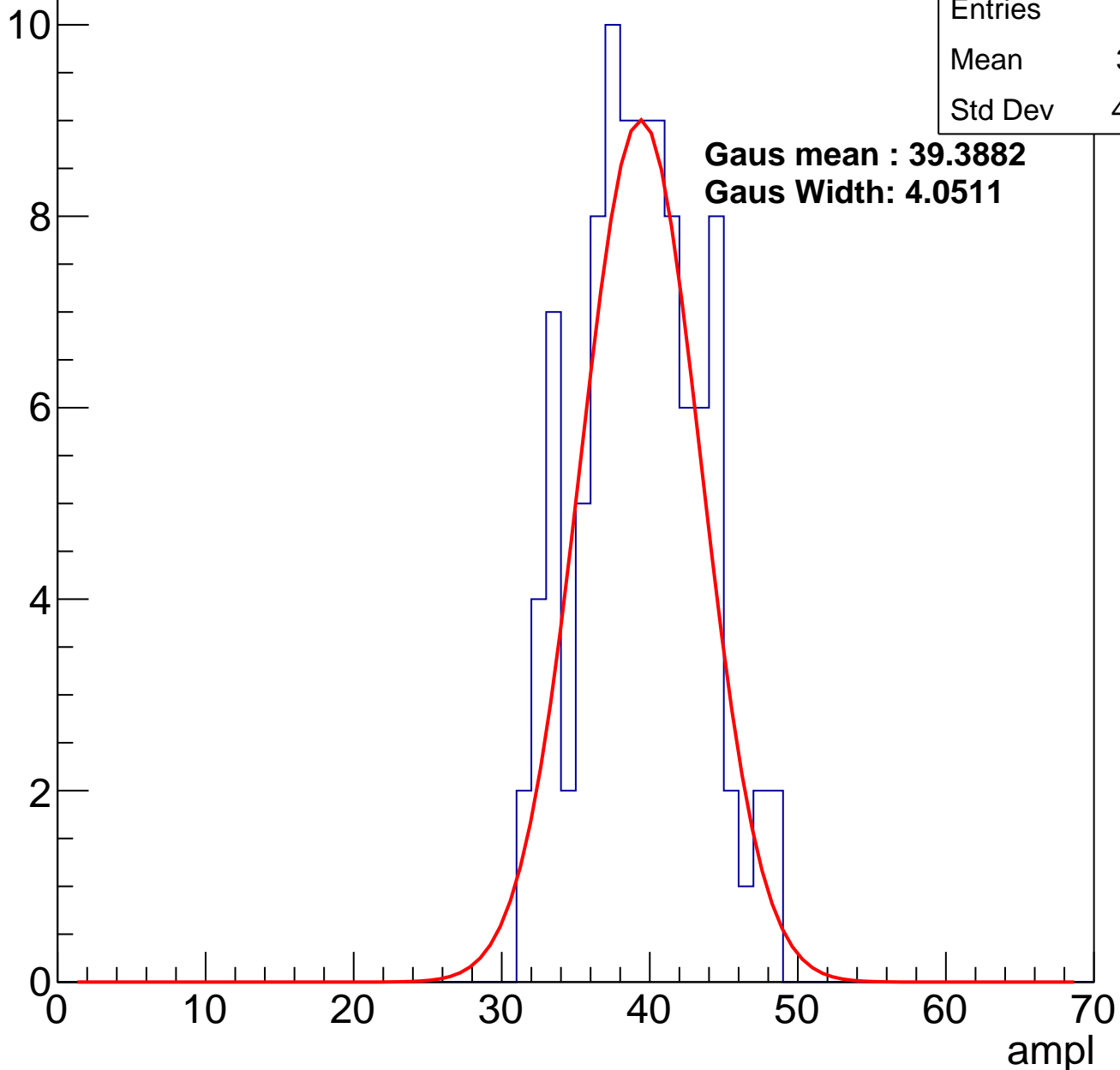
calib_packv5_042523_0143.root, FC#9, port A1

Entries	100
Mean	38.91
Std Dev	4.035

Gaus mean : 39.3882

Gaus Width: 4.0511

Entry



B0L001S, U22-ch49, adc2

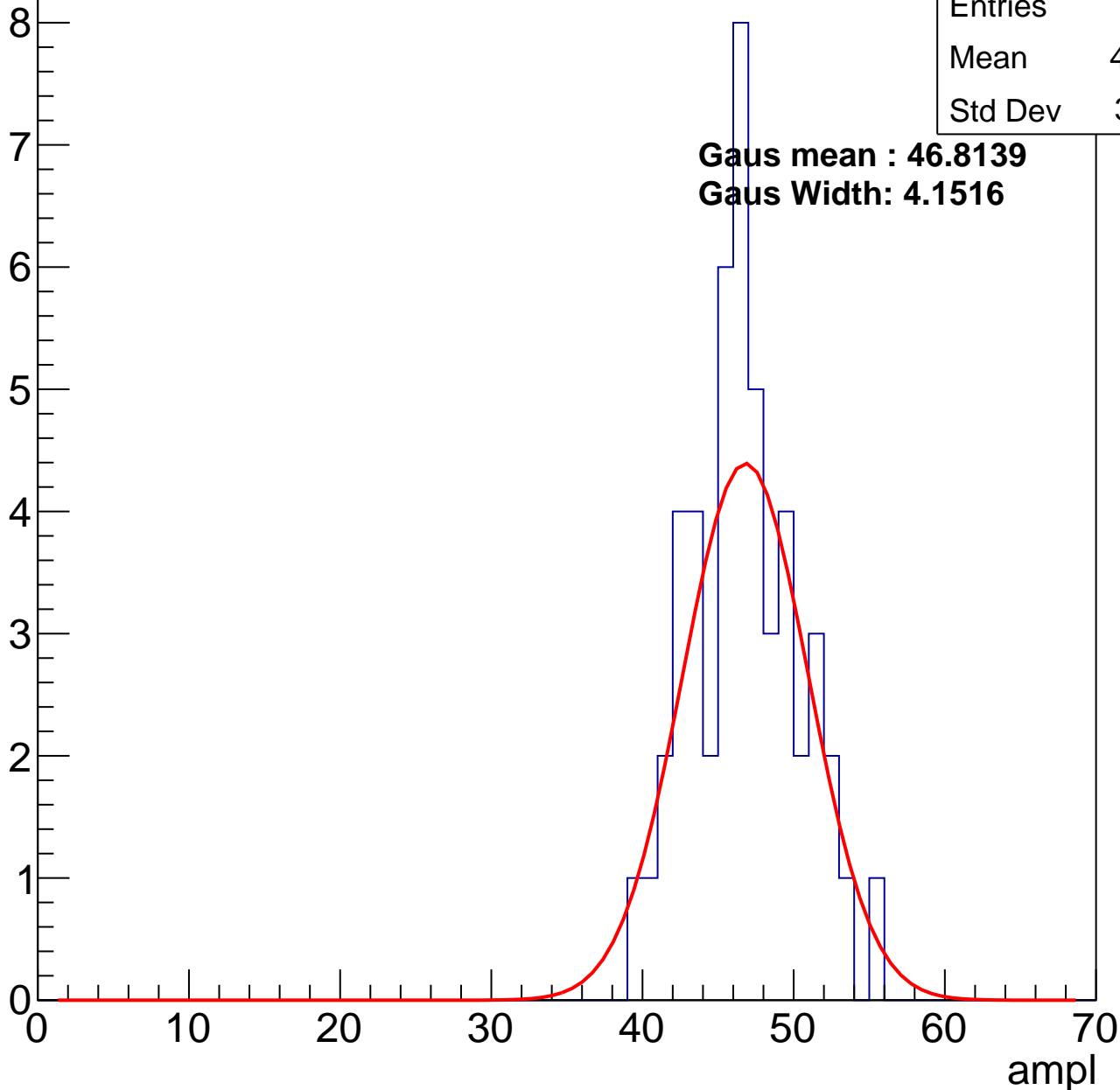
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	46.27
Std Dev	3.521

Gaus mean : 46.8139

Gaus Width: 4.1516

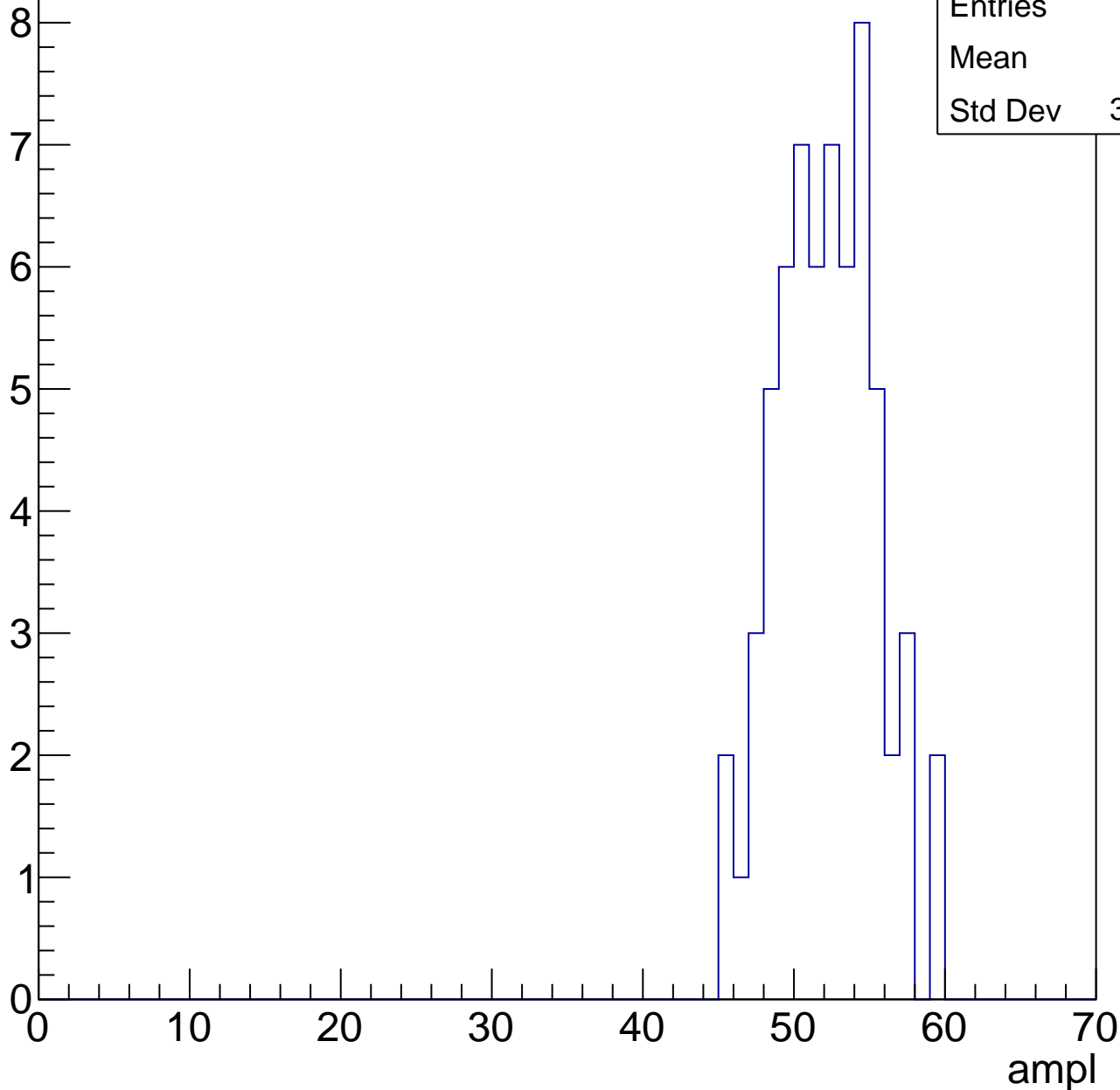


B0L001S, U22-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

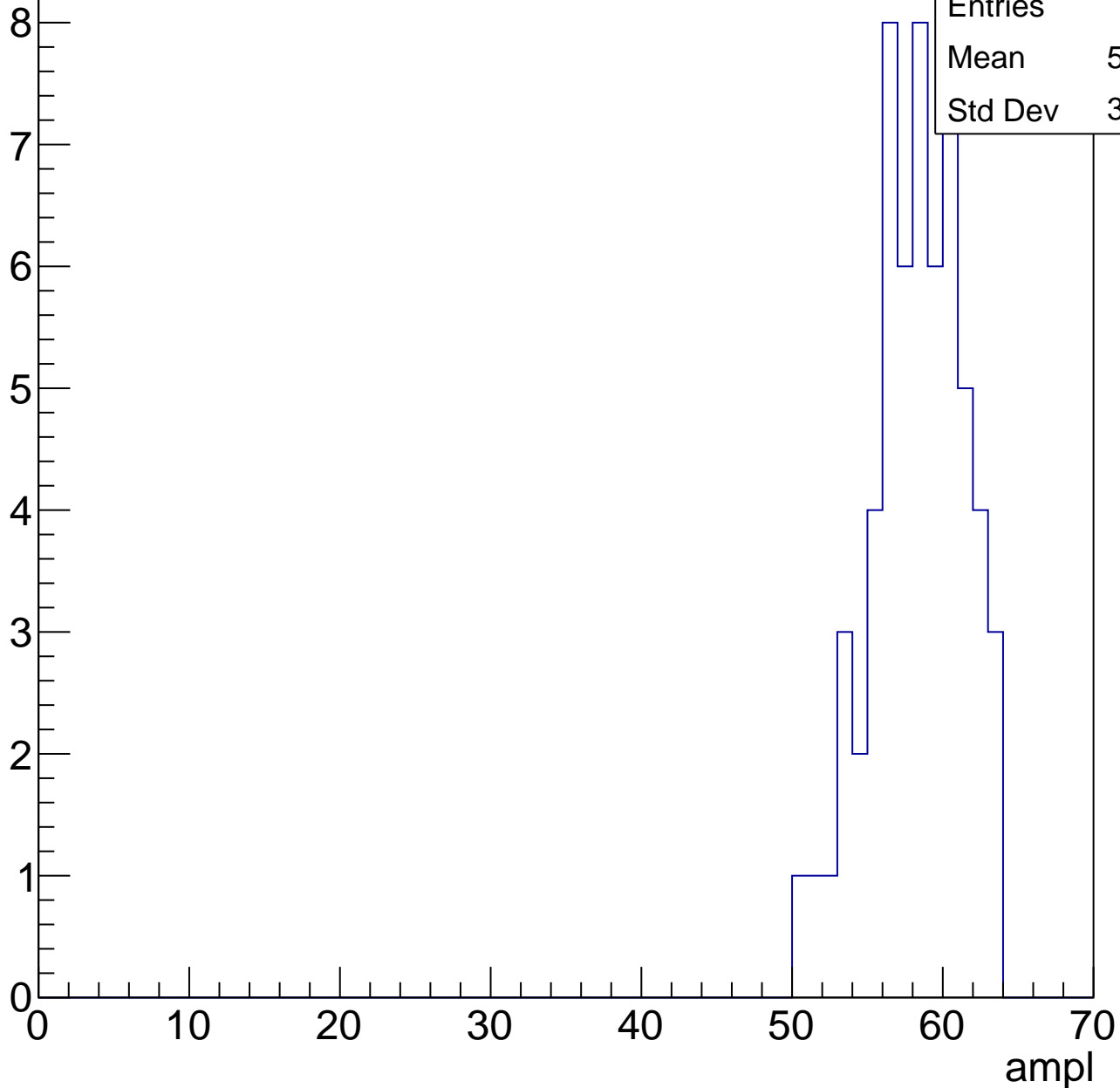
Entries	63
Mean	51.7
Std Dev	3.235



B0L001S, U22-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

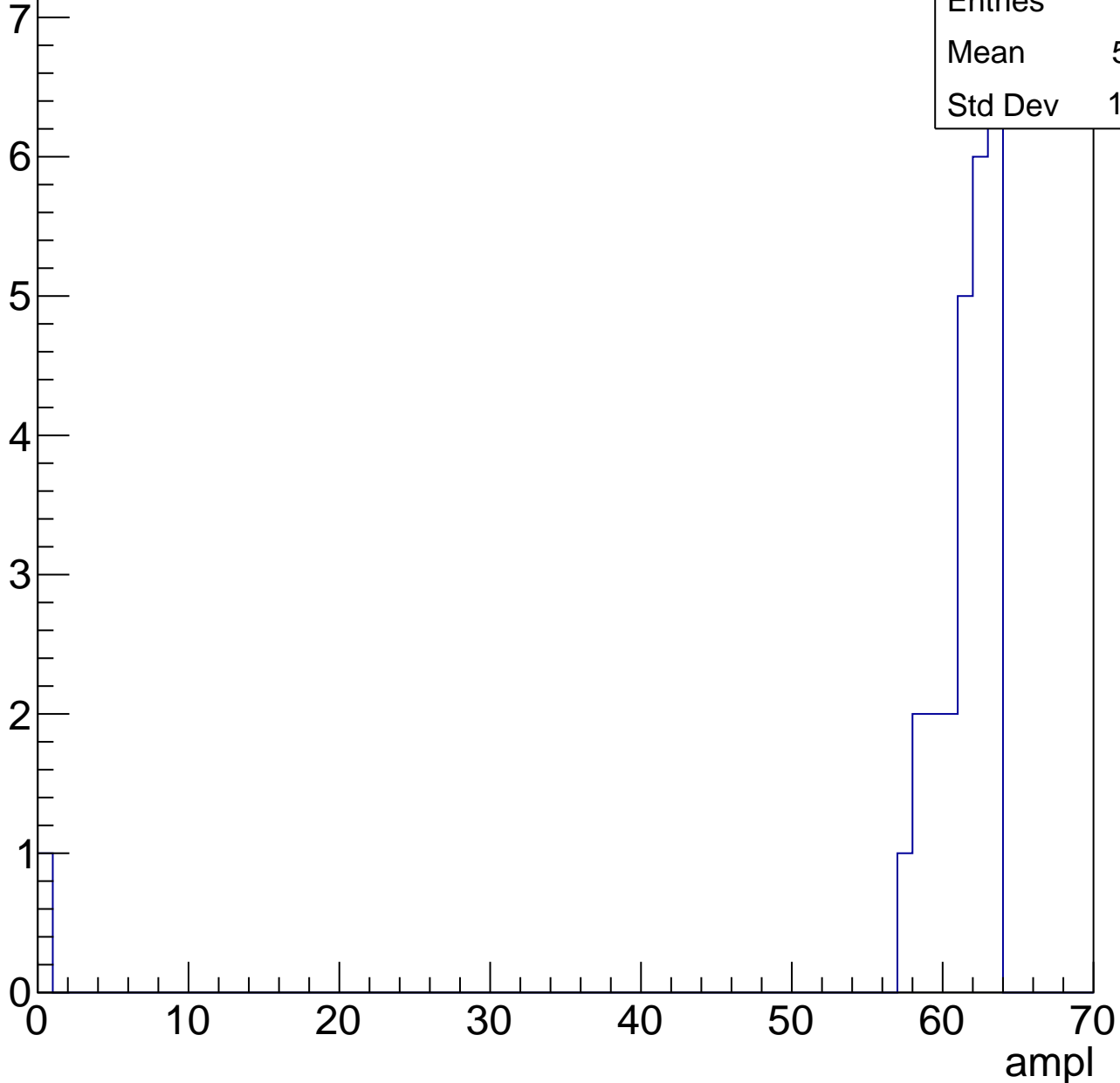


B0L001S, U22-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	26
Mean	58.81
Std Dev	11.89



B0L001S, U22-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch50, adc0

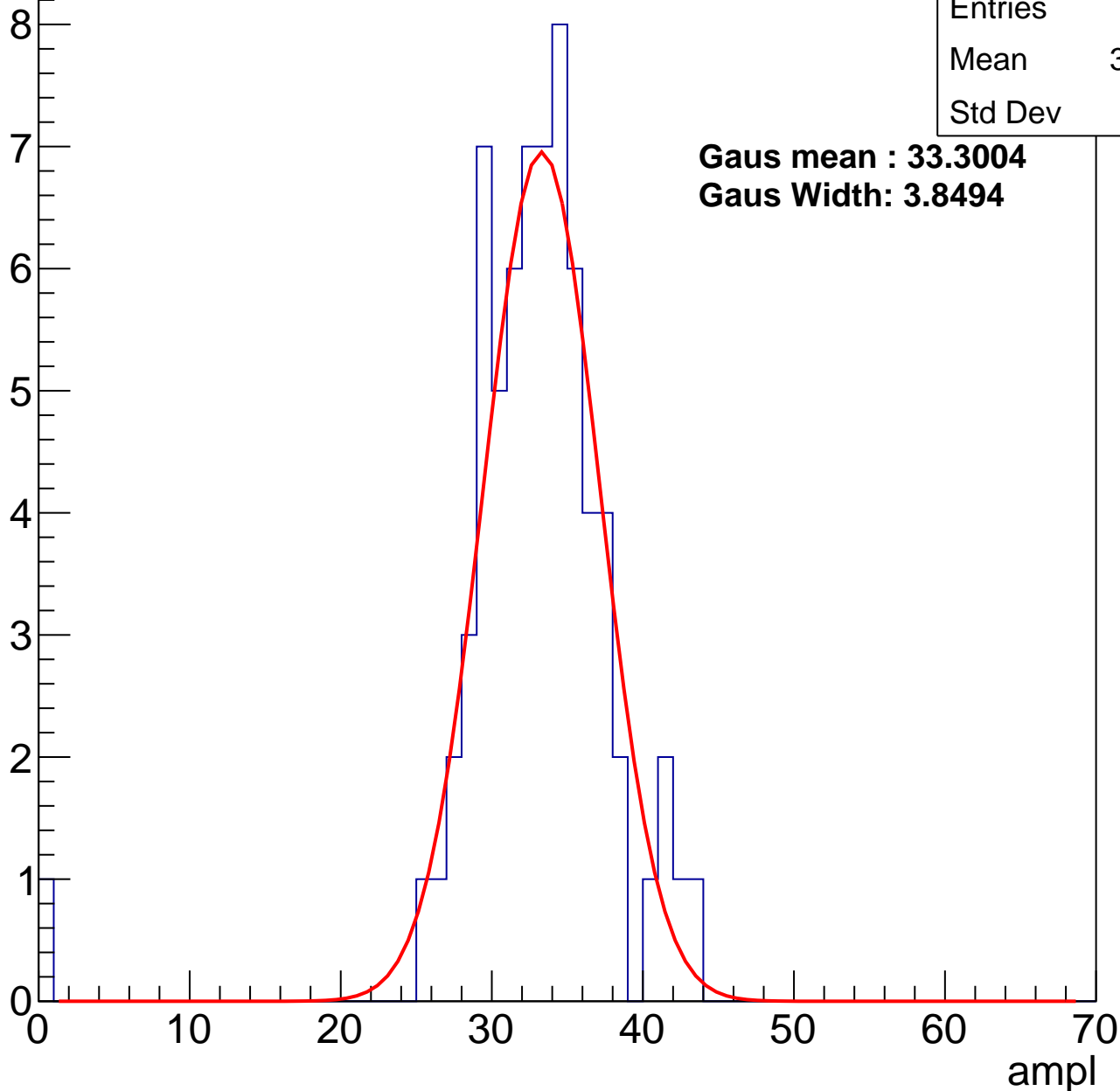
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	32.46
Std Dev	5.46

Gaus mean : 33.3004

Gaus Width: 3.8494



B0L001S, U22-ch50, adc1

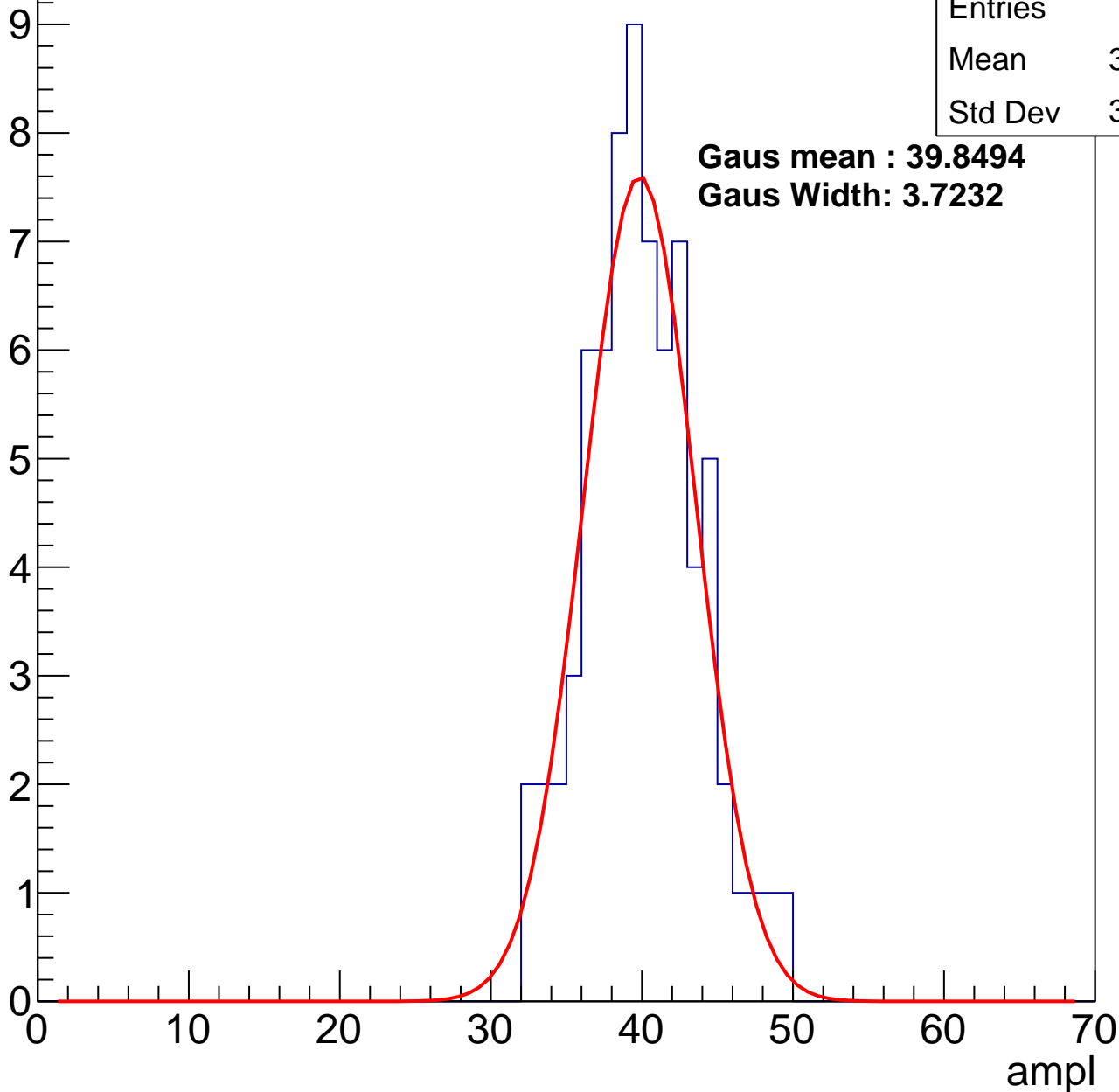
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	39.56
Std Dev	3.664

Gaus mean : 39.8494

Gaus Width: 3.7232



B0L001S, U22-ch50, adc2

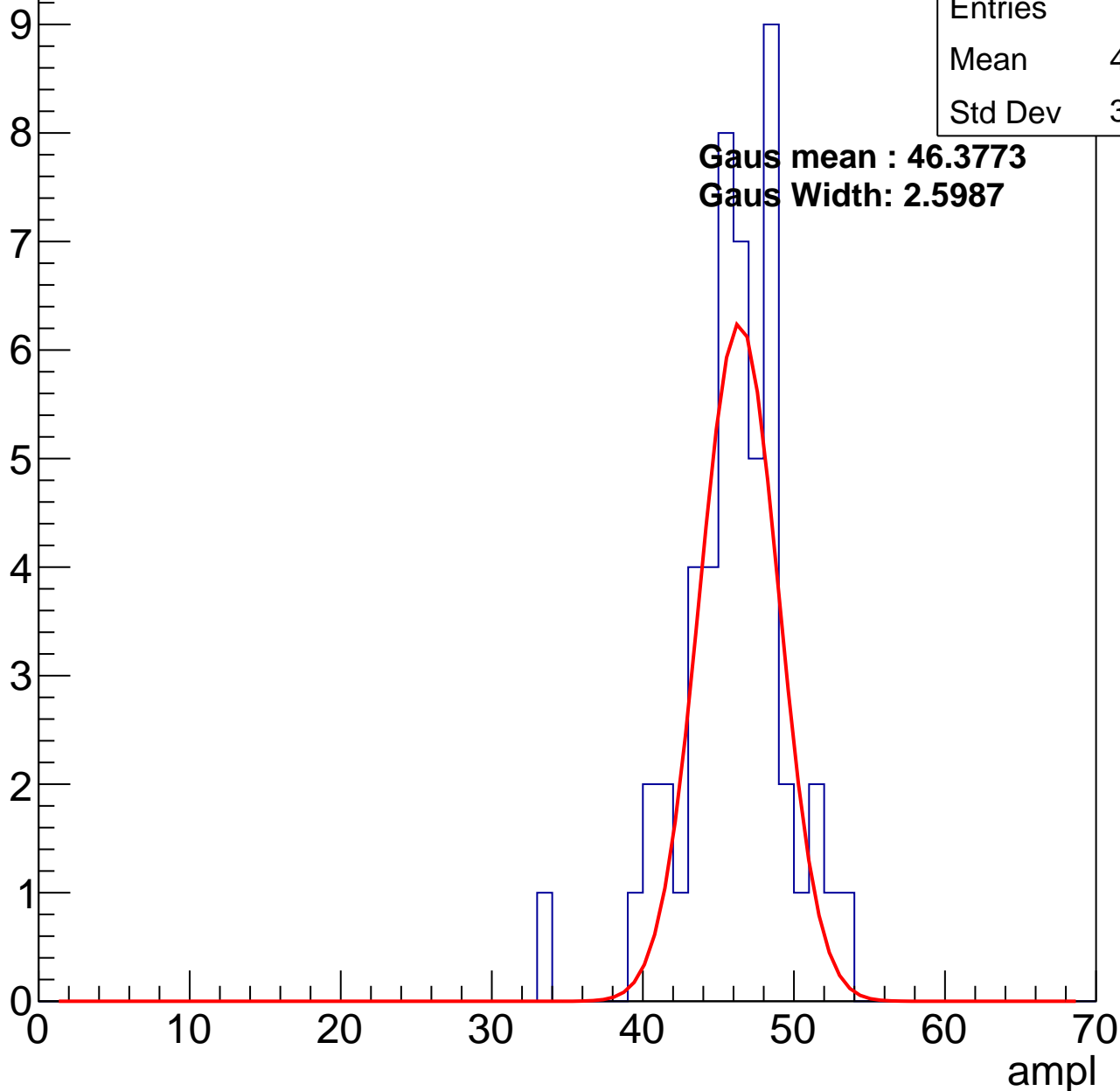
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	45.65
Std Dev	3.486

Gaus mean : 46.3773

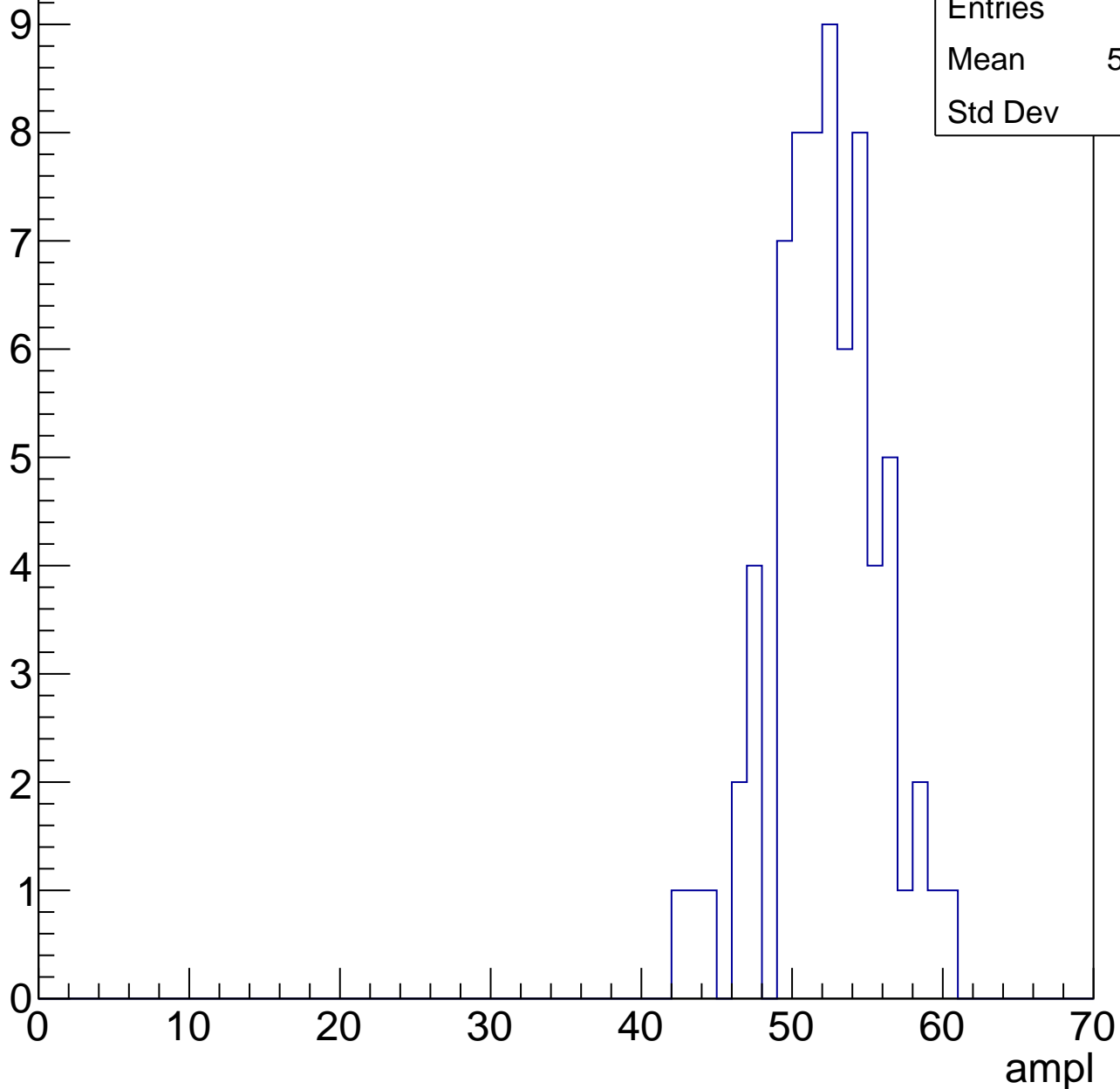
Gaus Width: 2.5987



B0L001S, U22-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

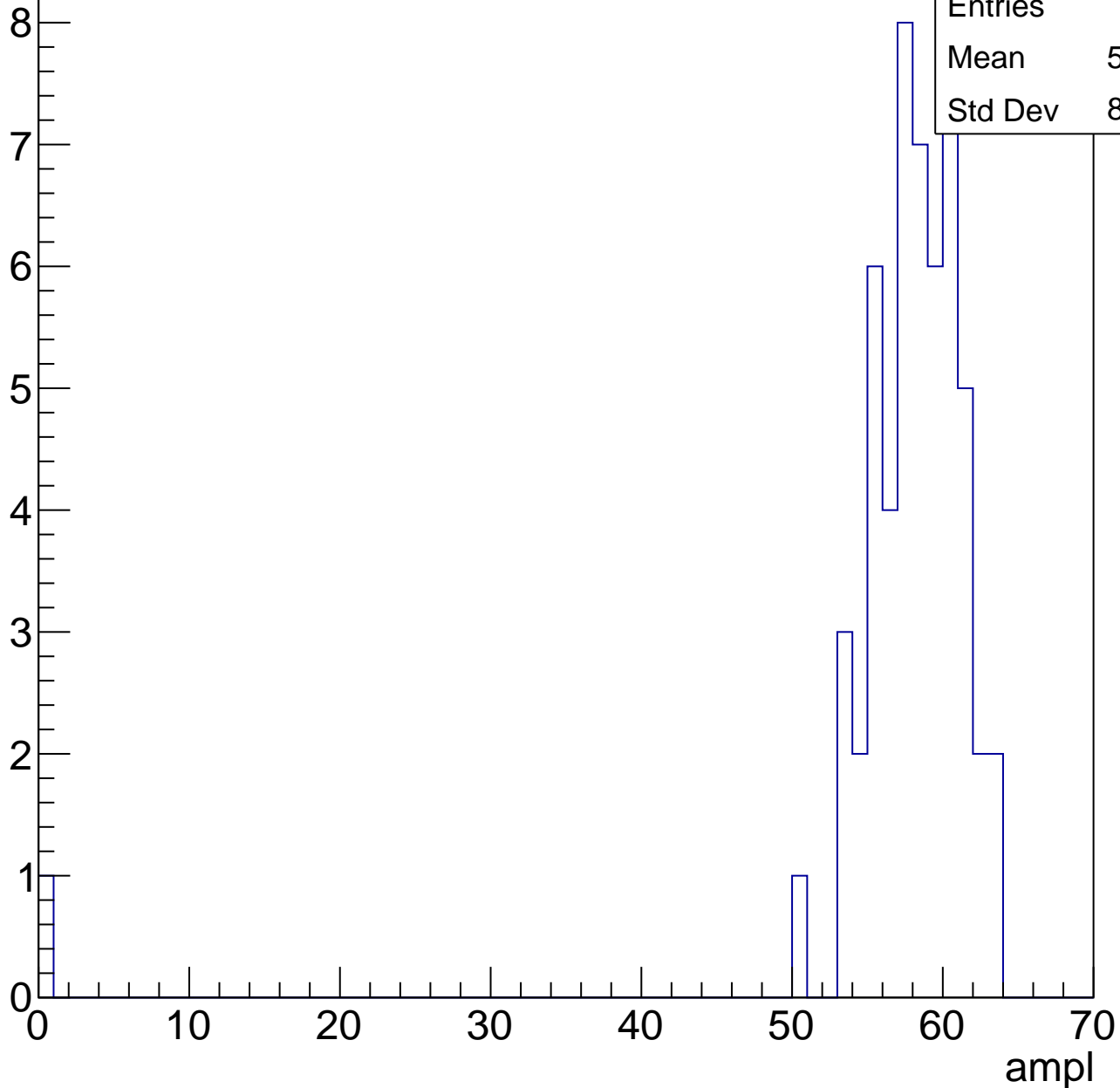
Entry



B0L001S, U22-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

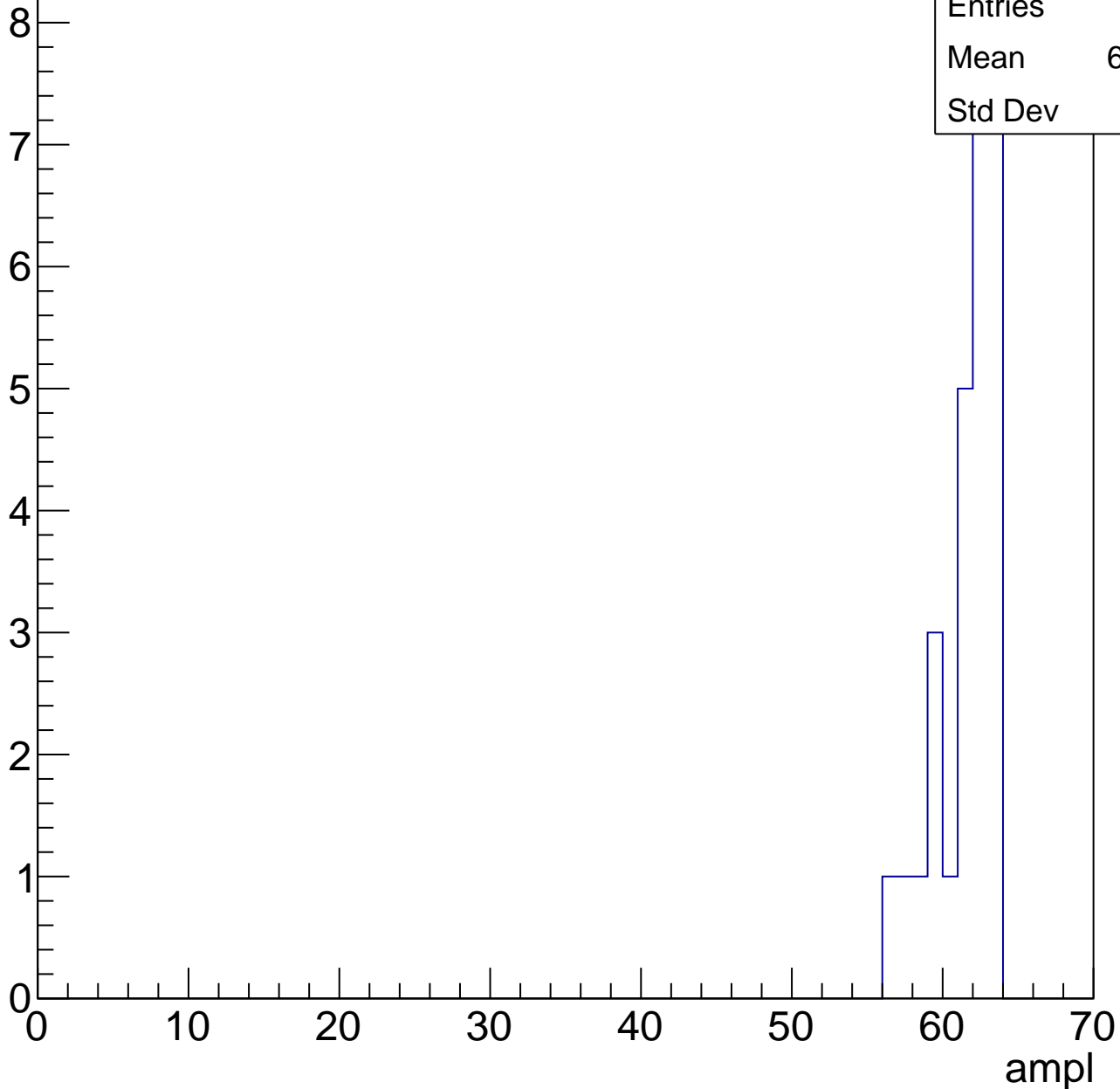


B0L001S, U22-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	61.18
Std Dev	1.91



B0L001S, U22-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch51, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	29.43
Std Dev	6.568

Gaus mean : 30.9535

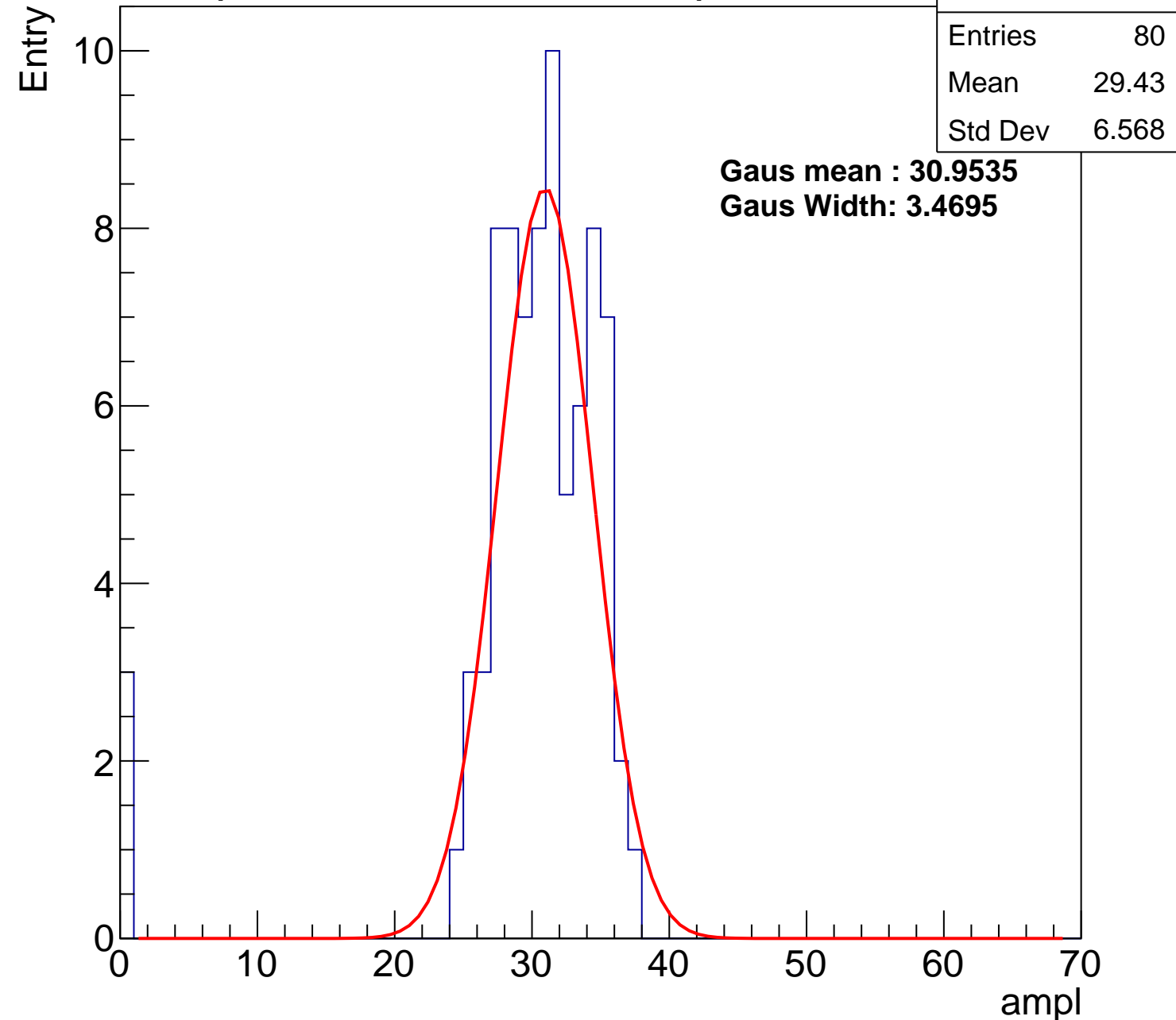
Gaus Width: 3.4695

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



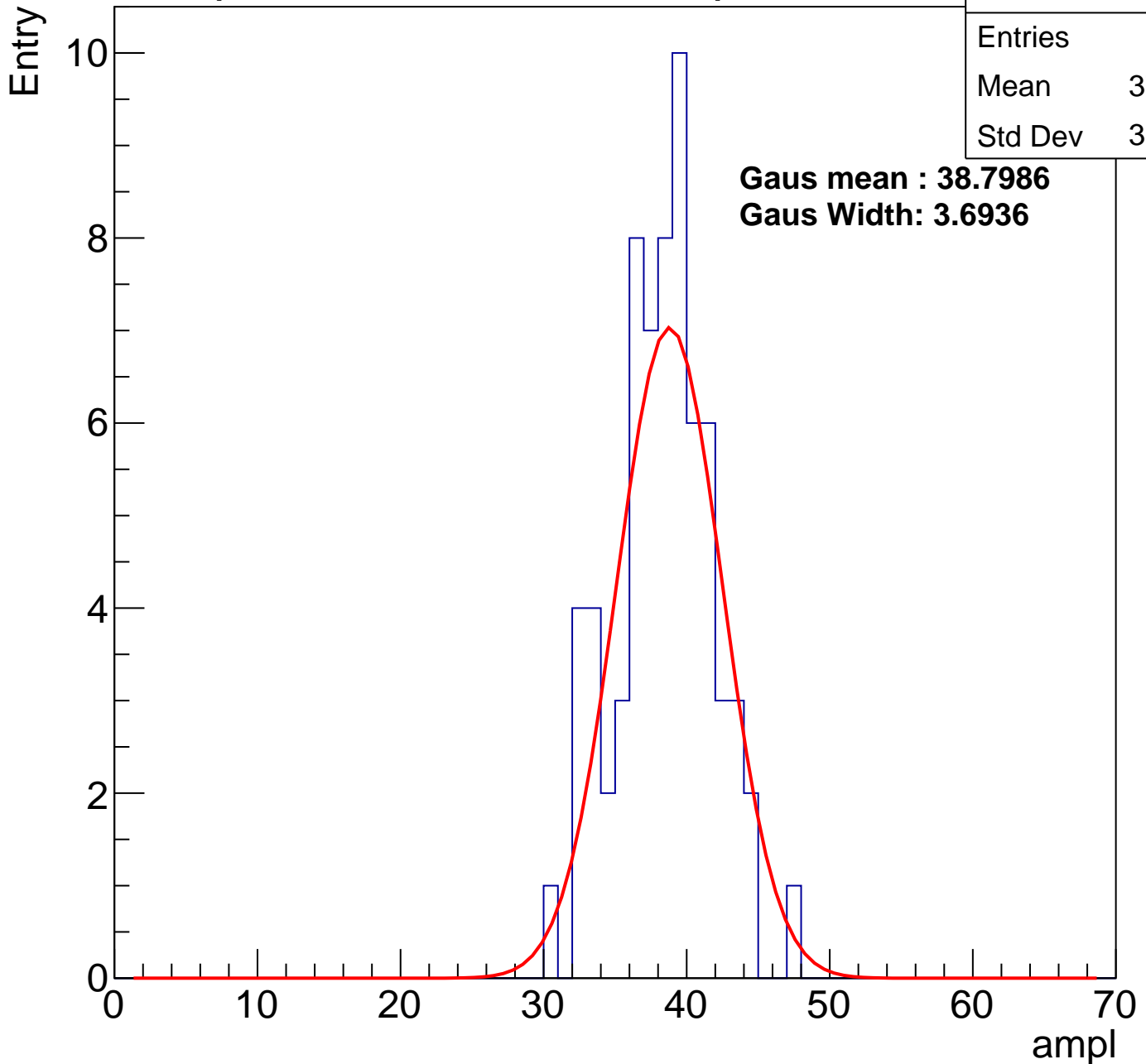
B0L001S, U22-ch51, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	37.94
Std Dev	3.365

Gaus mean : 38.7986

Gaus Width: 3.6936



B0L001S, U22-ch51, adc2

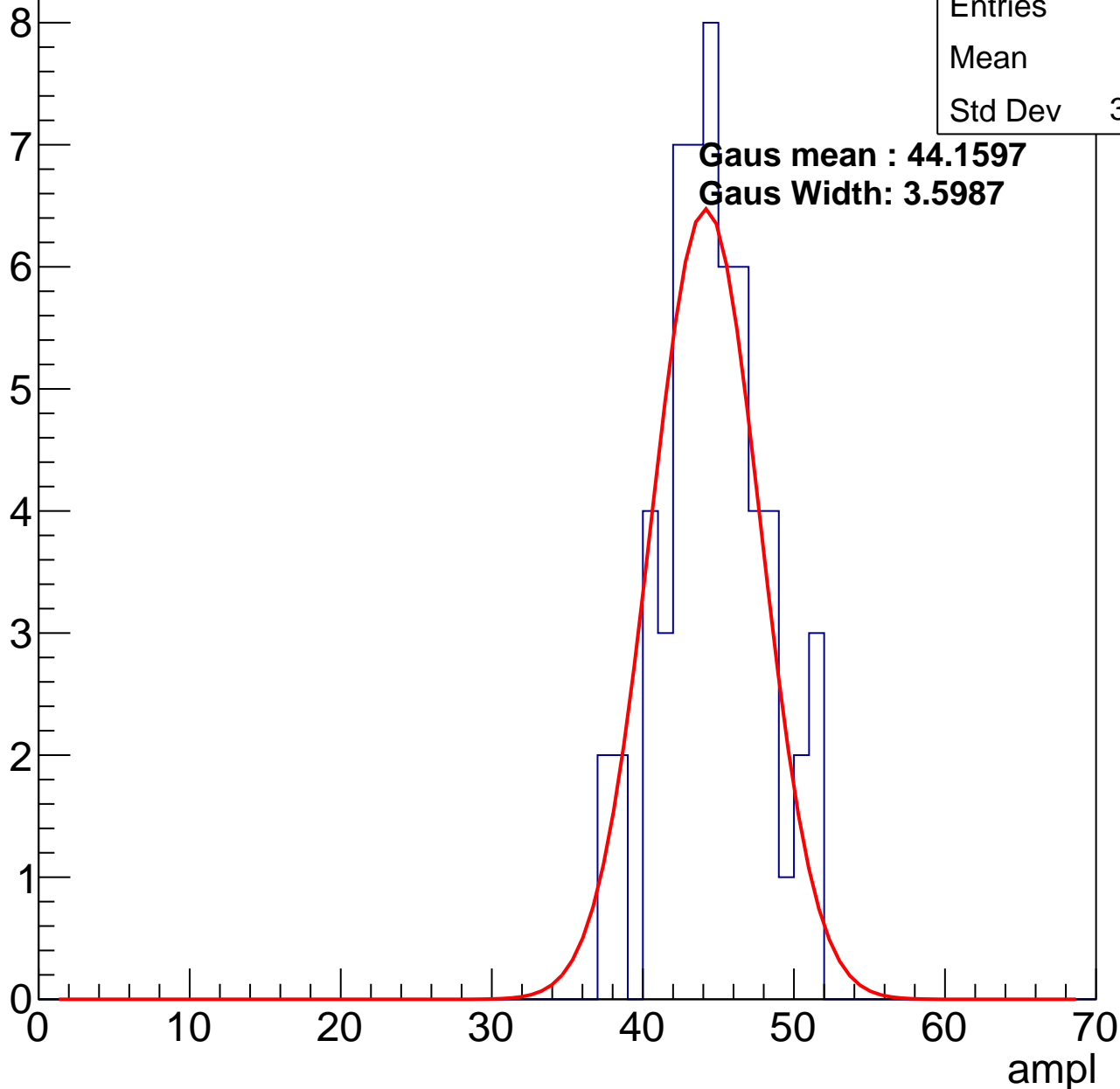
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.2
Std Dev	3.364

Gaus mean : 44.1597

Gaus Width: 3.5987

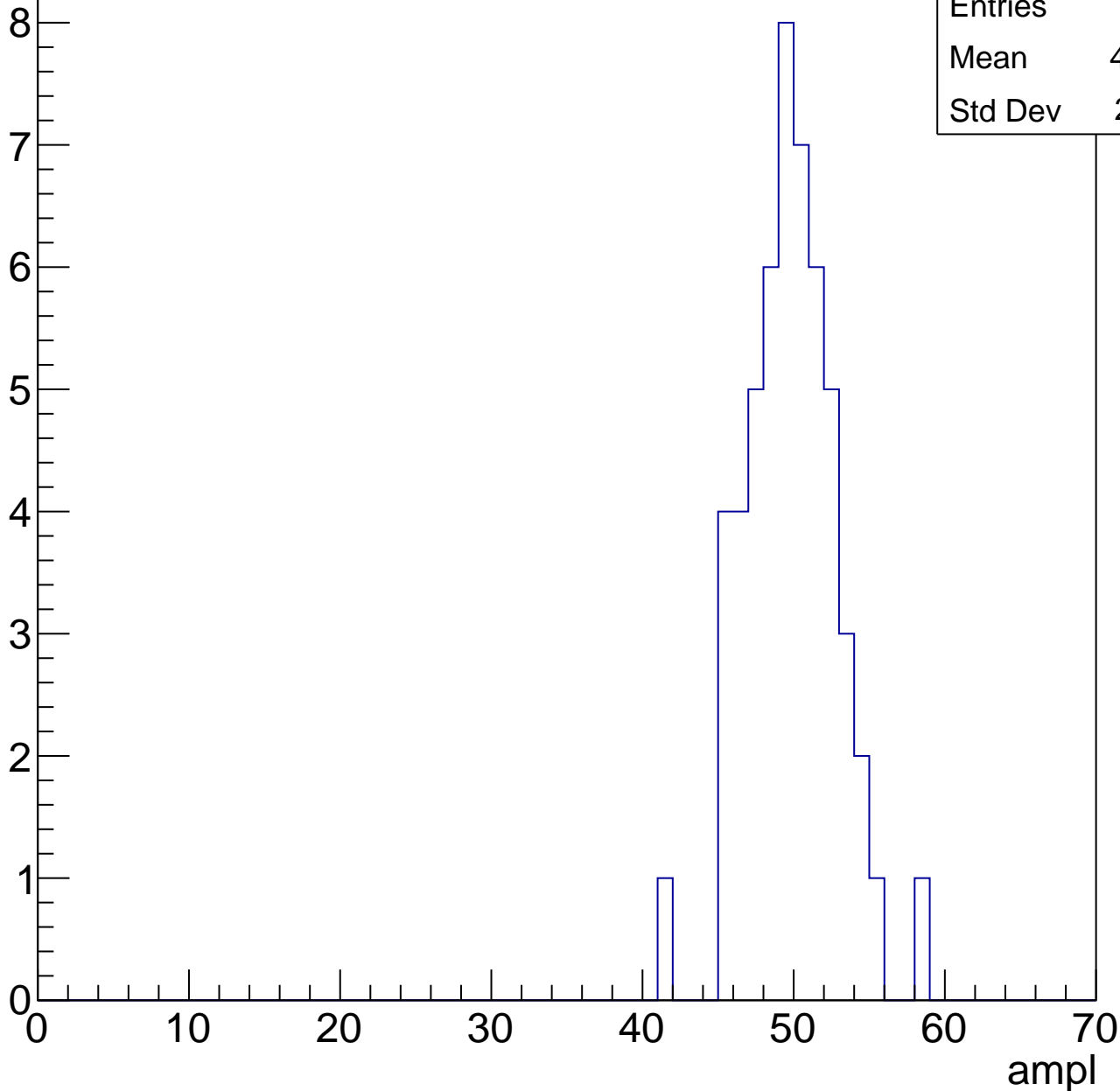


B0L001S, U22-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	49.36
Std Dev	2.991



B0L001S, U22-ch51, adc4

calib_packv5_042523_0143.root, FC#9, port A1

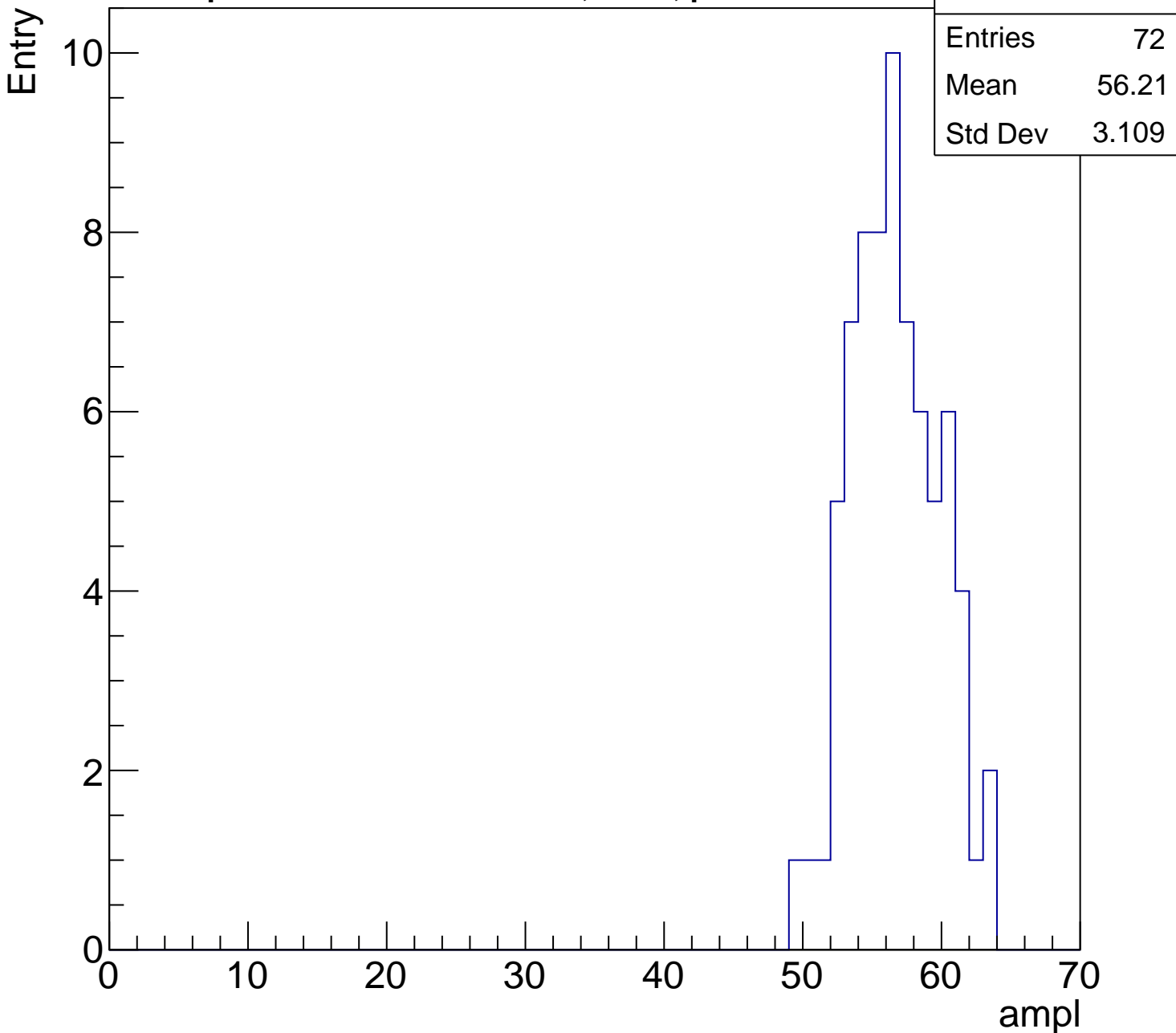
Entries	72
Mean	56.21
Std Dev	3.109

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

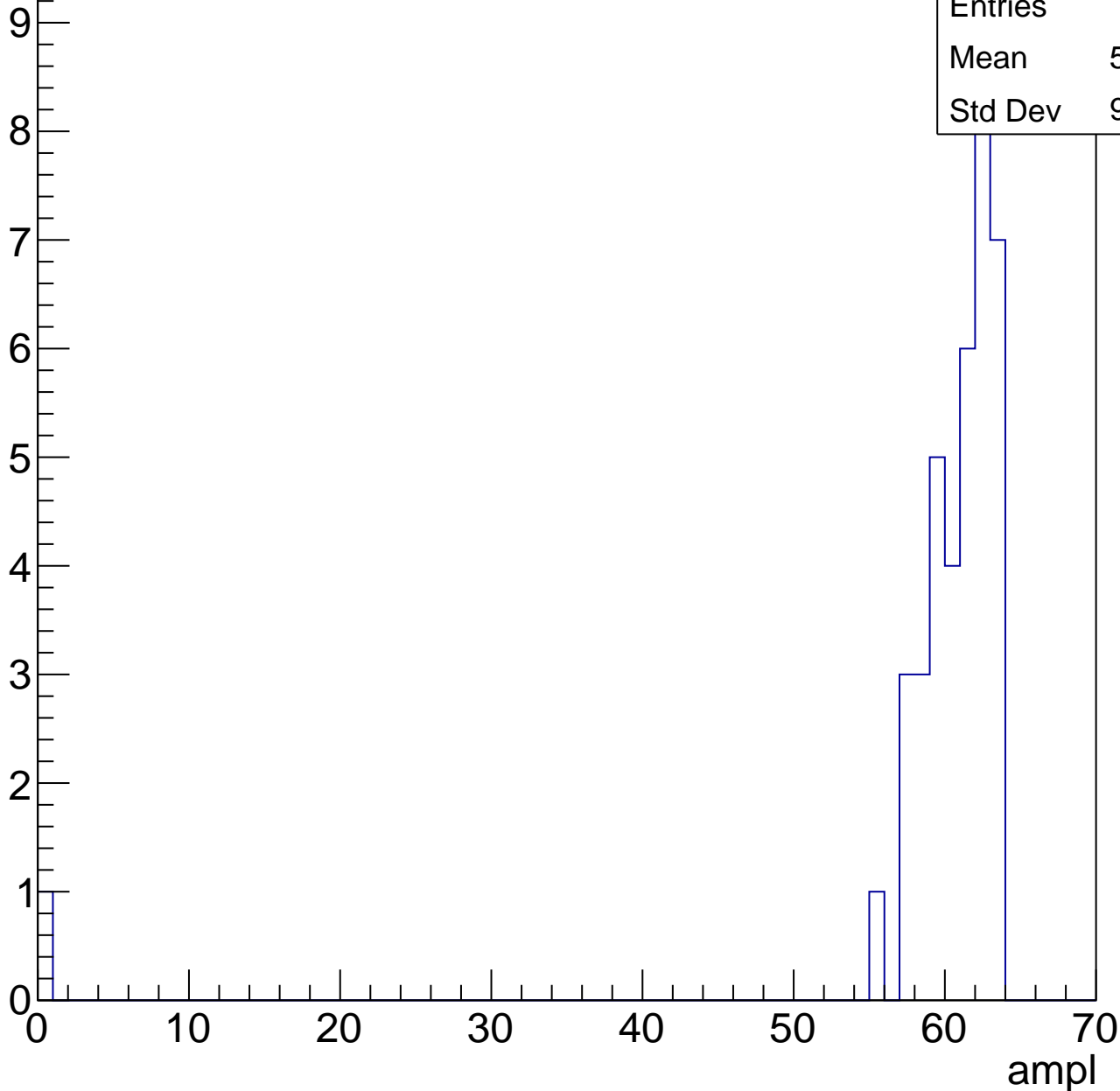


B0L001S, U22-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	58.97
Std Dev	9.784



B0L001S, U22-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U22-ch52, adc0

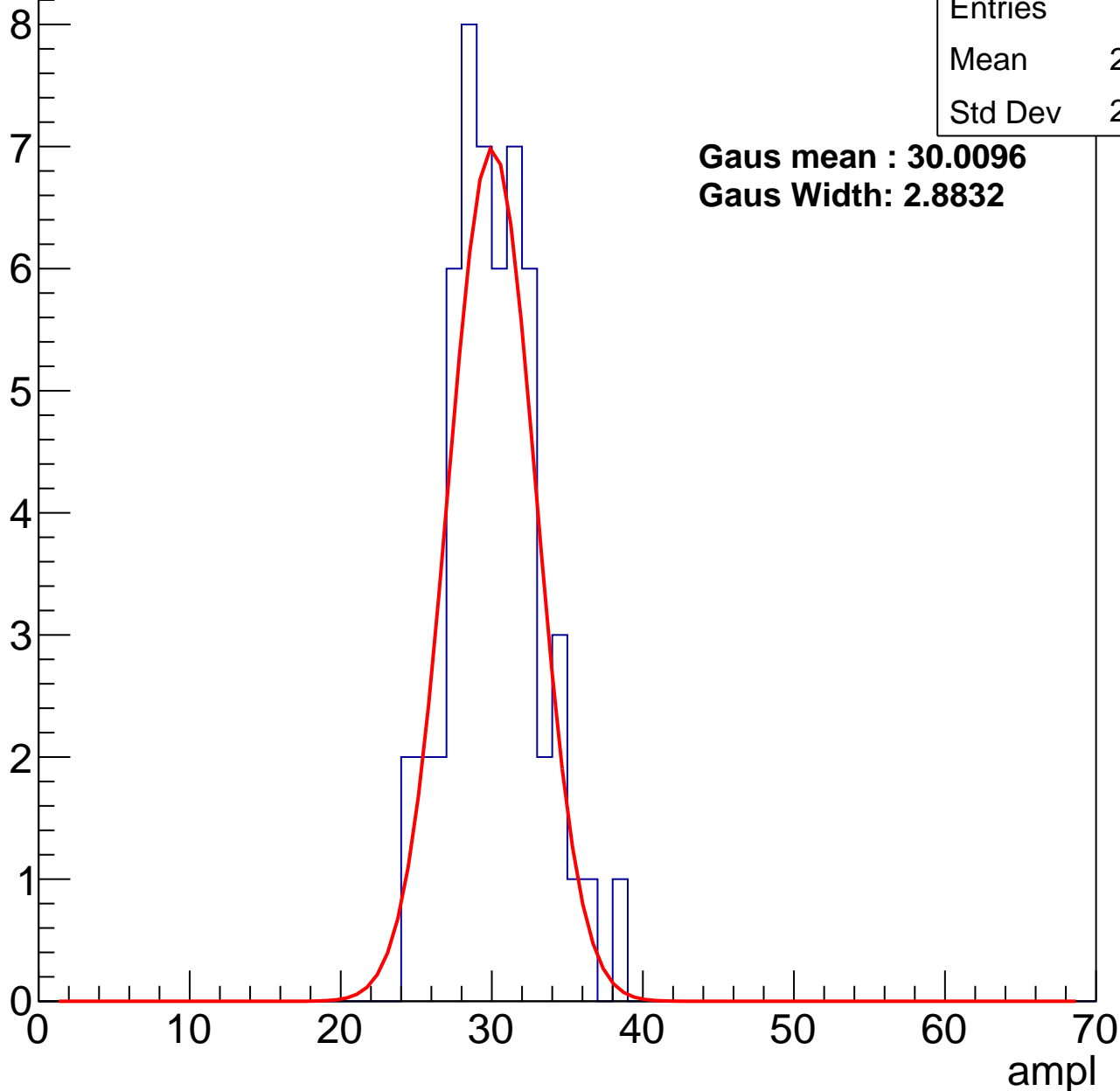
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	29.72
Std Dev	2.934

Gaus mean : 30.0096

Gaus Width: 2.8832



B0L001S, U22-ch52, adc1

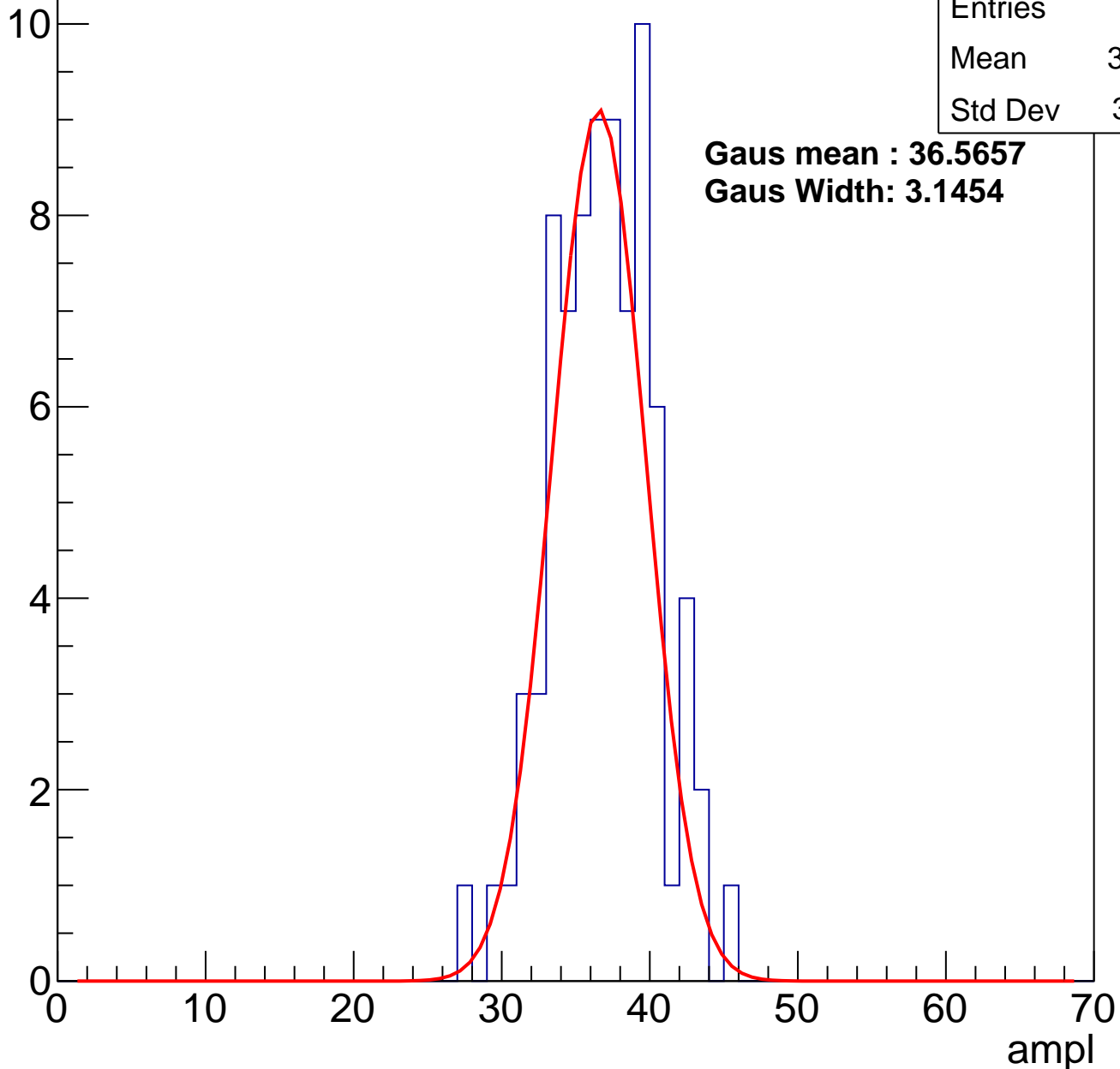
calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	36.42
Std Dev	3.431

Gaus mean : 36.5657

Gaus Width: 3.1454

Entry



B0L001S, U22-ch52, adc2

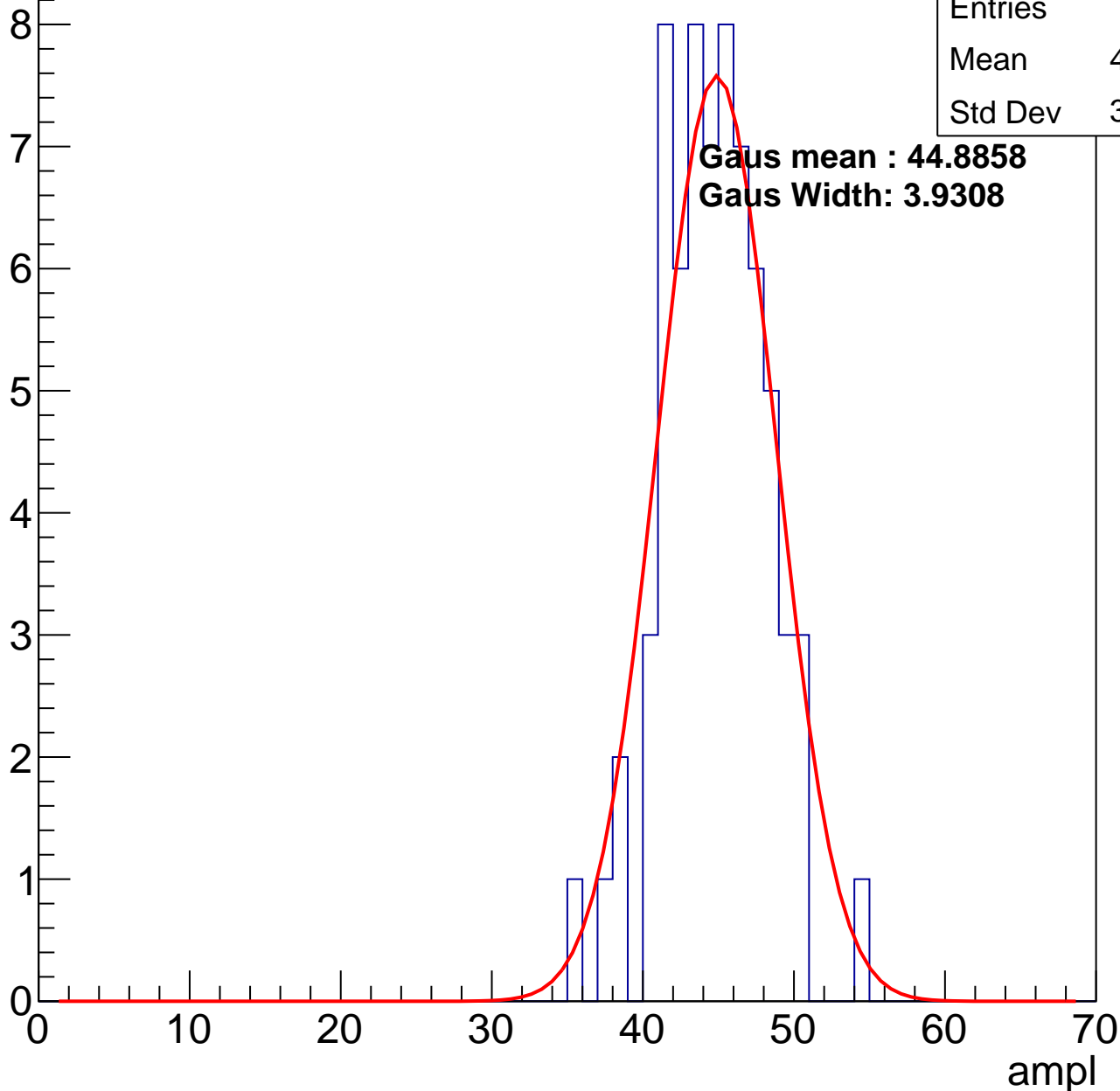
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.28
Std Dev	3.409

Gaus mean : 44.8858

Gaus Width: 3.9308

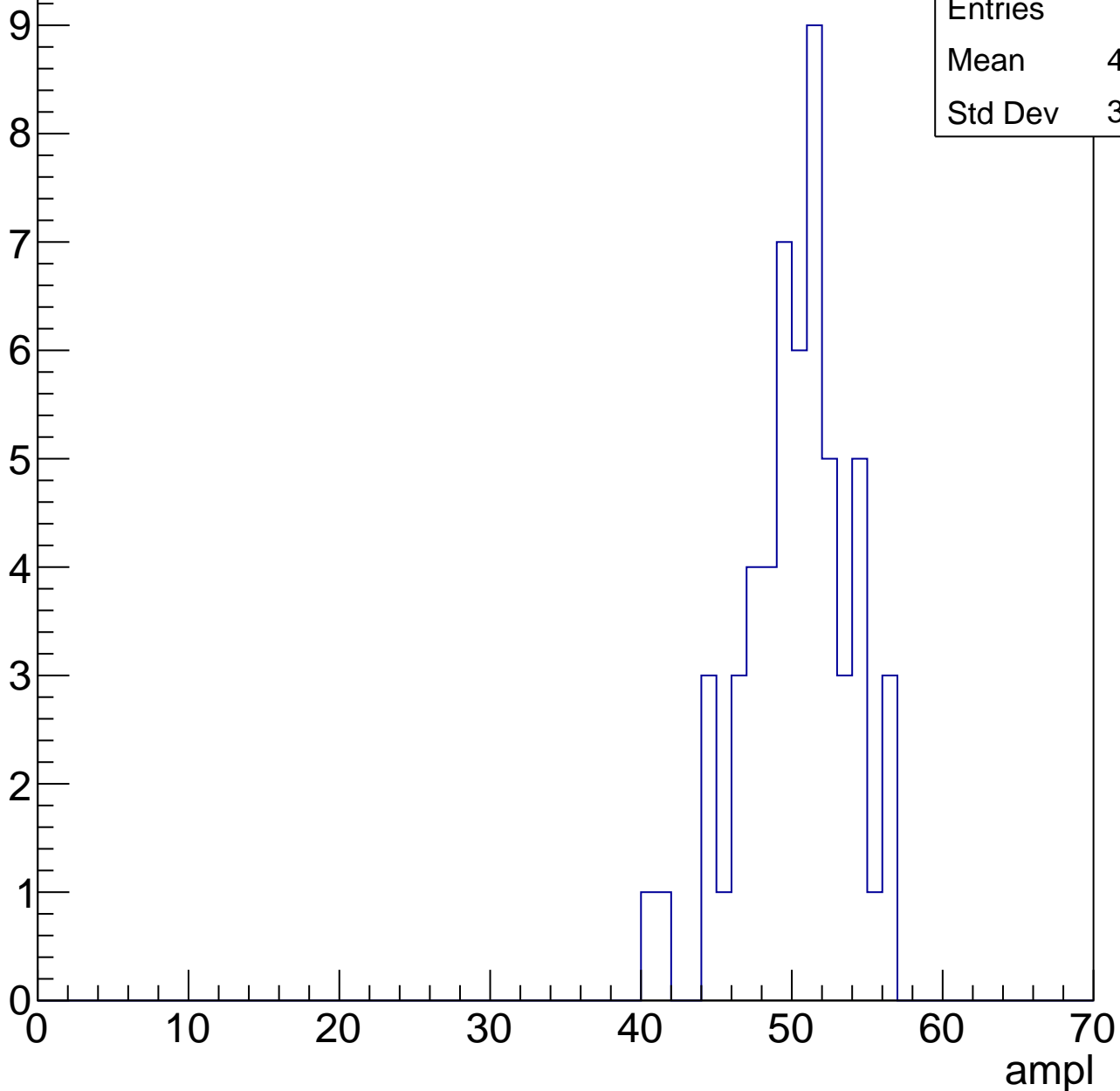


B0L001S, U22-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	49.82
Std Dev	3.506

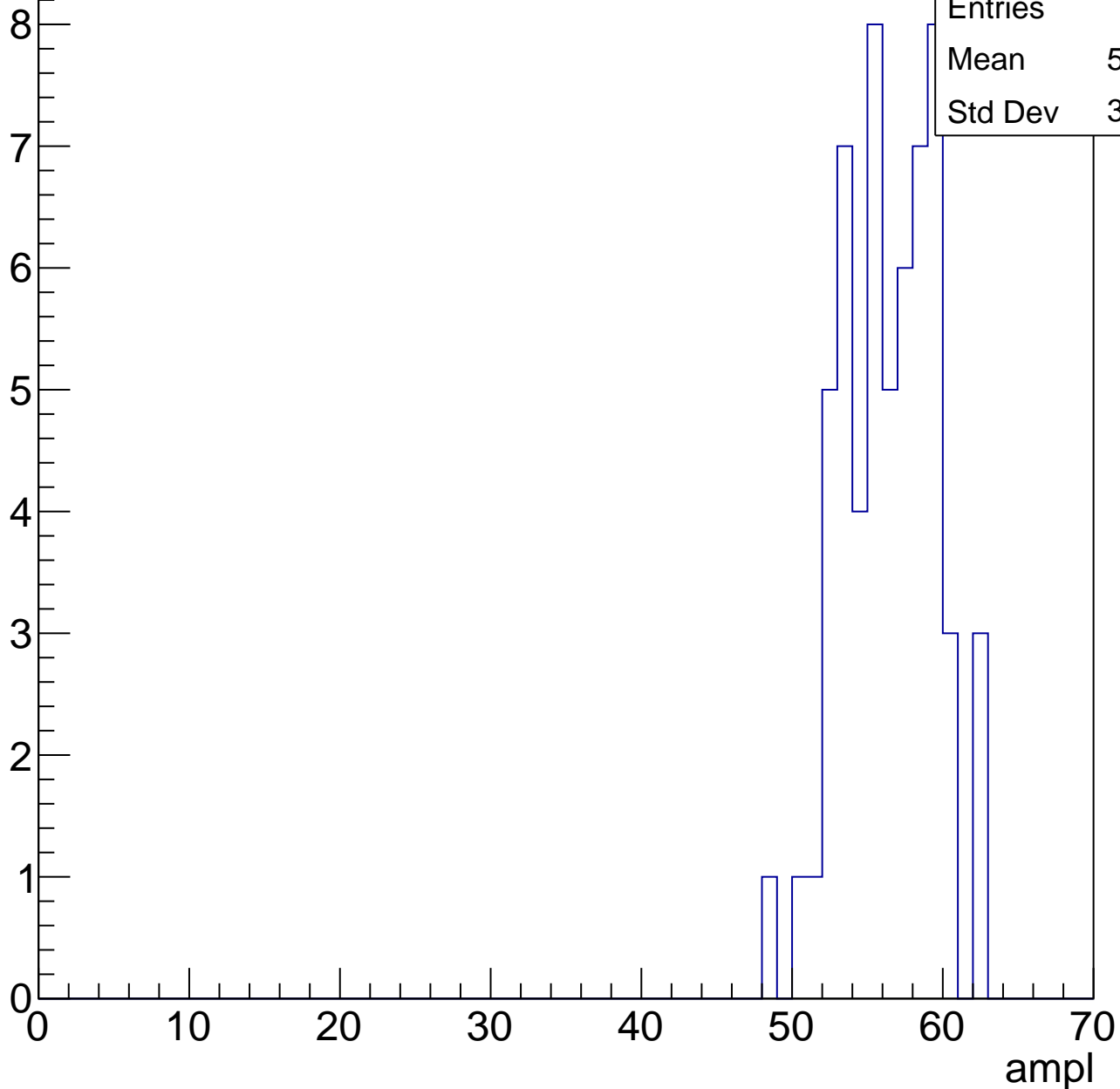


B0L001S, U22-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.97
Std Dev	3.064

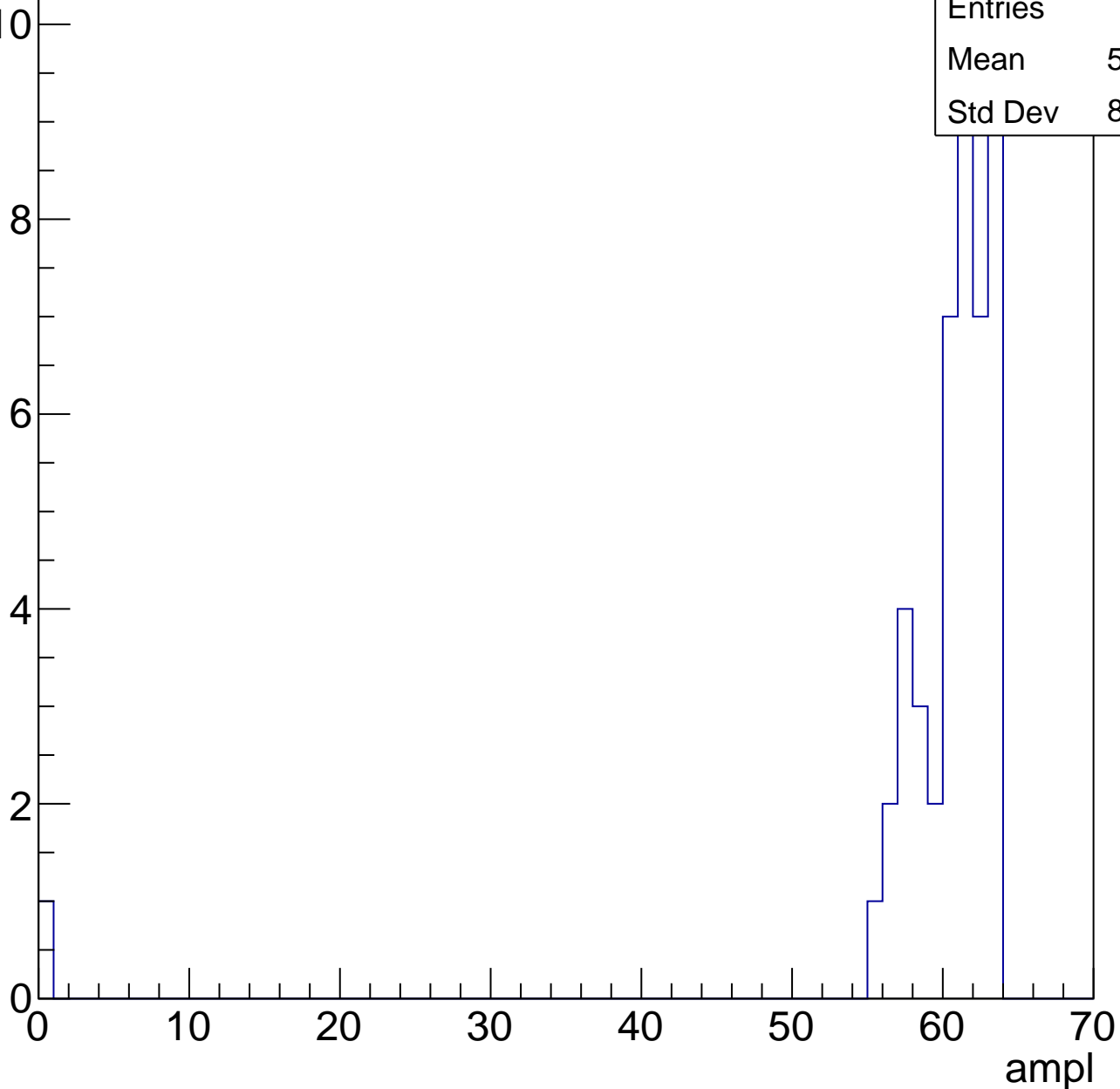


B0L001S, U22-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	59.17
Std Dev	8.995



B0L001S, U22-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U22-ch53, adc0

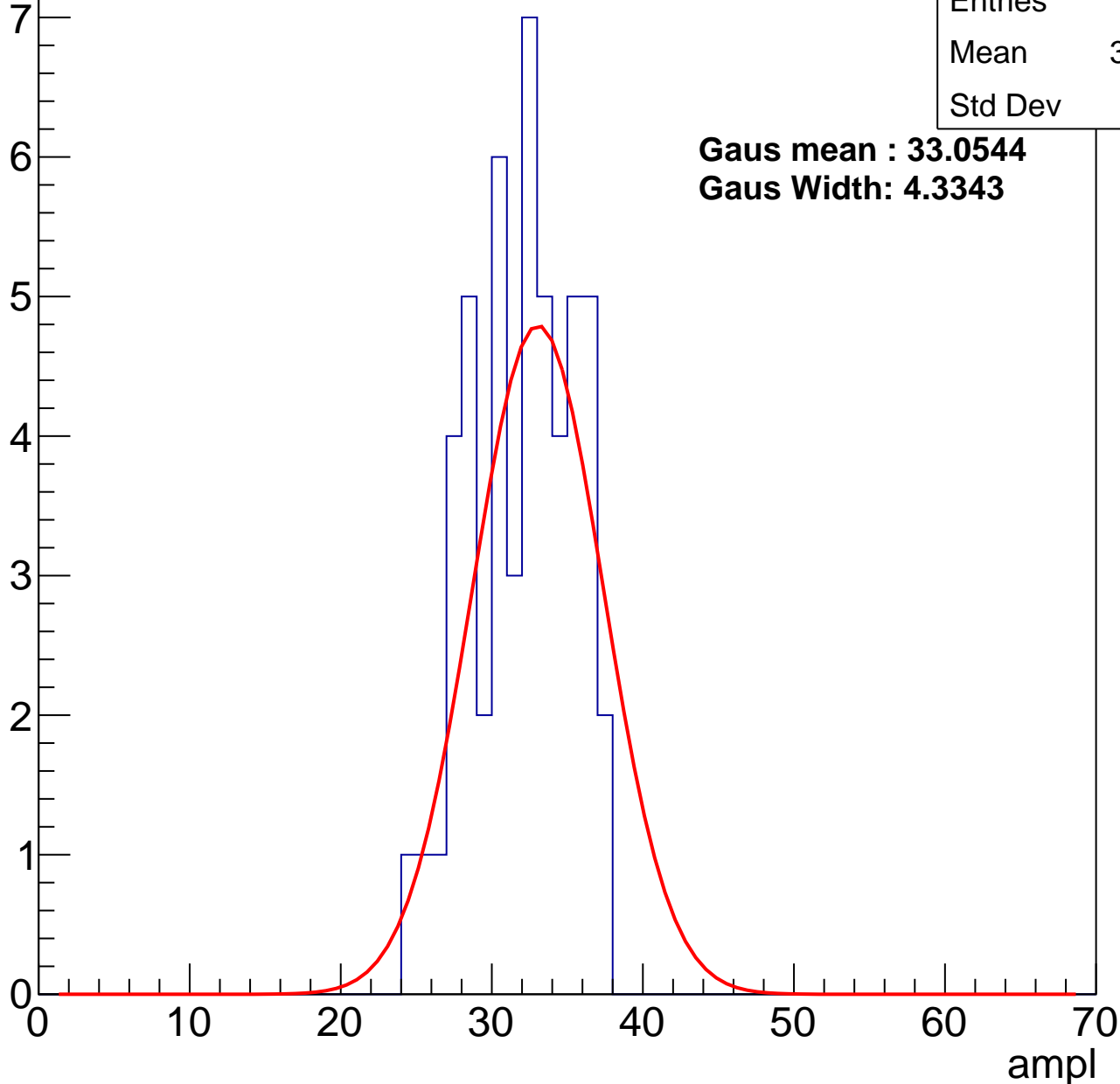
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	31.53
Std Dev	3.31

Gaus mean : 33.0544

Gaus Width: 4.3343



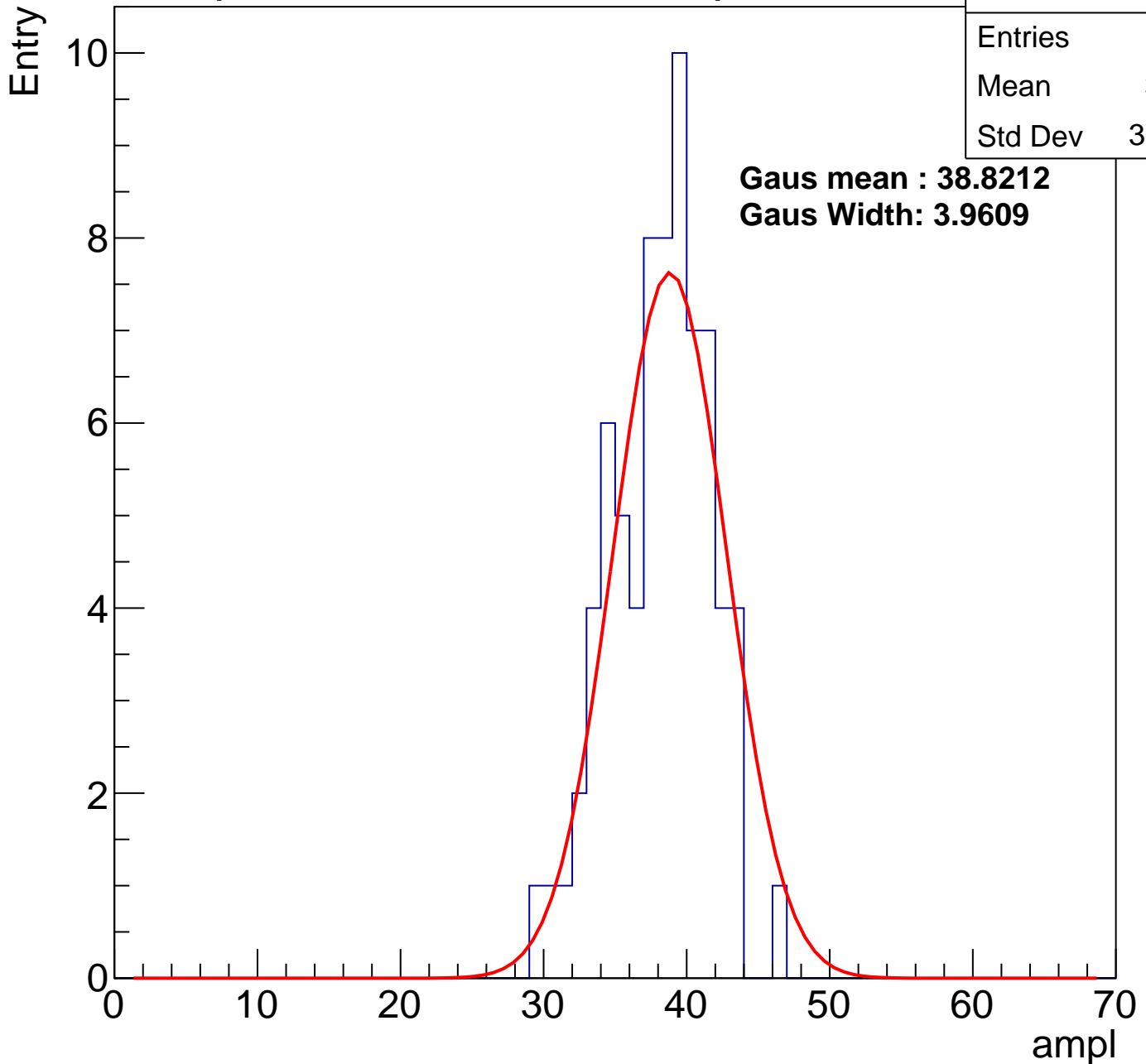
B0L001S, U22-ch53, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	37.7
Std Dev	3.419

Gaus mean : 38.8212

Gaus Width: 3.9609



B0L001S, U22-ch53, adc2

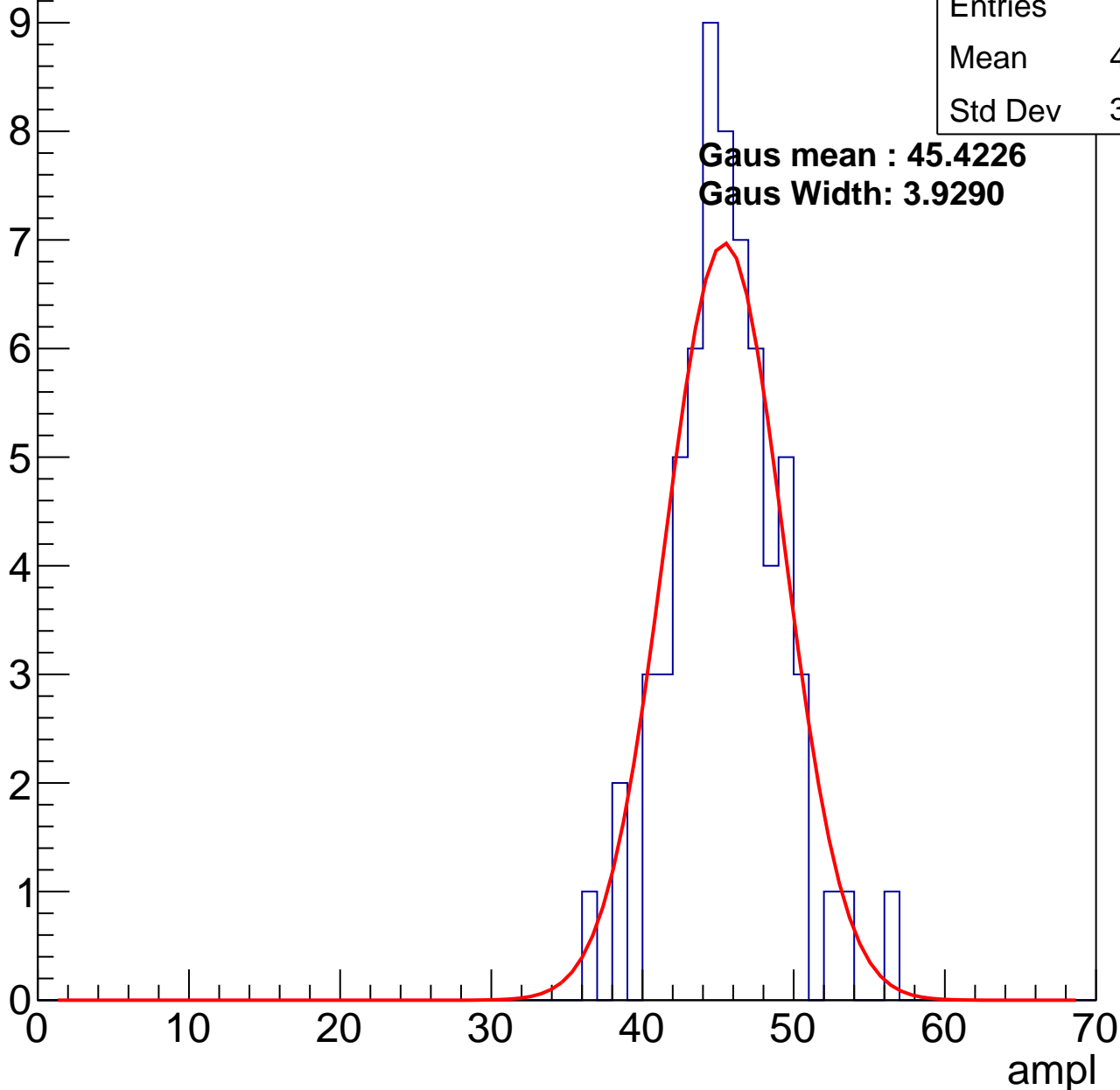
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.09
Std Dev	3.585

Gaus mean : 45.4226

Gaus Width: 3.9290

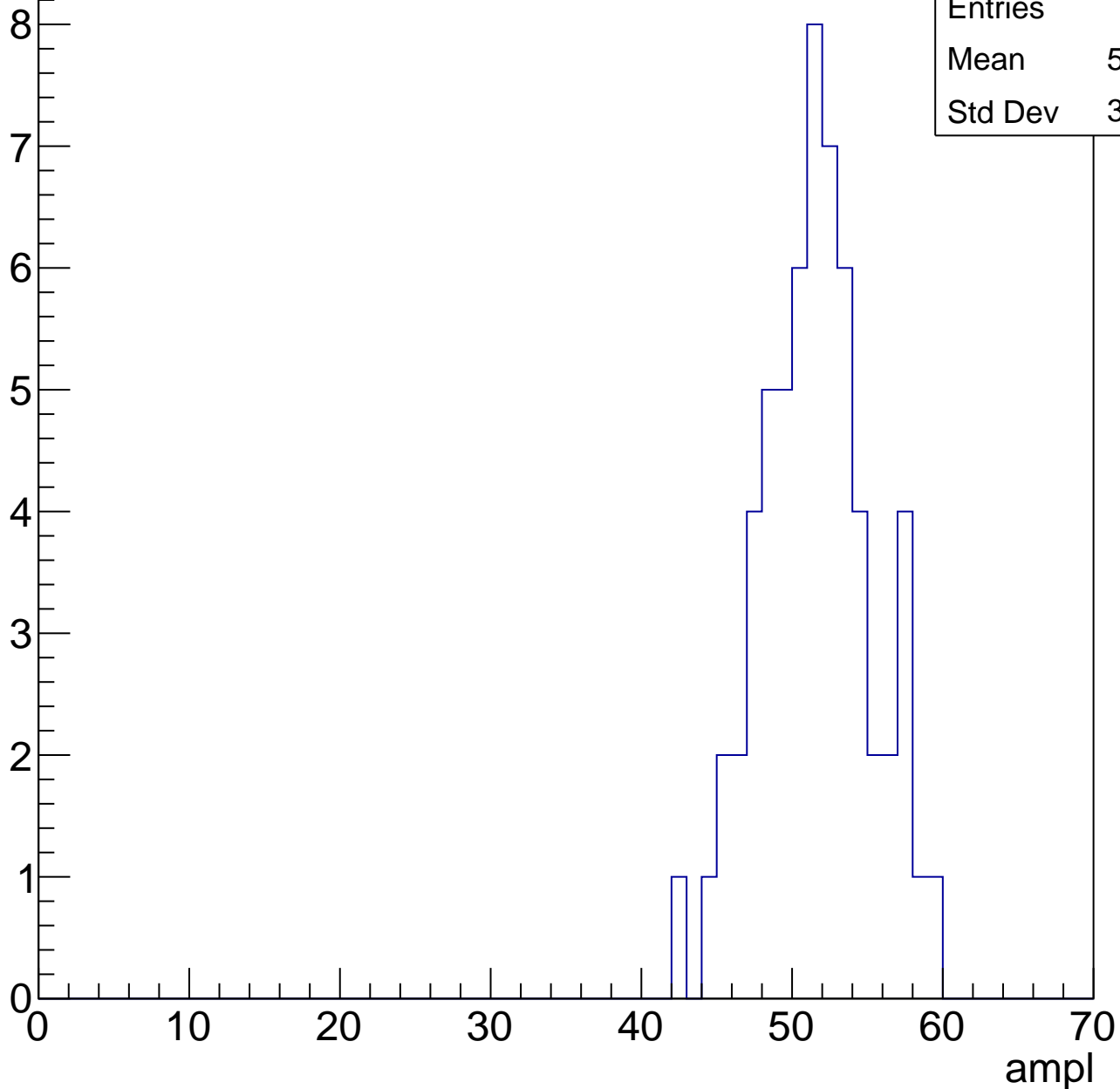


B0L001S, U22-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	51.05
Std Dev	3.605

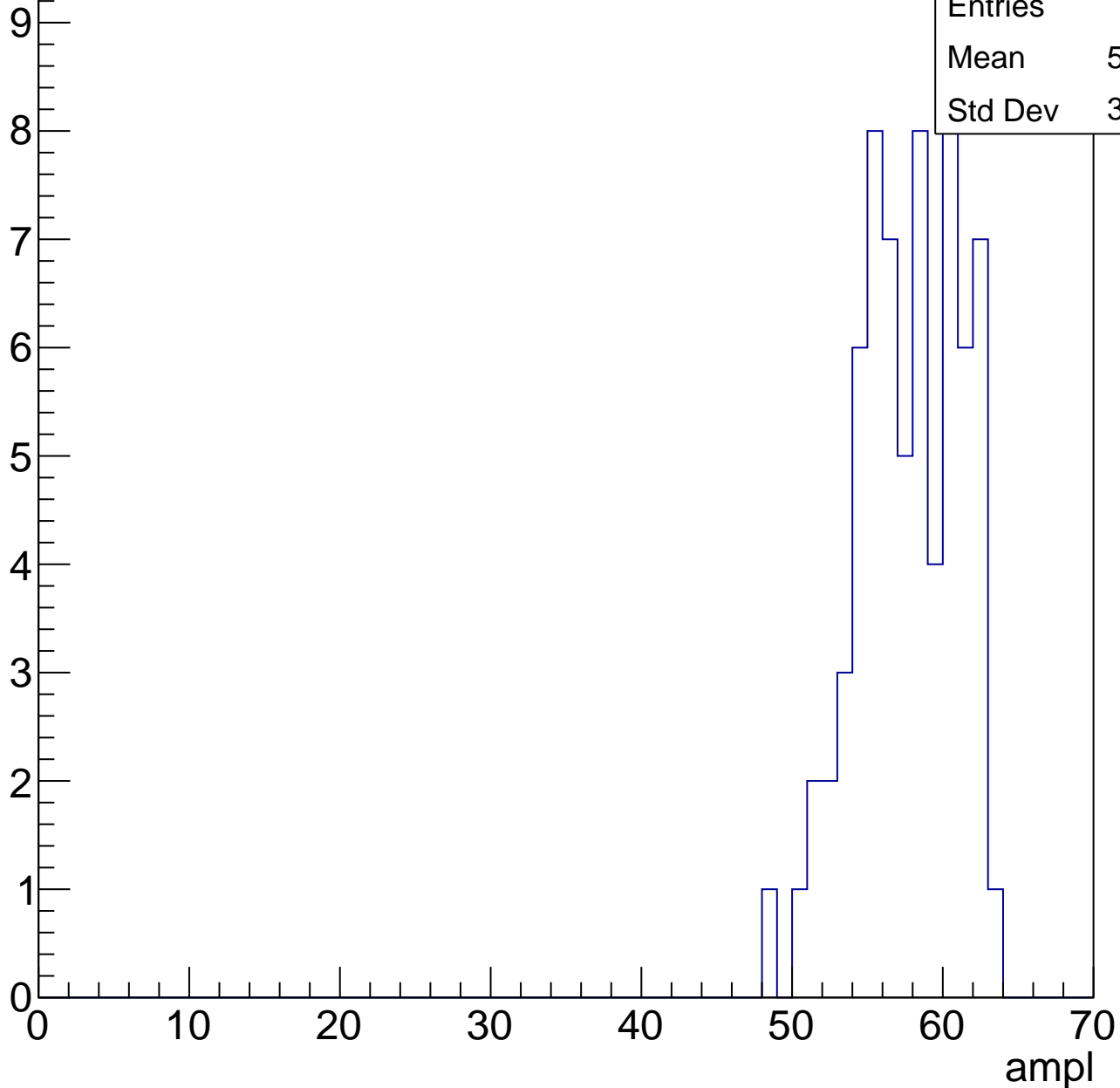


B0L001S, U22-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	57.24
Std Dev	3.382

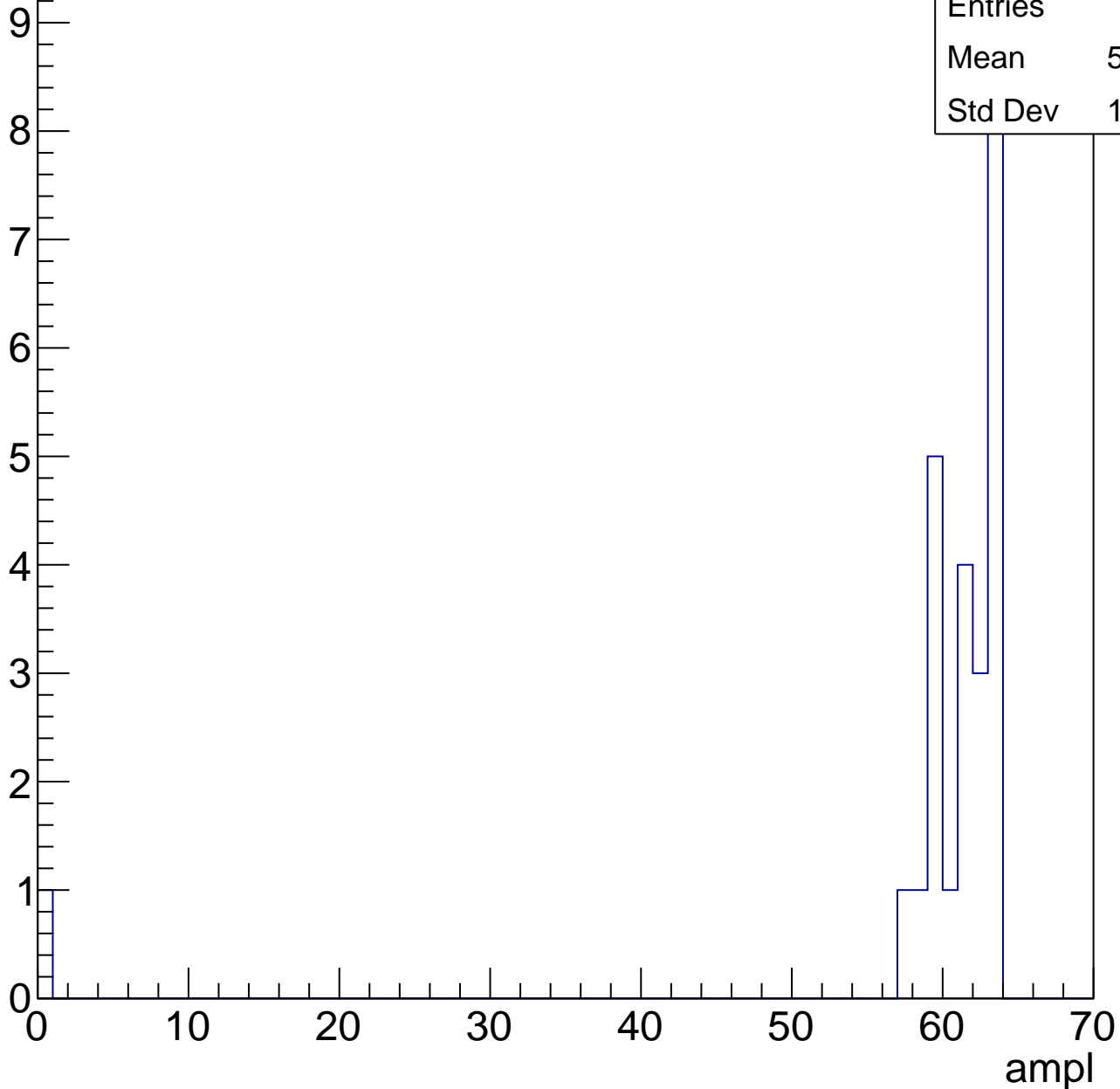


B0L001S, U22-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.68
Std Dev	12.12



B0L001S, U22-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch54, adc0

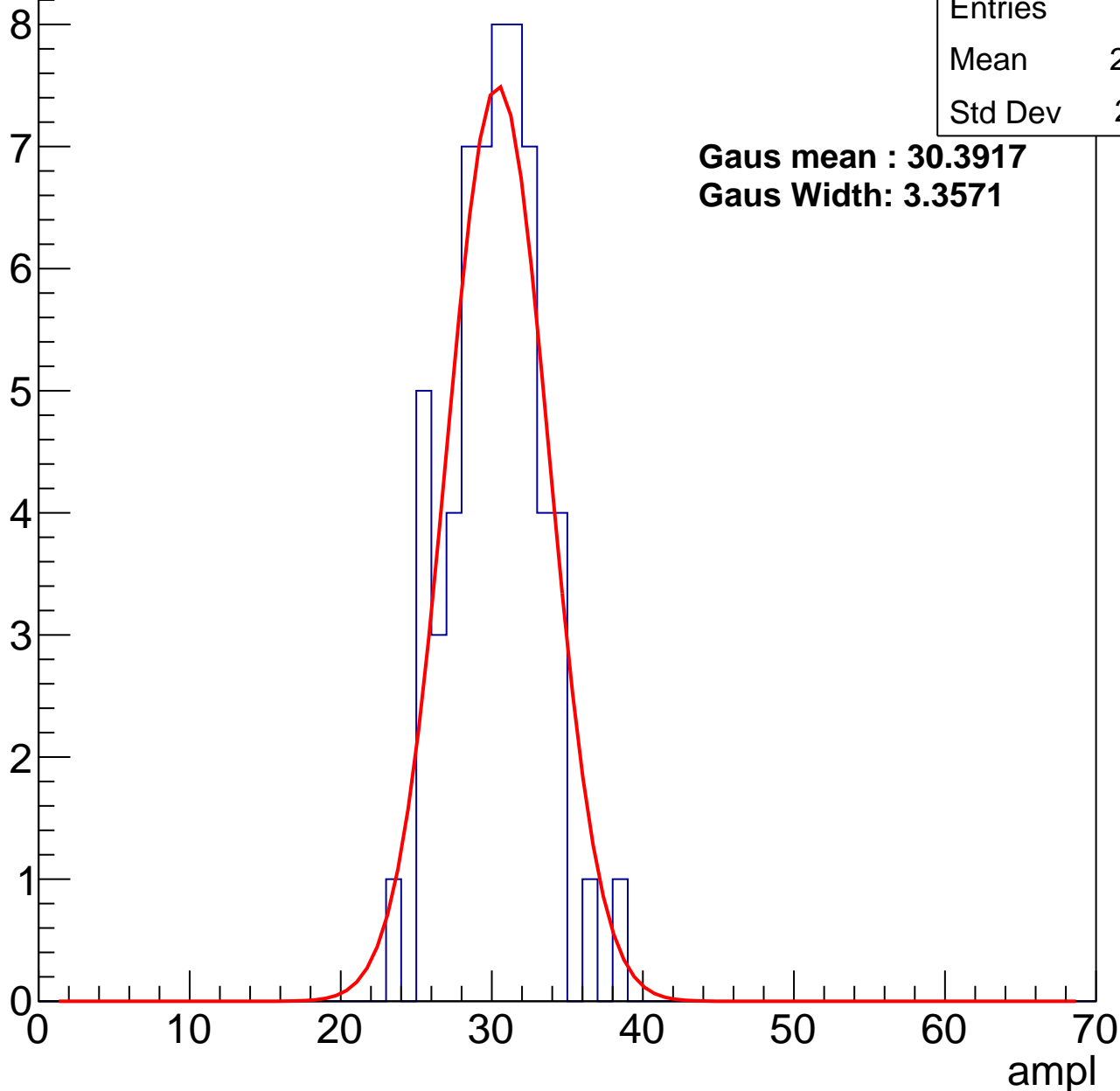
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.78
Std Dev	2.961

Gaus mean : 30.3917

Gaus Width: 3.3571



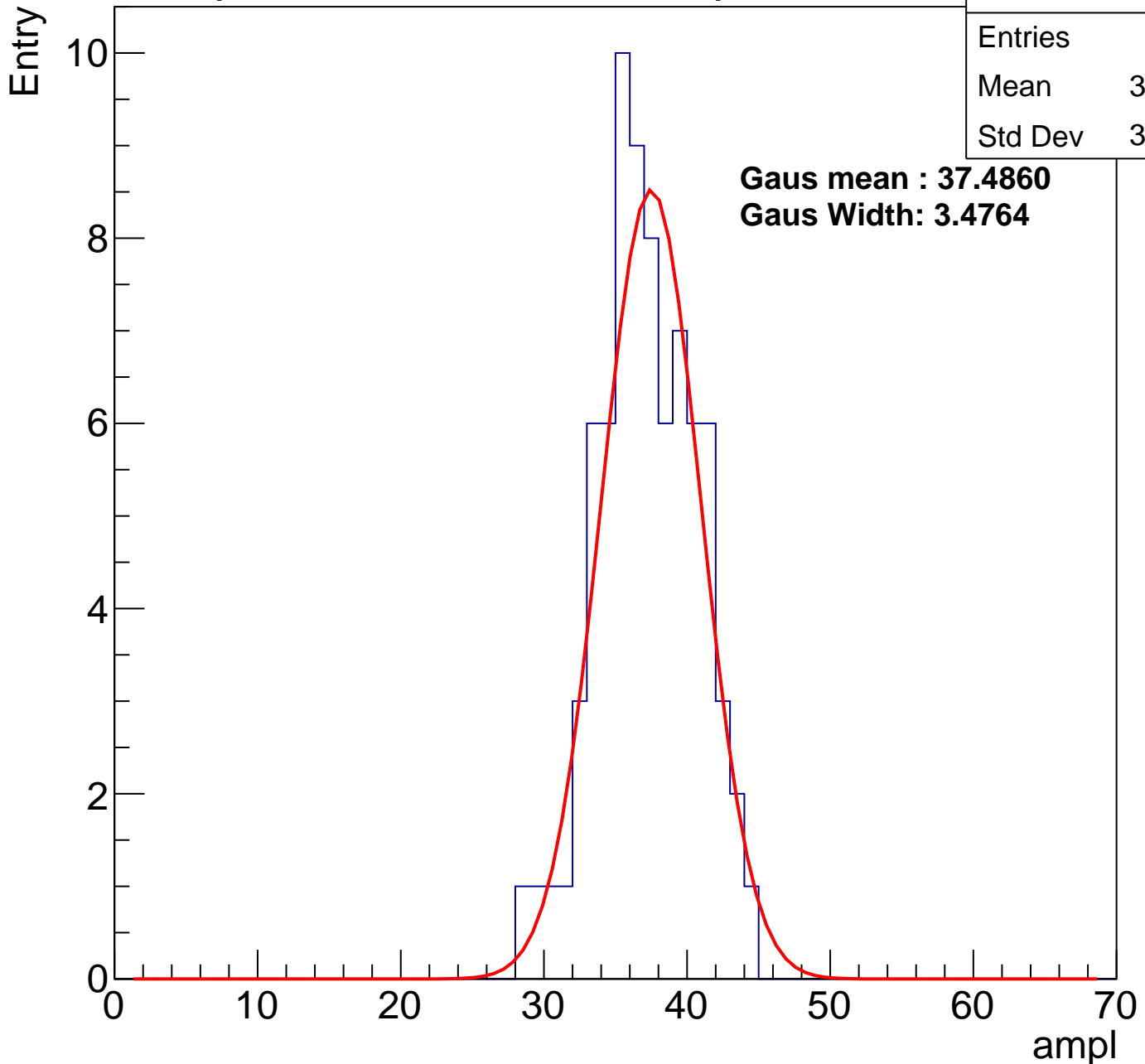
B0L001S, U22-ch54, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	36.74
Std Dev	3.378

Gaus mean : 37.4860

Gaus Width: 3.4764



B0L001S, U22-ch54, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	44.03
Std Dev	3.443

Gaus mean : 44.3863

Gaus Width: 4.1737

Entry

10

8

6

4

2

0

0

10

20

30

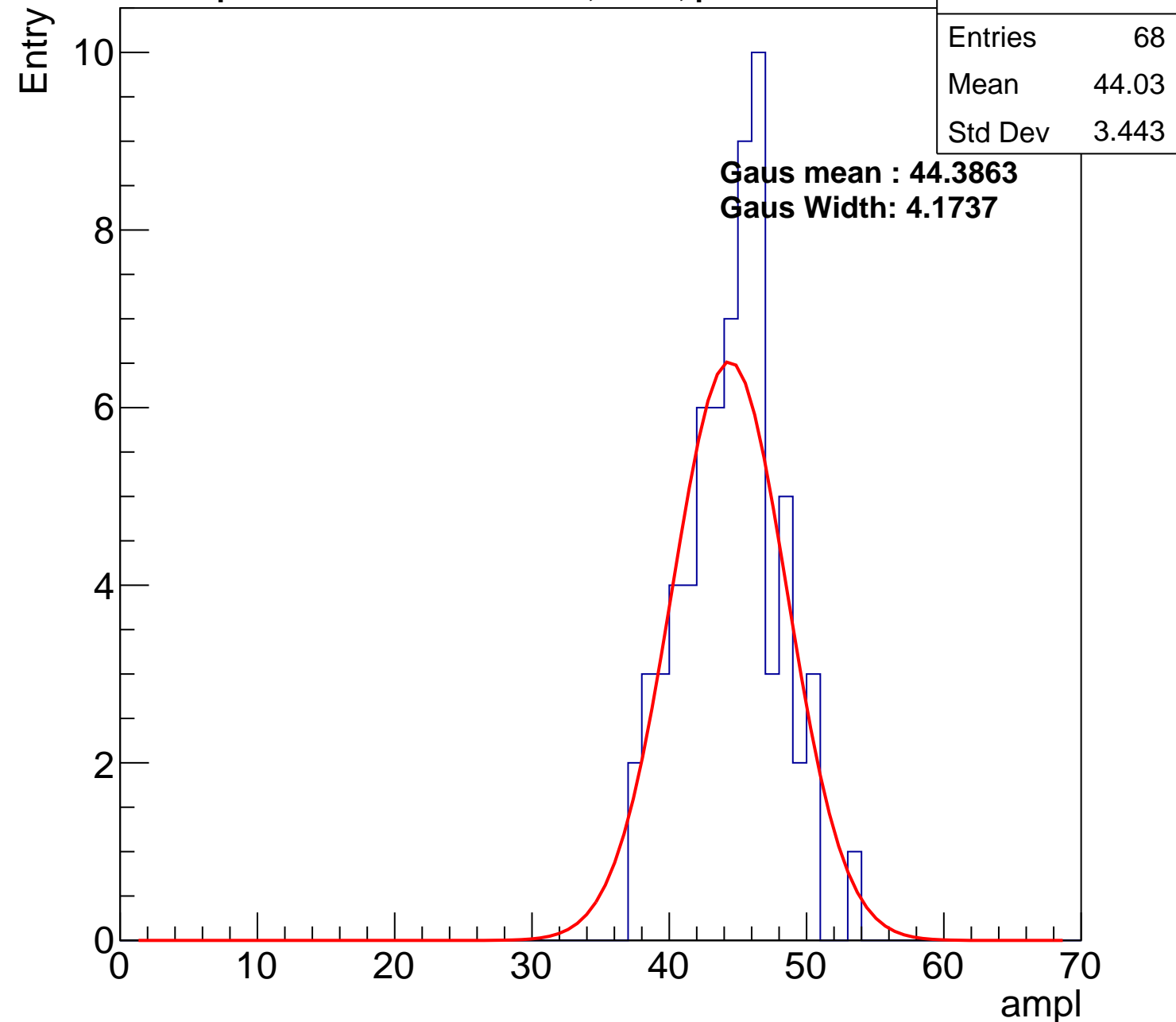
40

50

60

70

ampl



B0L001S, U22-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	50.11
Std Dev	3.343

Entry

10

8

6

4

2

0

0

10

20

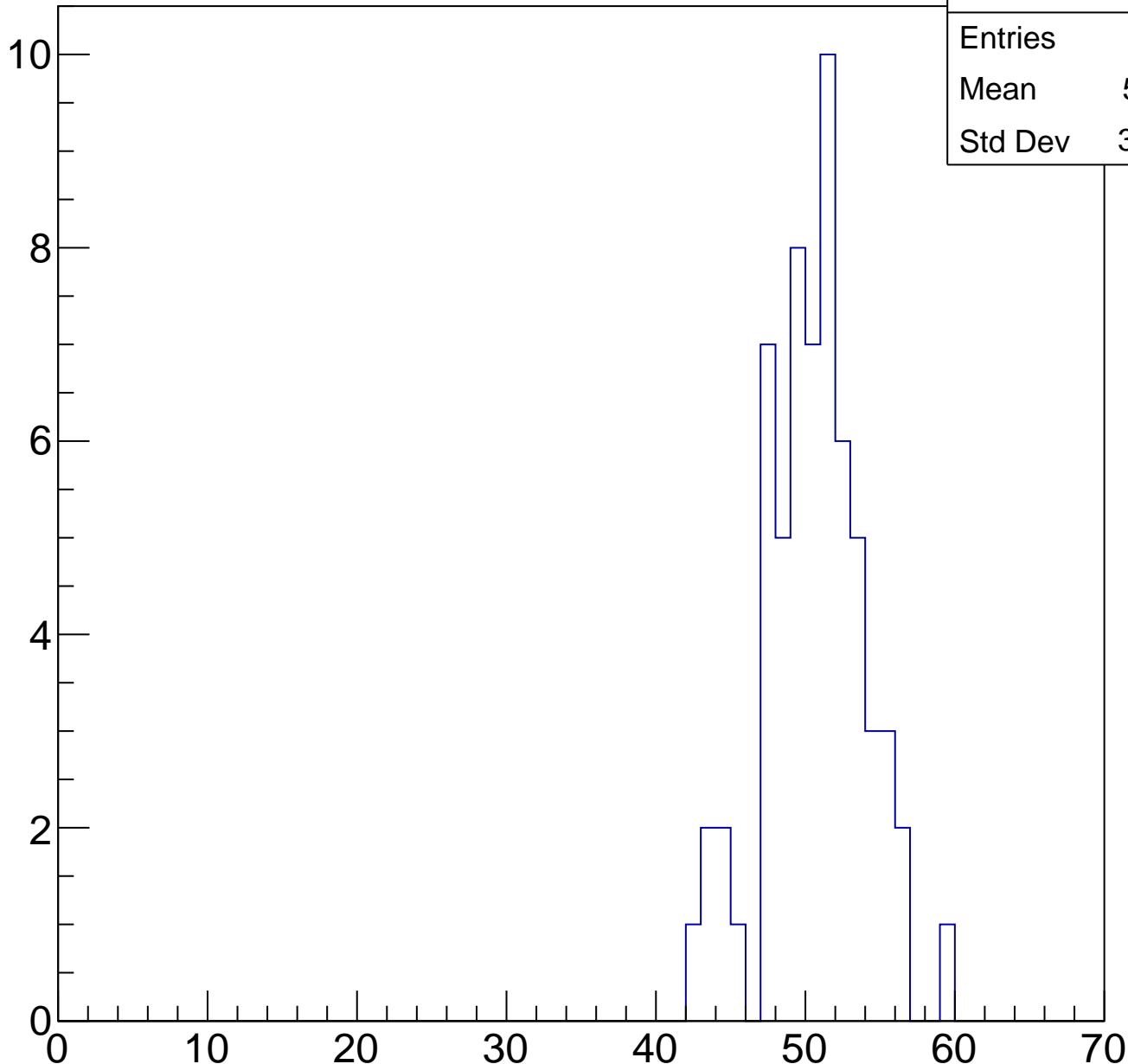
30

40

50

60

ampl

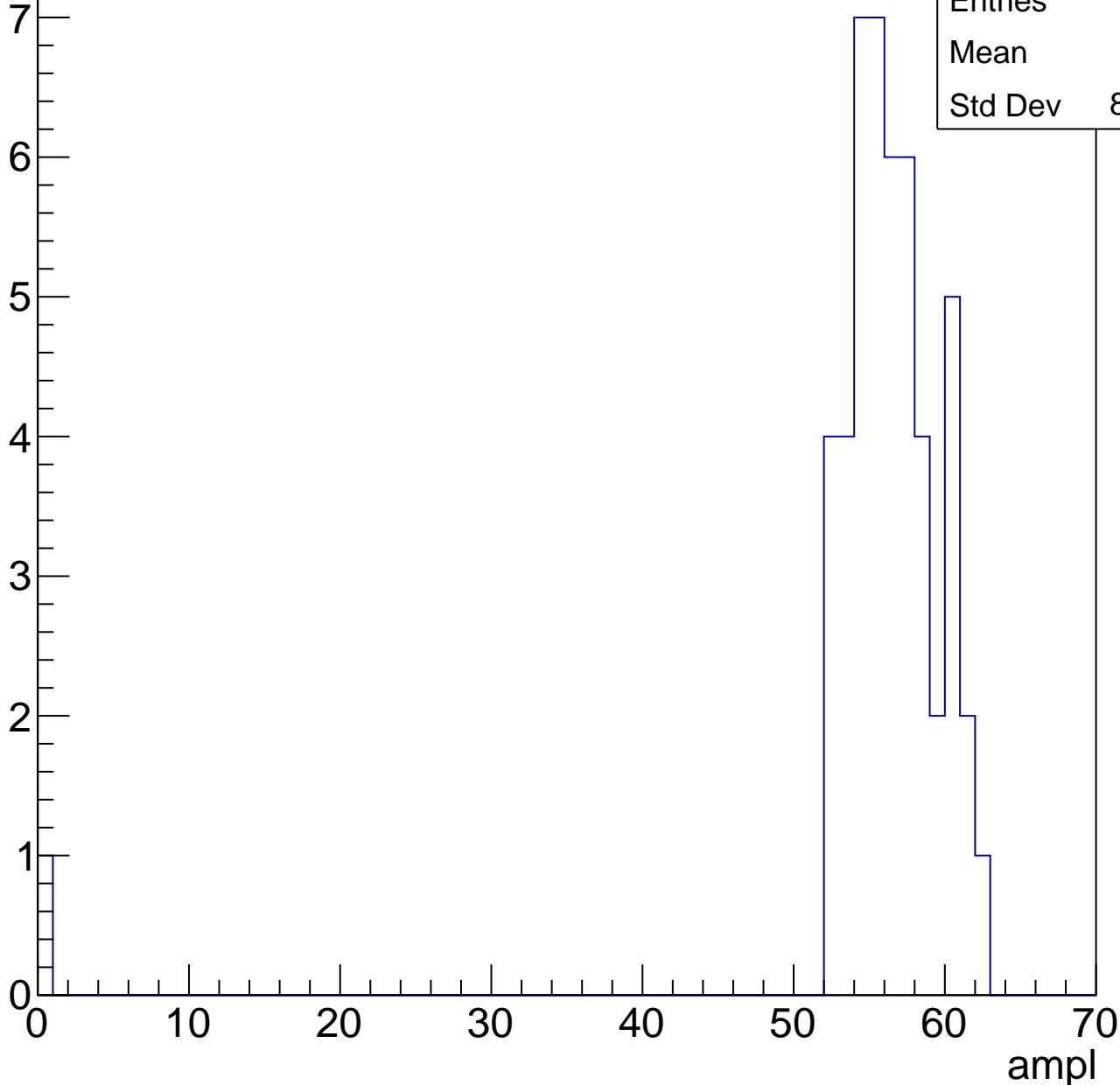


B0L001S, U22-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	55
Std Dev	8.364

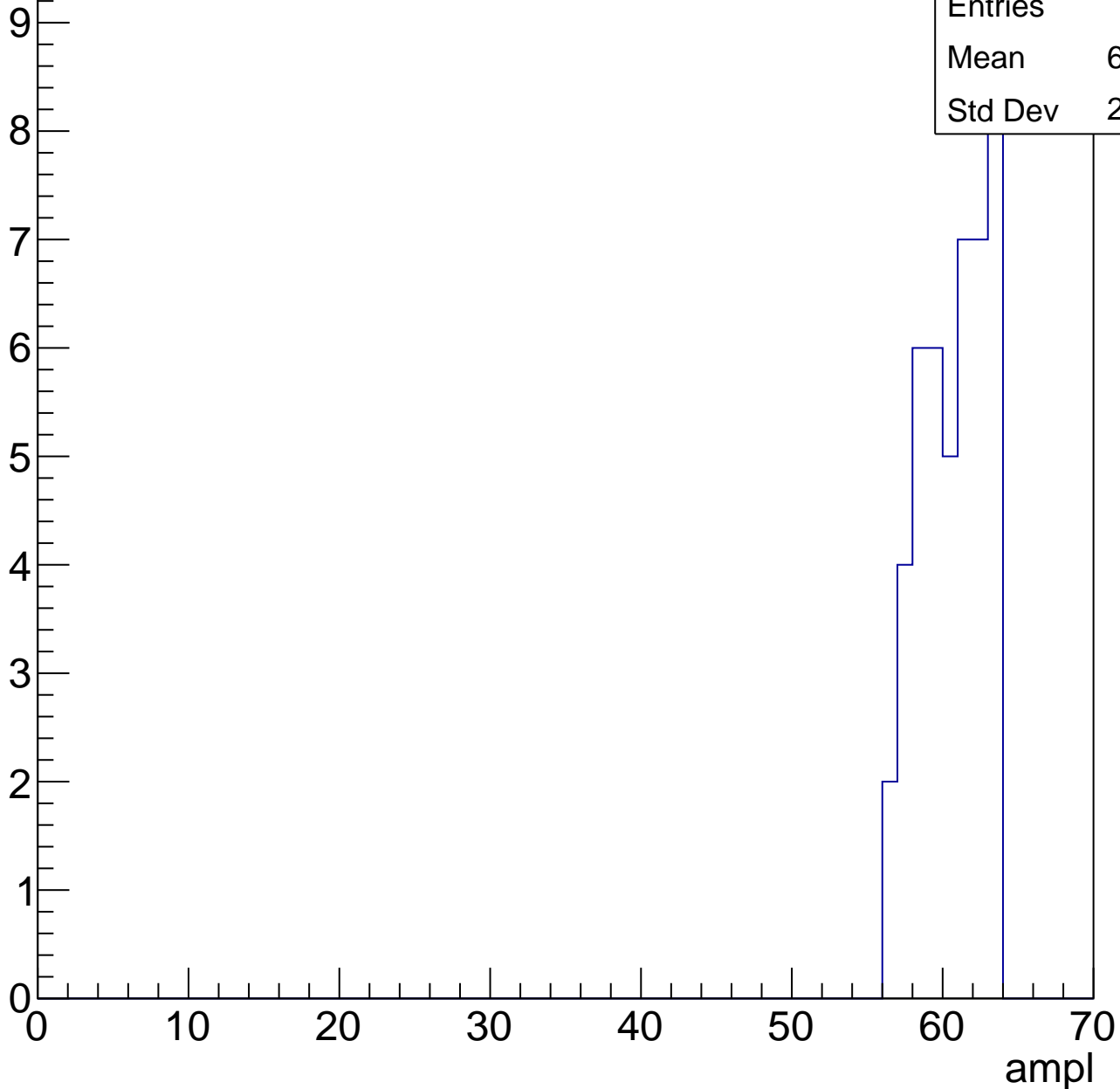


B0L001S, U22-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

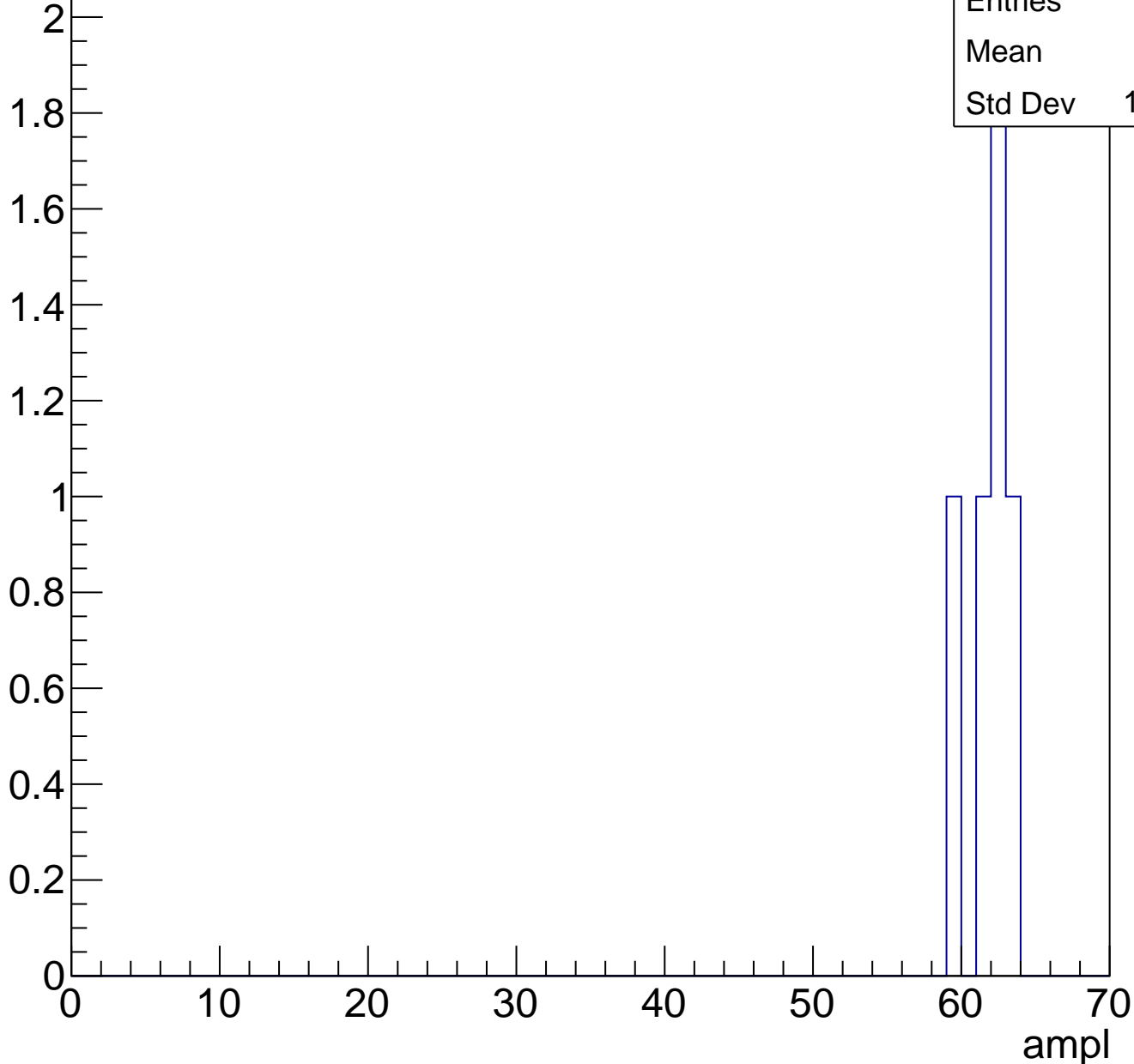
Entries	46
Mean	60.22
Std Dev	2.146



B0L001S, U22-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch55, adc0

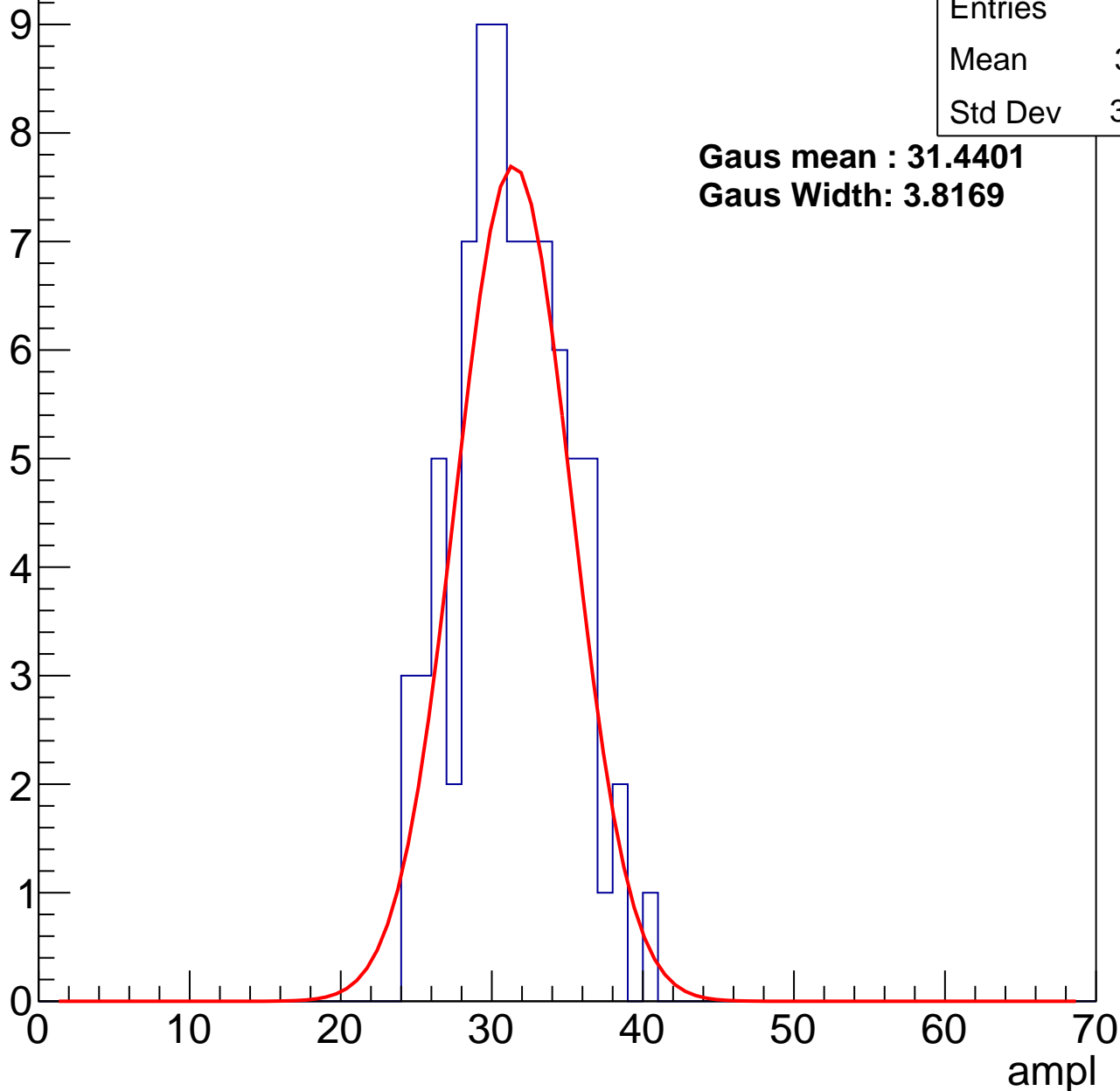
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.91
Std Dev	3.608

Gaus mean : 31.4401

Gaus Width: 3.8169



B0L001S, U22-ch55, adc1

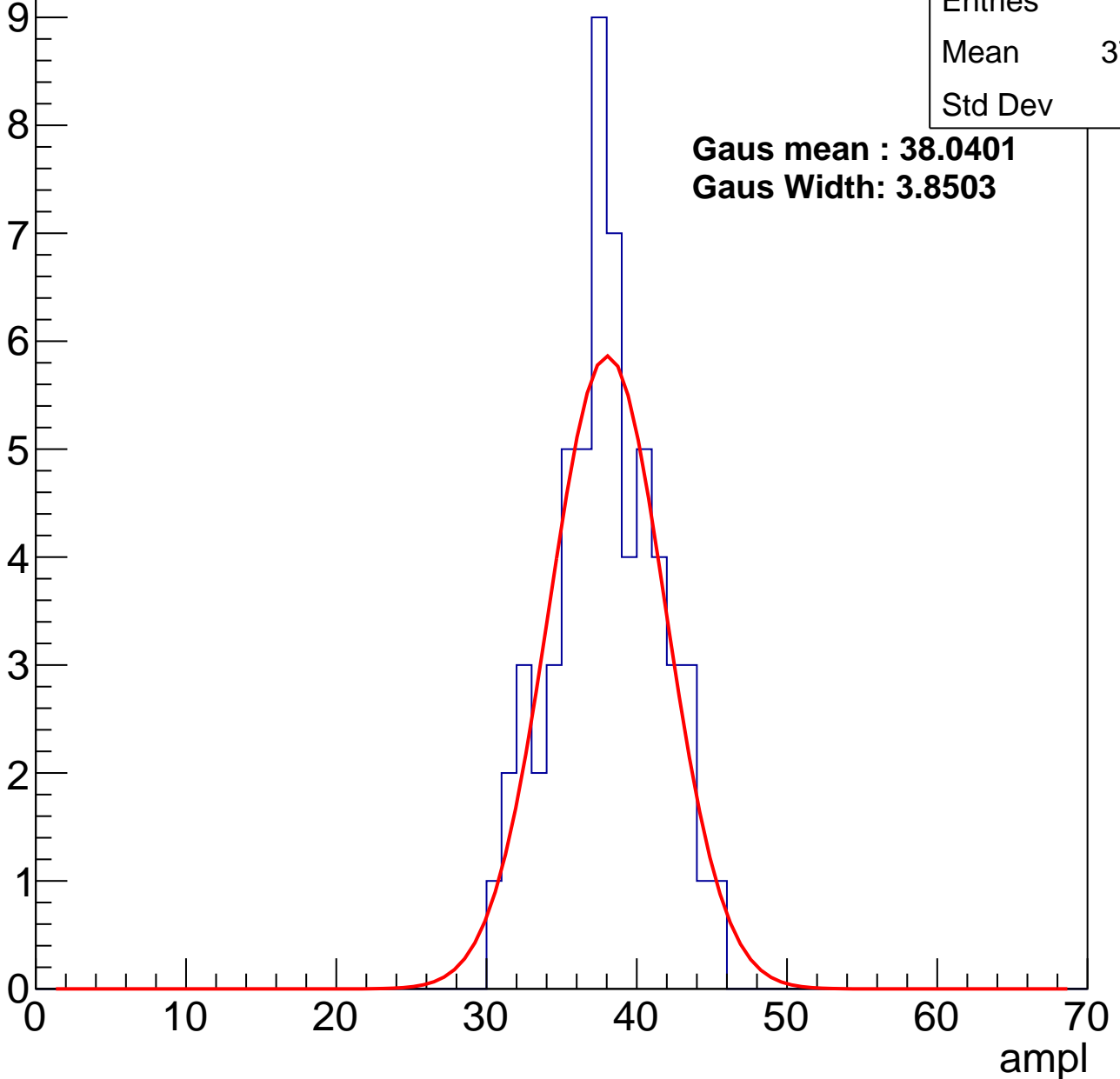
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	37.48
Std Dev	3.46

Gaus mean : 38.0401

Gaus Width: 3.8503



B0L001S, U22-ch55, adc2

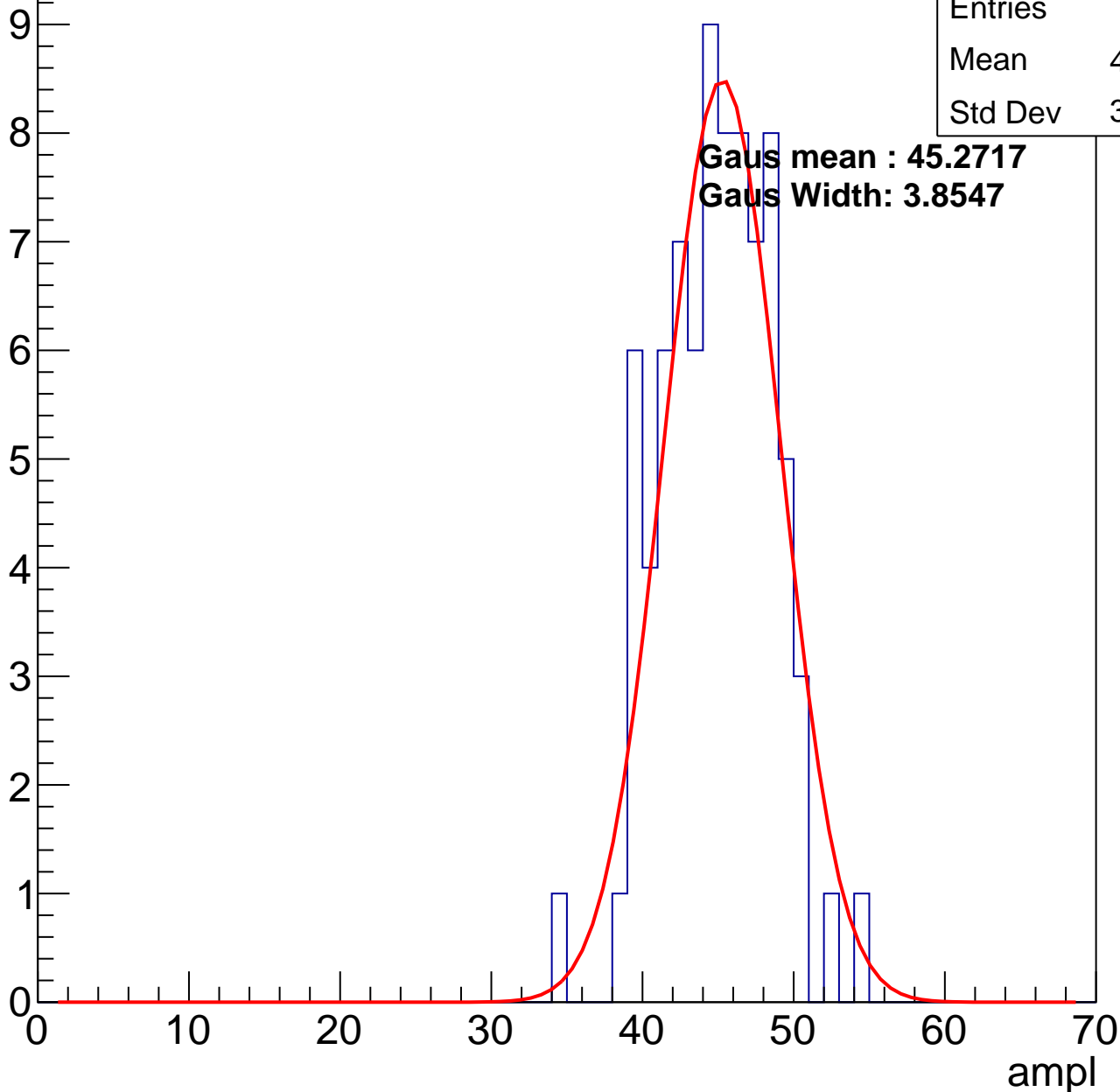
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	44.47
Std Dev	3.597

Gaus mean : 45.2717

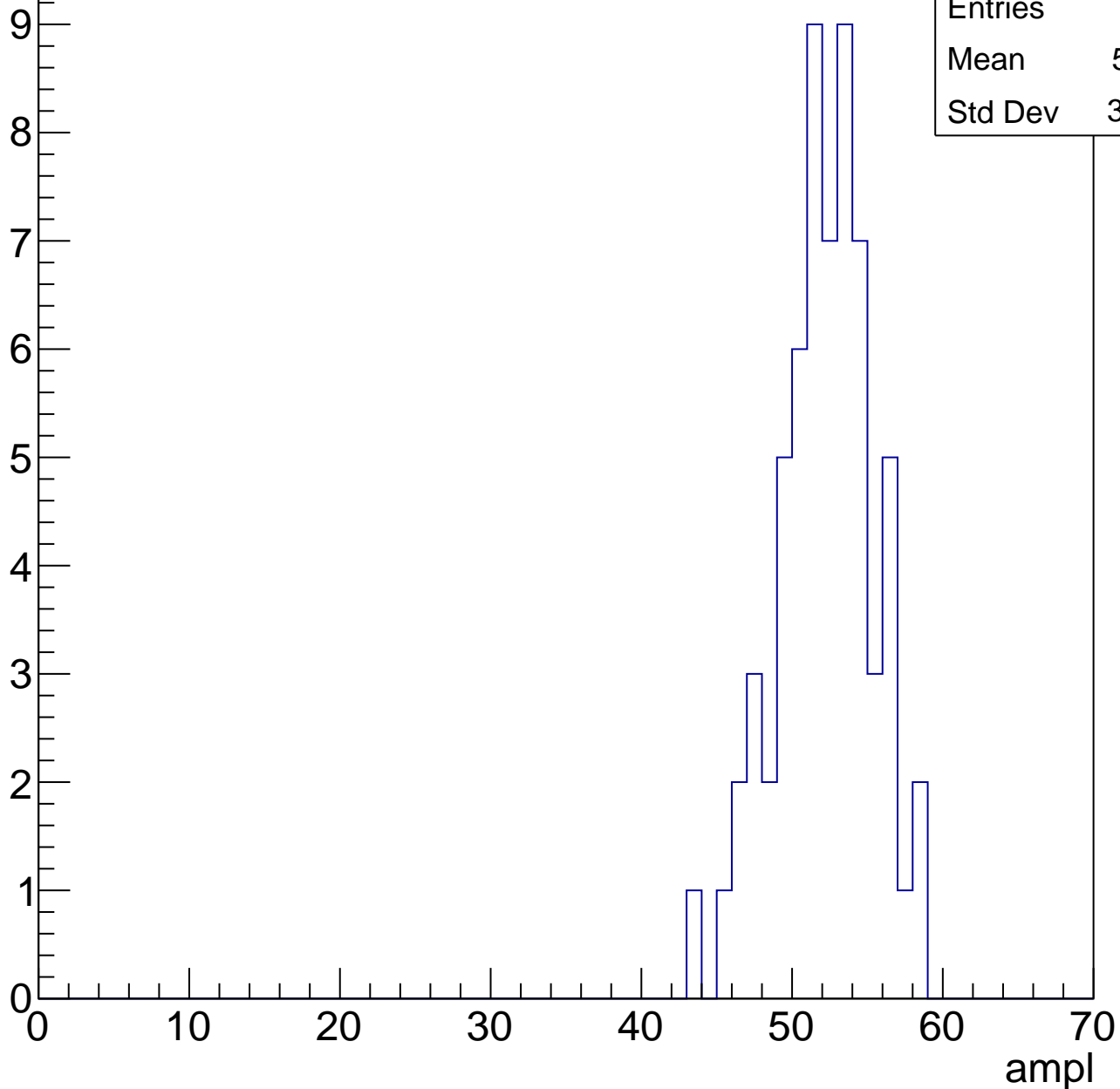
Gaus Width: 3.8547



B0L001S, U22-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	63
Mean	51.71
Std Dev	3.144

B0L001S, U22-ch55, adc4

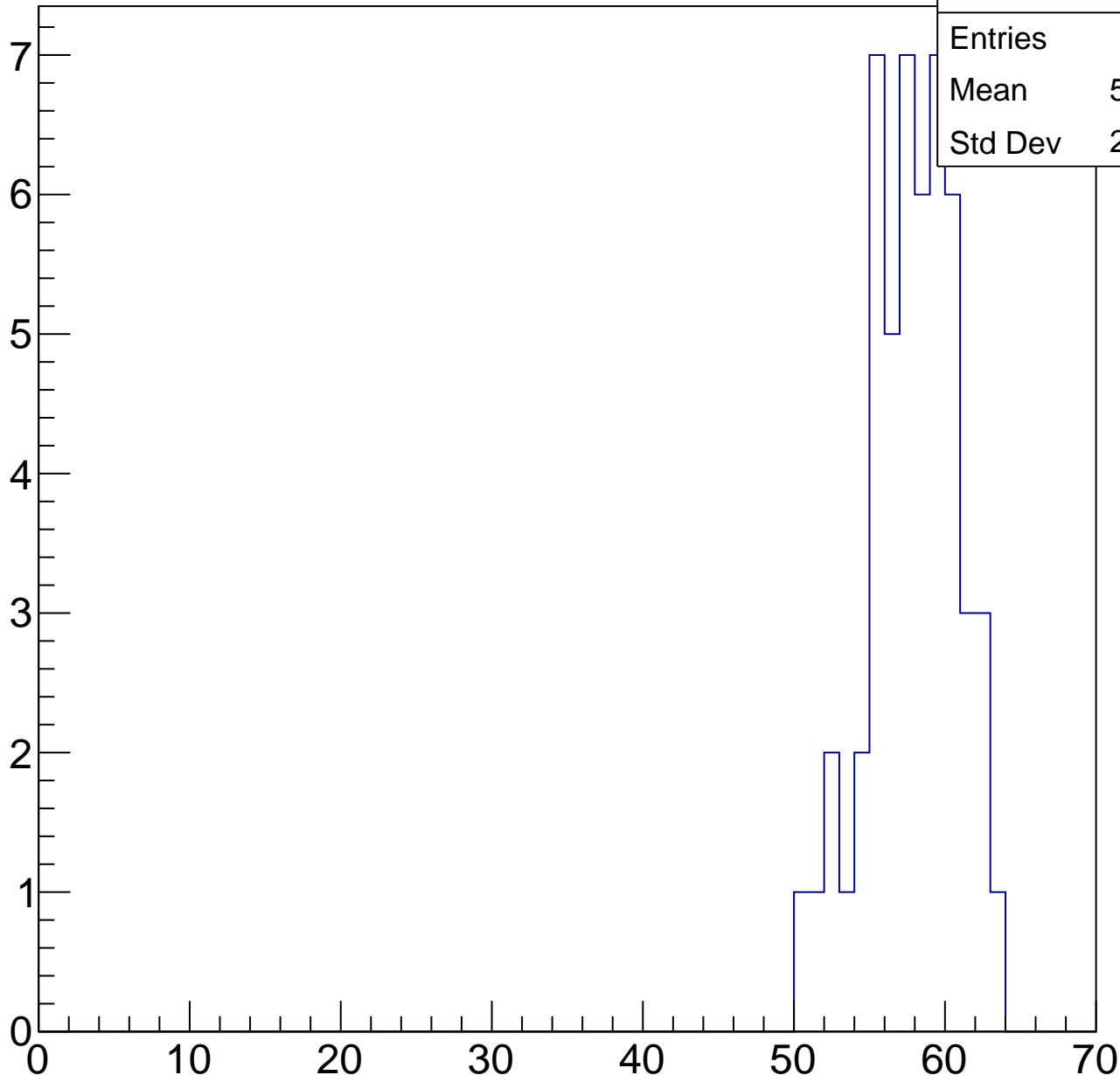
calib_packv5_042523_0143.root, FC#9, port A1

Entry

7
6
5
4
3
2
1
0

Entries	52
Mean	57.37
Std Dev	2.916

ampl

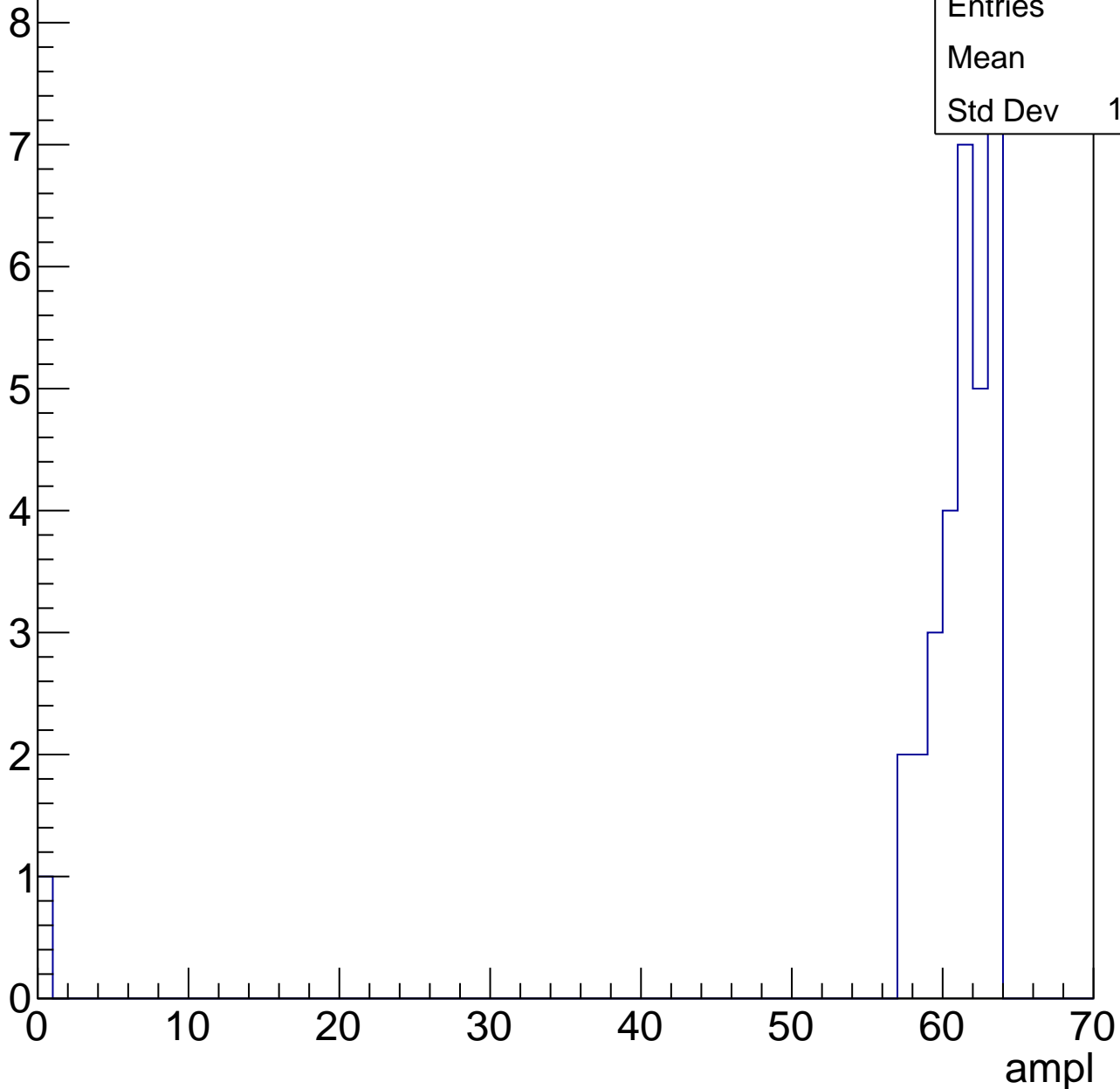


B0L001S, U22-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	59
Std Dev	10.75



B0L001S, U22-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U22-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U22-ch56, adc0

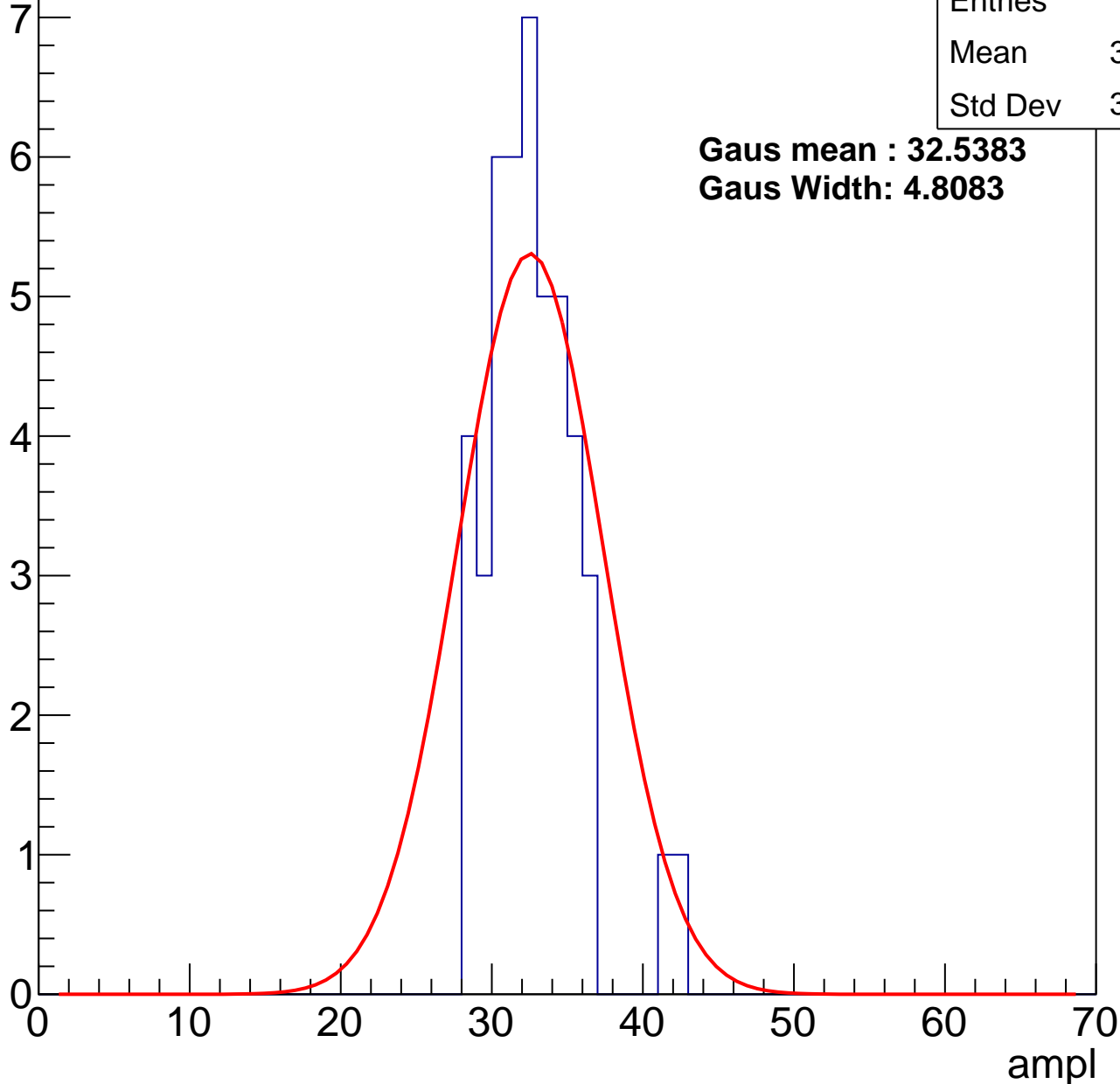
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	32.33
Std Dev	3.004

Gaus mean : 32.5383

Gaus Width: 4.8083



B0L001S, U22-ch56, adc1

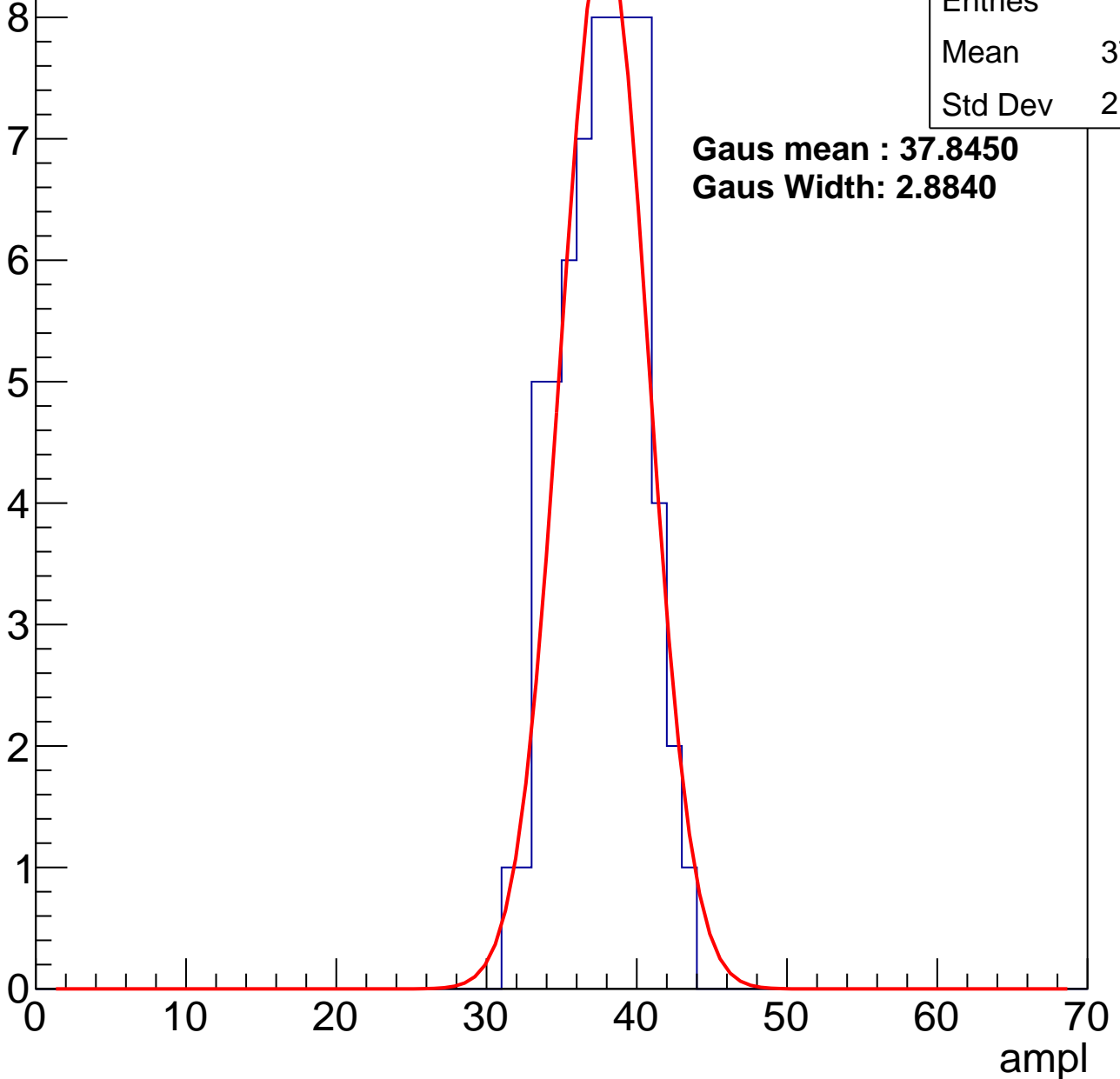
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	37.23
Std Dev	2.726

Gaus mean : 37.8450

Gaus Width: 2.8840



B0L001S, U22-ch56, adc2

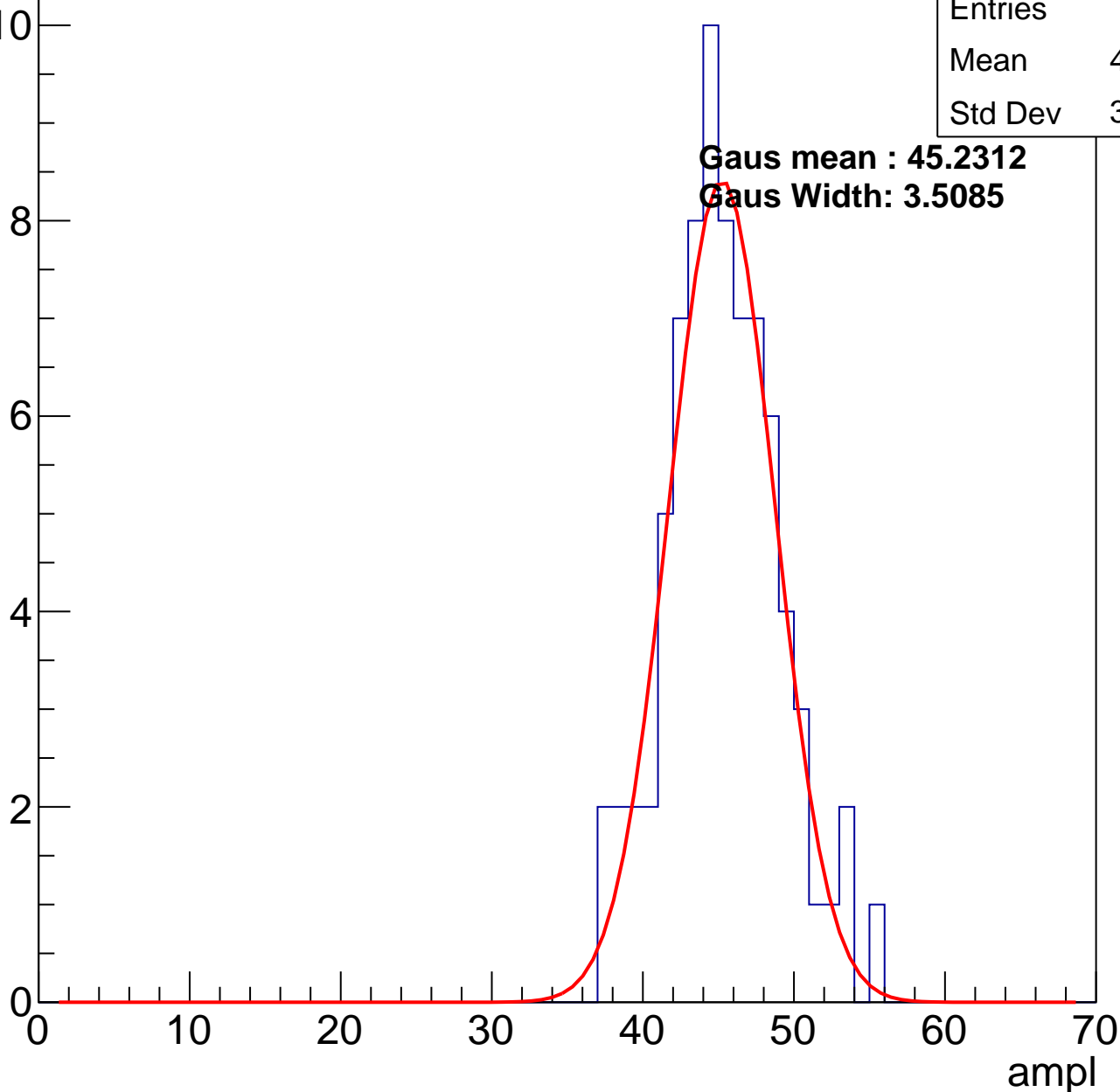
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	44.87
Std Dev	3.708

Gaus mean : 45.2312

Gaus Width: 3.5085

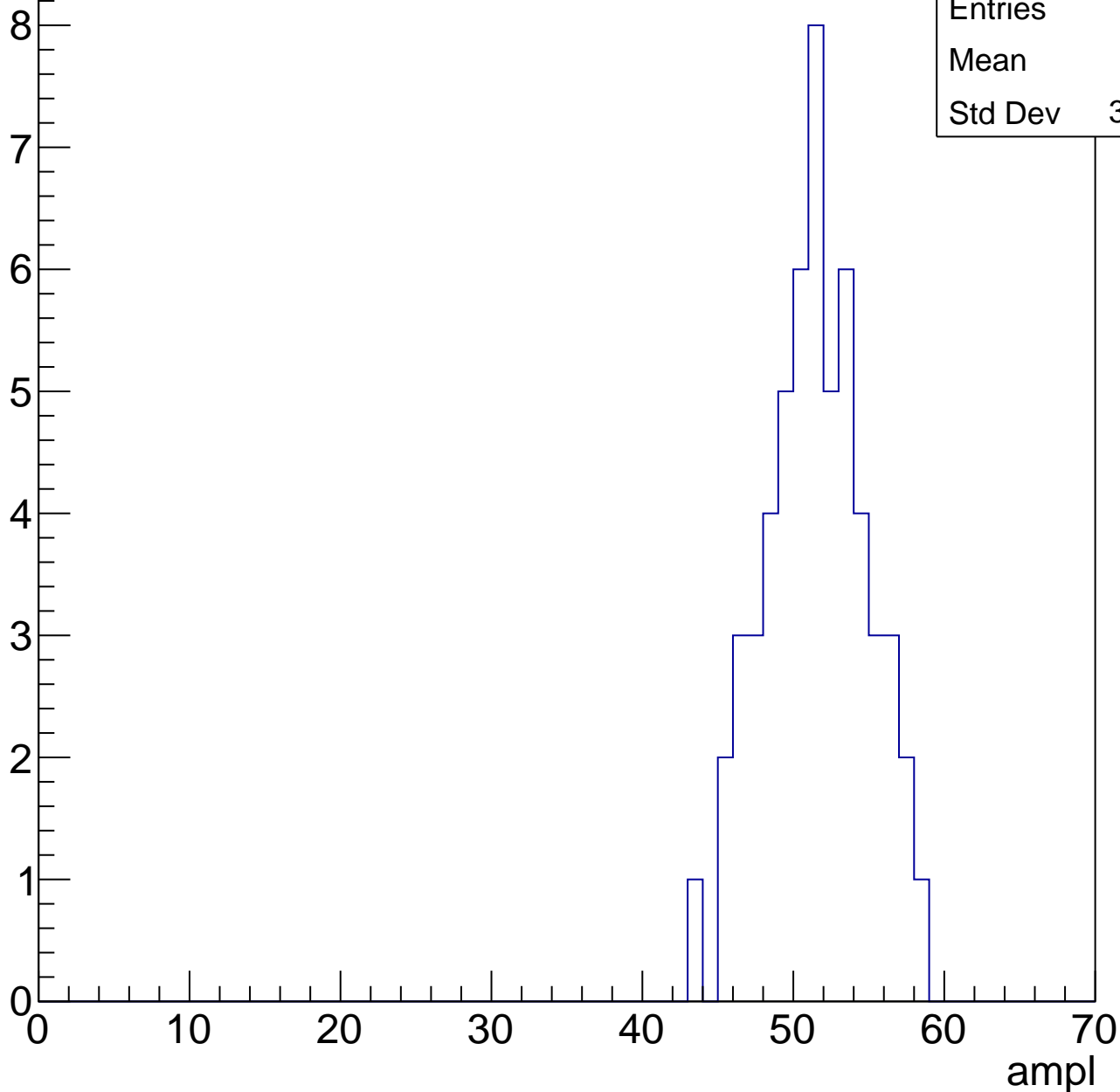


B0L001S, U22-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

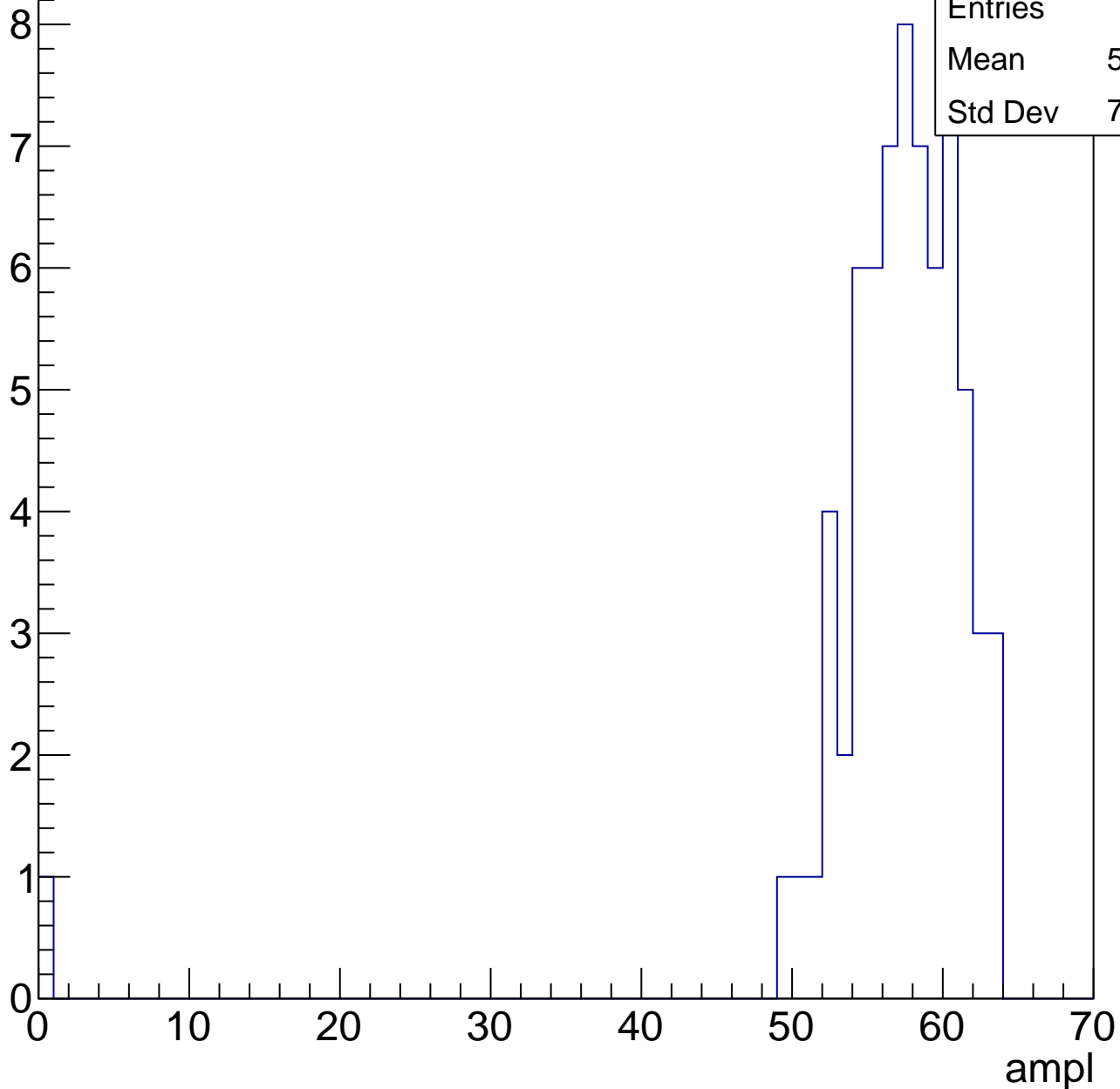
Entries	56
Mean	51
Std Dev	3.354



B0L001S, U22-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

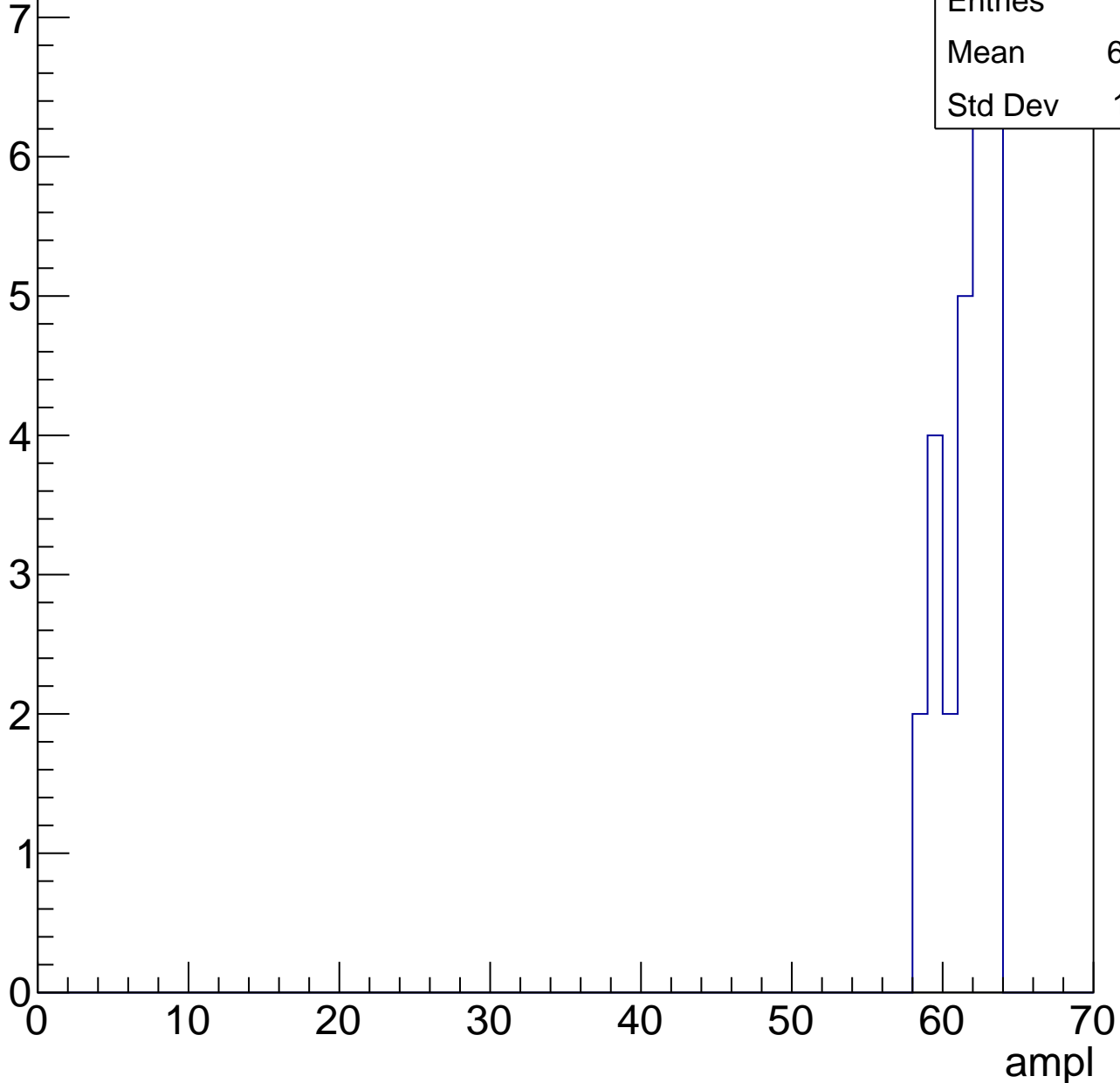


B0L001S, U22-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	61.19
Std Dev	1.611



B0L001S, U22-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch57, adc0

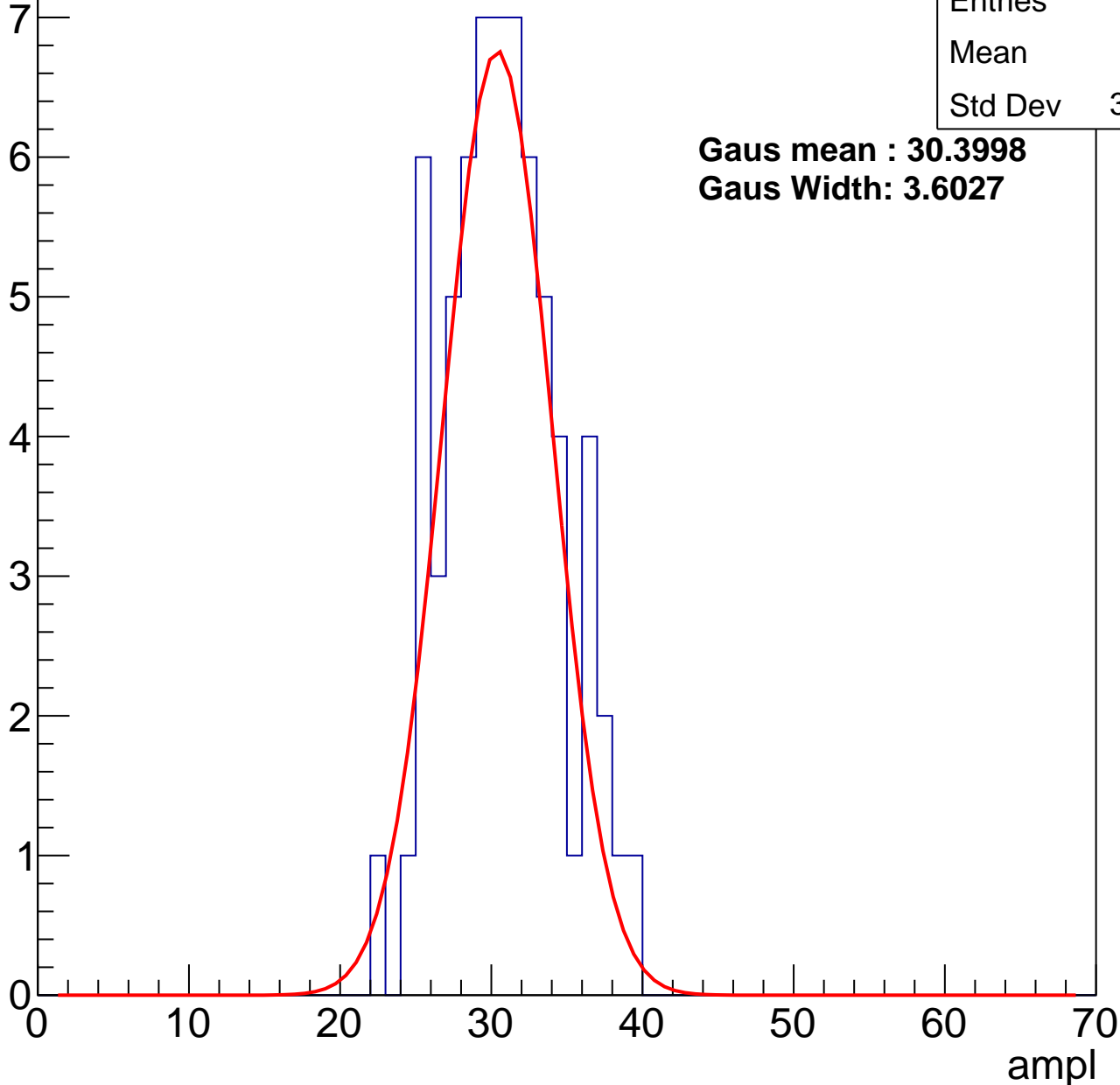
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.3
Std Dev	3.706

Gaus mean : 30.3998

Gaus Width: 3.6027



B0L001S, U22-ch57, adc1

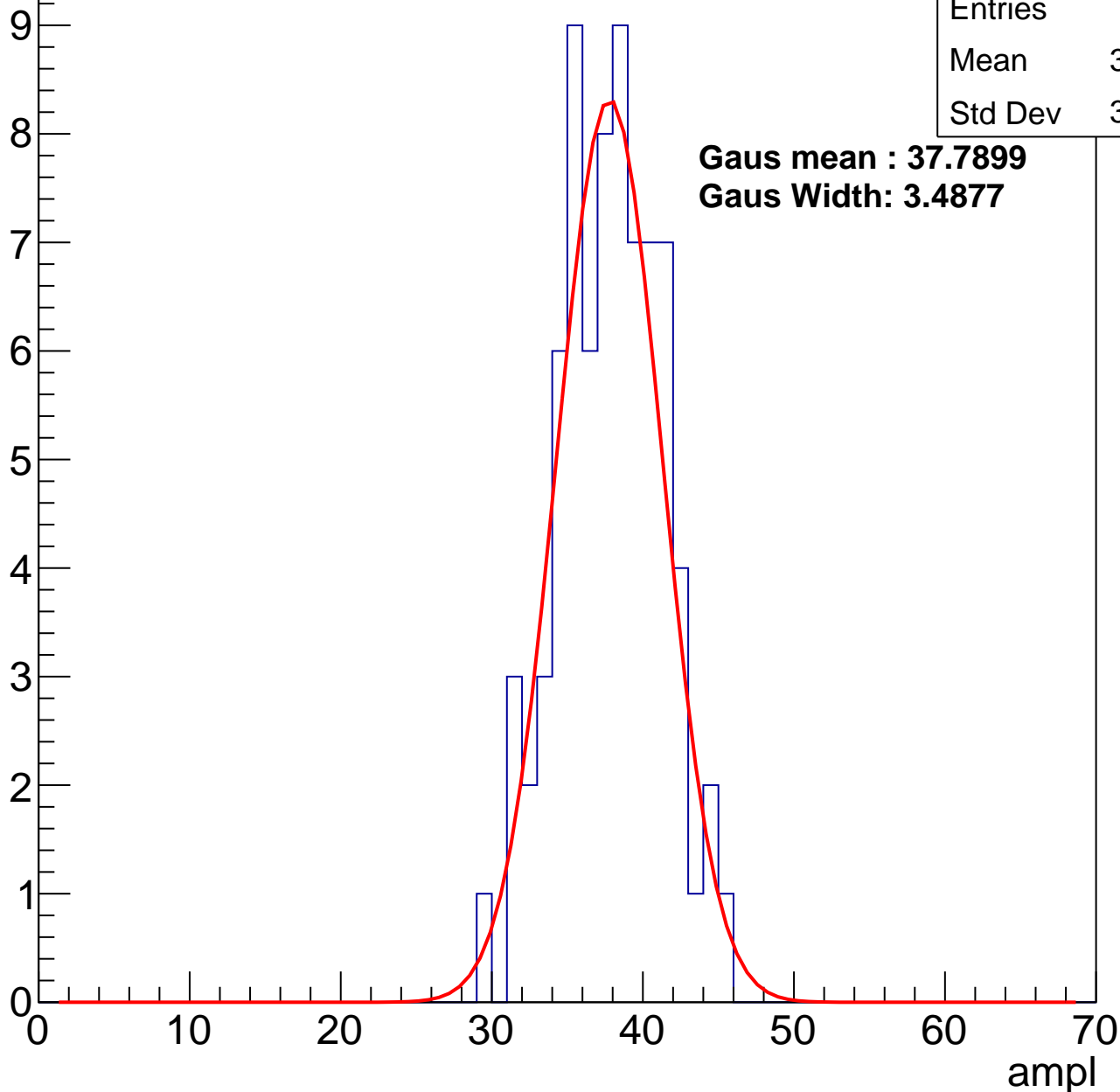
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	37.39
Std Dev	3.372

Gaus mean : 37.7899

Gaus Width: 3.4877



B0L001S, U22-ch57, adc2

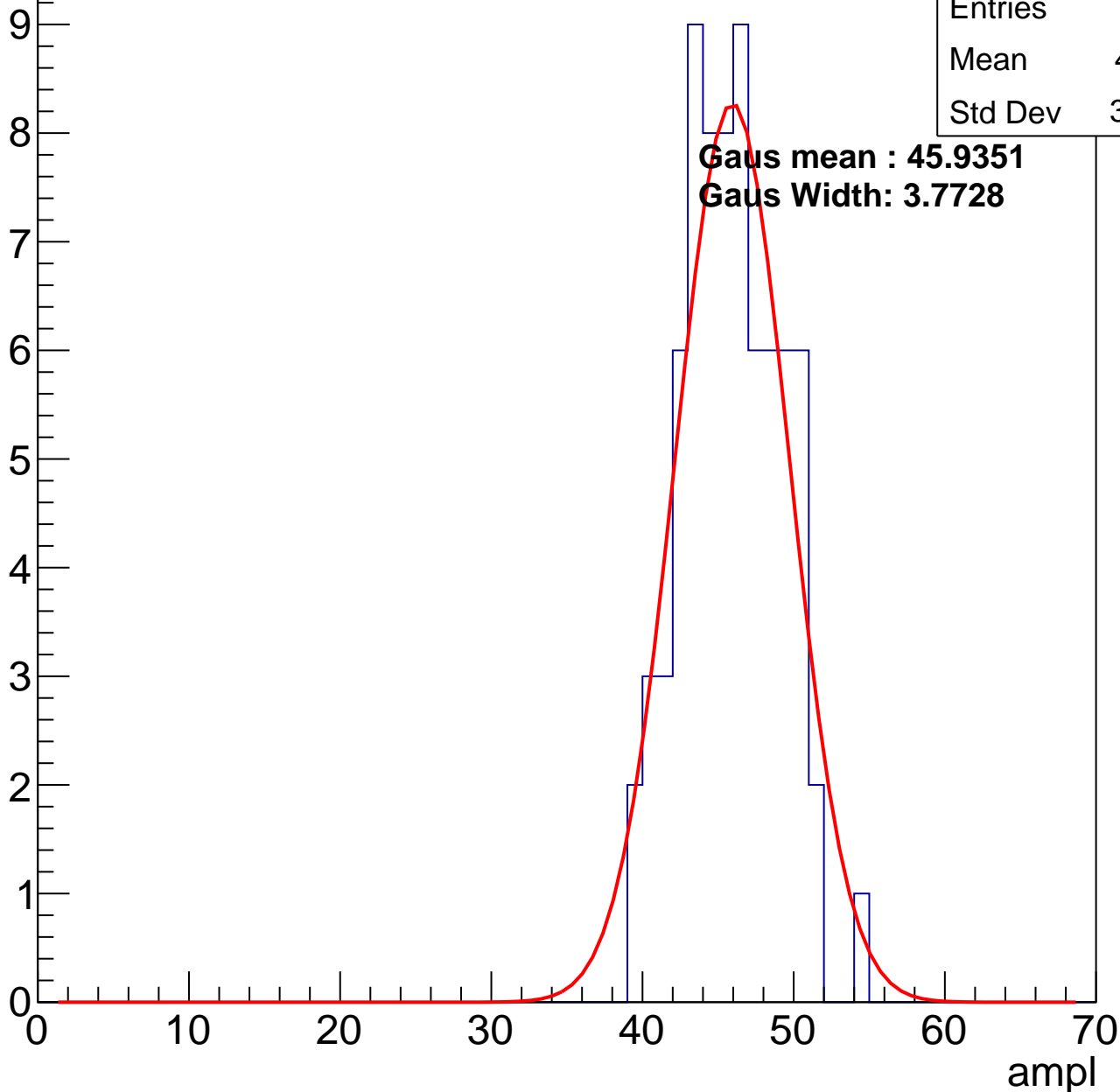
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	45.41
Std Dev	3.196

Gaus mean : 45.9351

Gaus Width: 3.7728

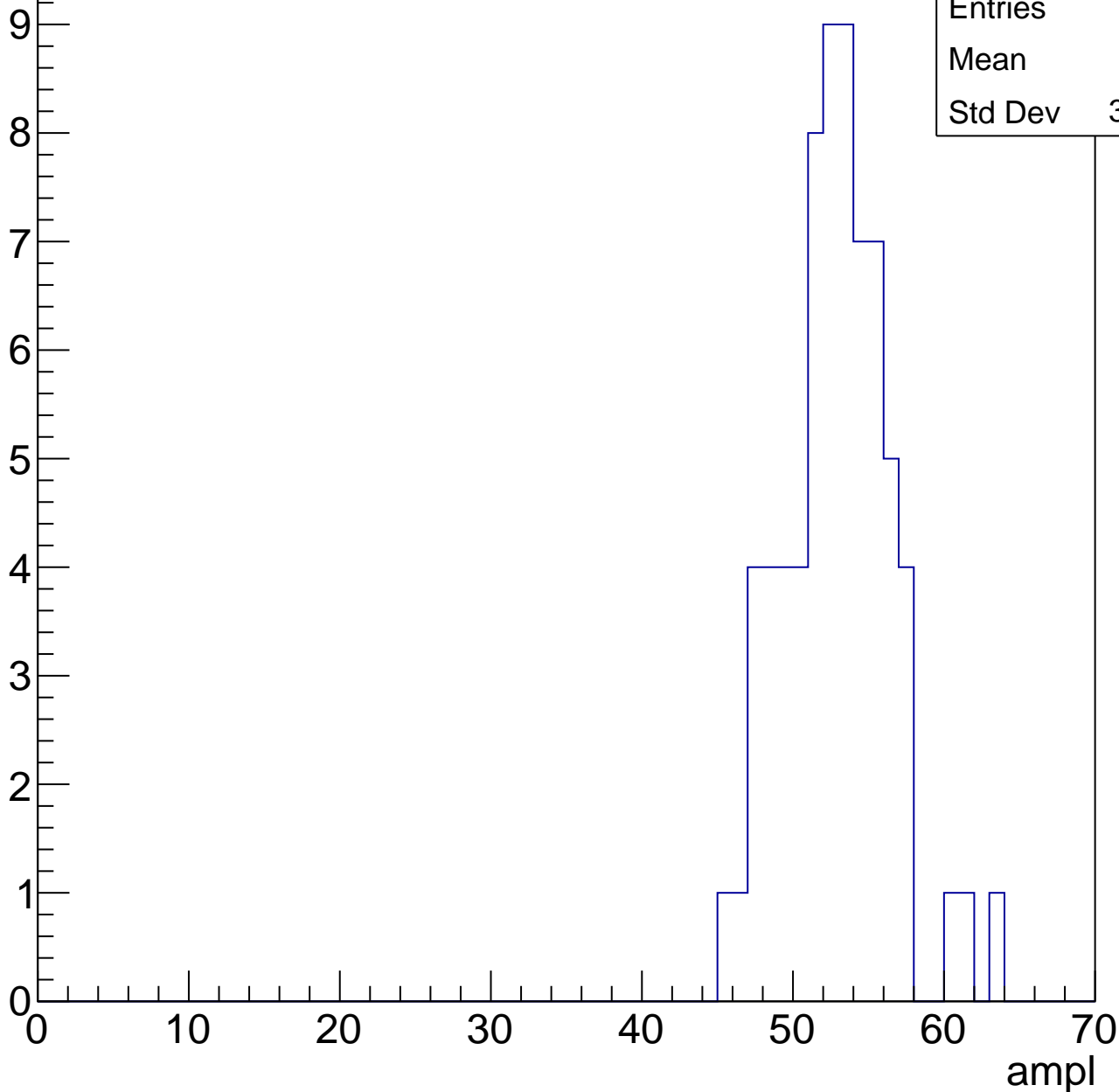


B0L001S, U22-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.5
Std Dev	3.459

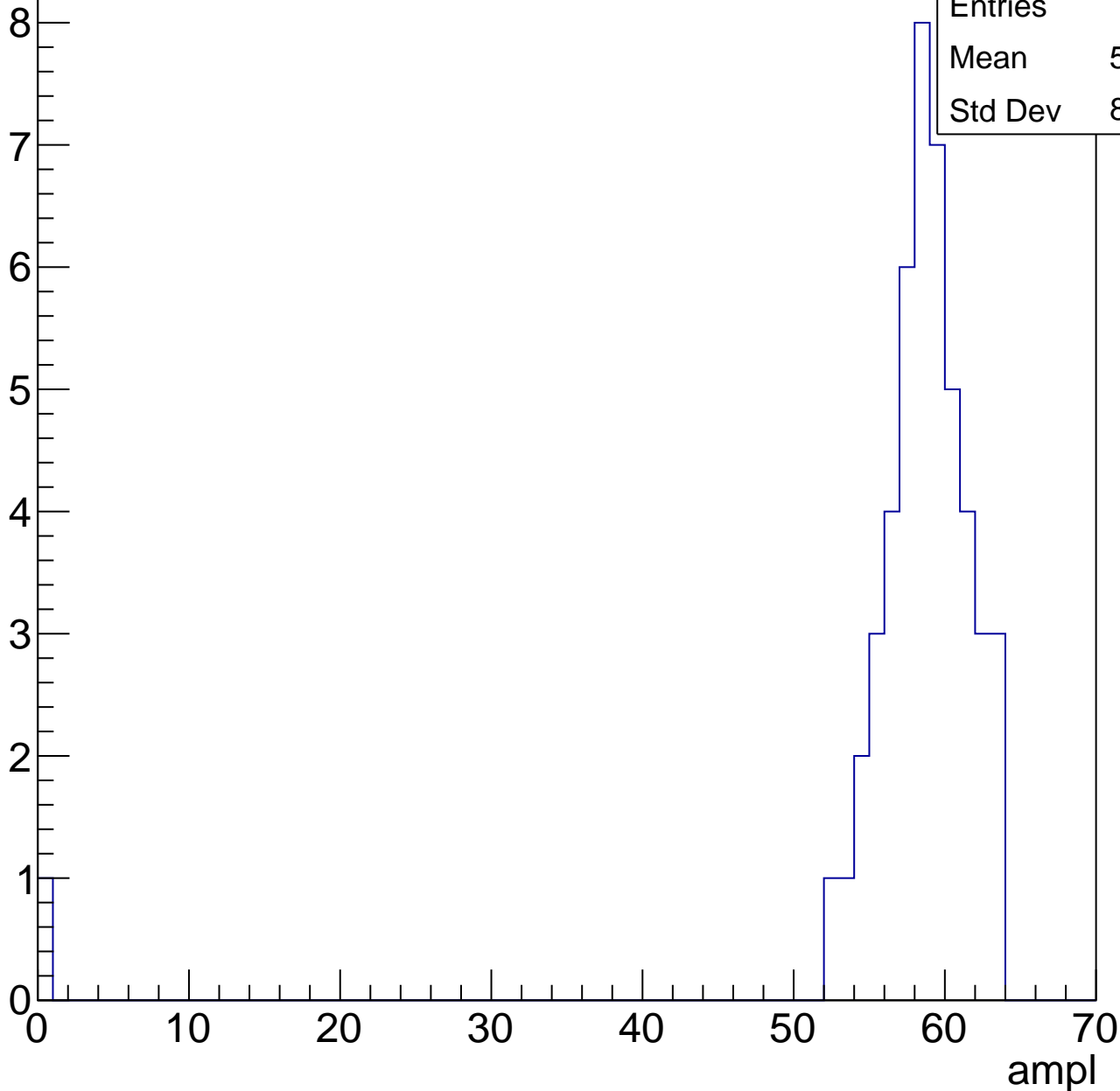


B0L001S, U22-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57.08
Std Dev	8.722

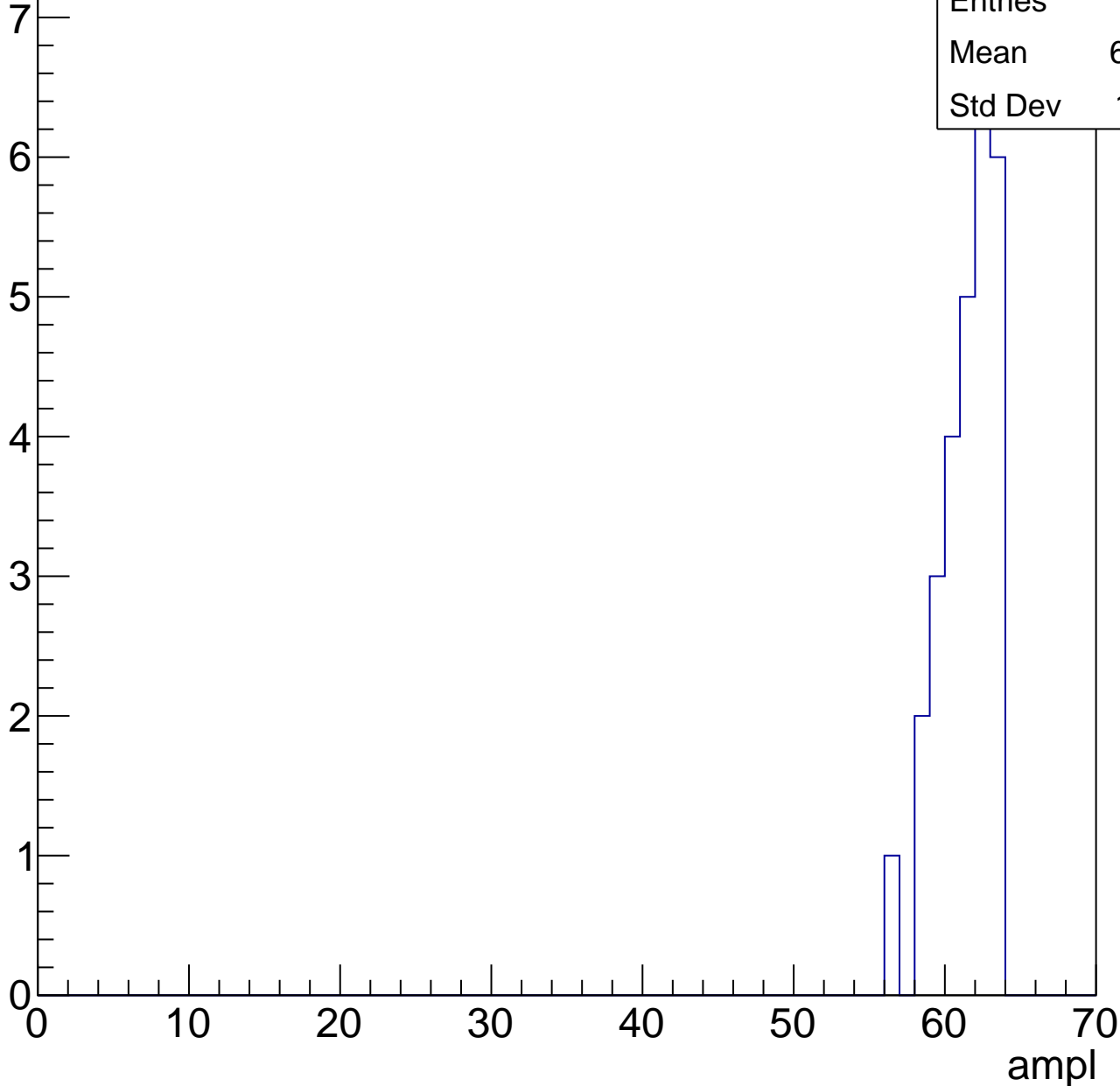


B0L001S, U22-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	60.93
Std Dev	1.791



B0L001S, U22-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U22-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch58, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	31.53
Std Dev	6.377

Gaus mean : 33.0114

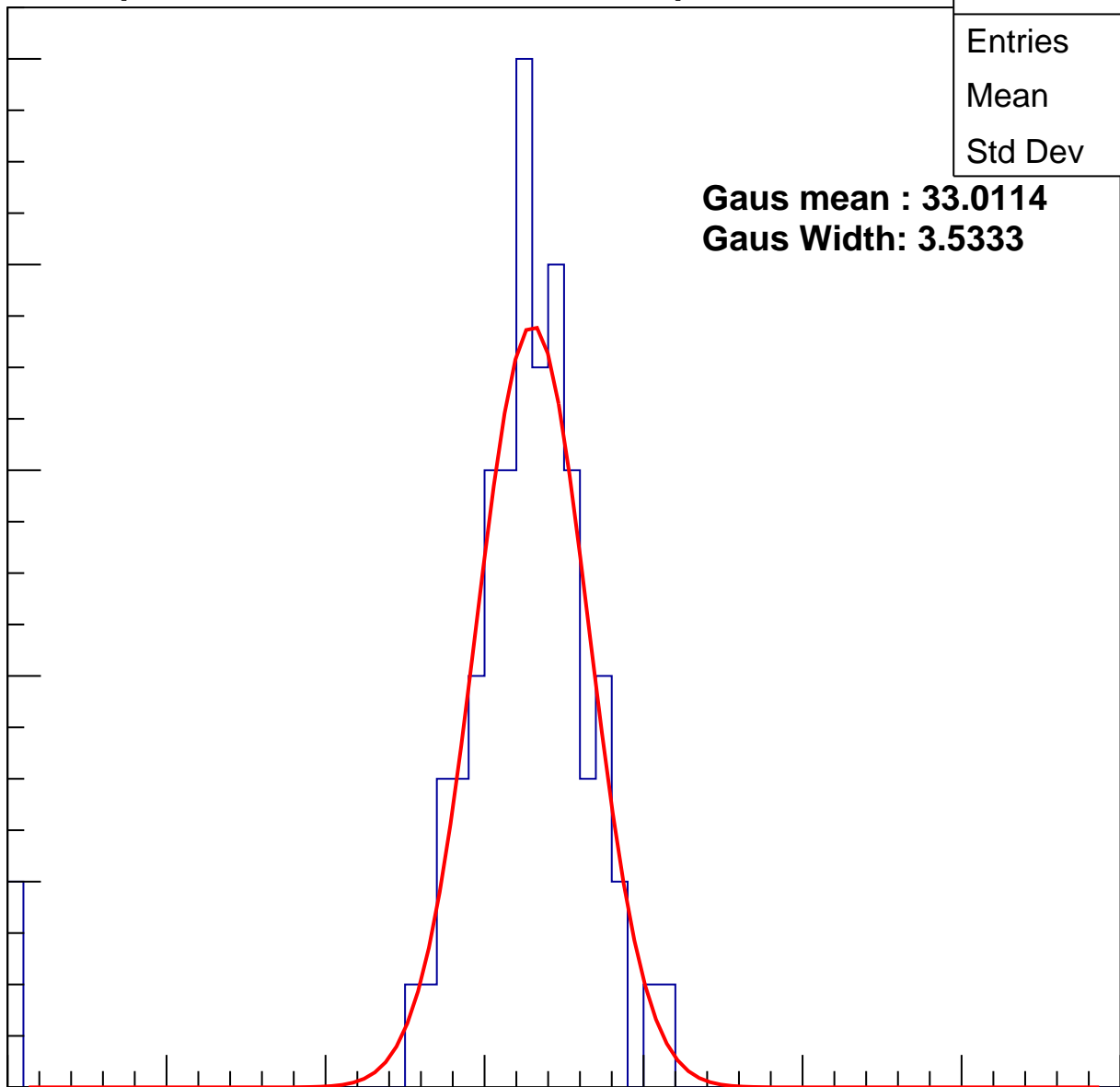
Gaus Width: 3.5333

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch58, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	39.02
Std Dev	3.14

Gaus mean : 39.6056

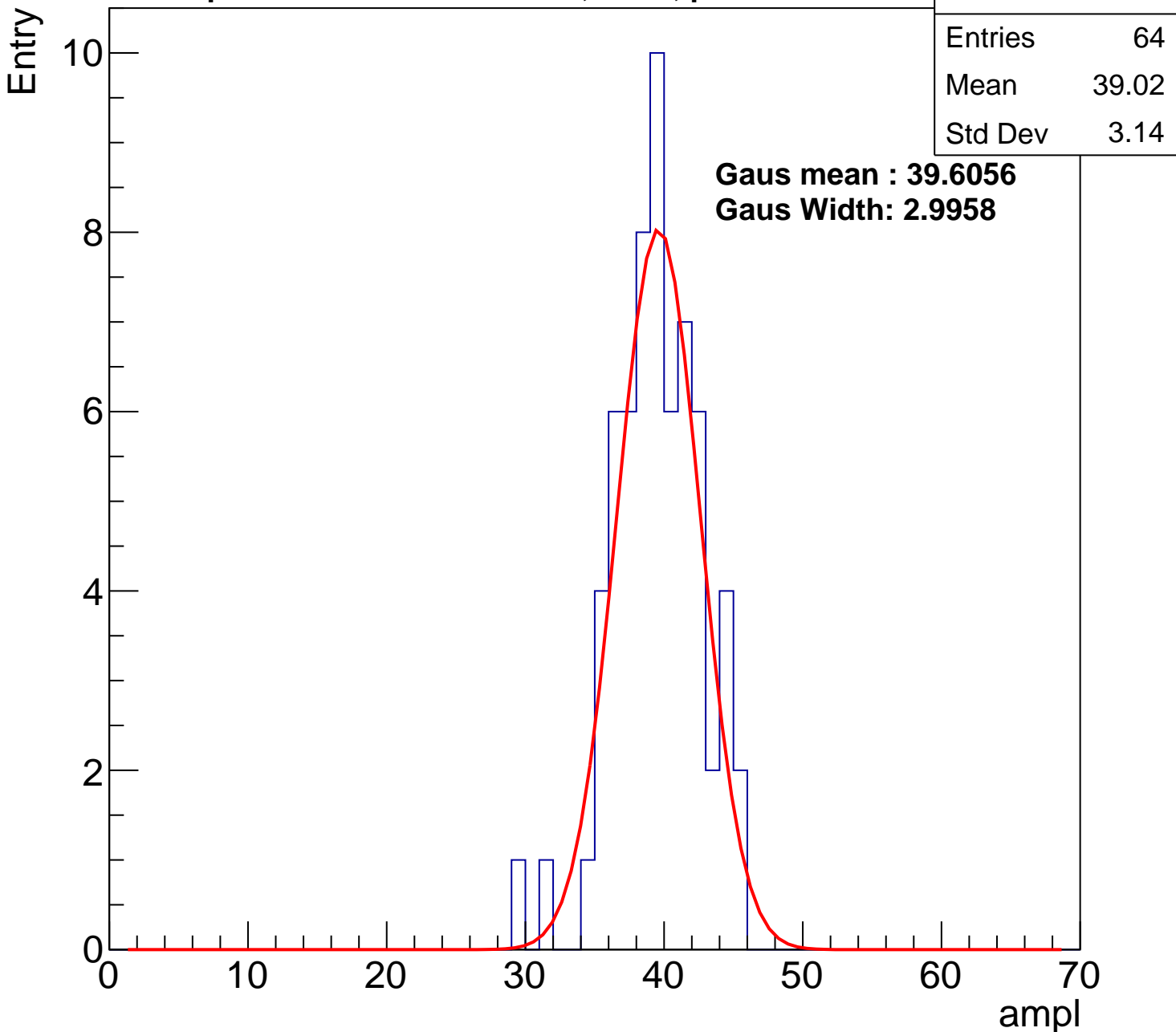
Gaus Width: 2.9958

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch58, adc2

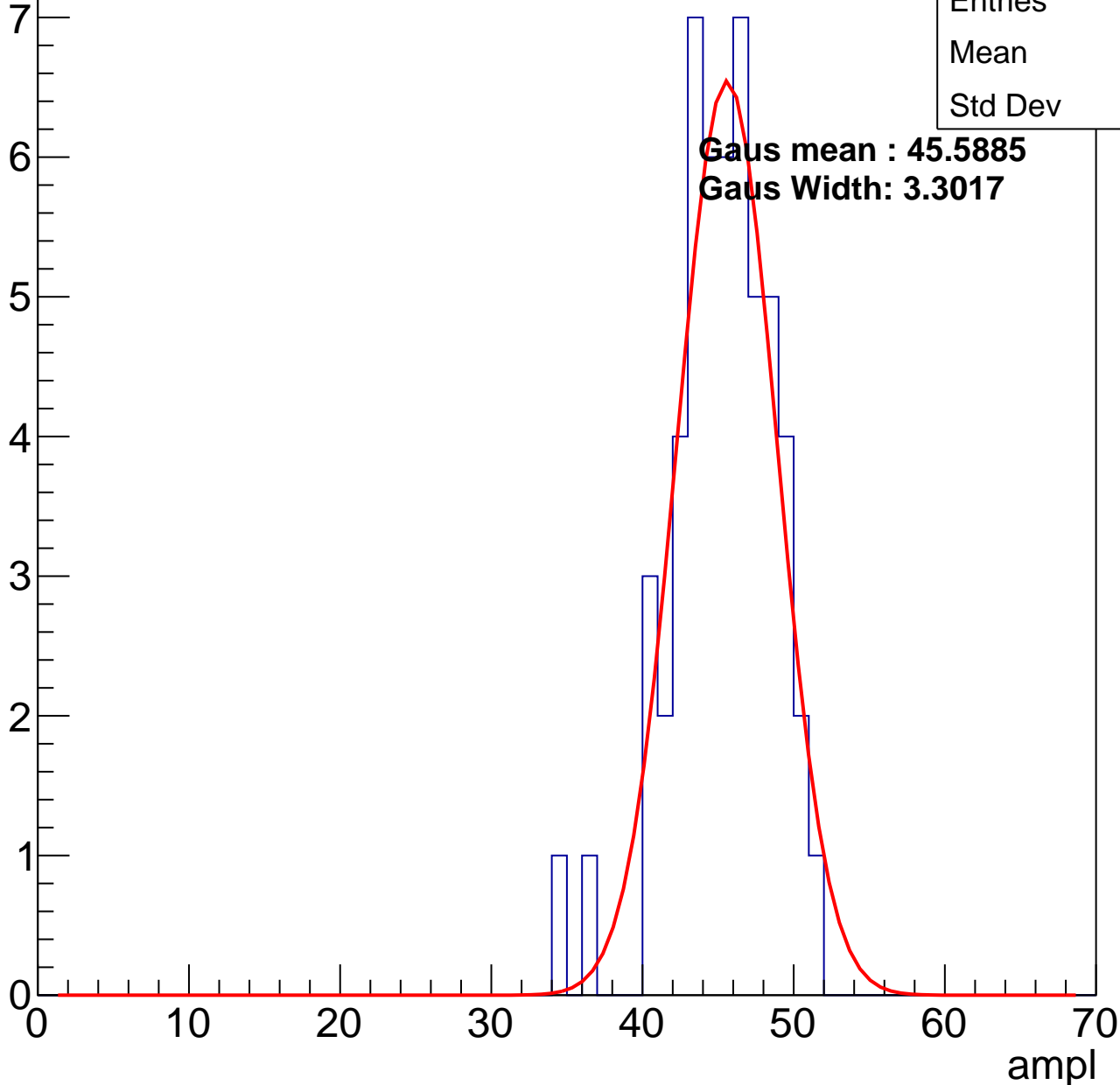
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	44.8
Std Dev	3.33

Gaus mean : 45.5885

Gaus Width: 3.3017

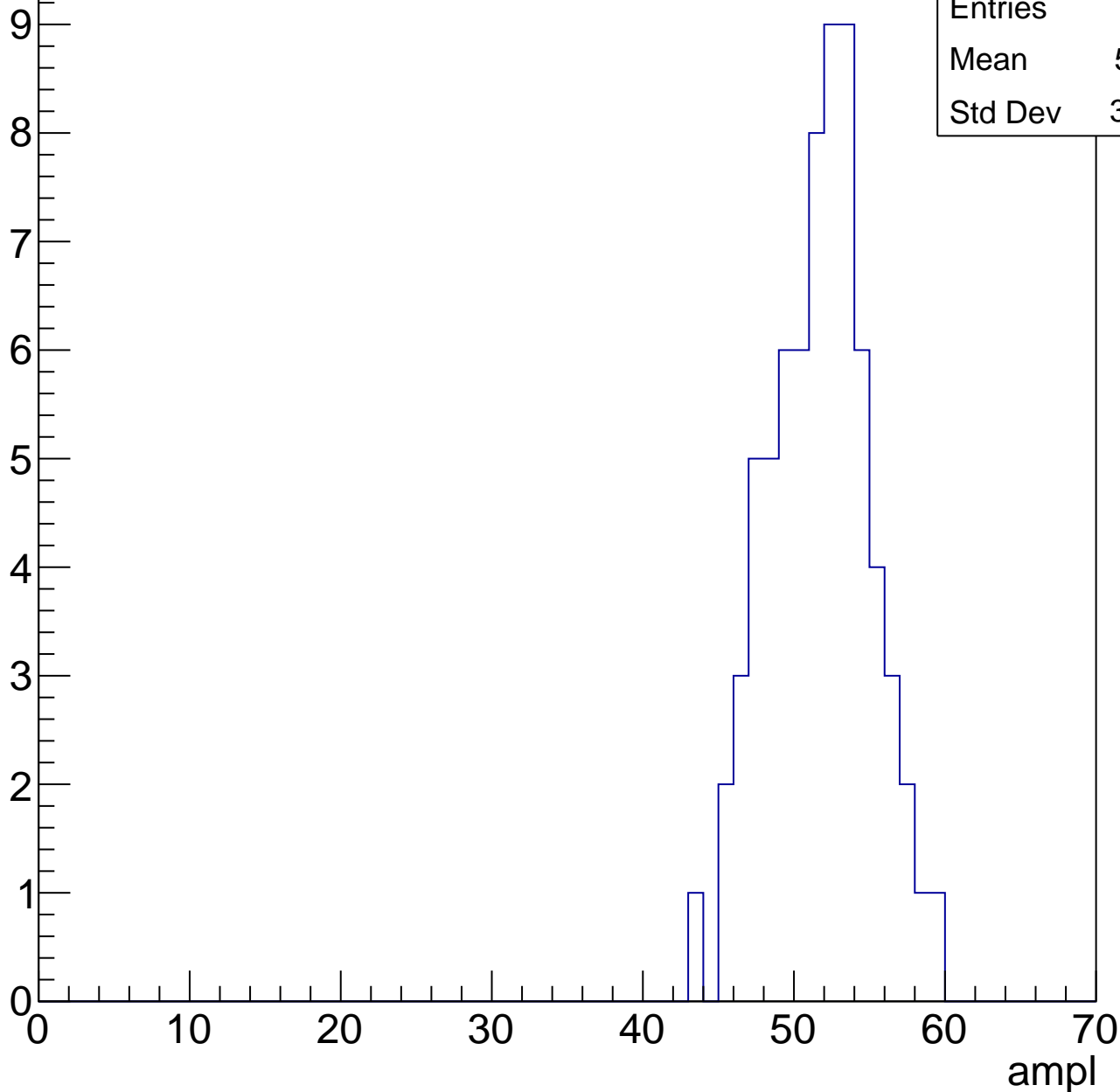


B0L001S, U22-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.21
Std Dev	3.327

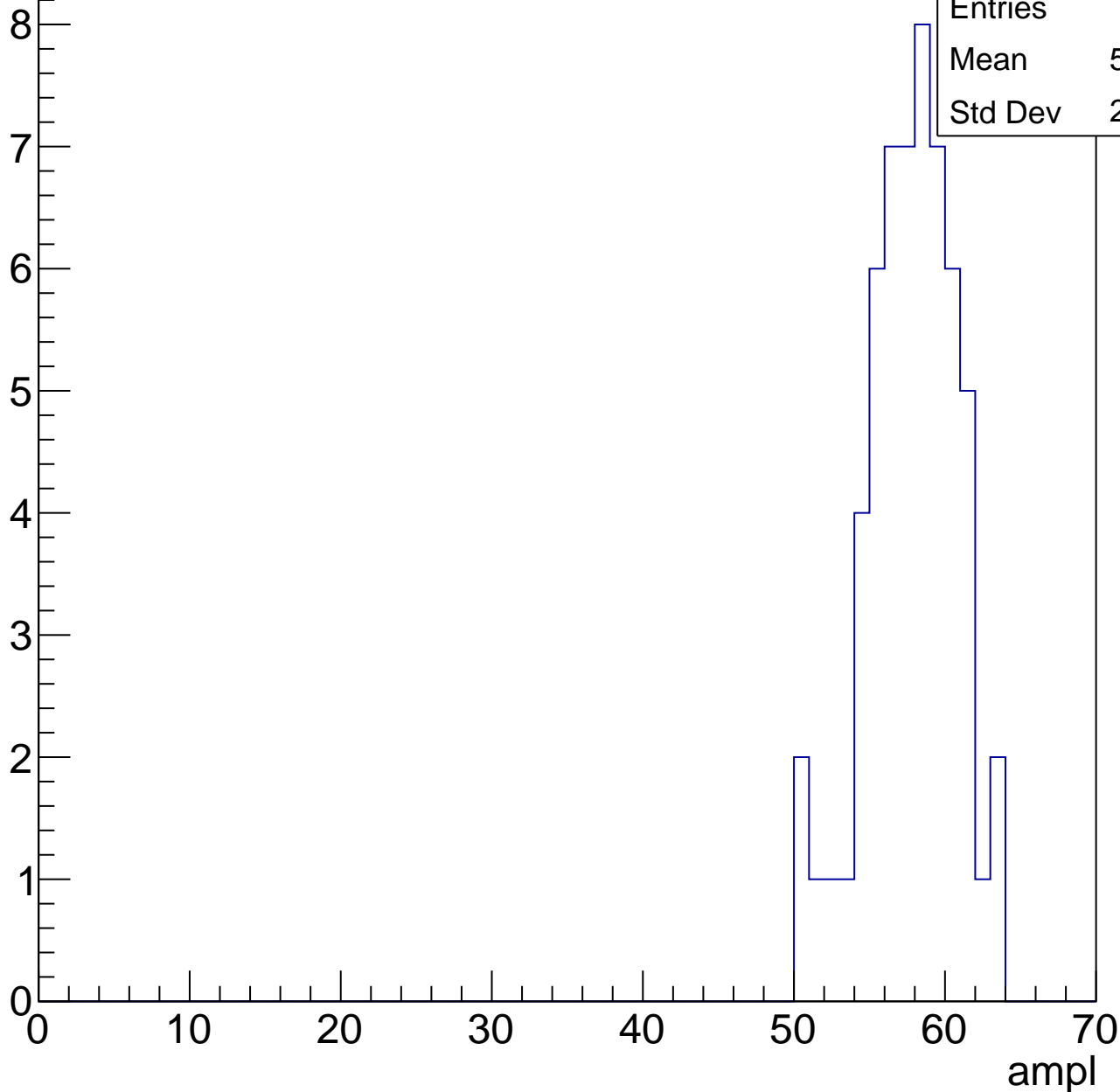


B0L001S, U22-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.29
Std Dev	2.948

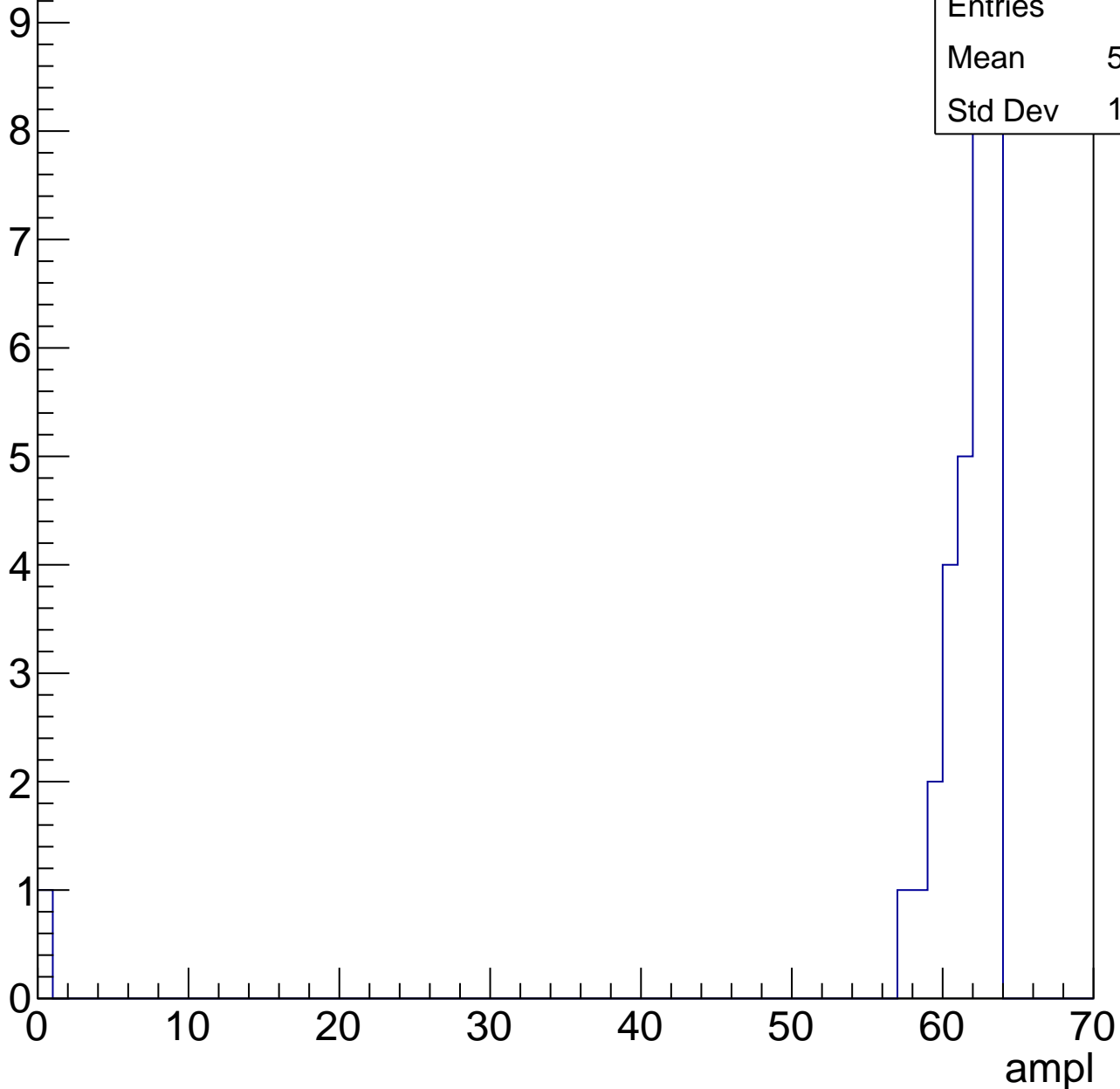


B0L001S, U22-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59.35
Std Dev	10.95



B0L001S, U22-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch59, adc0

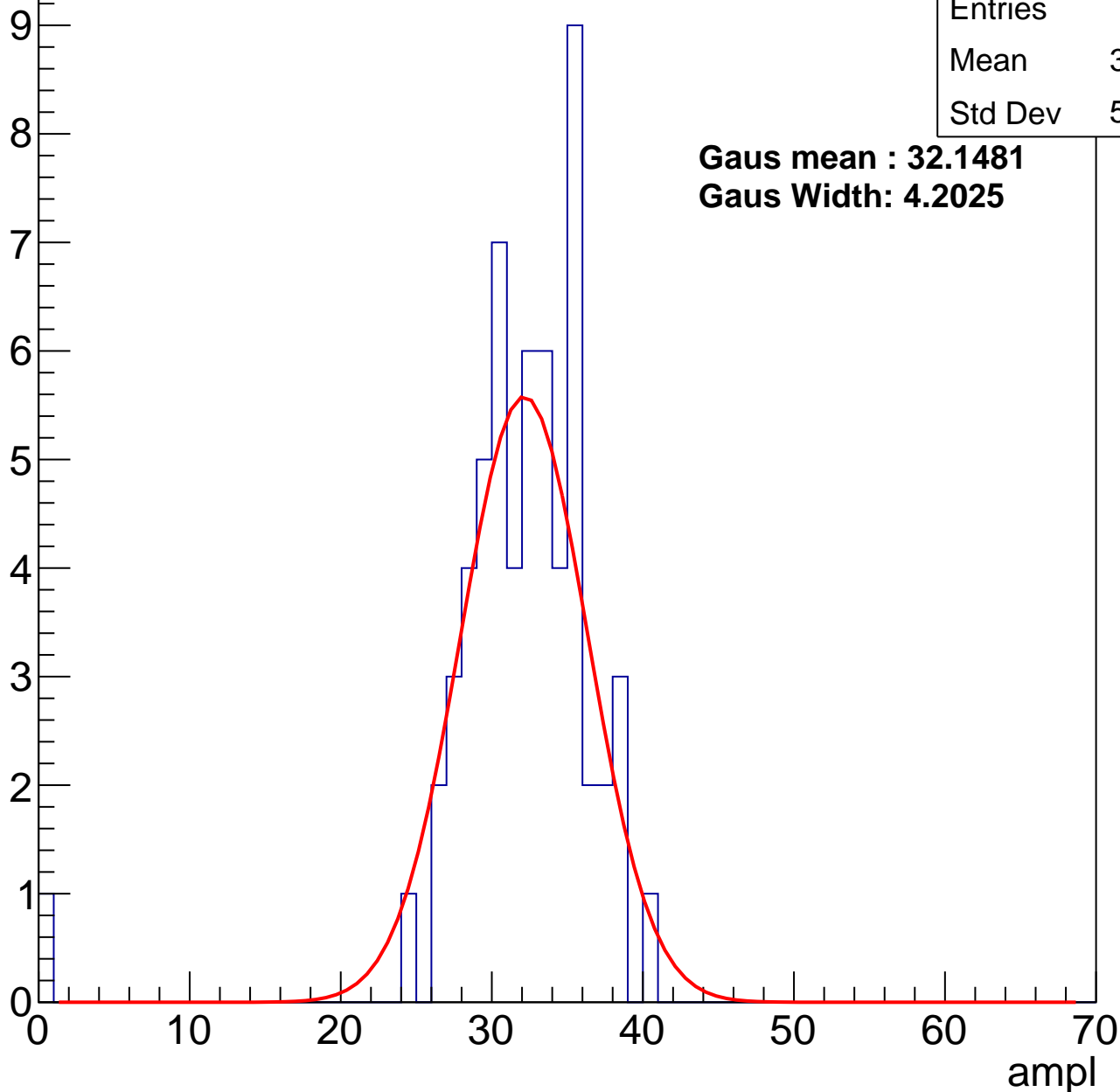
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	31.48
Std Dev	5.349

Gaus mean : 32.1481

Gaus Width: 4.2025



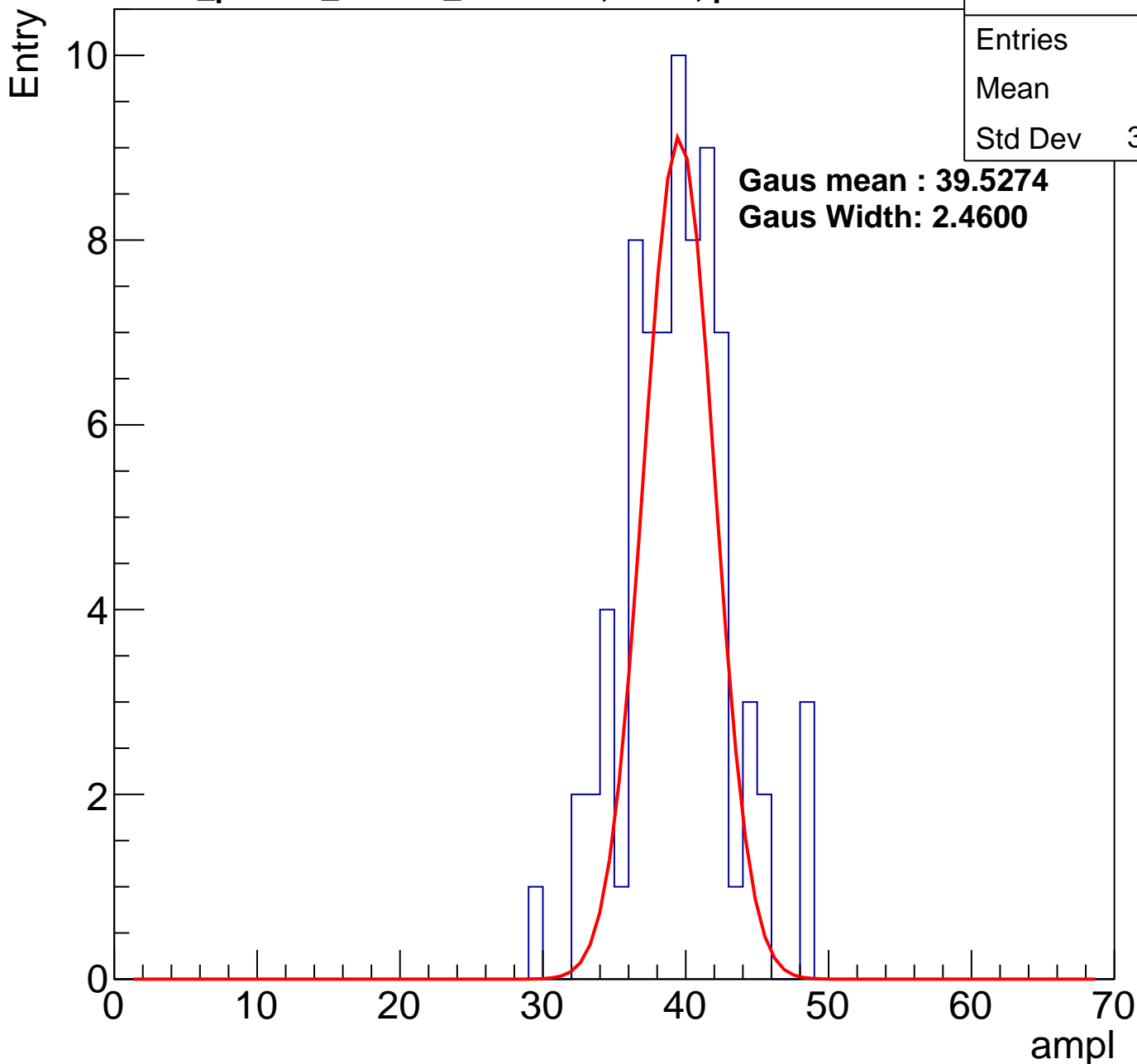
B0L001S, U22-ch59, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	39
Std Dev	3.662

Gaus mean : 39.5274

Gaus Width: 2.4600

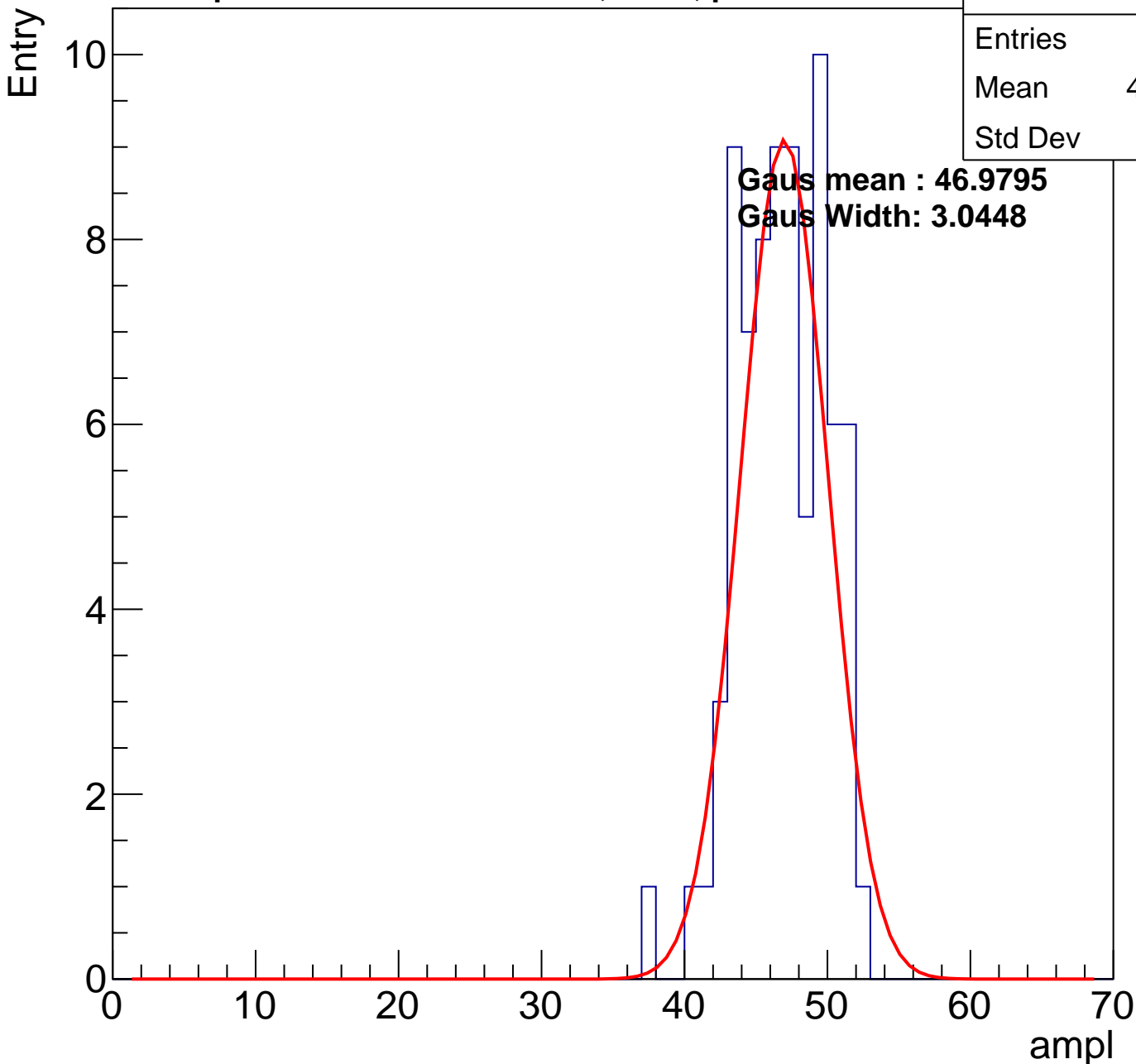


B0L001S, U22-ch59, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	46.37
Std Dev	3.03

Gaus mean : 46.9795
Gaus Width: 3.0448

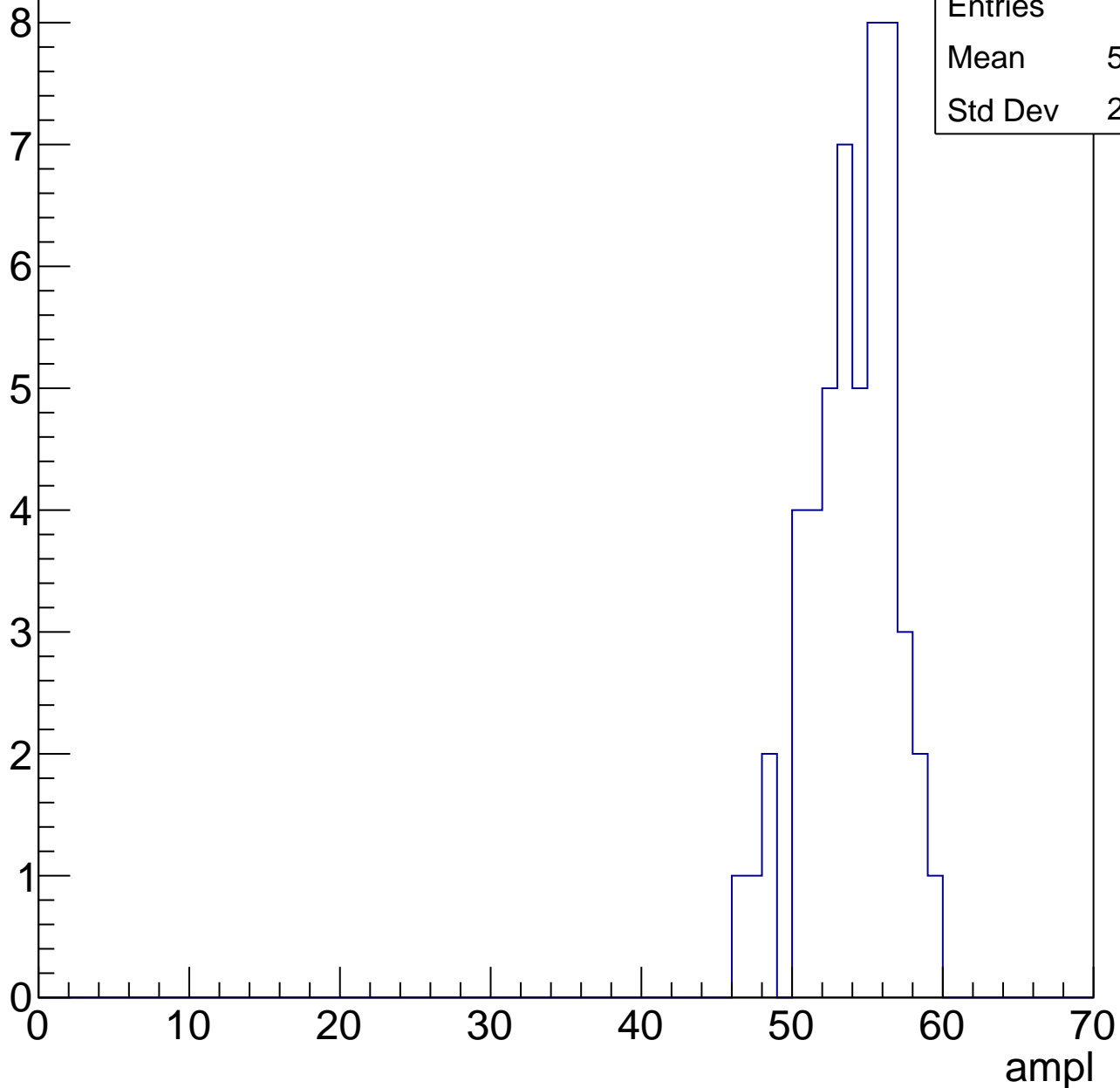


B0L001S, U22-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

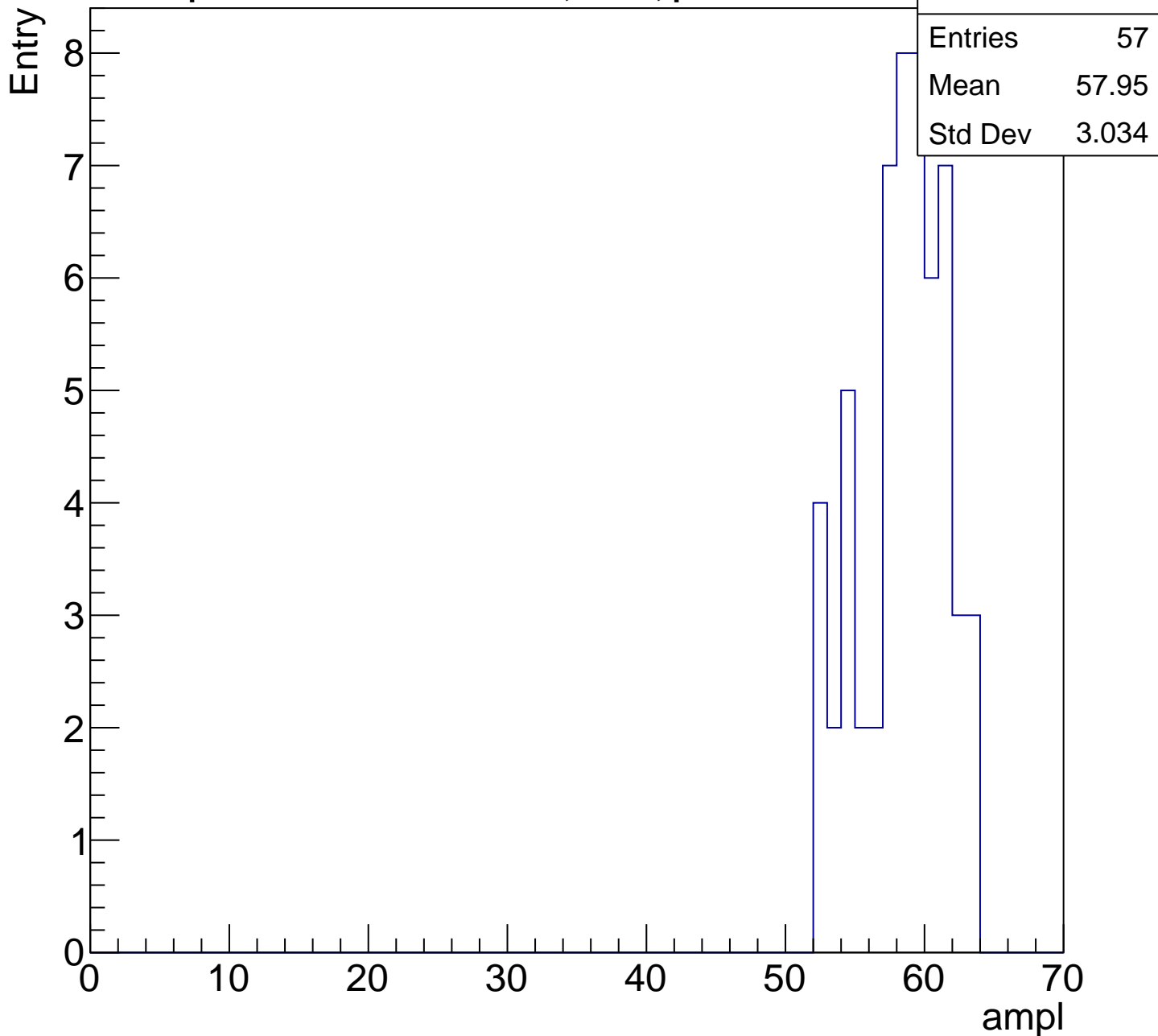
Entry

Entries	51
Mean	53.49
Std Dev	2.879



B0L001S, U22-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

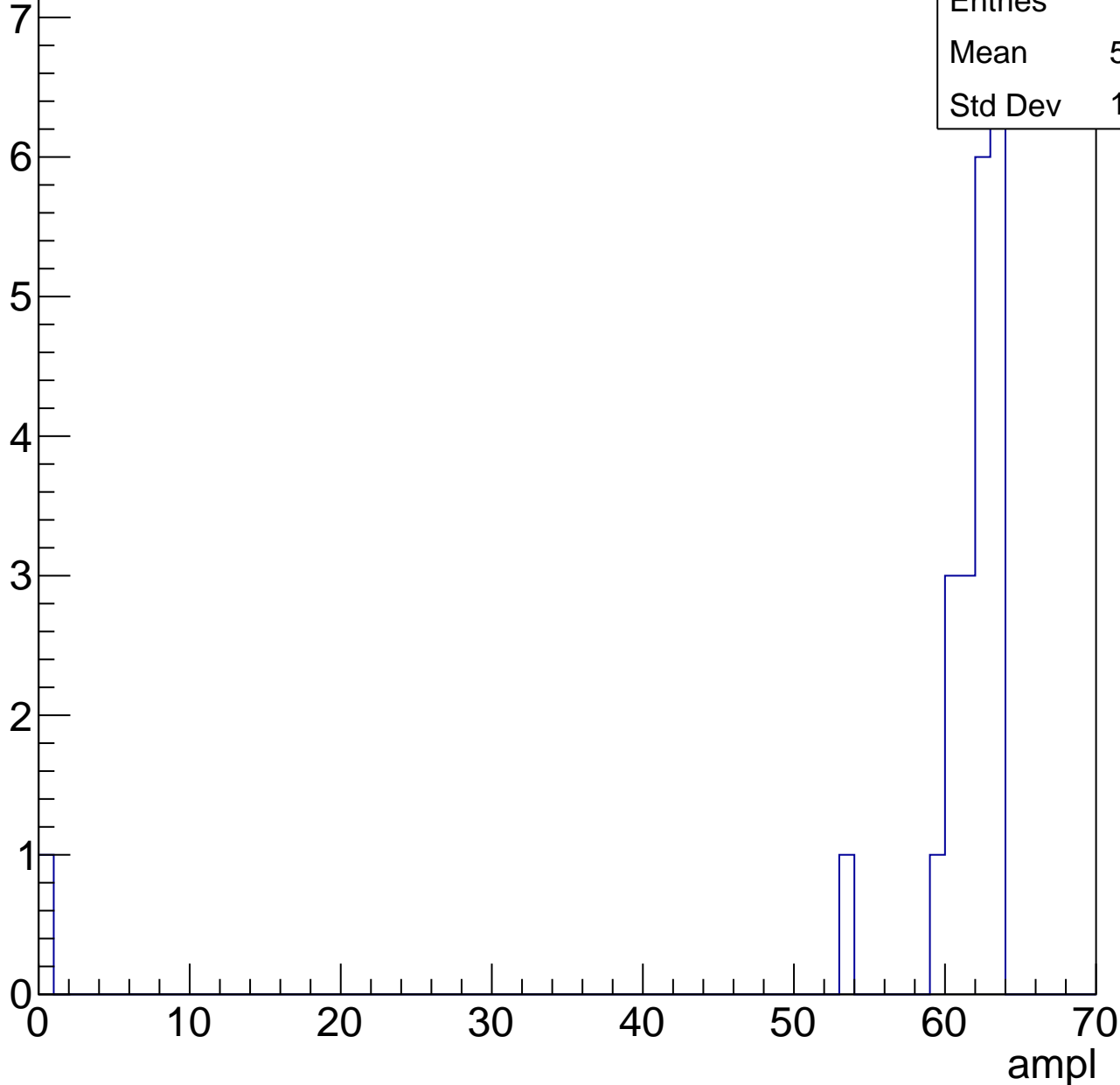


B0L001S, U22-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.55
Std Dev	12.96



B0L001S, U22-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch60, adc0

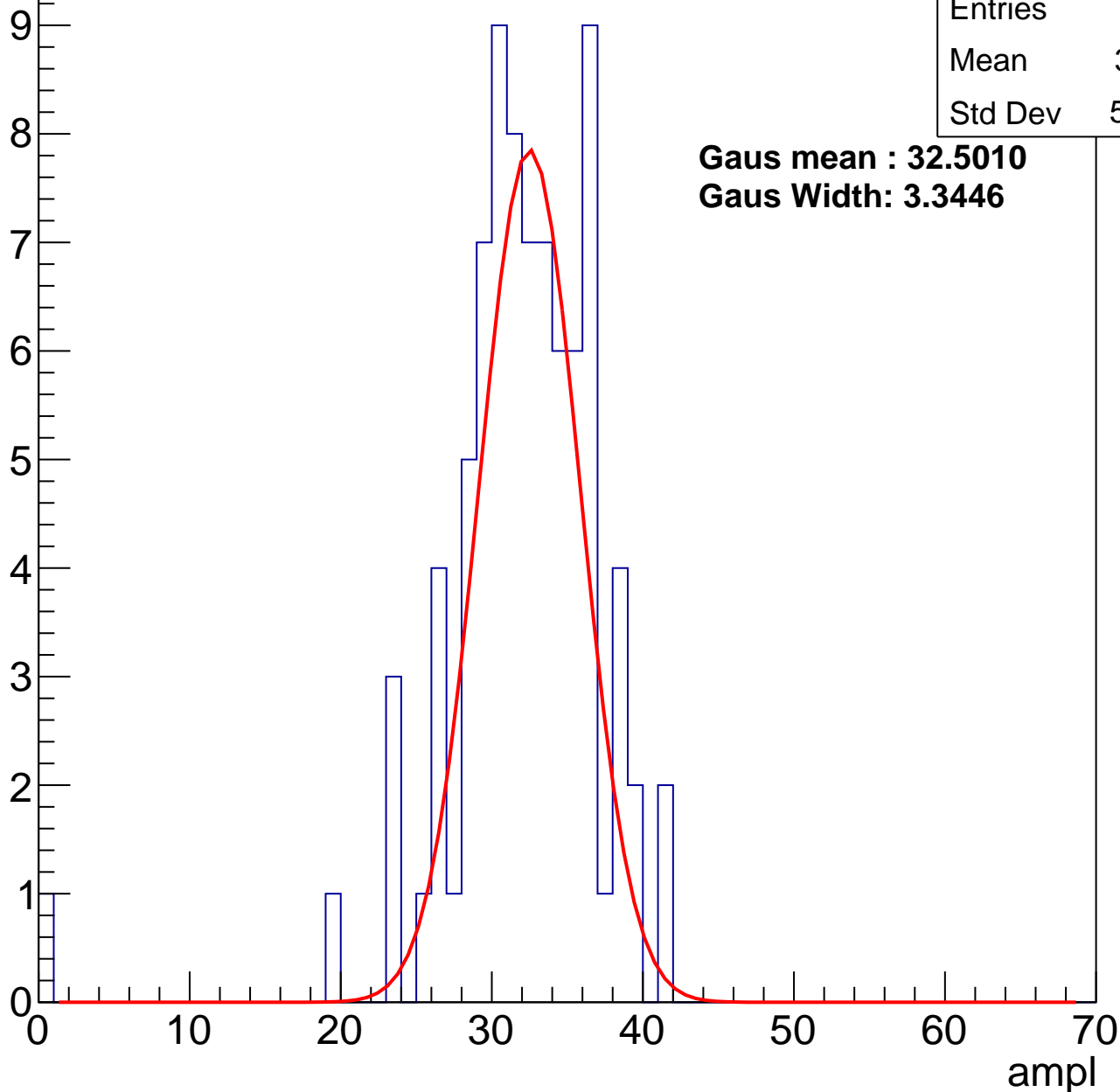
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	31.51
Std Dev	5.432

Gaus mean : 32.5010

Gaus Width: 3.3446



B0L001S, U22-ch60, adc1

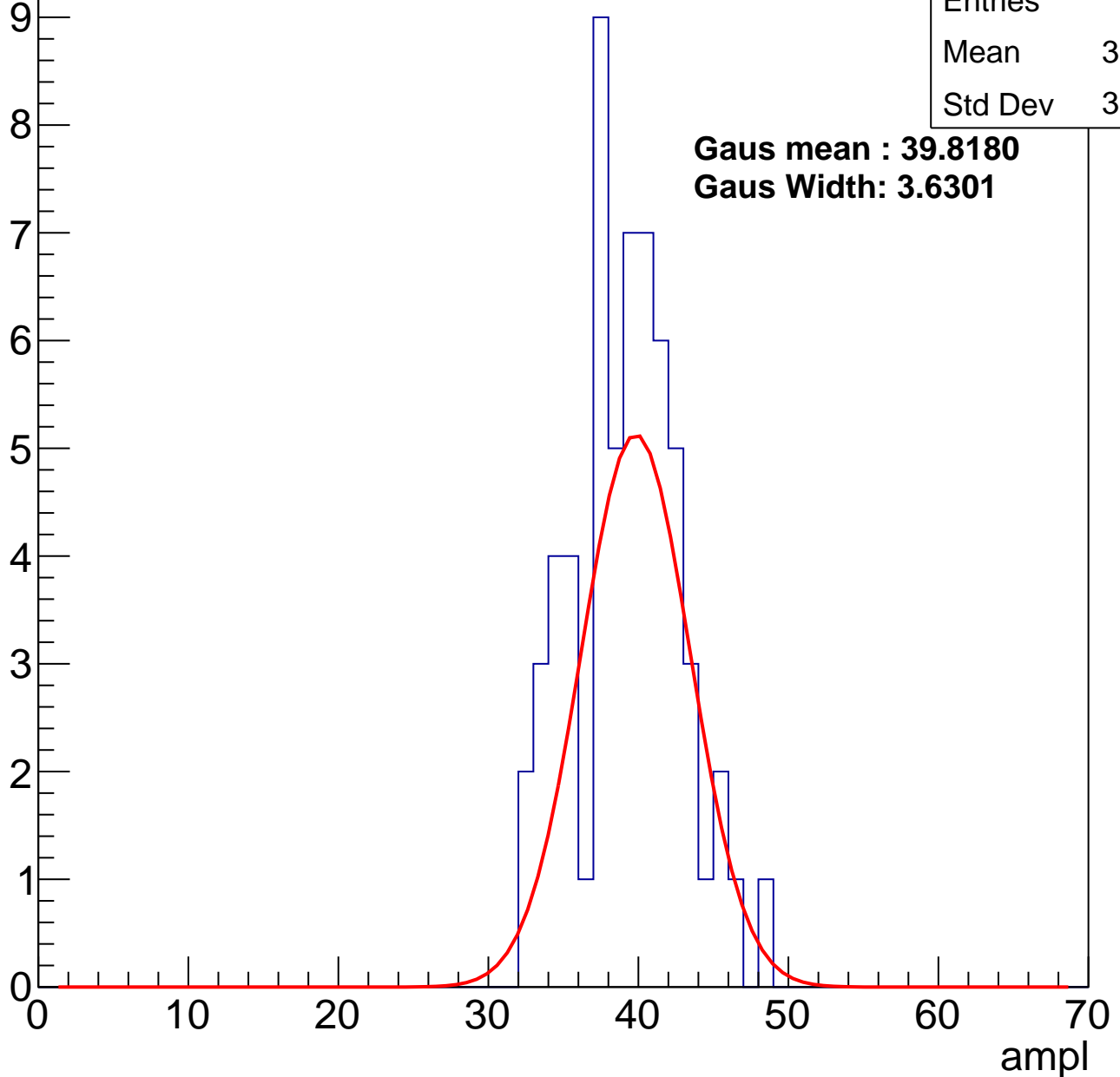
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	38.75
Std Dev	3.547

Gaus mean : 39.8180

Gaus Width: 3.6301



B0L001S, U22-ch60, adc2

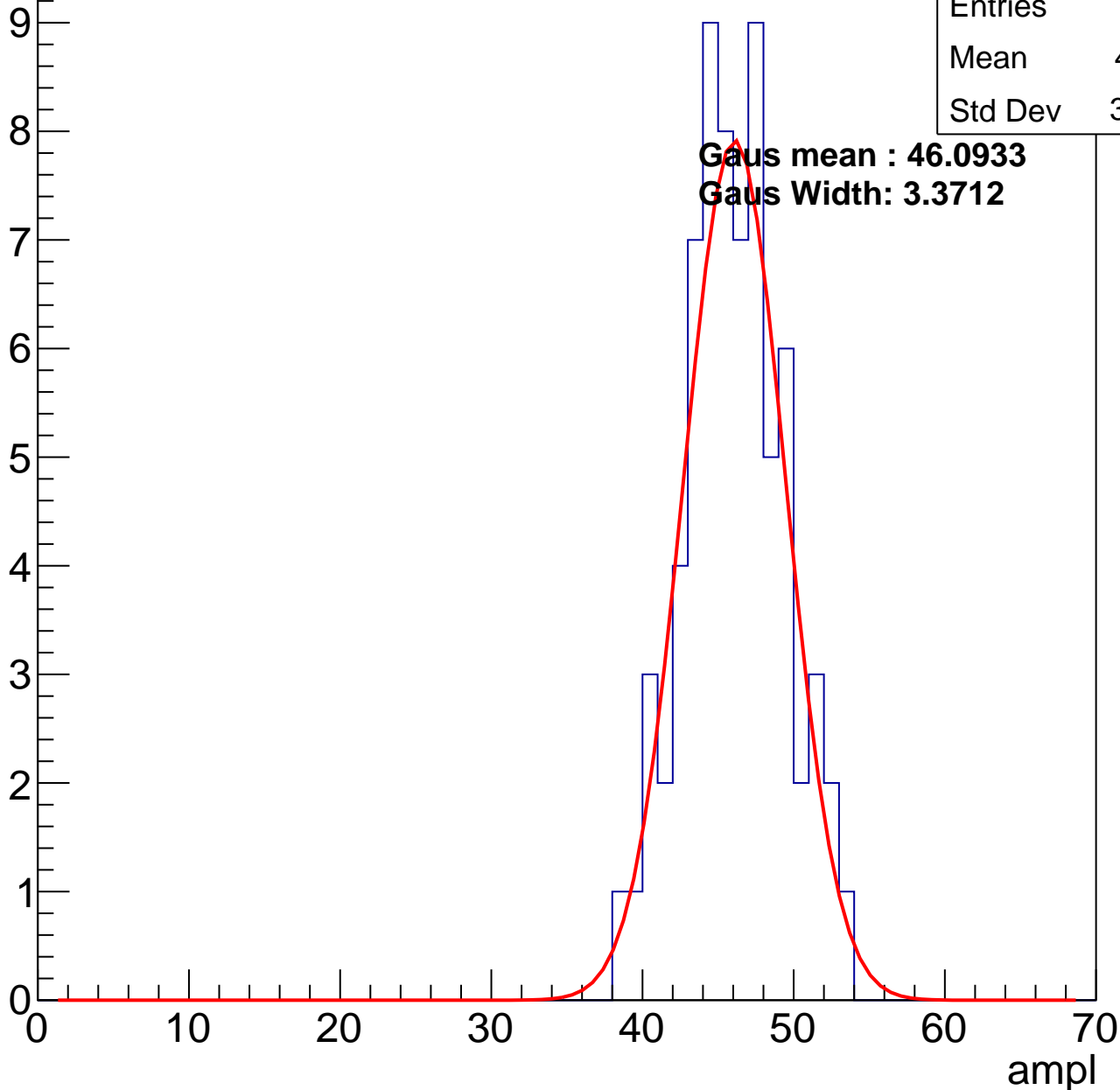
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	45.61
Std Dev	3.257

Gaus mean : 46.0933

Gaus Width: 3.3712

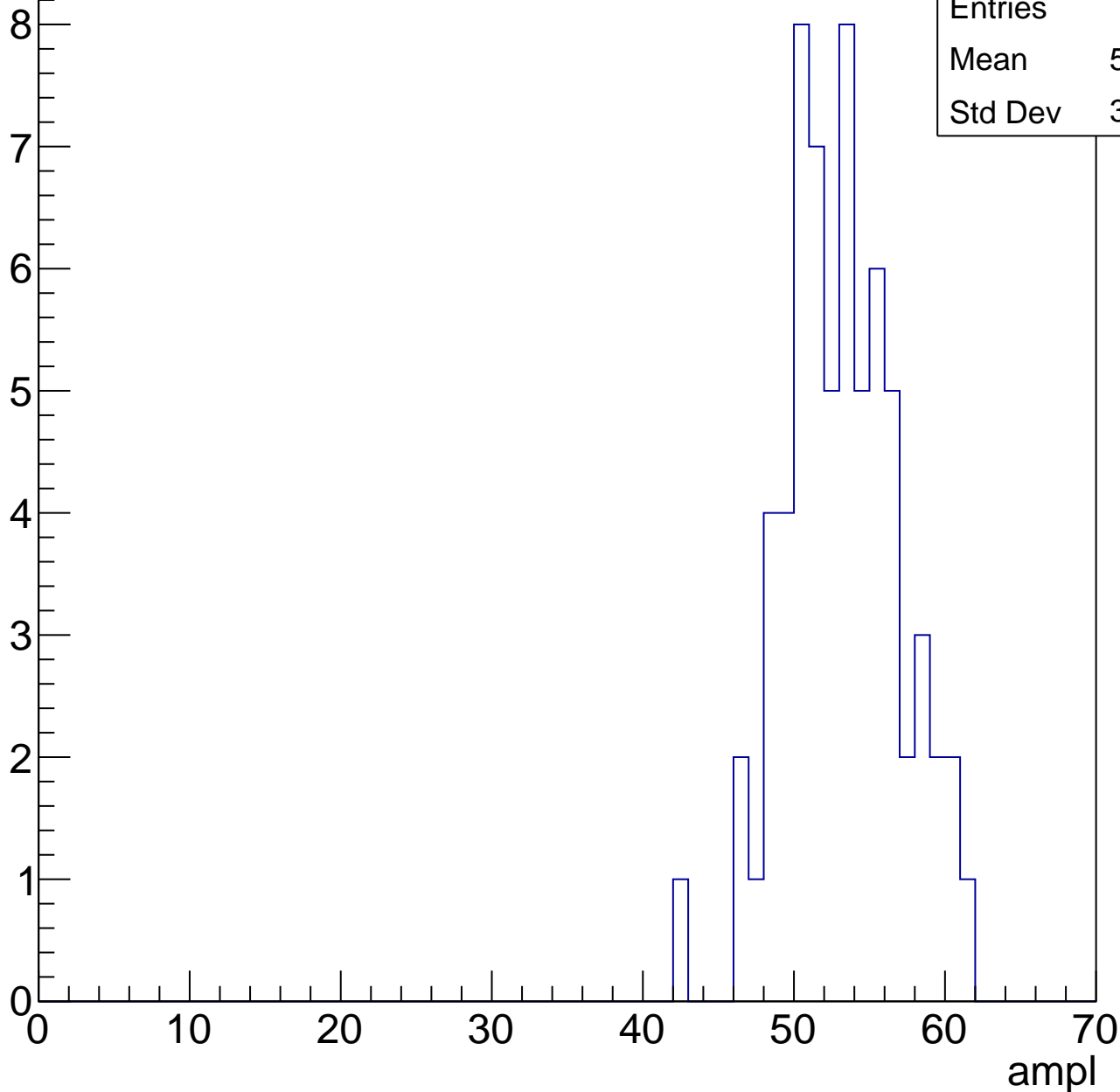


B0L001S, U22-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

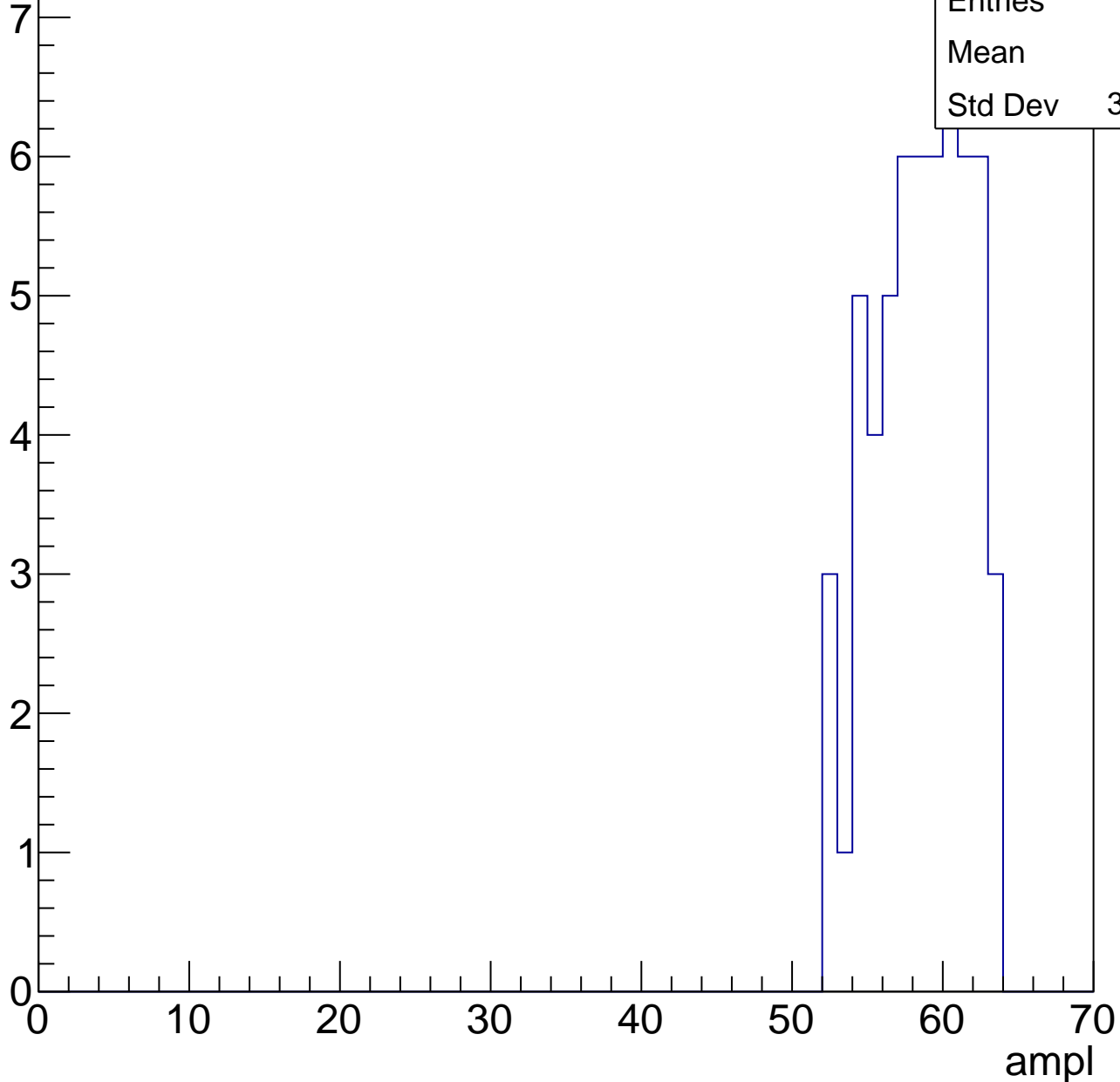
Entries	66
Mean	52.68
Std Dev	3.758



B0L001S, U22-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



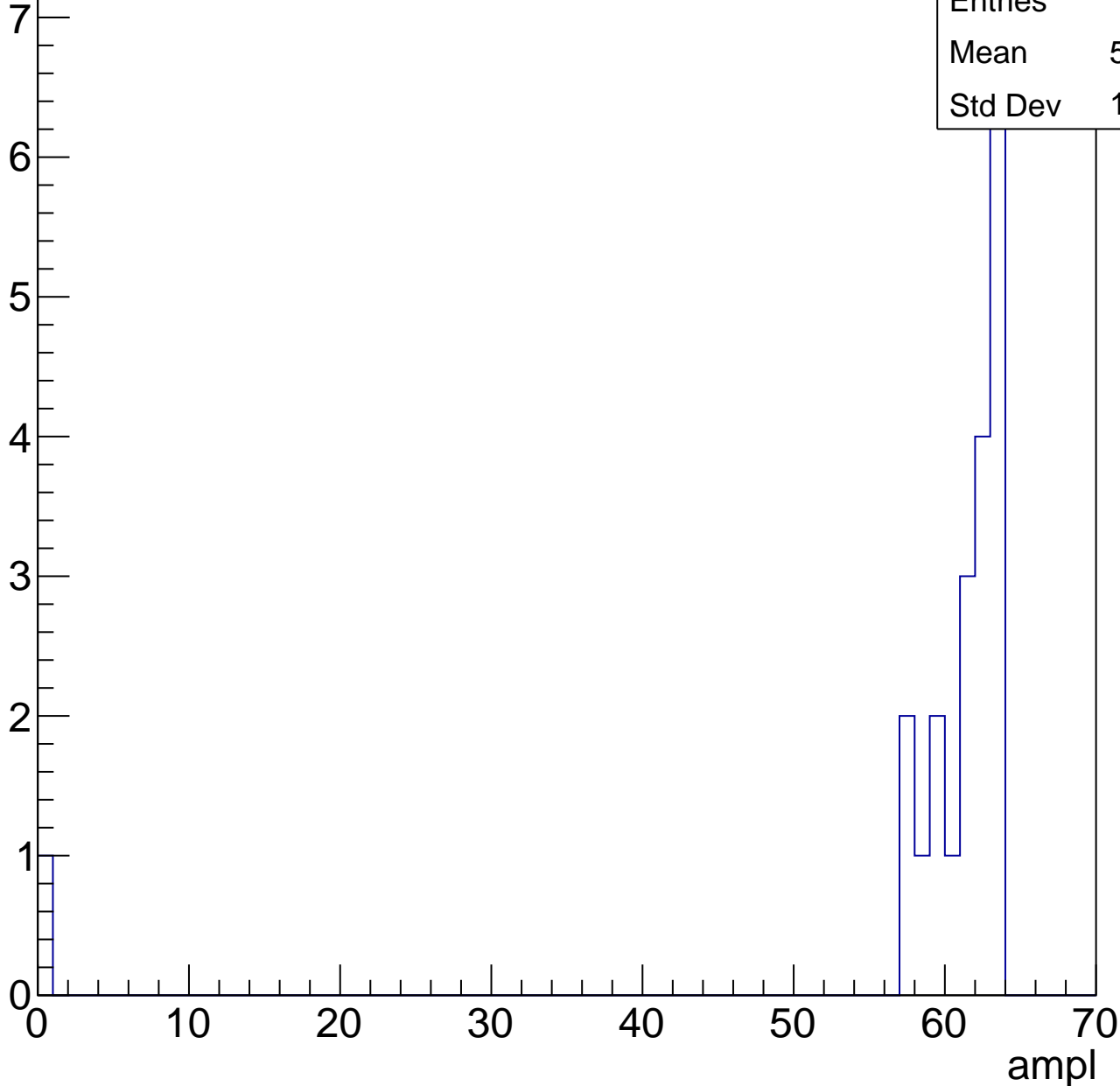
Entries	58
Mean	58.1
Std Dev	3.033

B0L001S, U22-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	21
Mean	58.19
Std Dev	13.16



B0L001S, U22-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch61, adc0

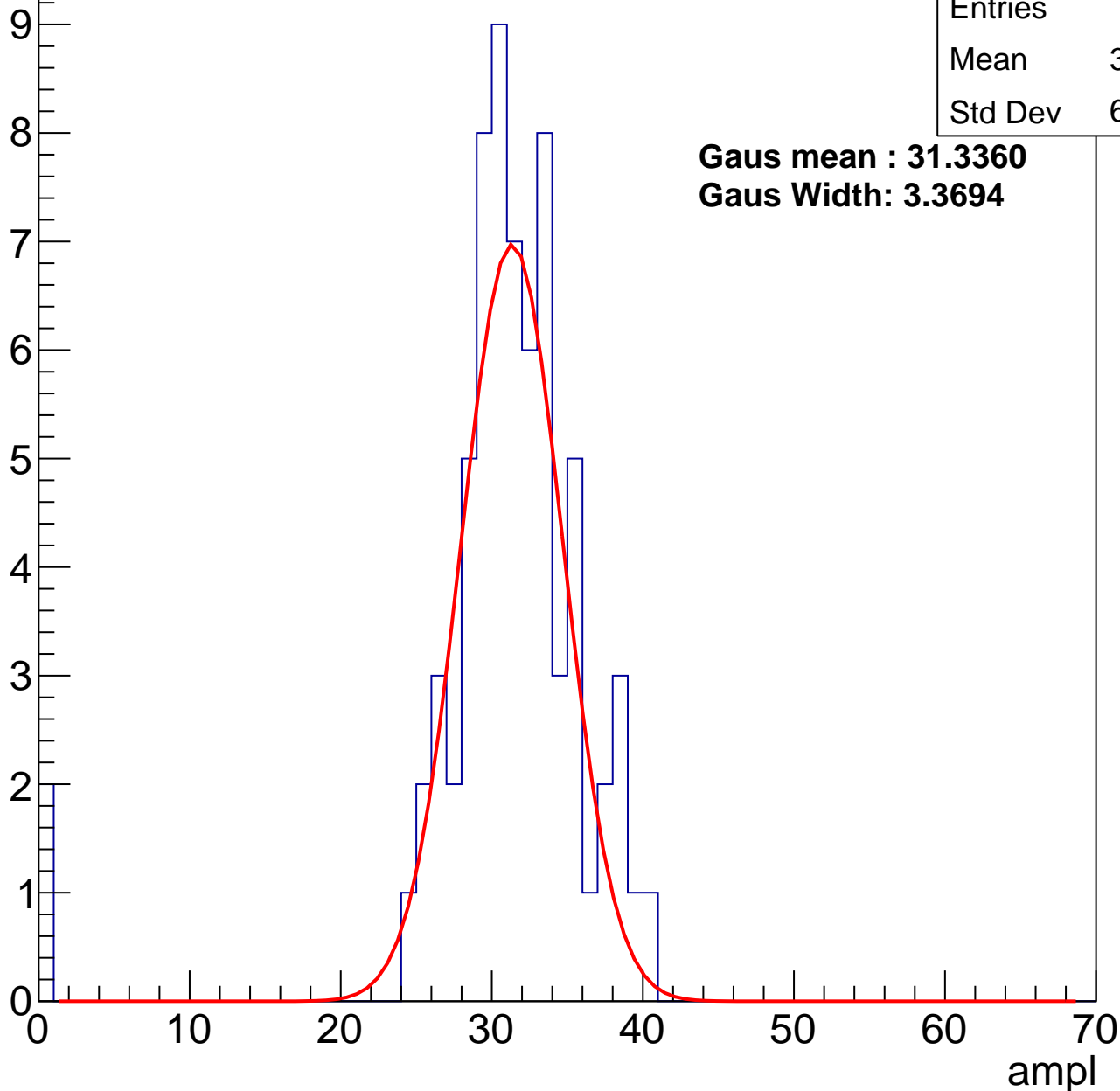
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.45
Std Dev	6.312

Gaus mean : 31.3360

Gaus Width: 3.3694



B0L001S, U22-ch61, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	38.38
Std Dev	3.928

Gaus mean : 39.3241

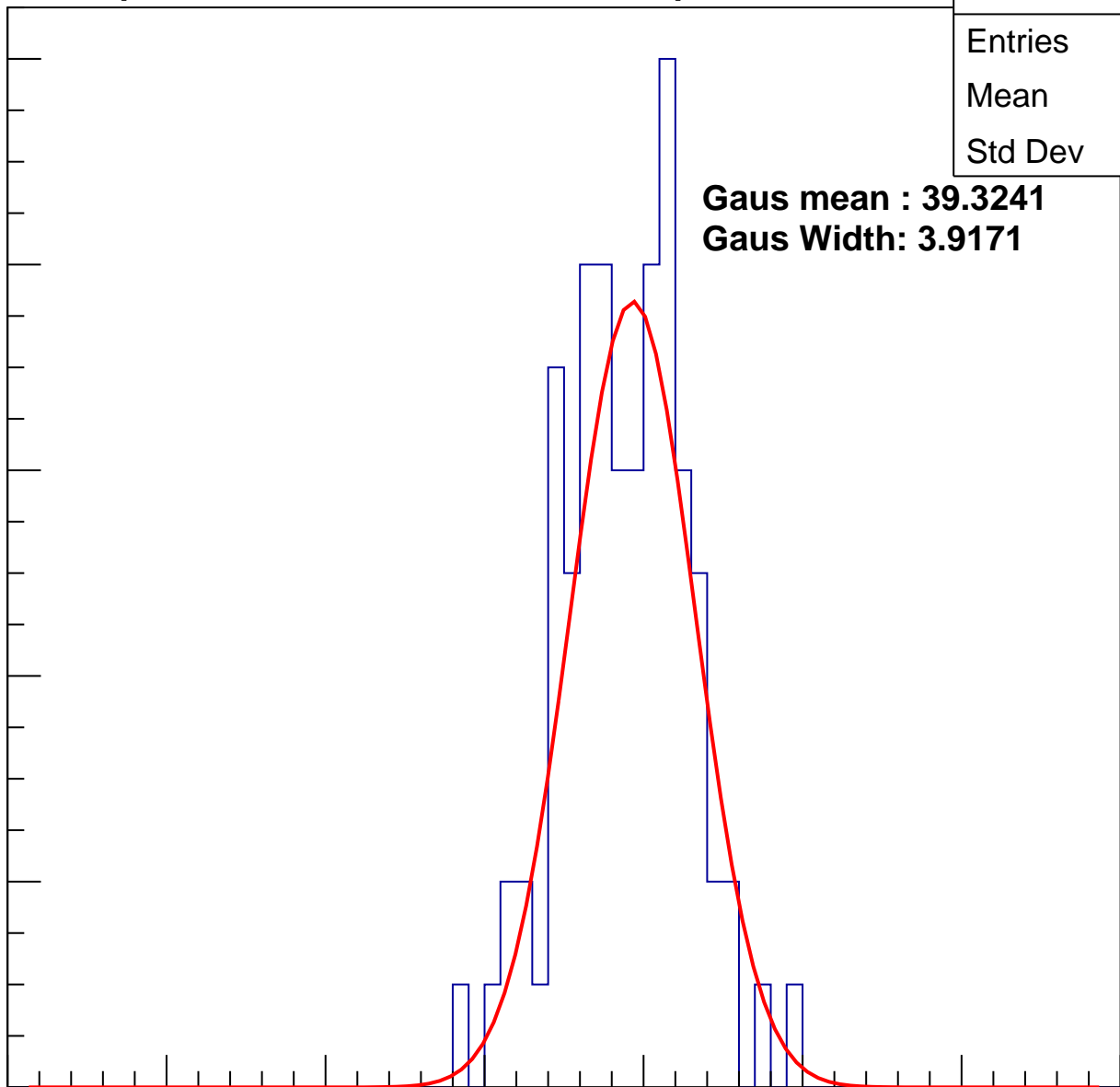
Gaus Width: 3.9171

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch61, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	46.01
Std Dev	3.663

Gaus mean : 46.6728

Gaus Width: 3.9071

Entry

10

8

6

4

2

0

0

10

20

30

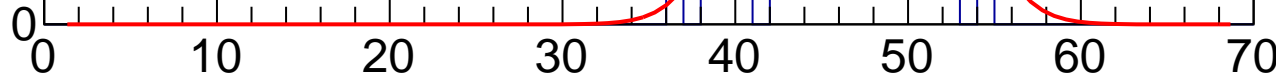
40

50

60

70

ampl

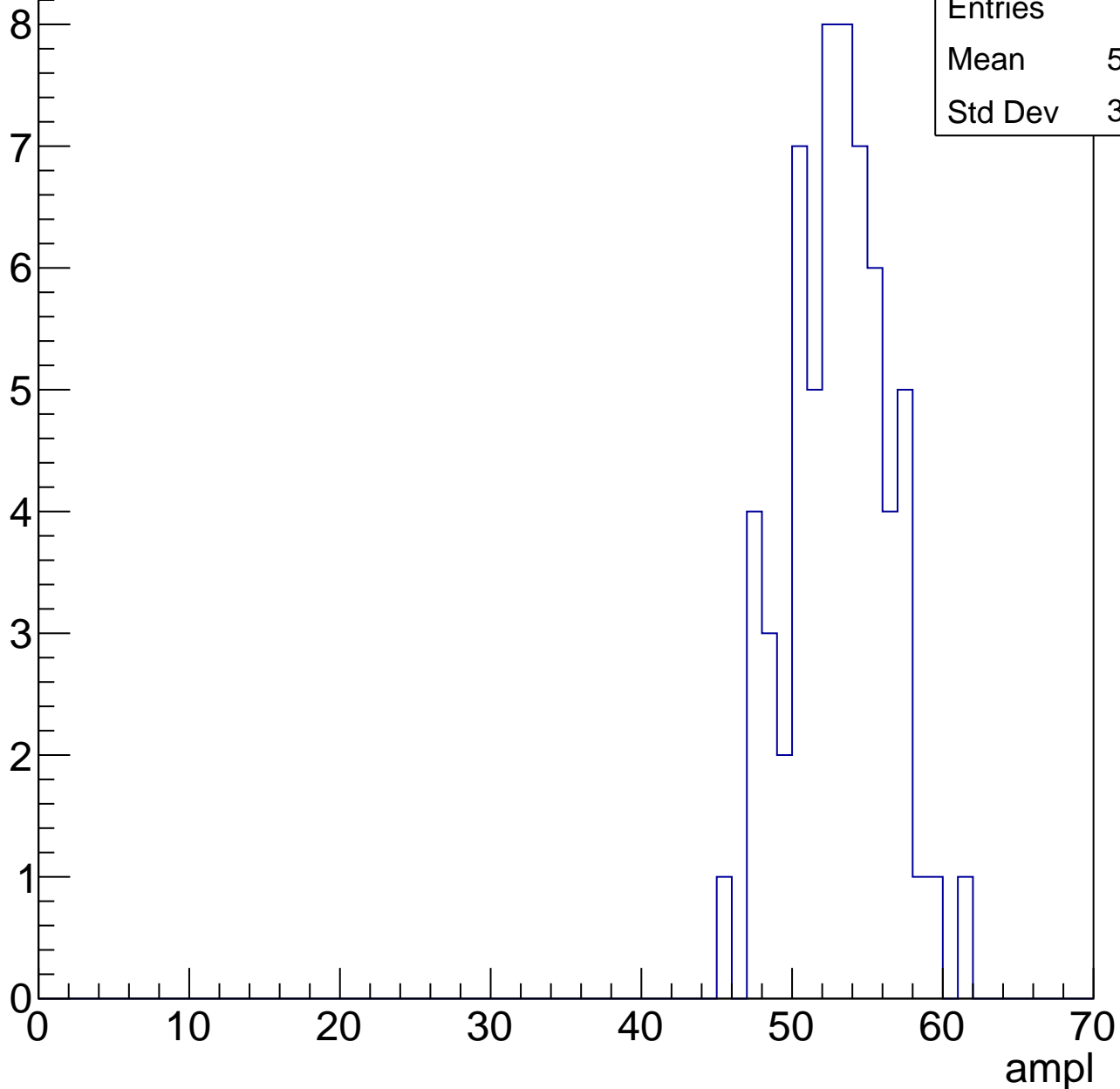


B0L001S, U22-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.62
Std Dev	3.258

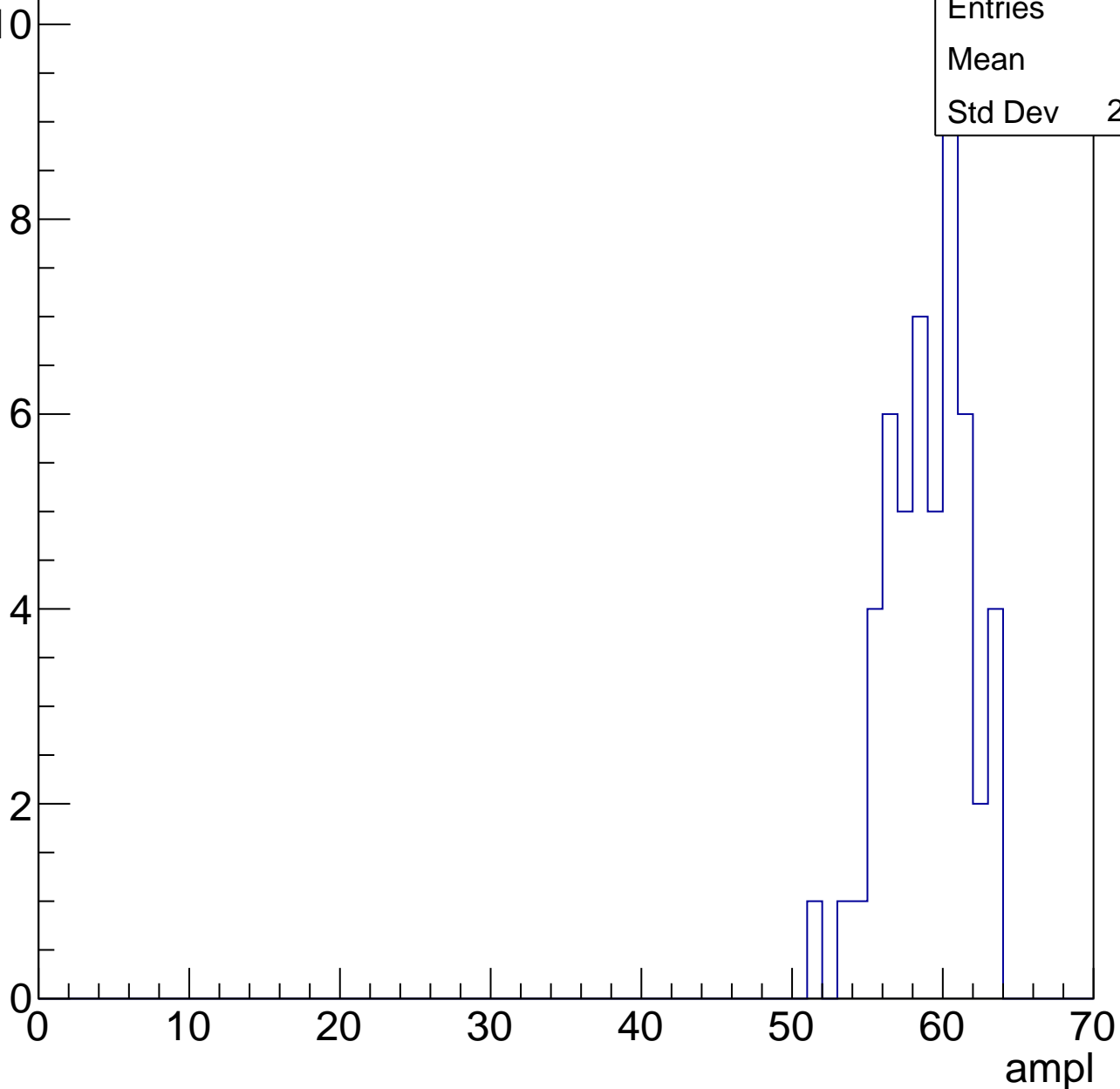


B0L001S, U22-ch61, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	58.5
Std Dev	2.678

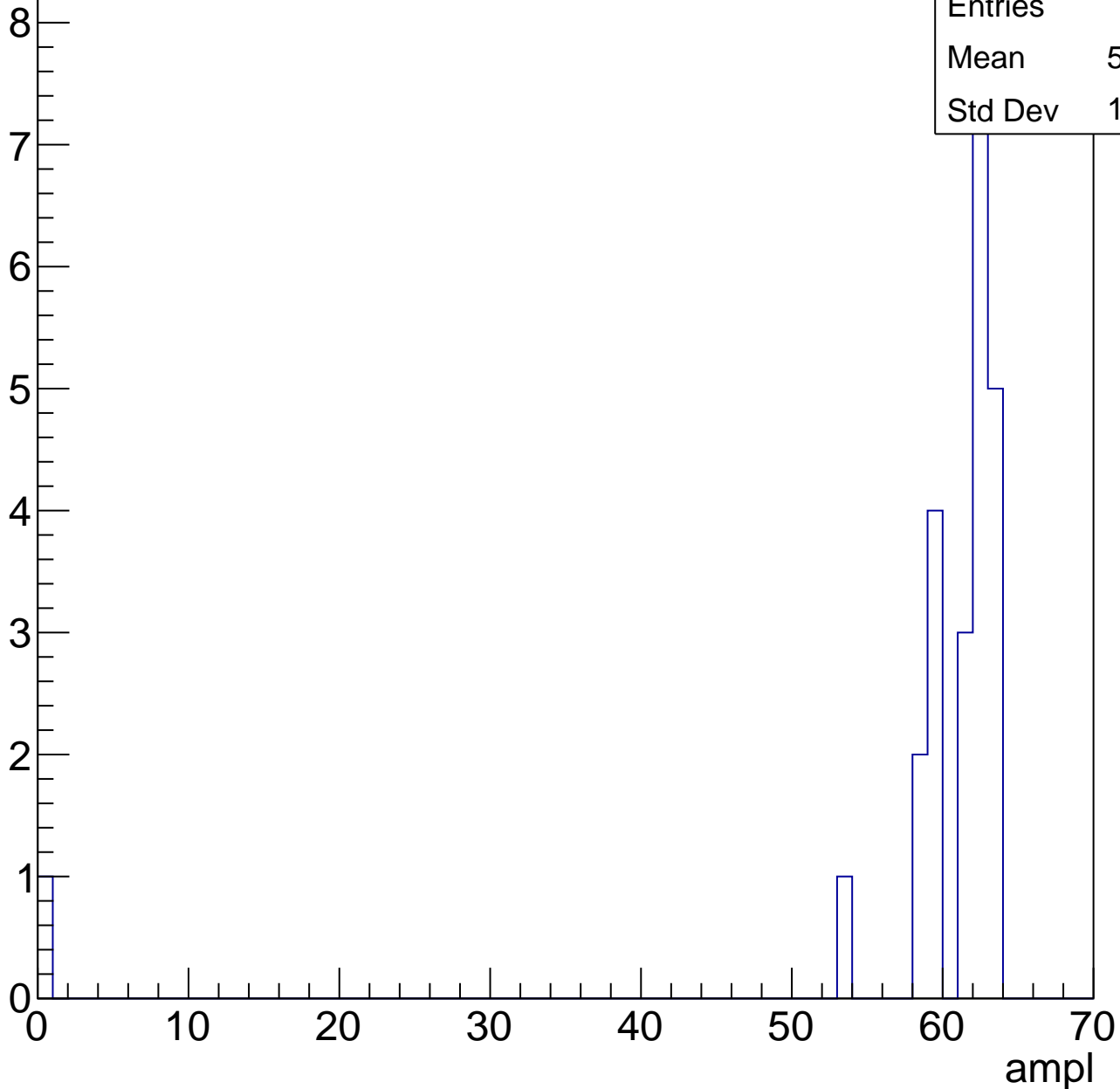


B0L001S, U22-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	58.29
Std Dev	12.37



B0L001S, U22-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch62, adc0

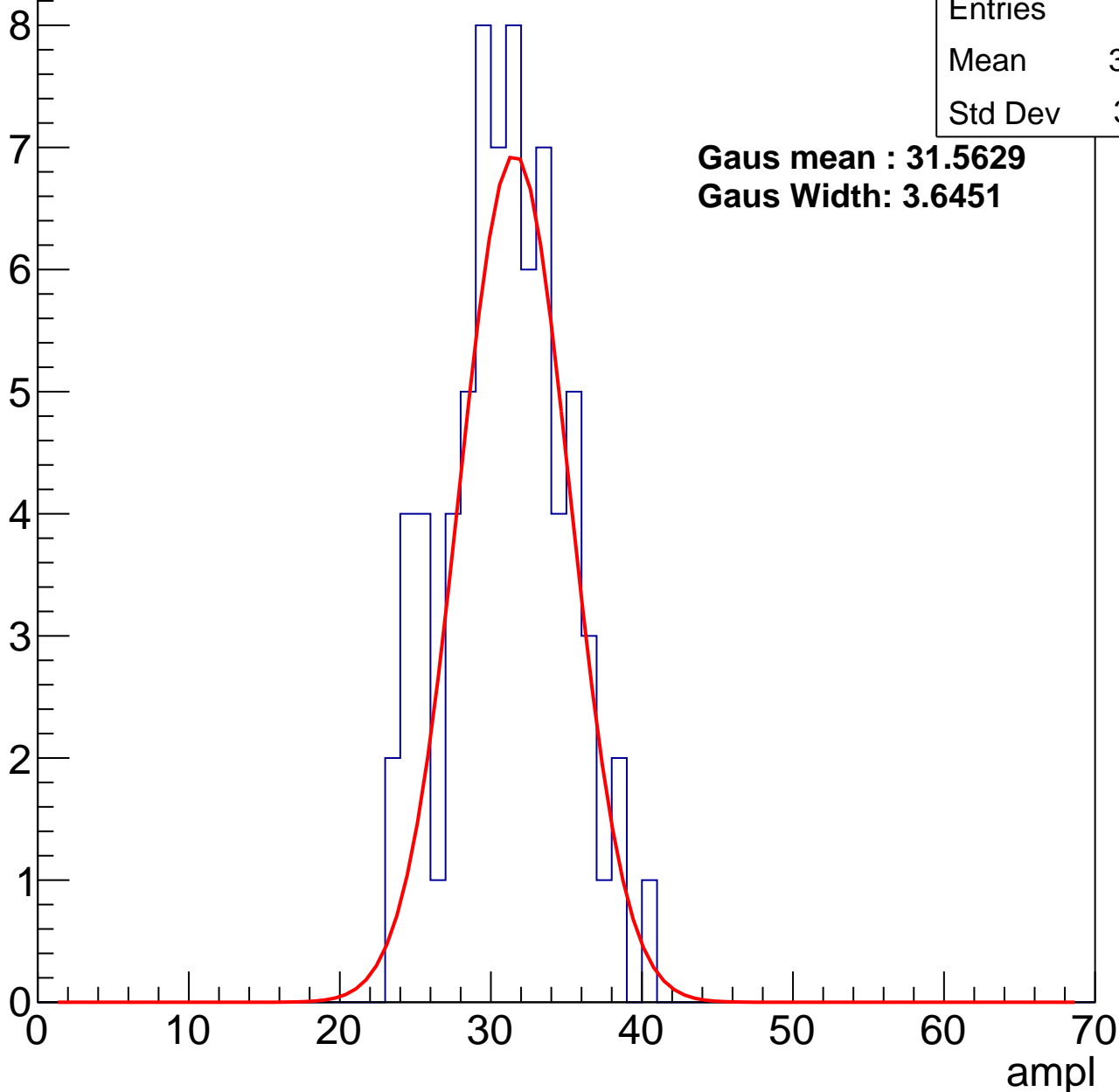
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	30.57
Std Dev	3.851

Gaus mean : 31.5629

Gaus Width: 3.6451



B0L001S, U22-ch62, adc1

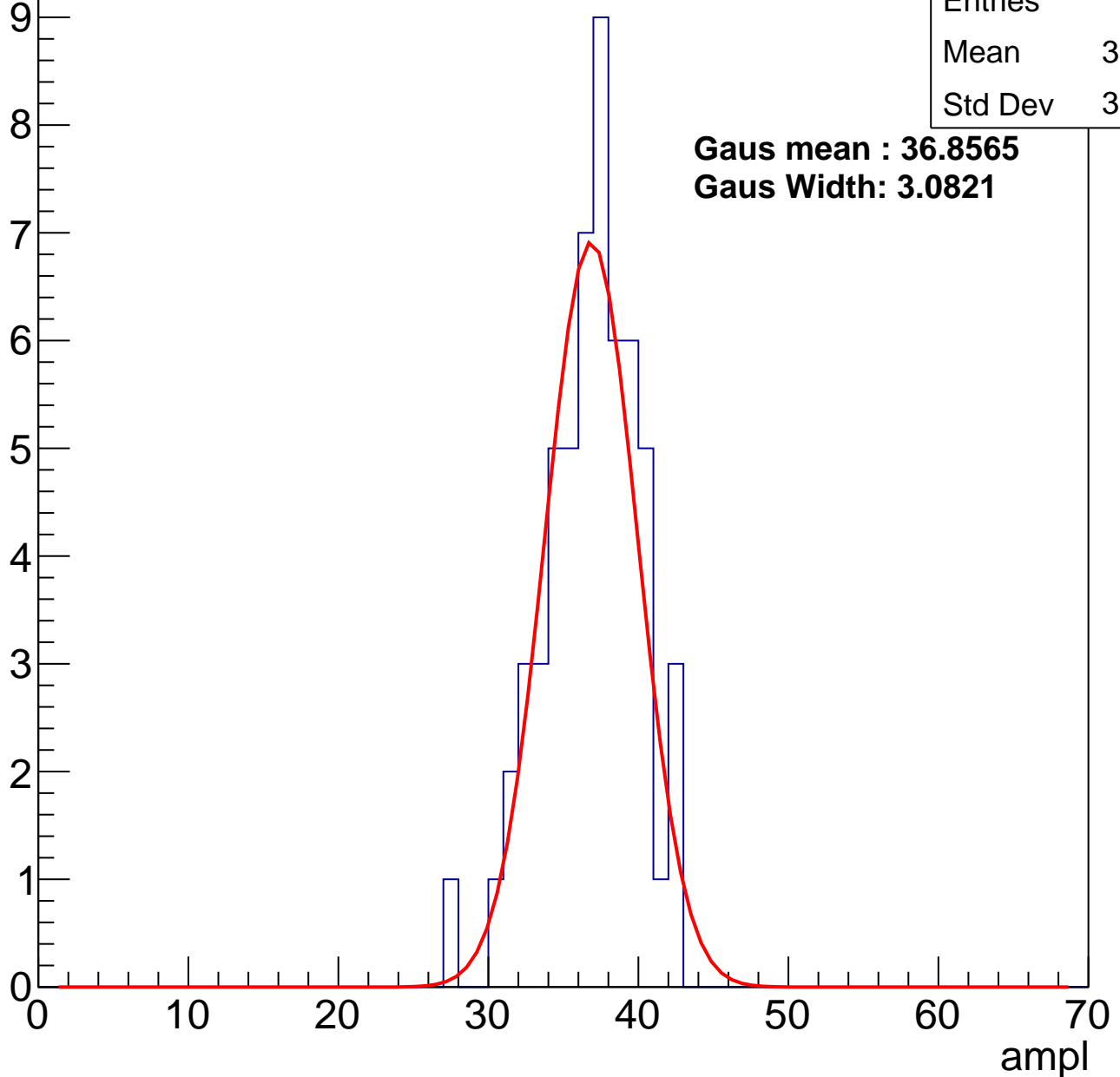
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	36.37
Std Dev	3.132

Gaus mean : 36.8565

Gaus Width: 3.0821



B0L001S, U22-ch62, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	43.06
Std Dev	3.146

Gaus mean : 43.8418

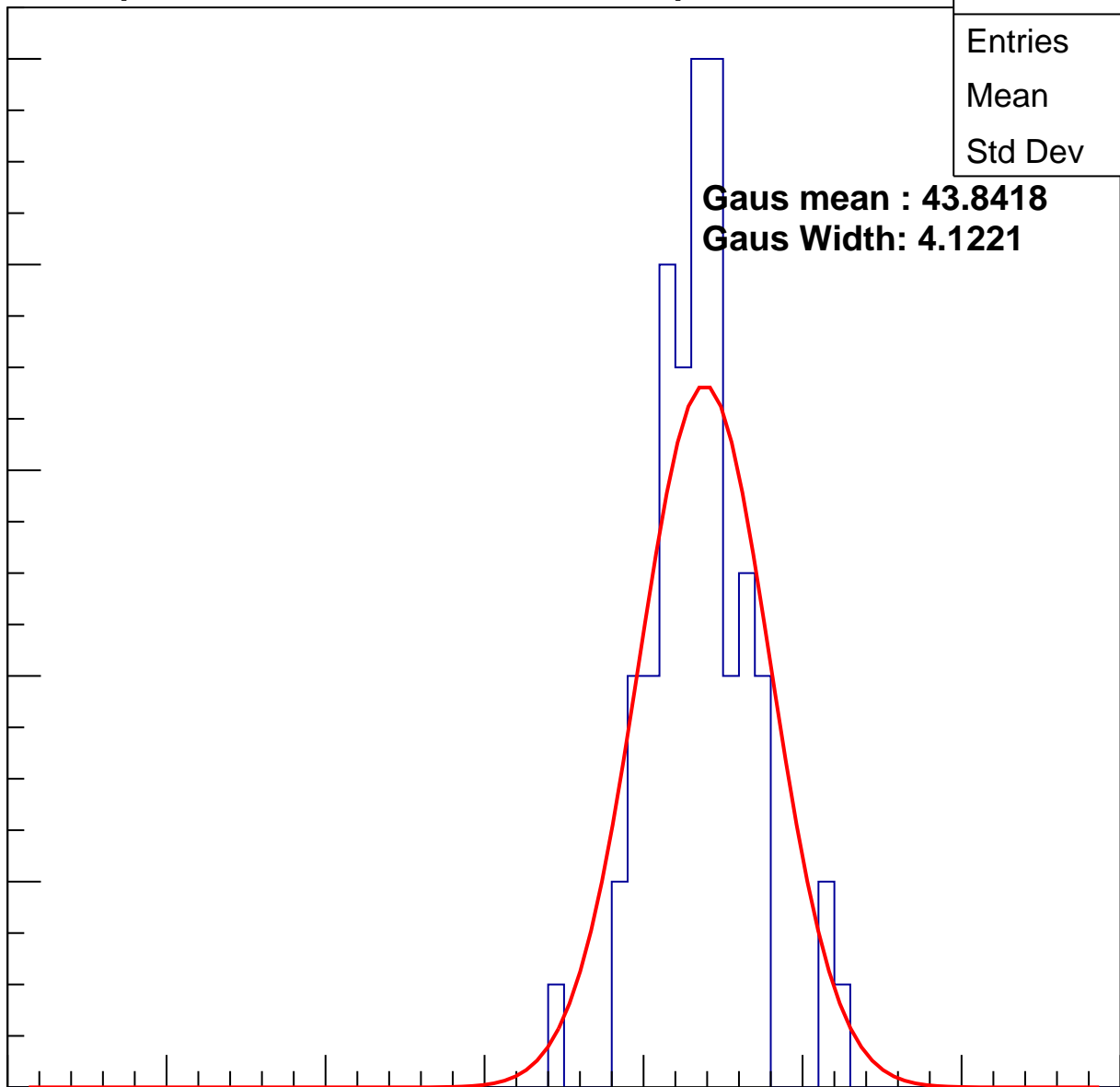
Gaus Width: 4.1221

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

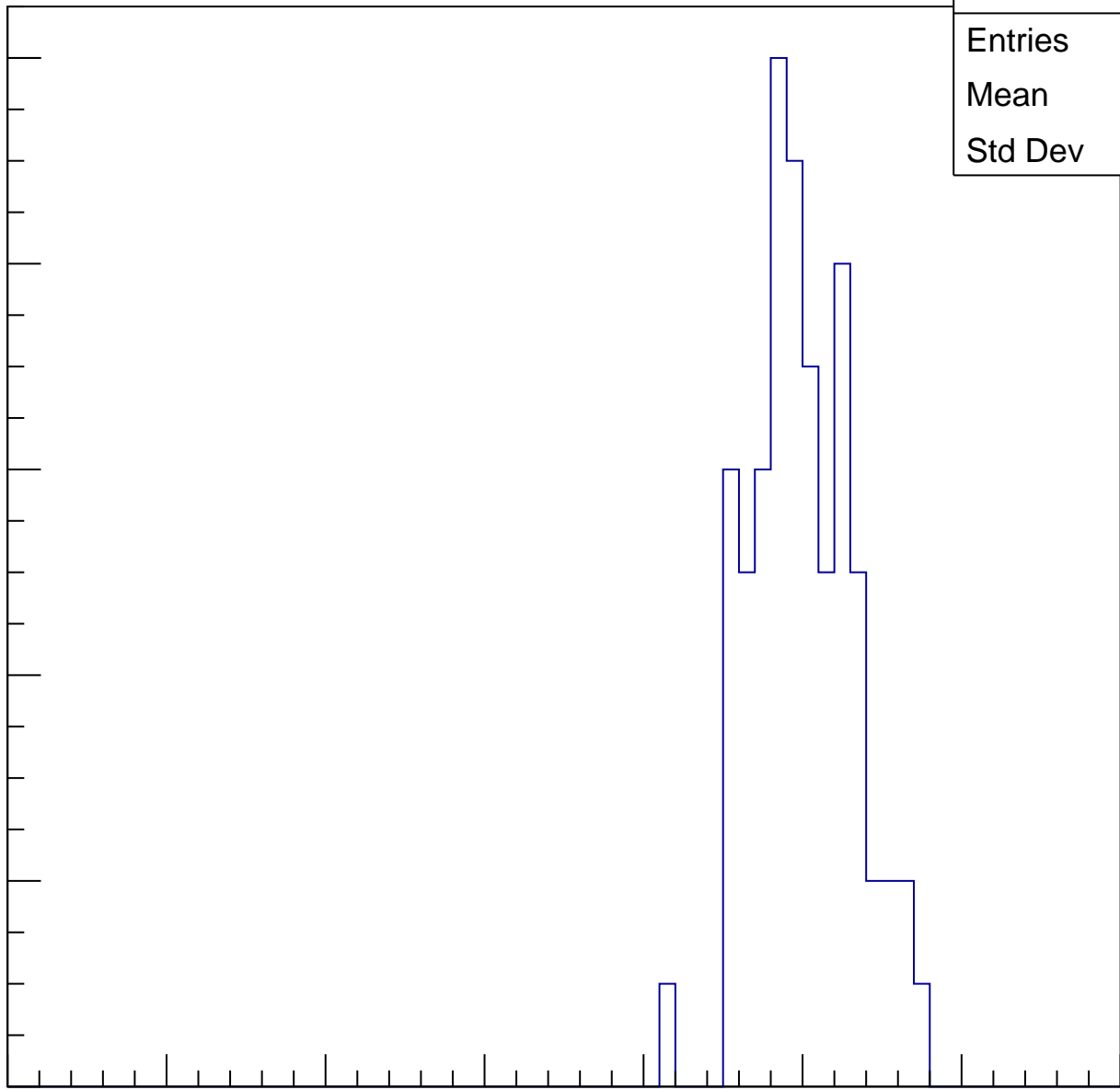
Entries	69
Mean	49.52
Std Dev	3.147

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

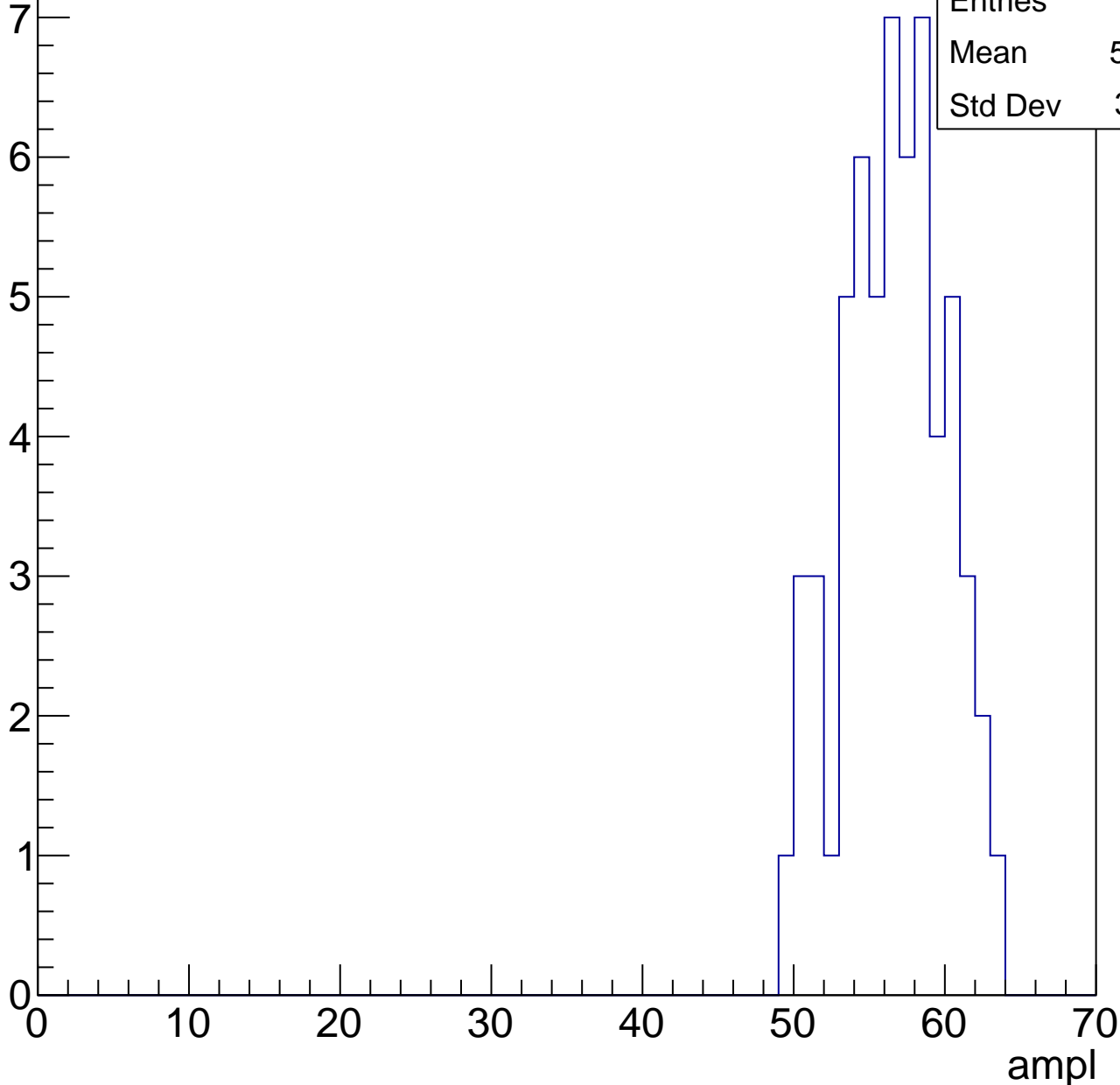


B0L001S, U22-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

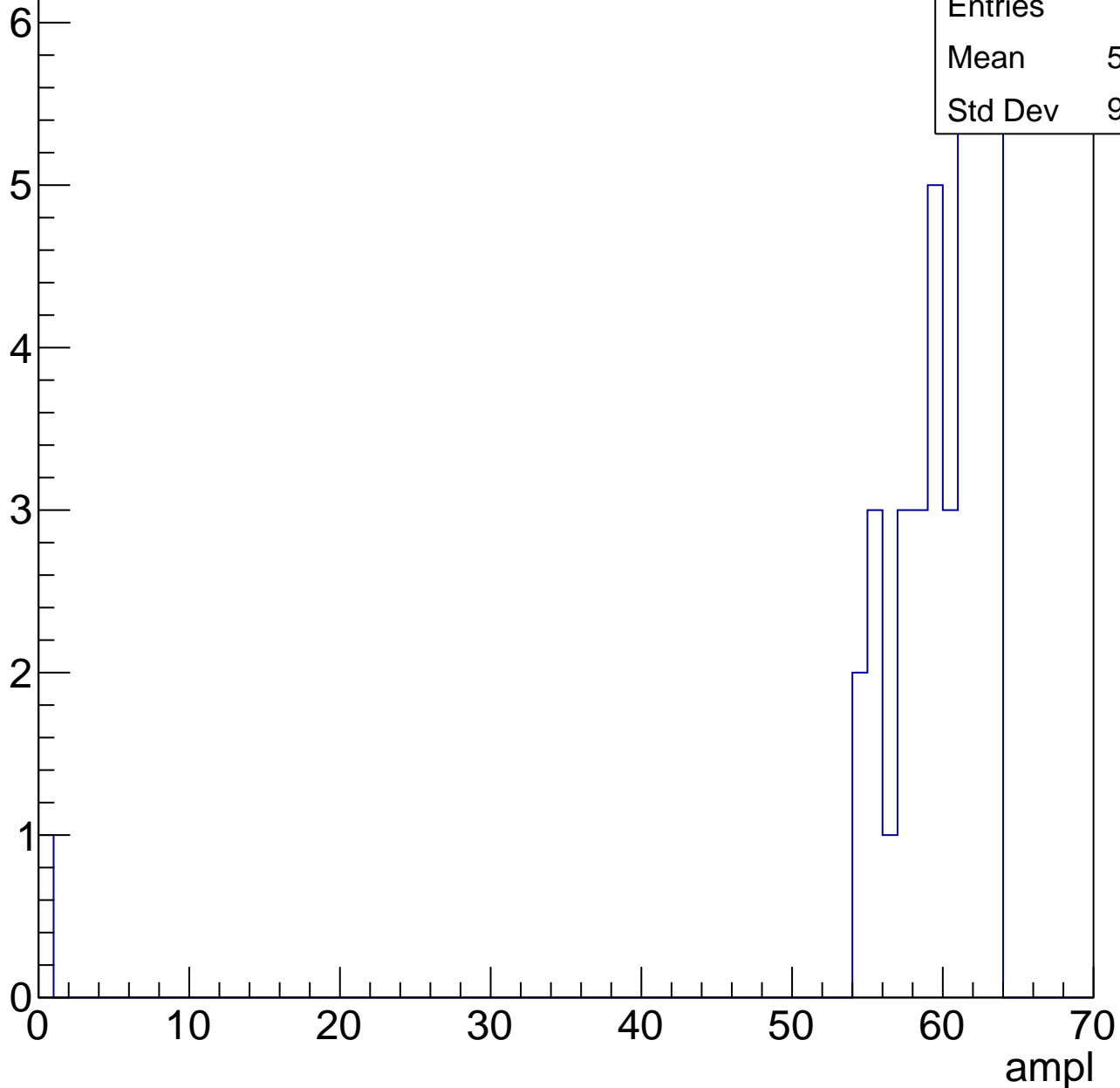
Entries	59
Mean	56.17
Std Dev	3.361



B0L001S, U22-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

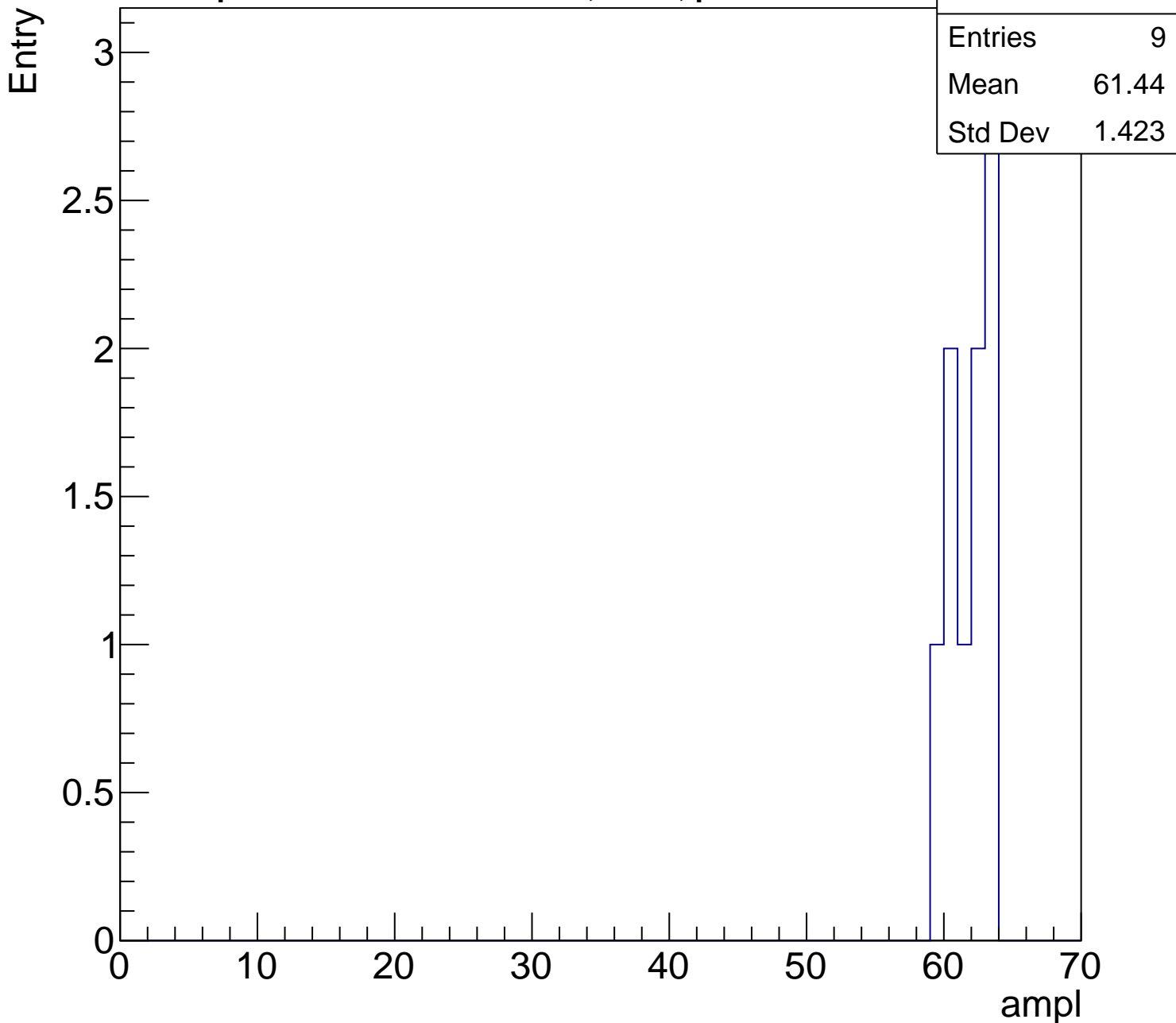
9

Mean

61.44

Std Dev

1.423



B0L001S, U22-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch63, adc0

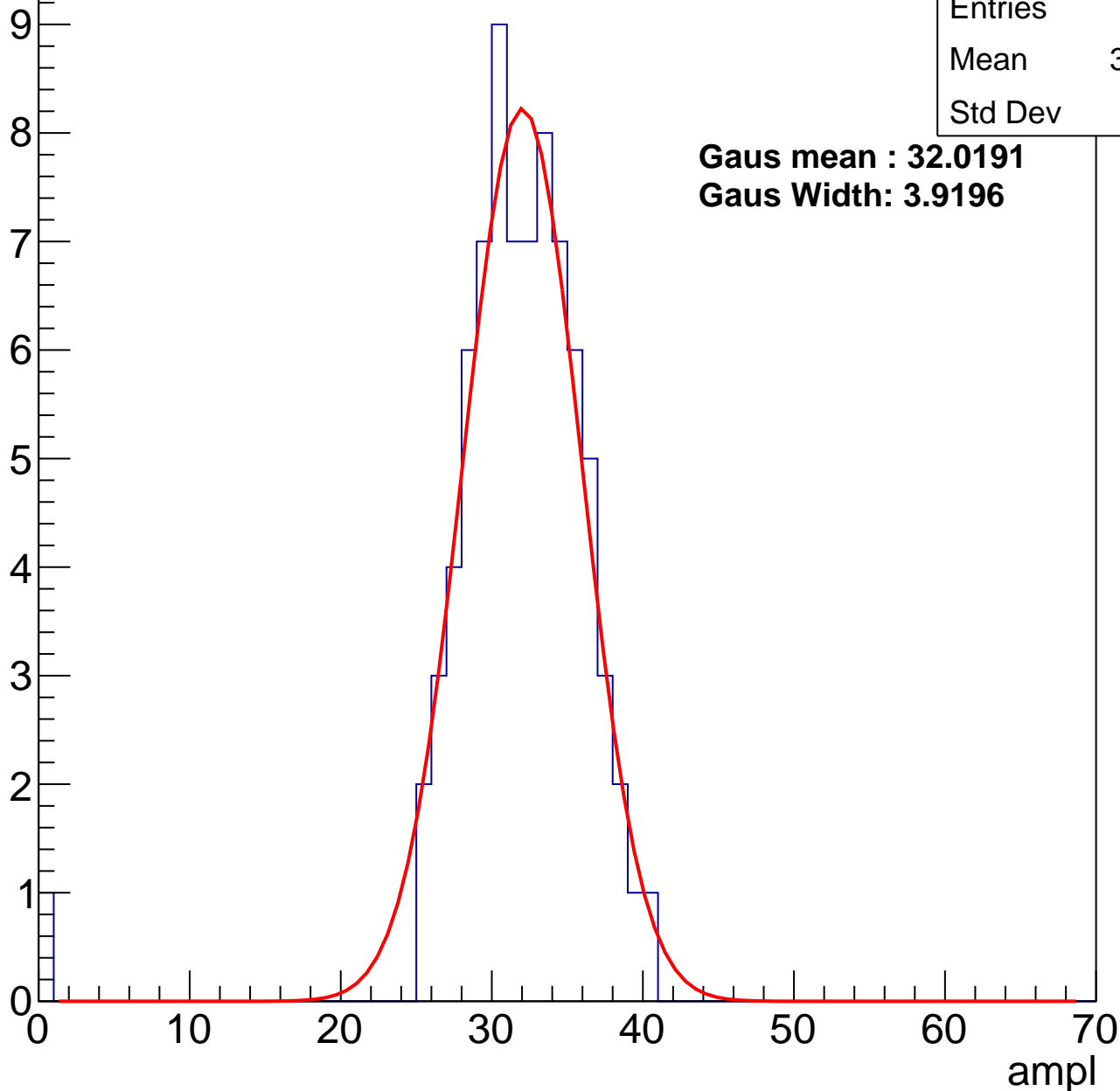
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	31.34
Std Dev	4.94

Gaus mean : 32.0191

Gaus Width: 3.9196



B0L001S, U22-ch63, adc1

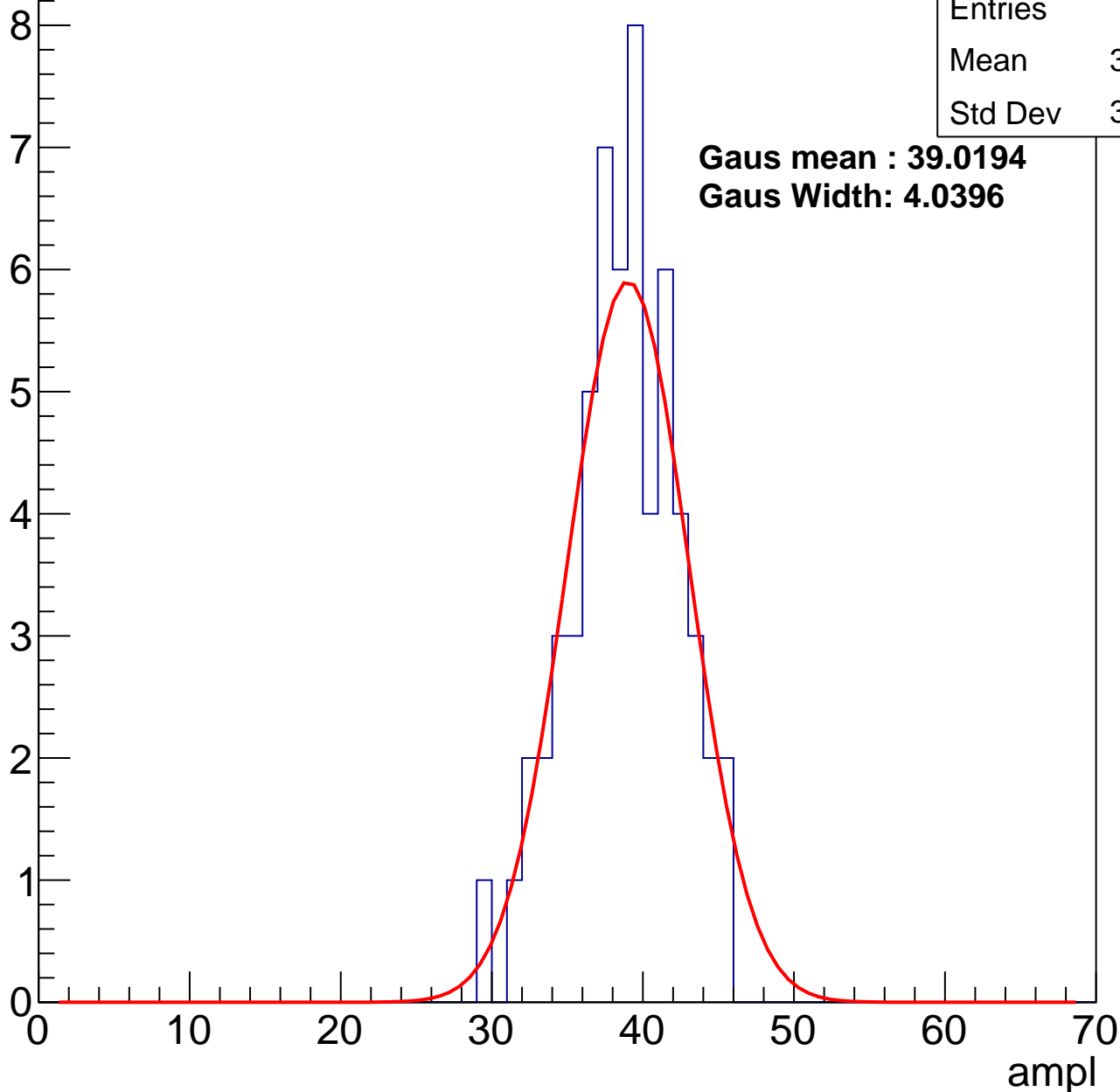
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.25
Std Dev	3.535

Gaus mean : 39.0194

Gaus Width: 4.0396



B0L001S, U22-ch63, adc2

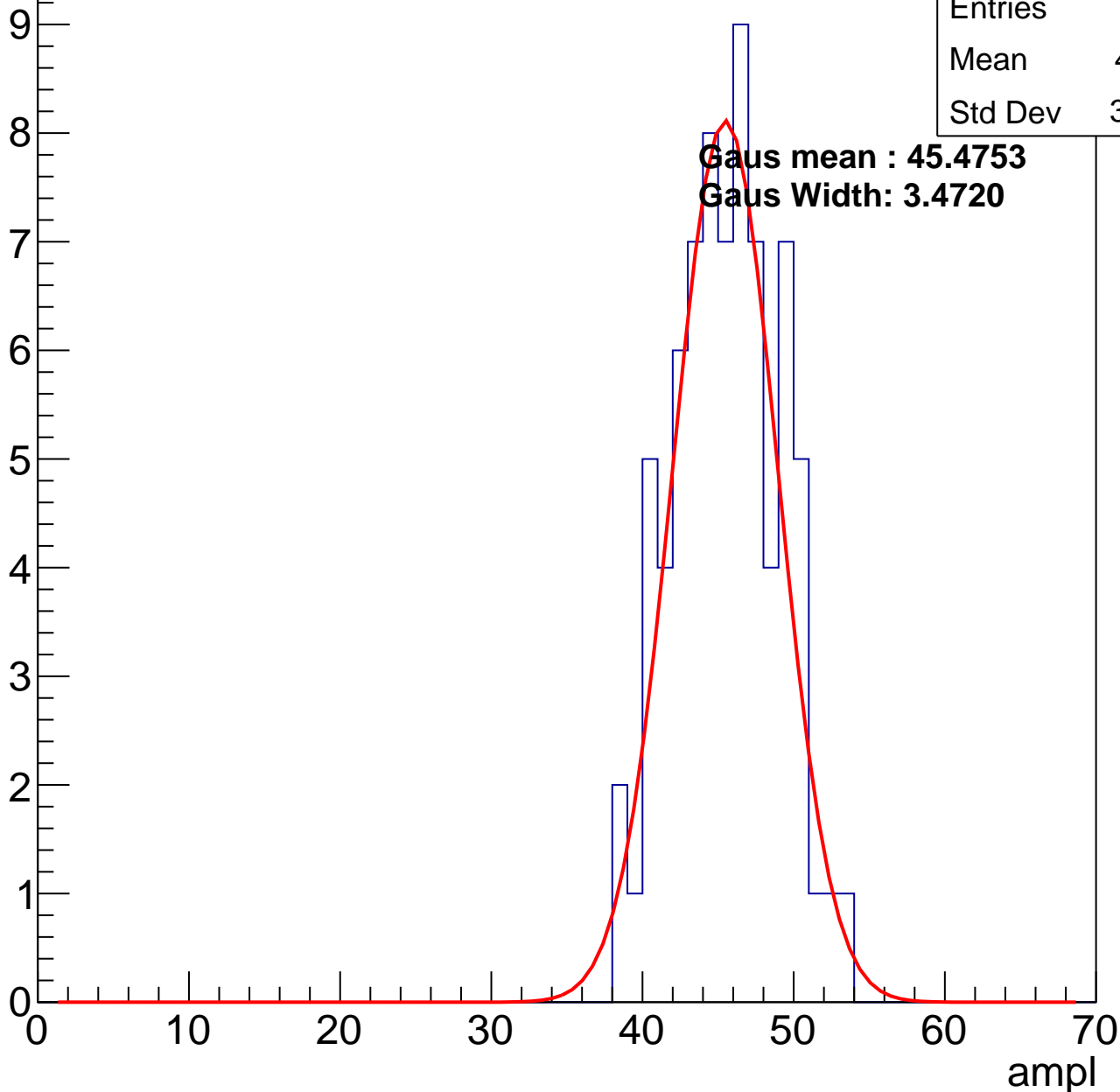
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	45.11
Std Dev	3.408

Gaus mean : 45.4753

Gaus Width: 3.4720

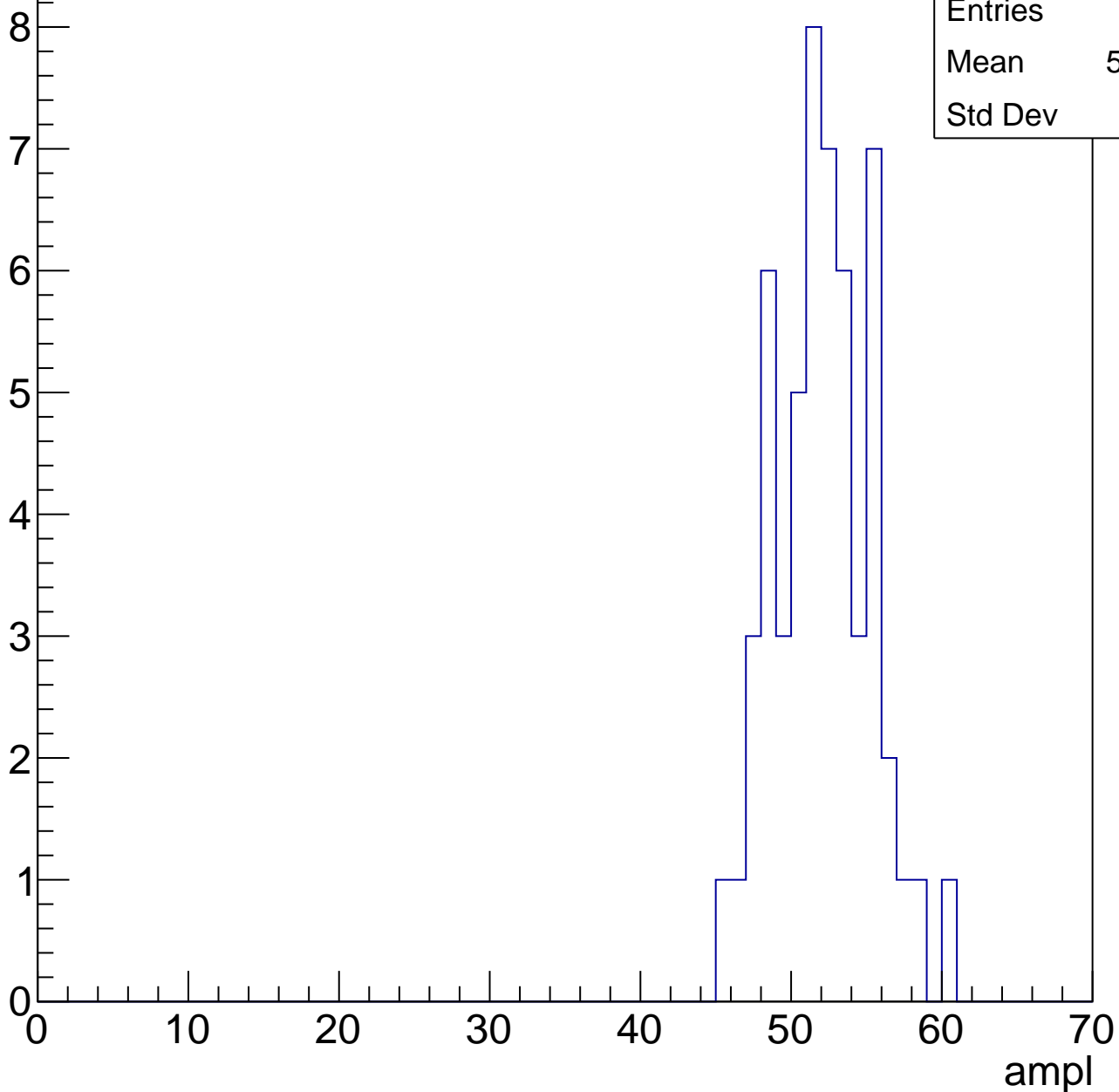


B0L001S, U22-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.65
Std Dev	3.14

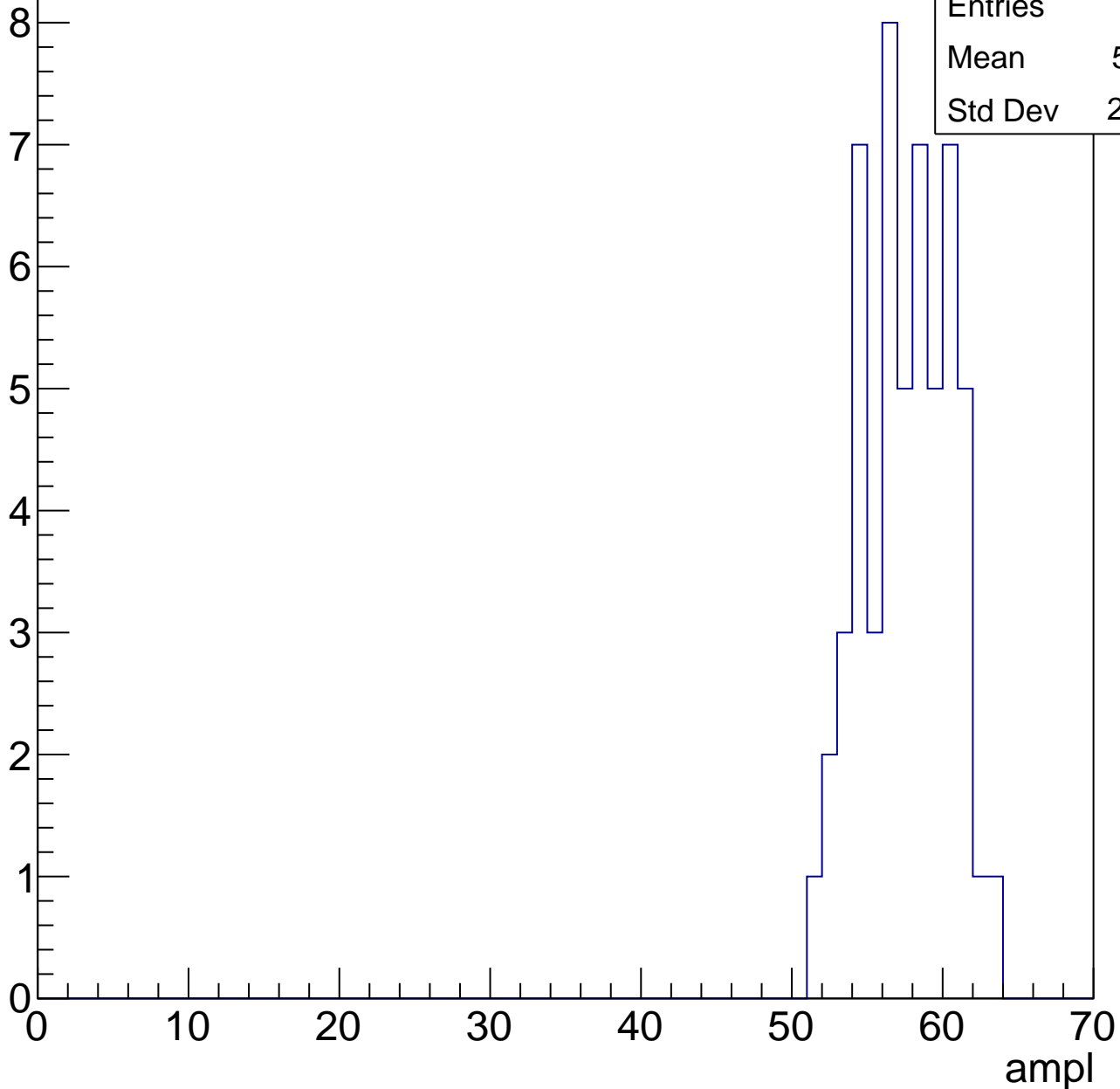


B0L001S, U22-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.11
Std Dev	2.852

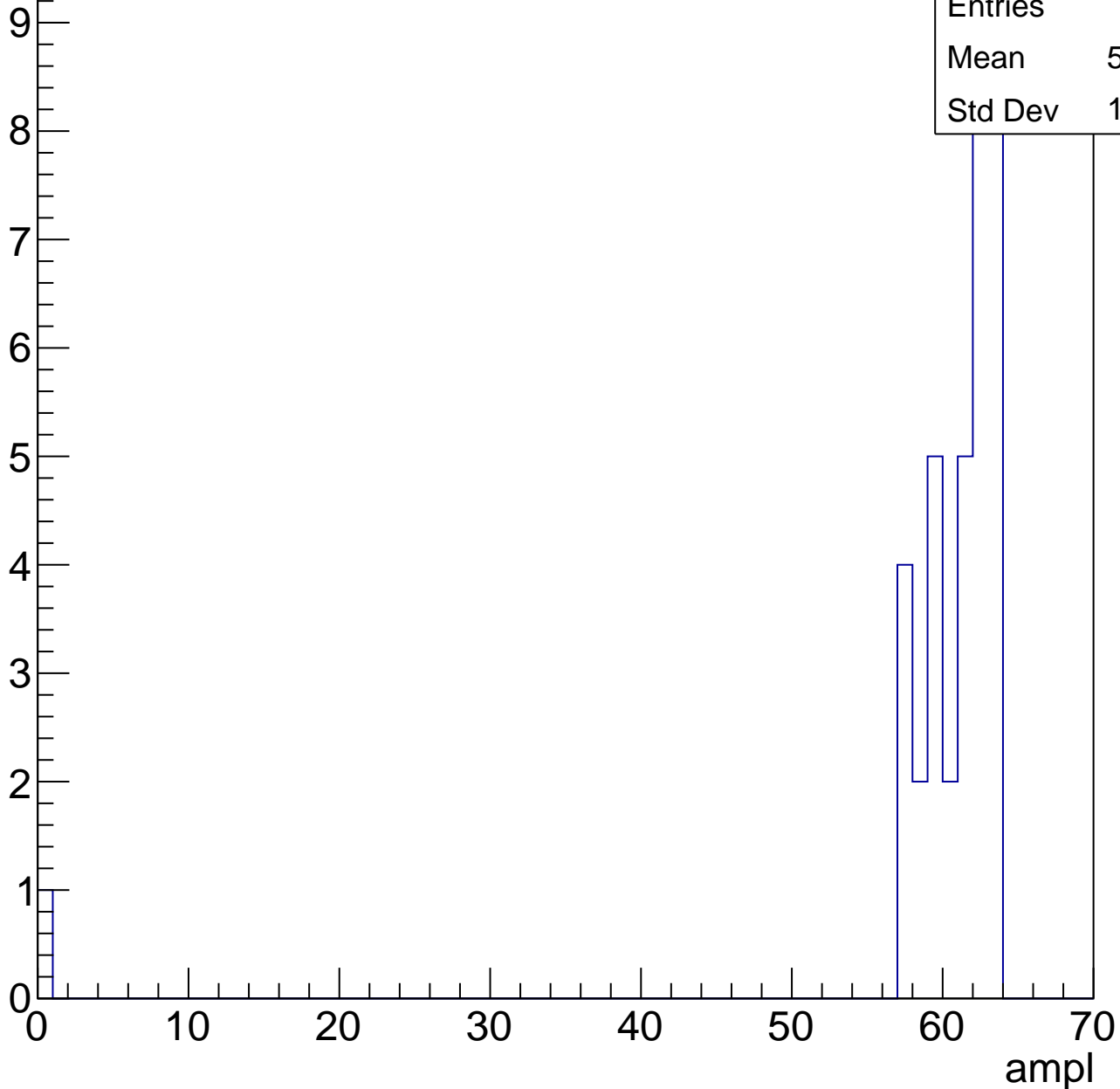


B0L001S, U22-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59.06
Std Dev	10.18



B0L001S, U22-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch64, adc0

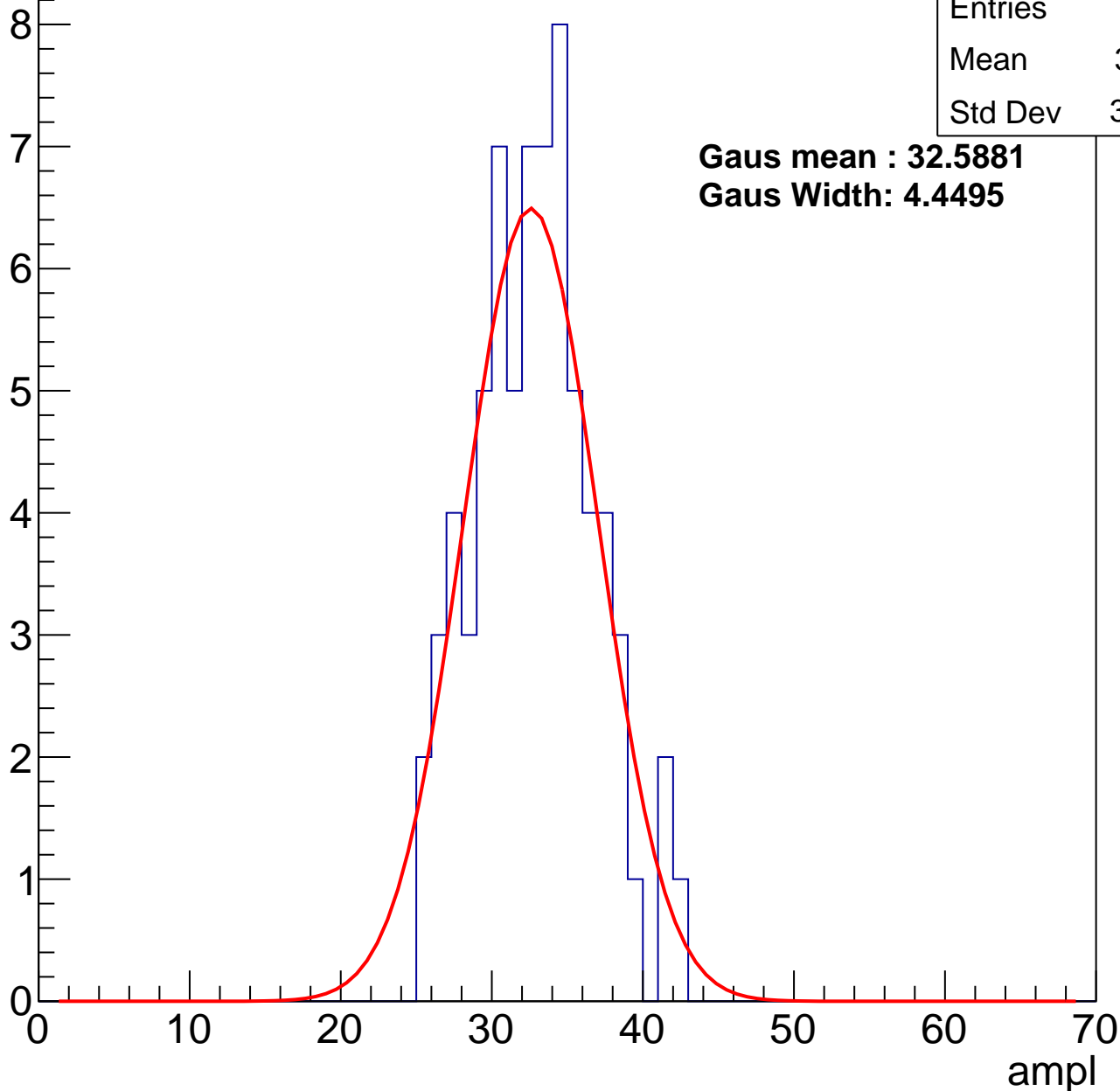
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	32.41
Std Dev	3.913

Gaus mean : 32.5881

Gaus Width: 4.4495

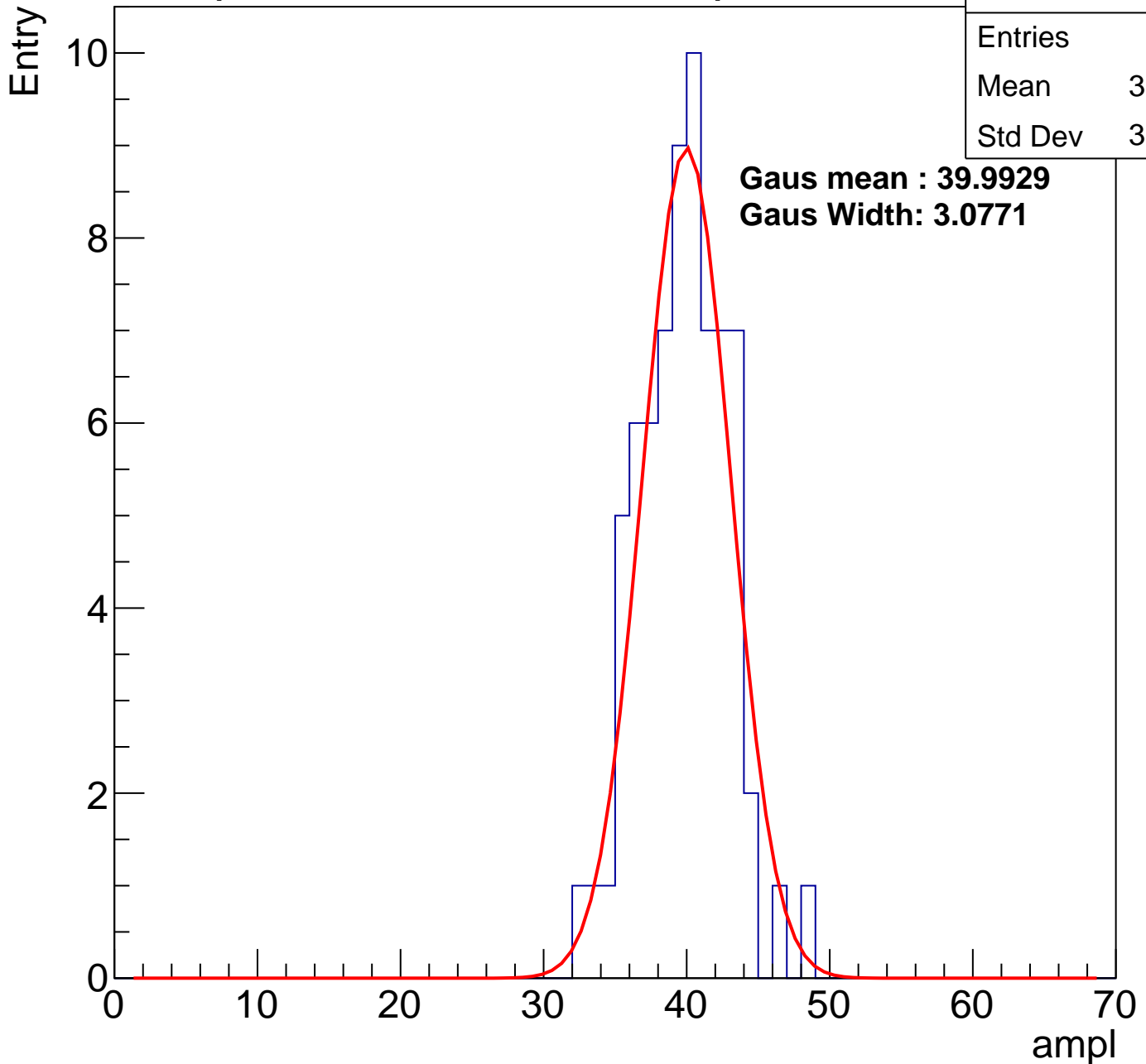


B0L001S, U22-ch64, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	39.34
Std Dev	3.049

Gaus mean : 39.9929
Gaus Width: 3.0771



B0L001S, U22-ch64, adc2

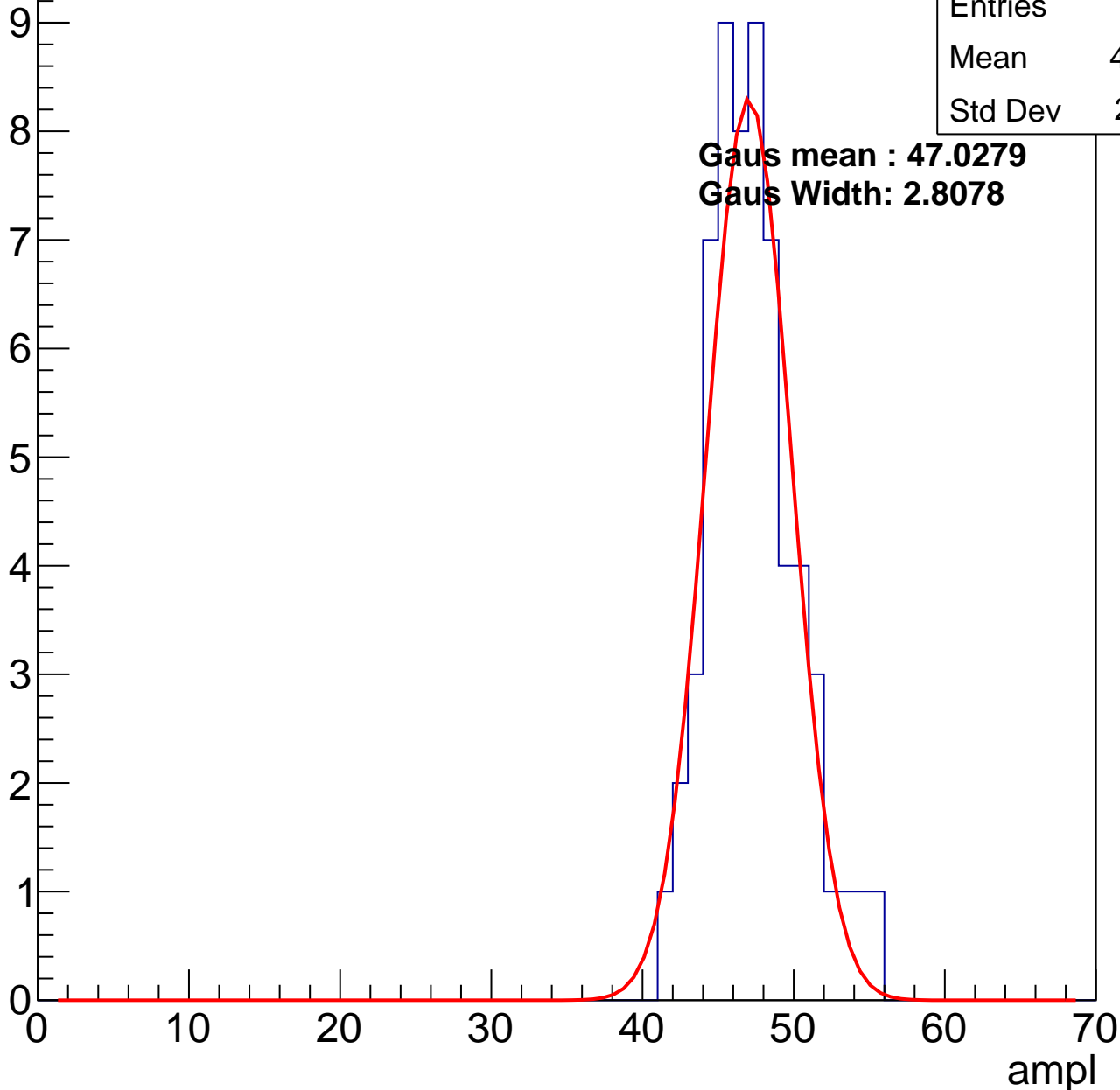
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	46.84
Std Dev	2.921

Gaus mean : 47.0279

Gaus Width: 2.8078

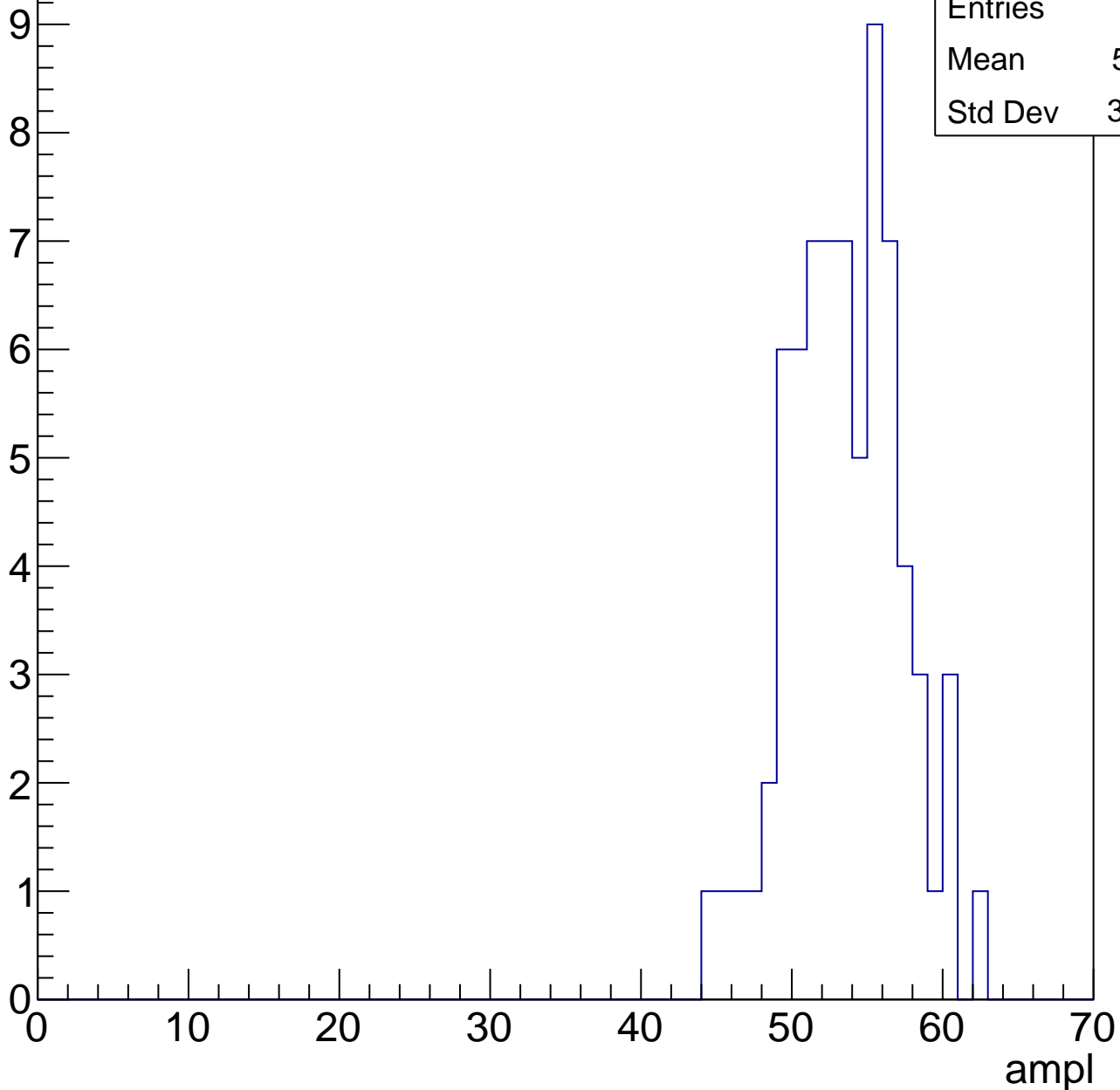


B0L001S, U22-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

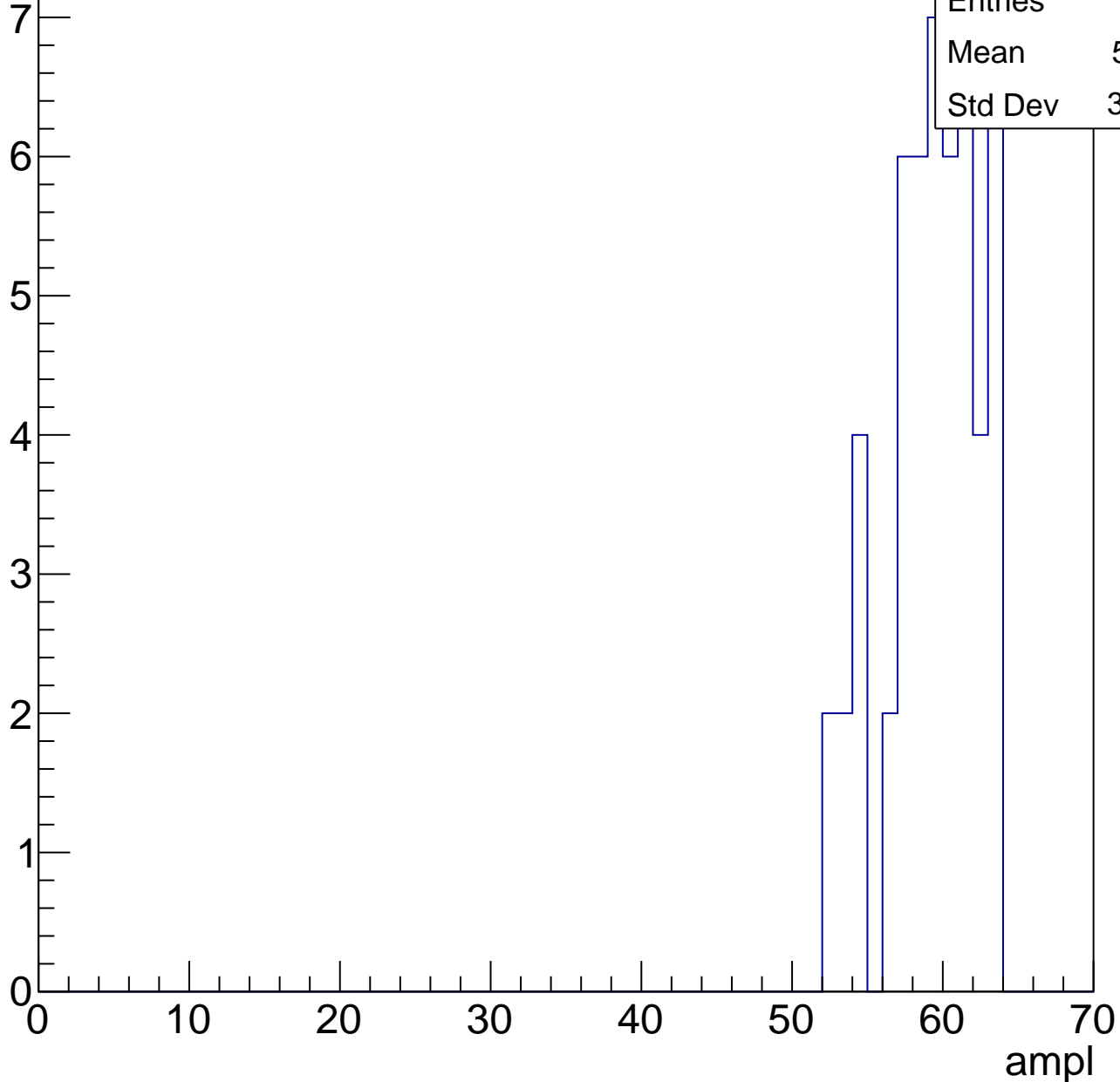
Entries	72
Mean	53.11
Std Dev	3.673



B0L001S, U22-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

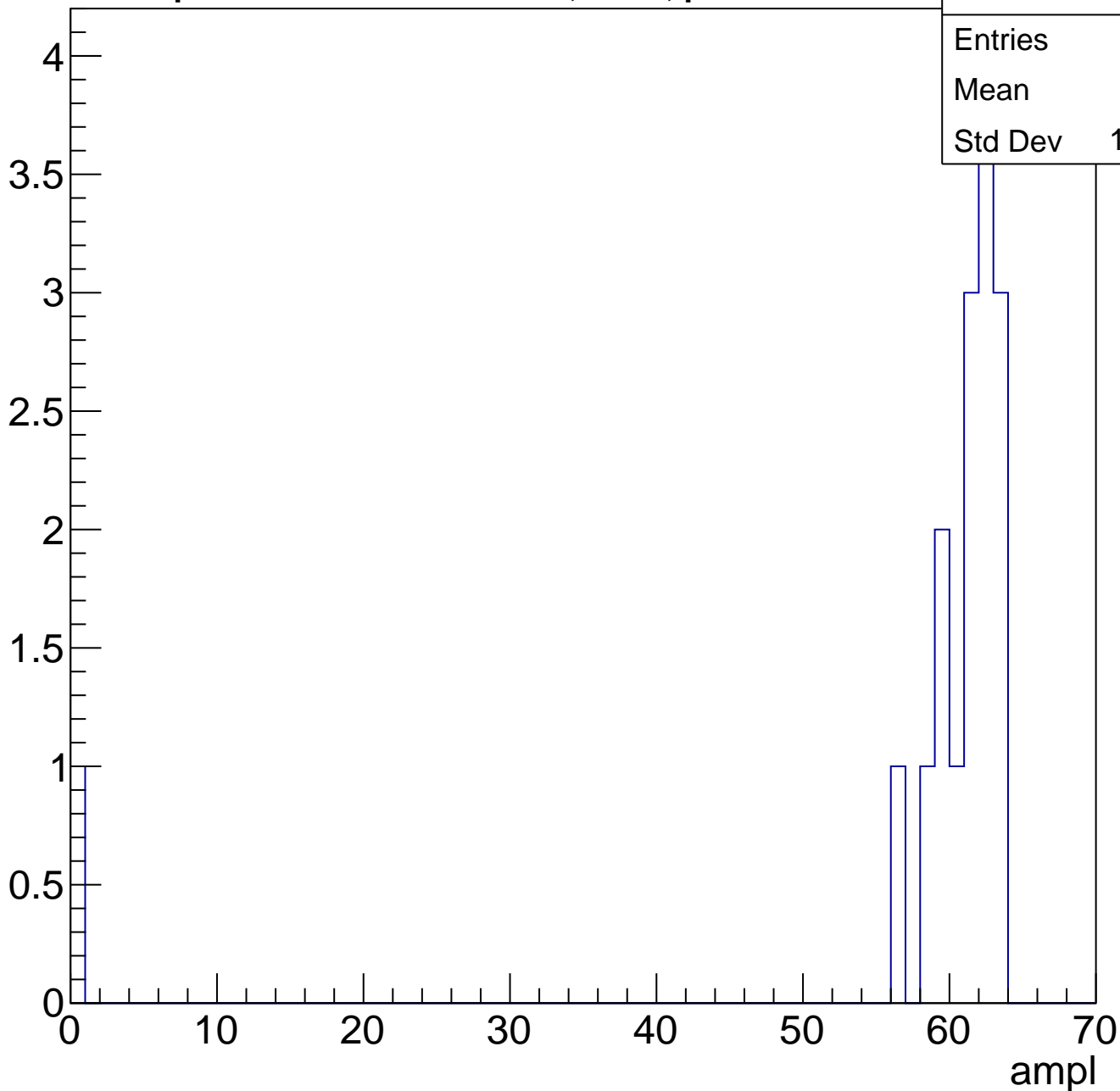
Entry



B0L001S, U22-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch65, adc0

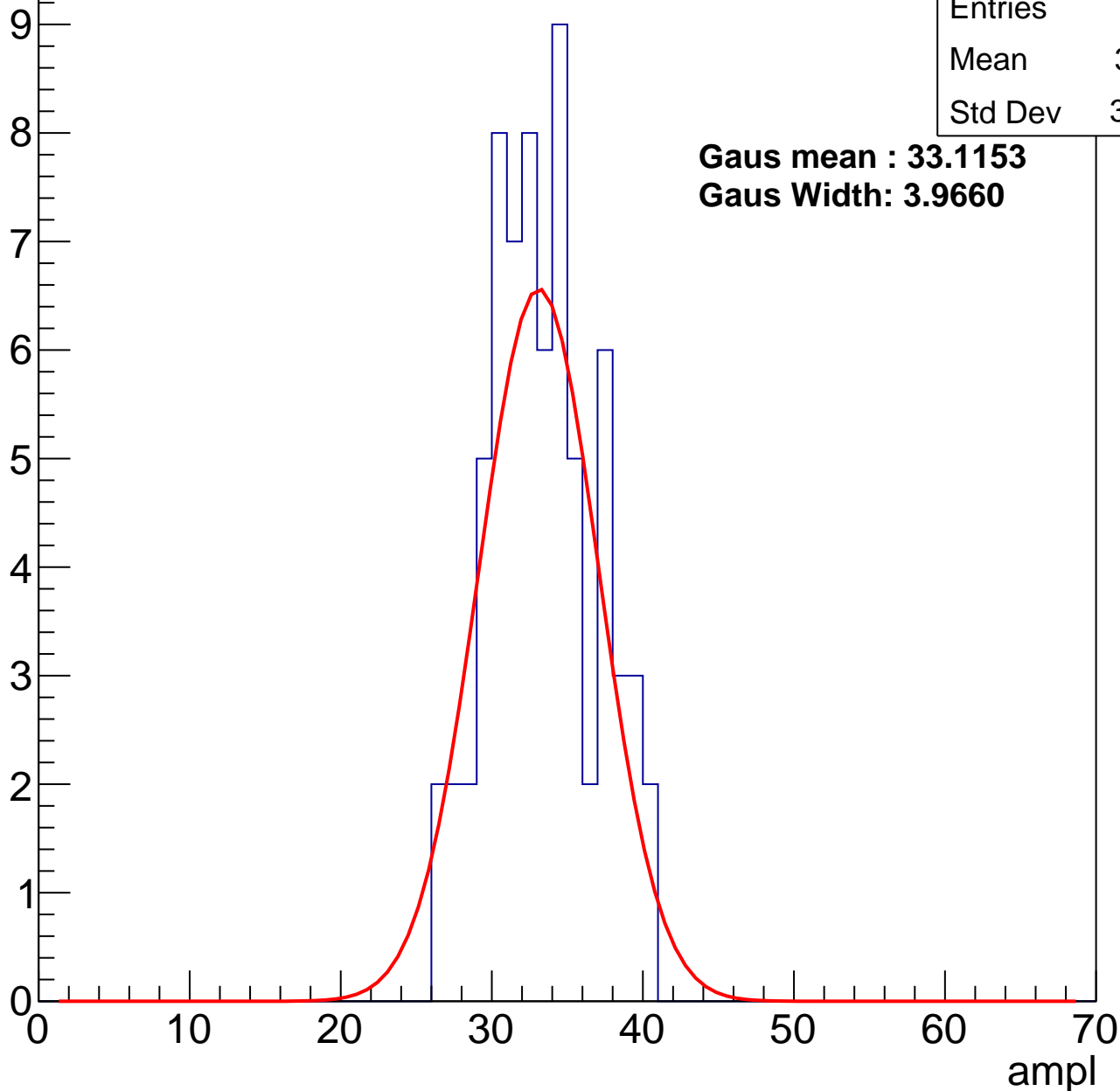
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	32.91
Std Dev	3.447

Gaus mean : 33.1153

Gaus Width: 3.9660



B0L001S, U22-ch65, adc1

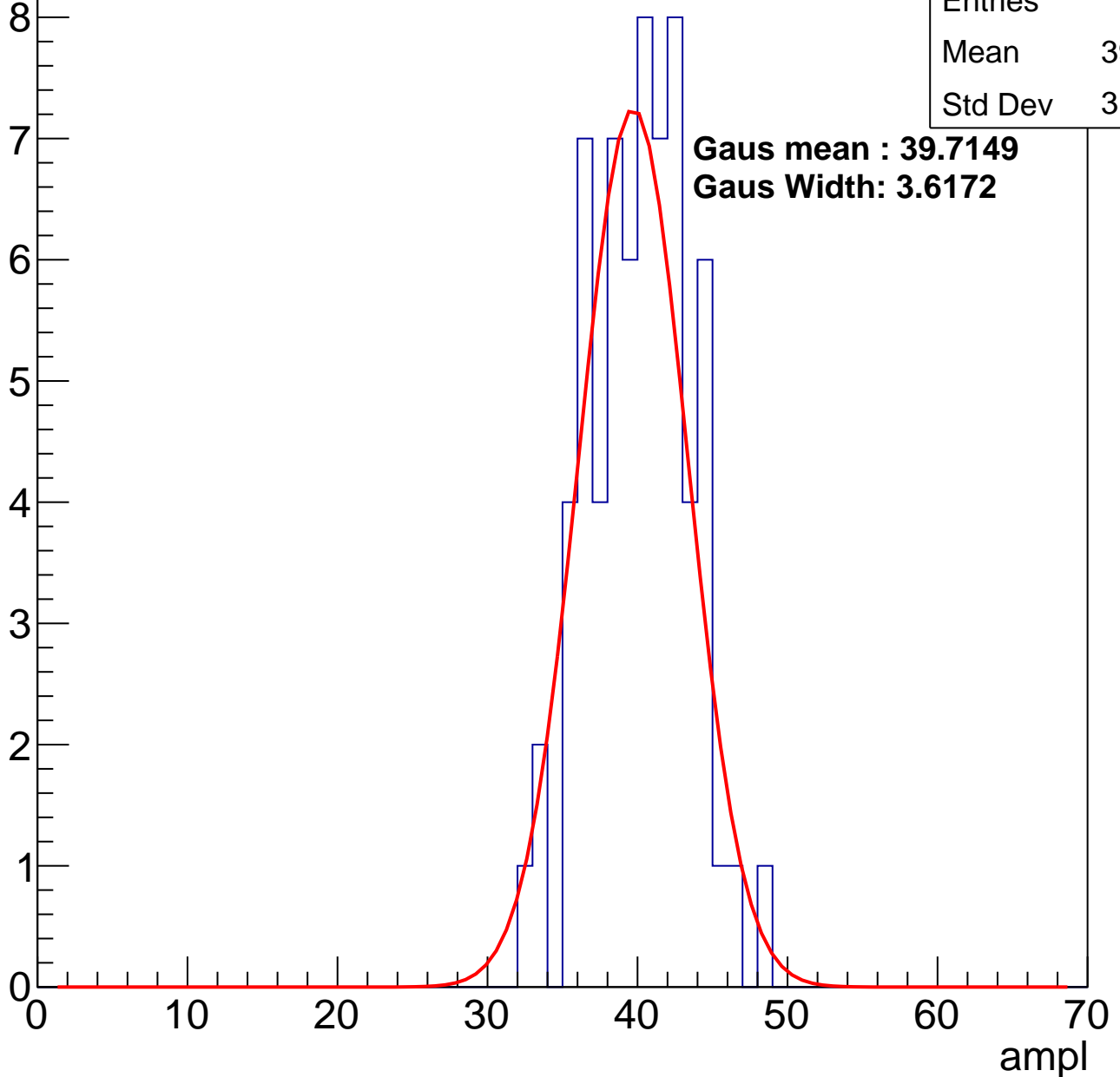
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	39.64
Std Dev	3.304

Gaus mean : 39.7149

Gaus Width: 3.6172



B0L001S, U22-ch65, adc2

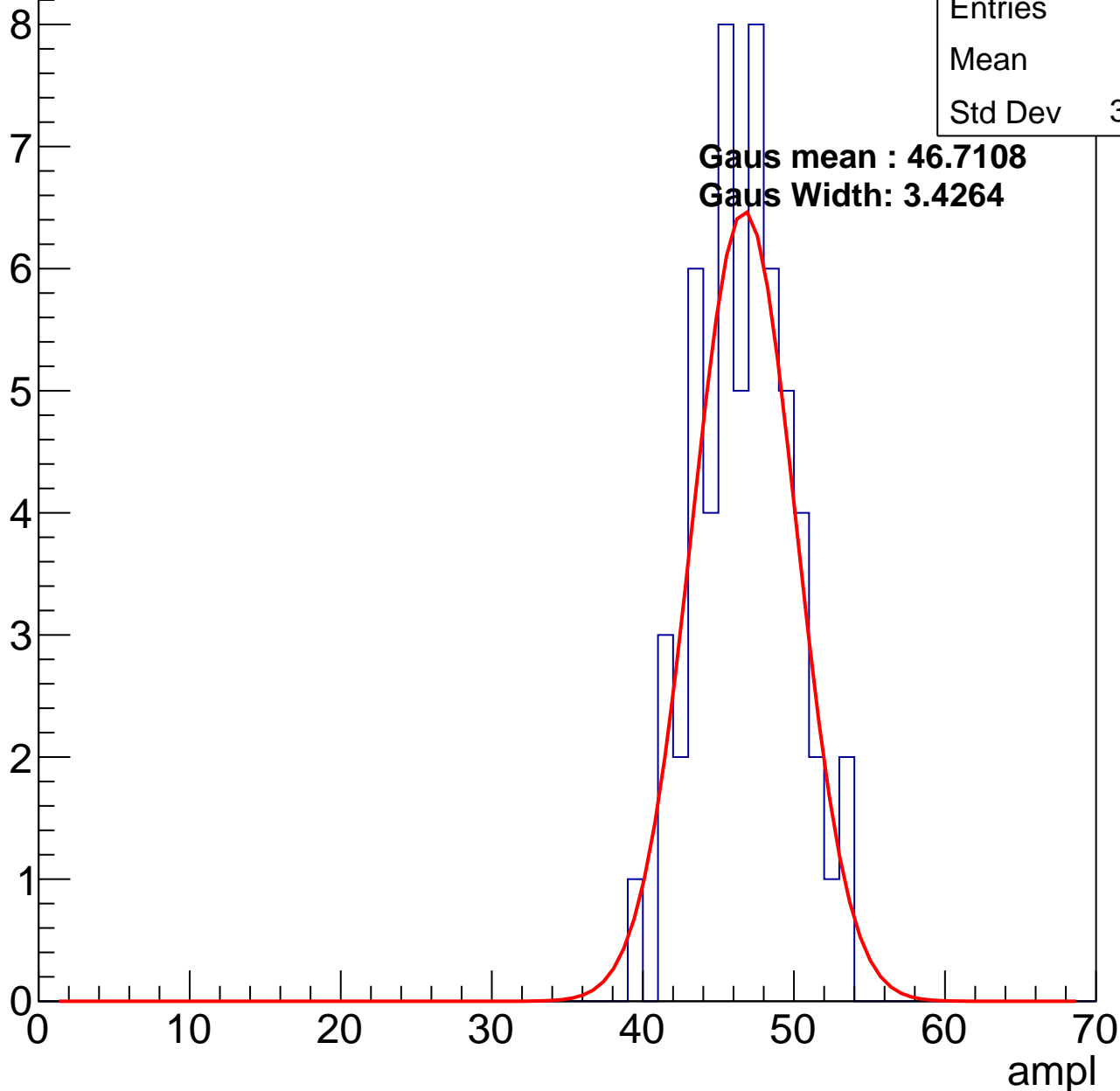
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	46.3
Std Dev	3.117

Gaus mean : 46.7108

Gaus Width: 3.4264

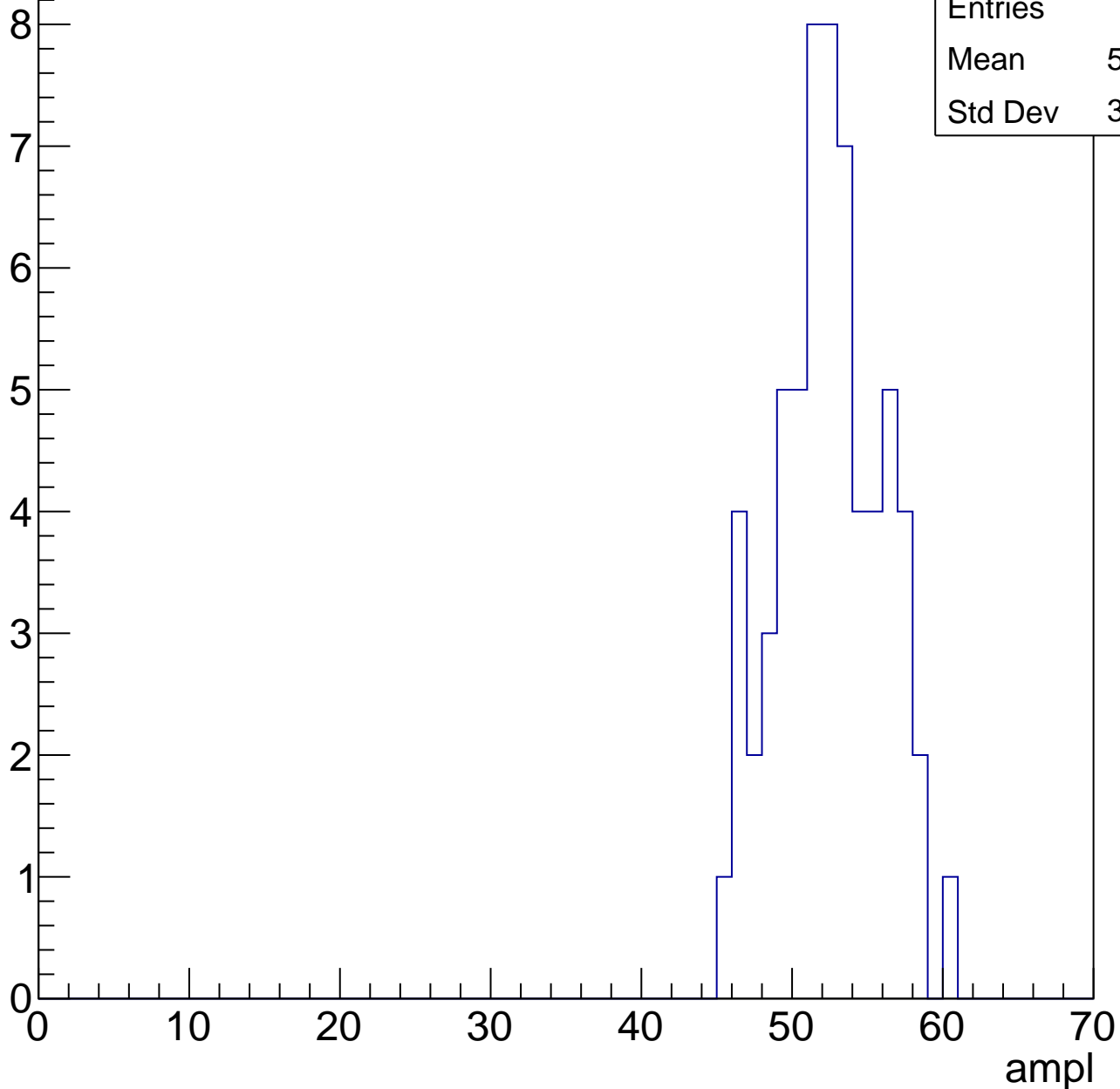


B0L001S, U22-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	52.02
Std Dev	3.425

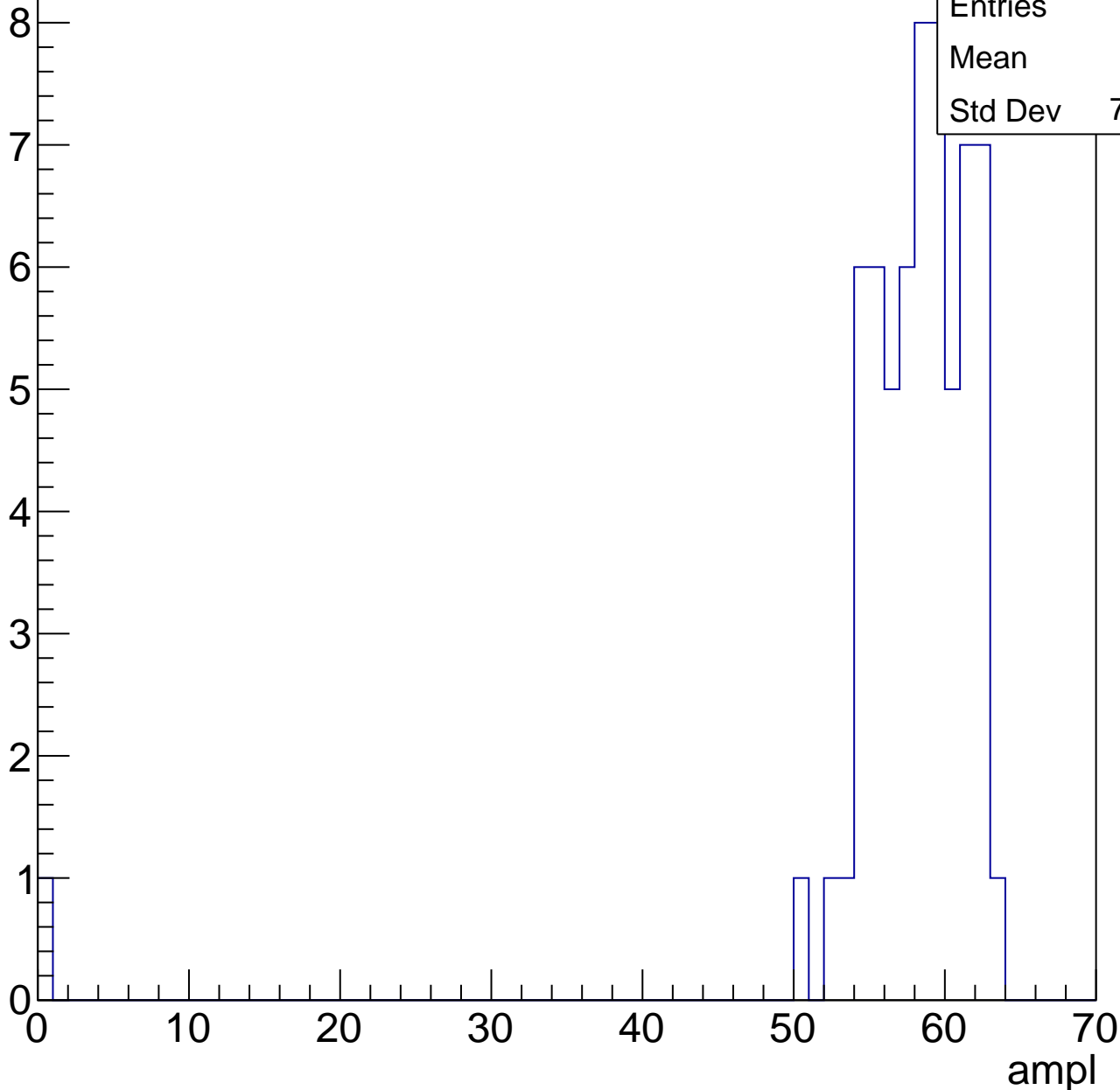


B0L001S, U22-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

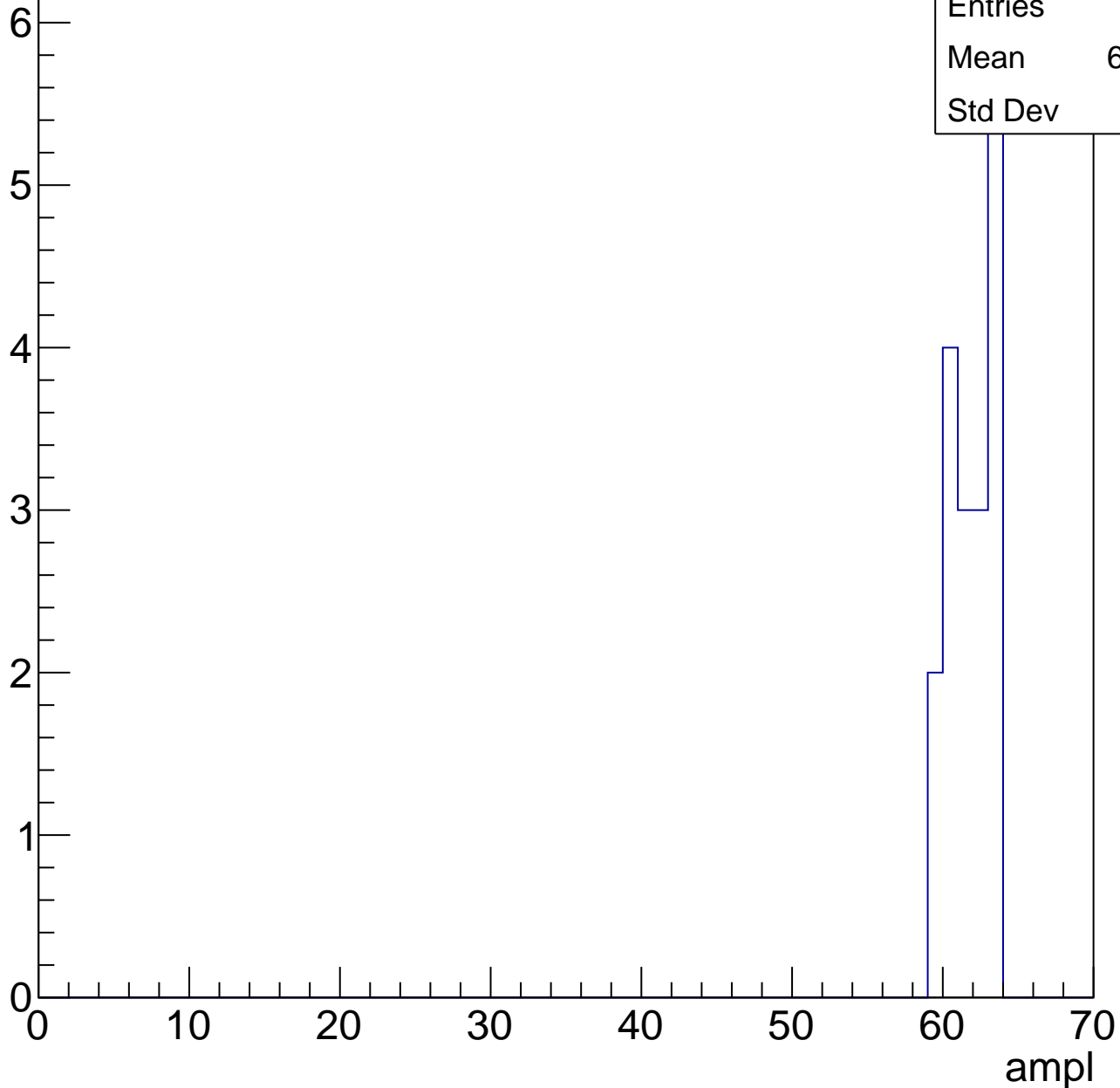
Entries	63
Mean	57
Std Dev	7.797



B0L001S, U22-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3
2.5
2
1.5
1
0.5
0

Entries	3
Mean	63
Std Dev	0

ampl

B0L001S, U22-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch66, adc0

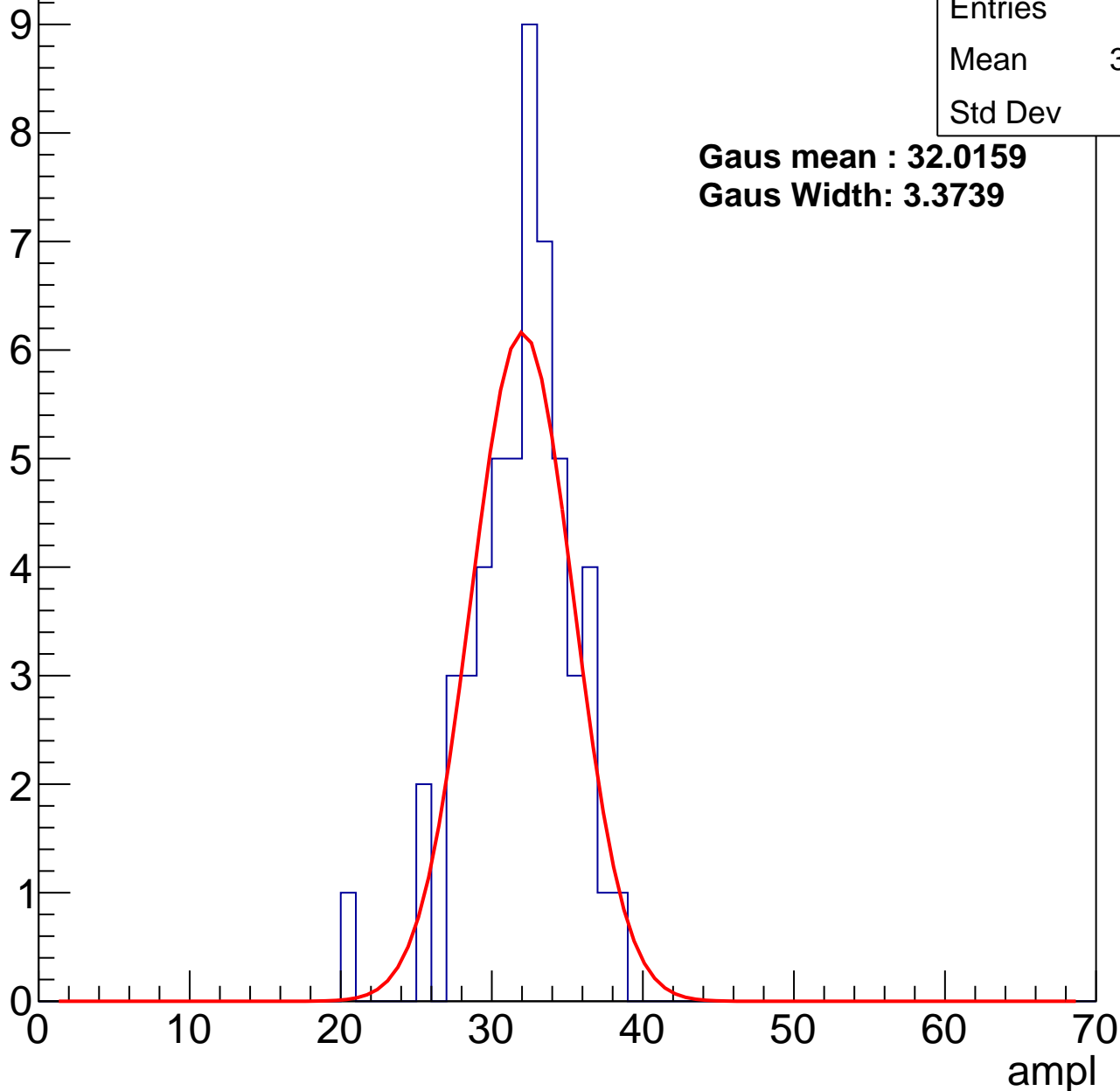
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	31.49
Std Dev	3.34

Gaus mean : 32.0159

Gaus Width: 3.3739



B0L001S, U22-ch66, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	37.93
Std Dev	3.478

Gaus mean : 38.7588

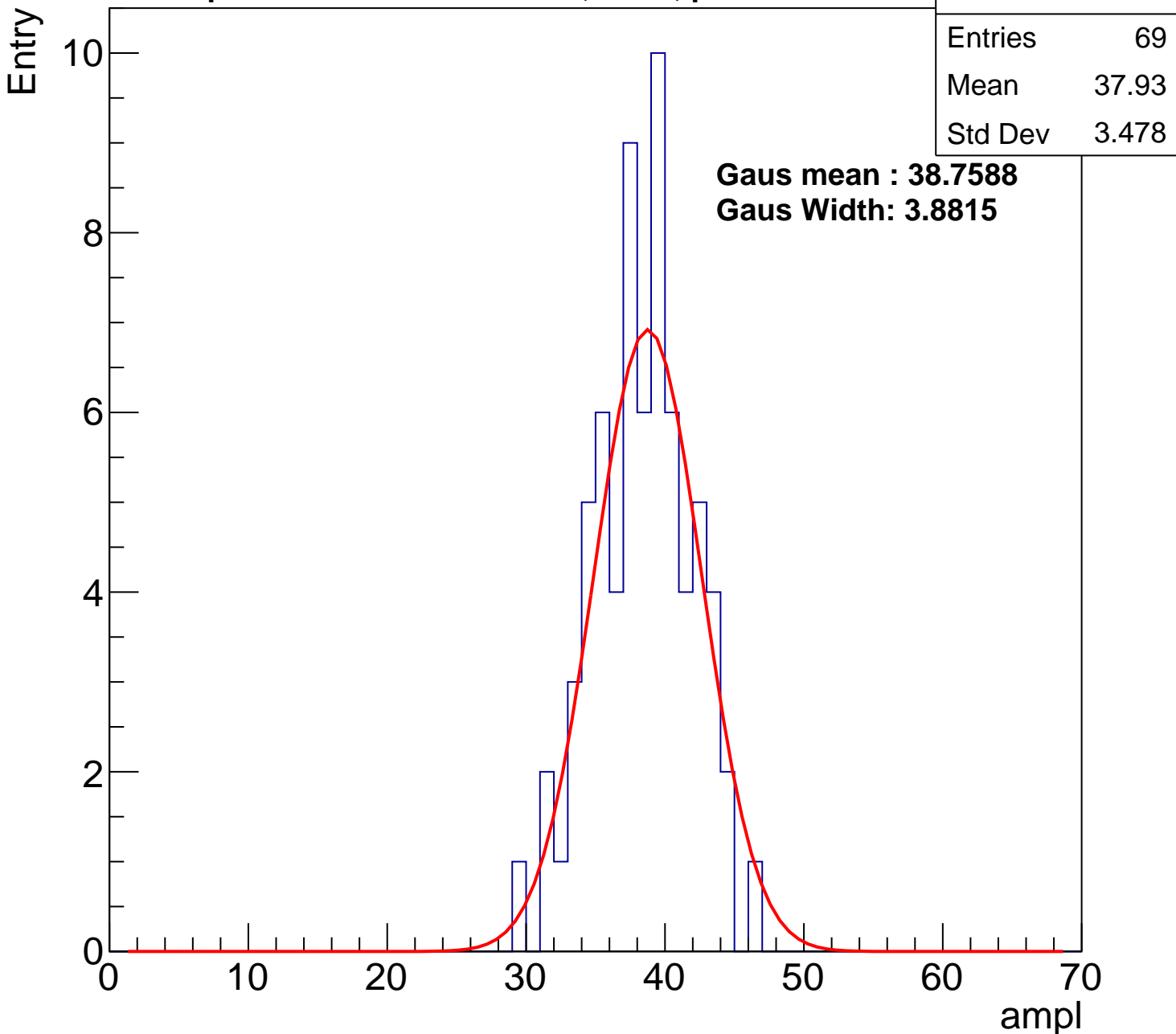
Gaus Width: 3.8815

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch66, adc2

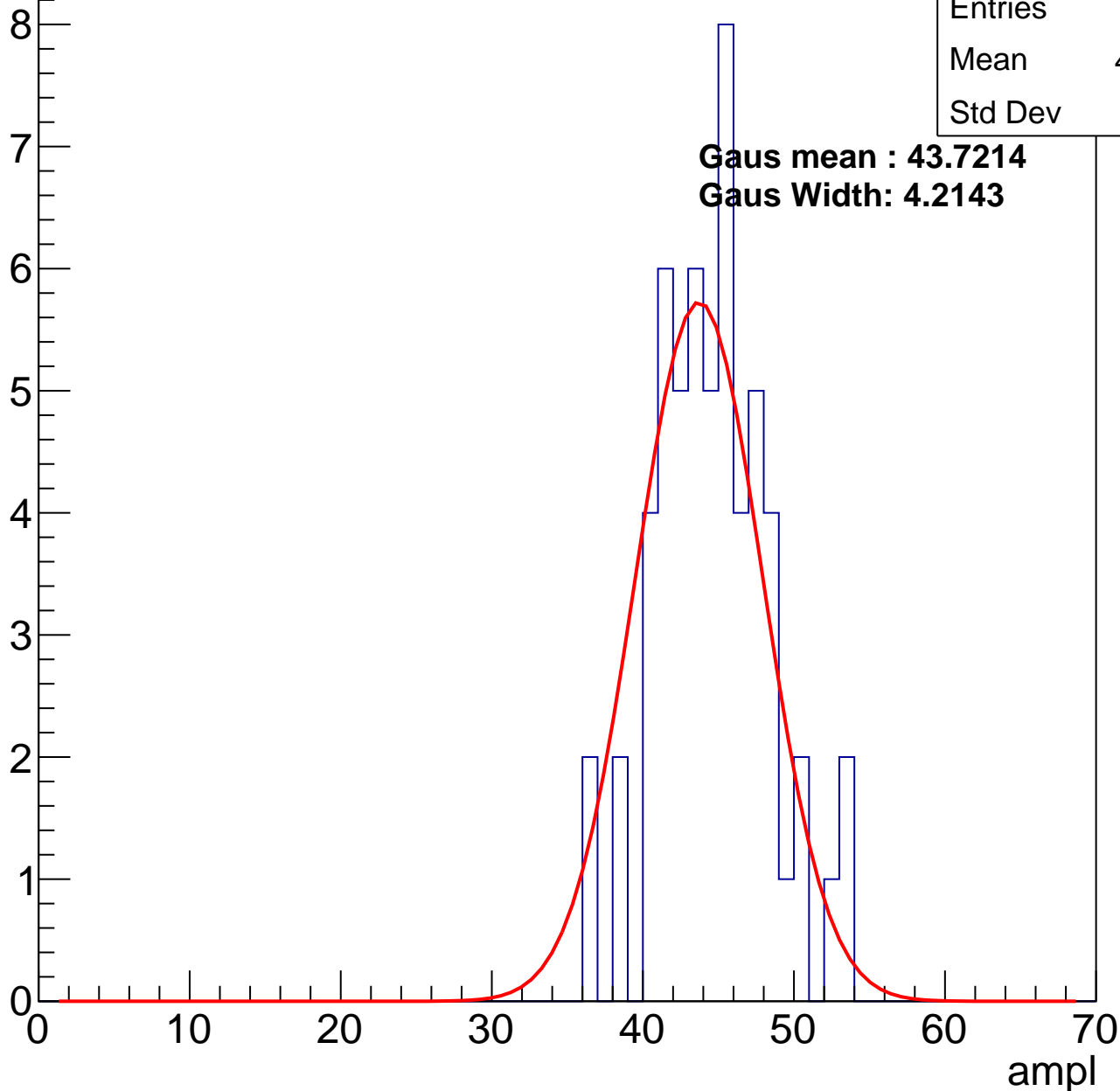
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.21
Std Dev	3.74

Gaus mean : 43.7214

Gaus Width: 4.2143

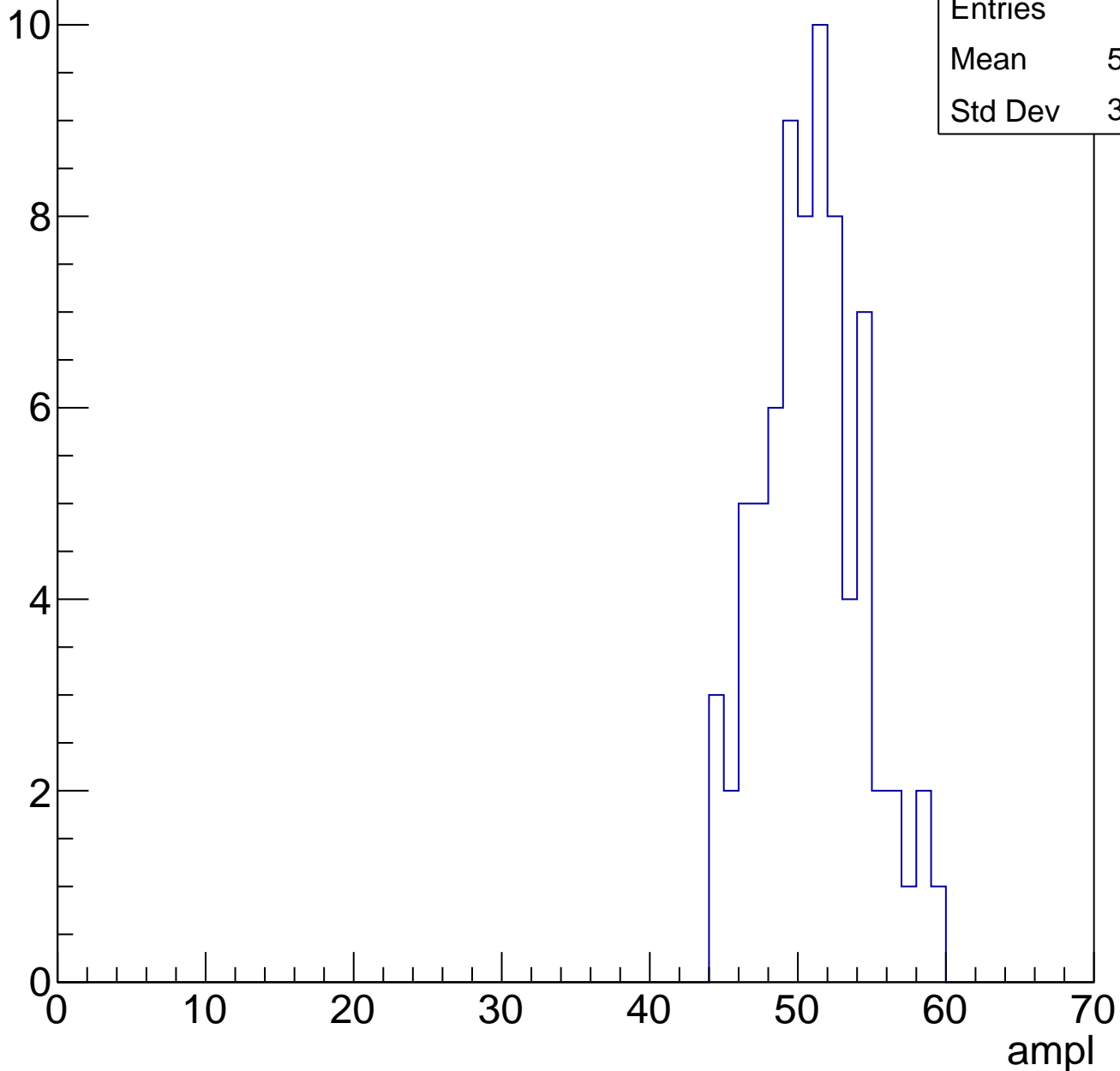


B0L001S, U22-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.48
Std Dev	3.403

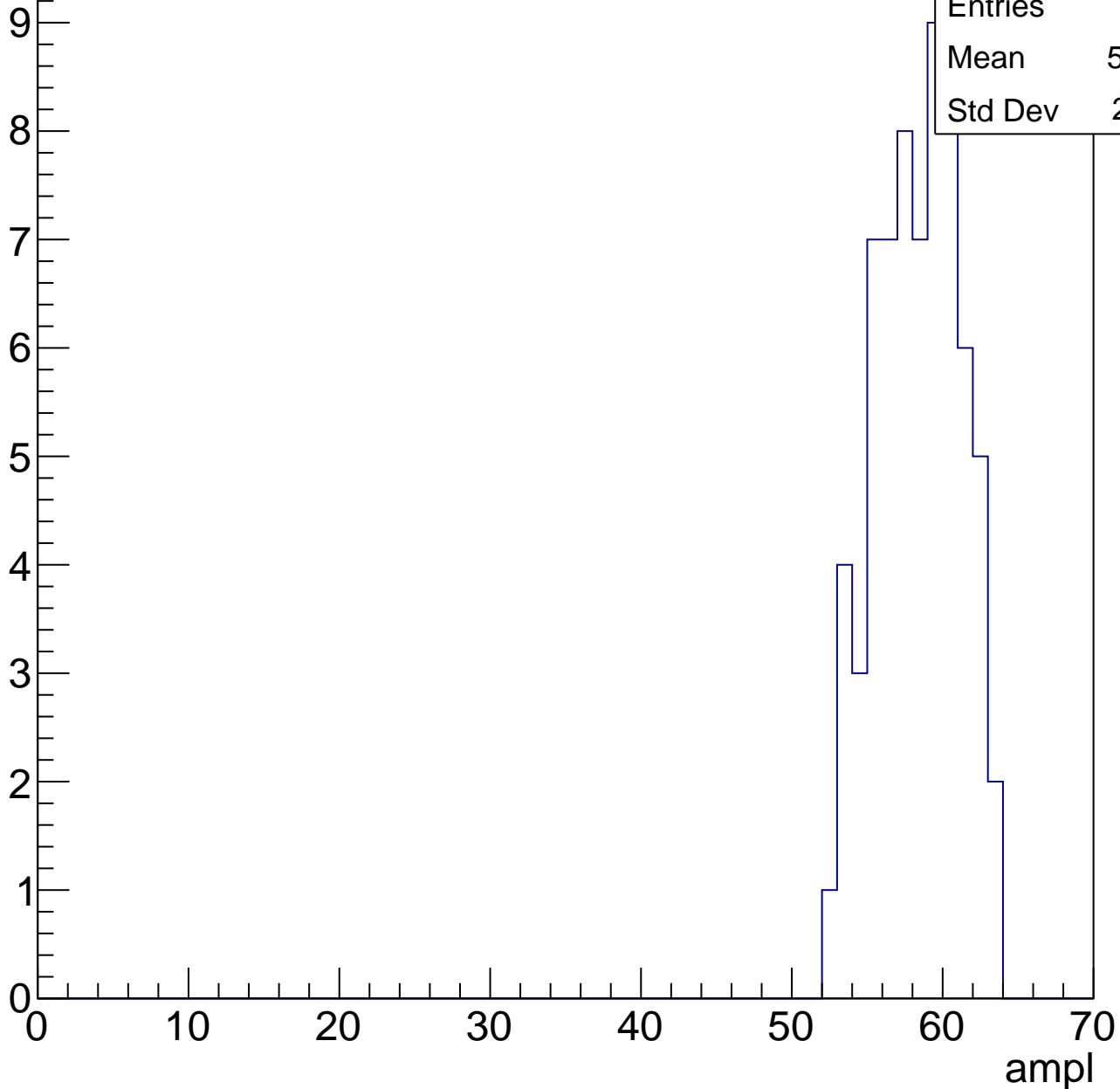


B0L001S, U22-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	57.88
Std Dev	2.751

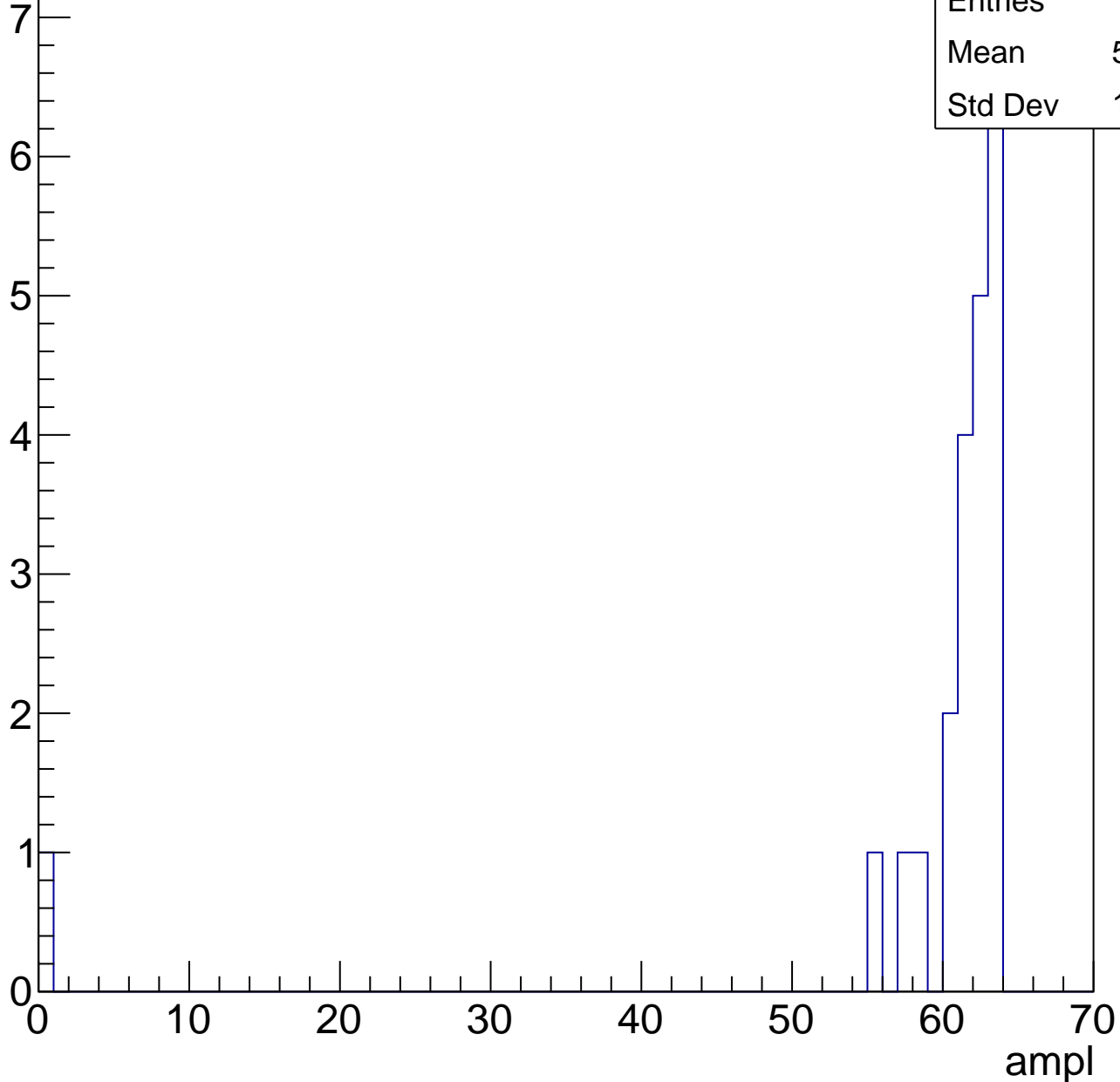


B0L001S, U22-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.41
Std Dev	12.91



B0L001S, U22-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

2

Mean

59.5

Std Dev

3.5

B0L001S, U22-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch67, adc0

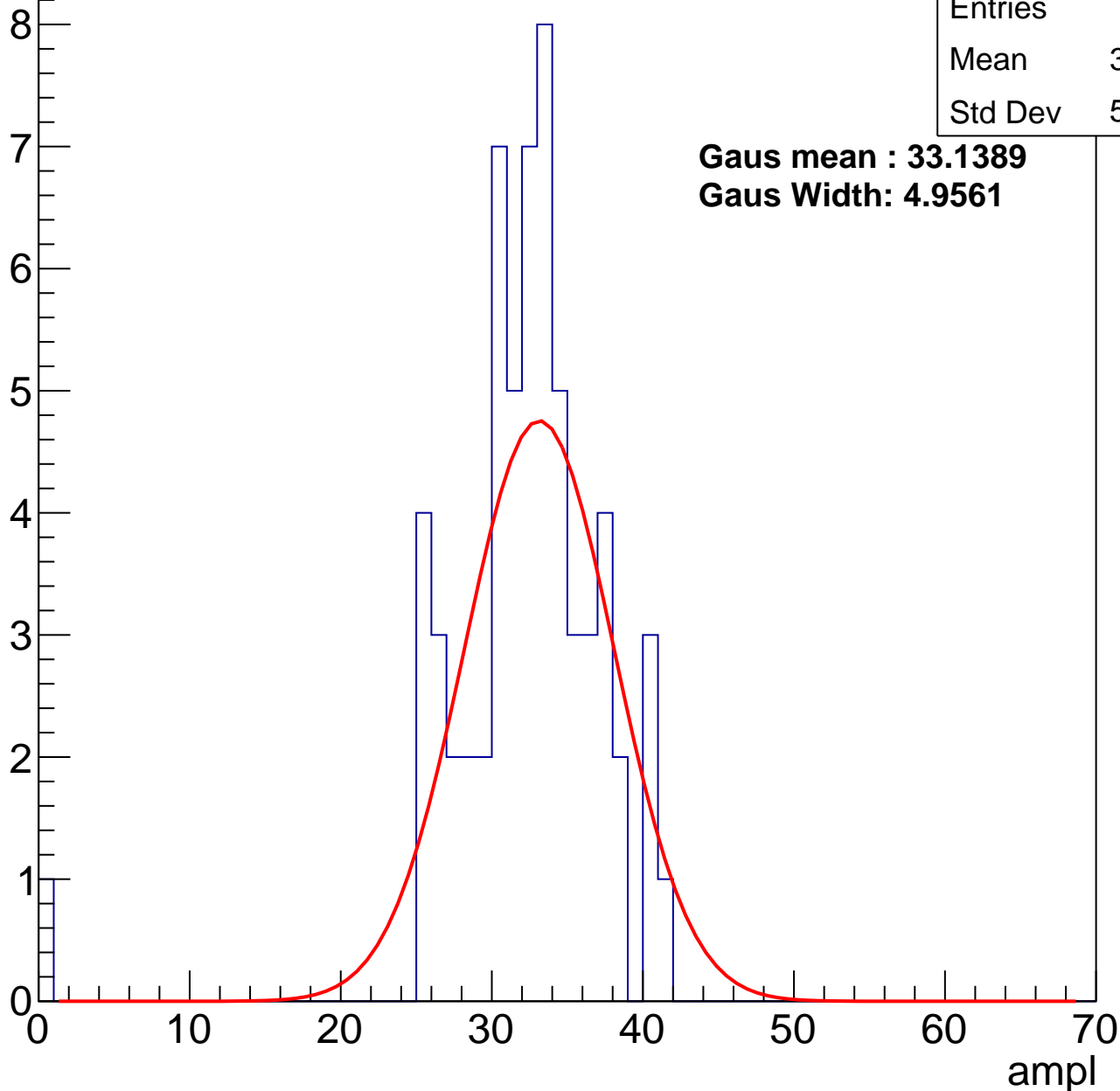
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	31.73
Std Dev	5.686

Gaus mean : 33.1389

Gaus Width: 4.9561



B0L001S, U22-ch67, adc1

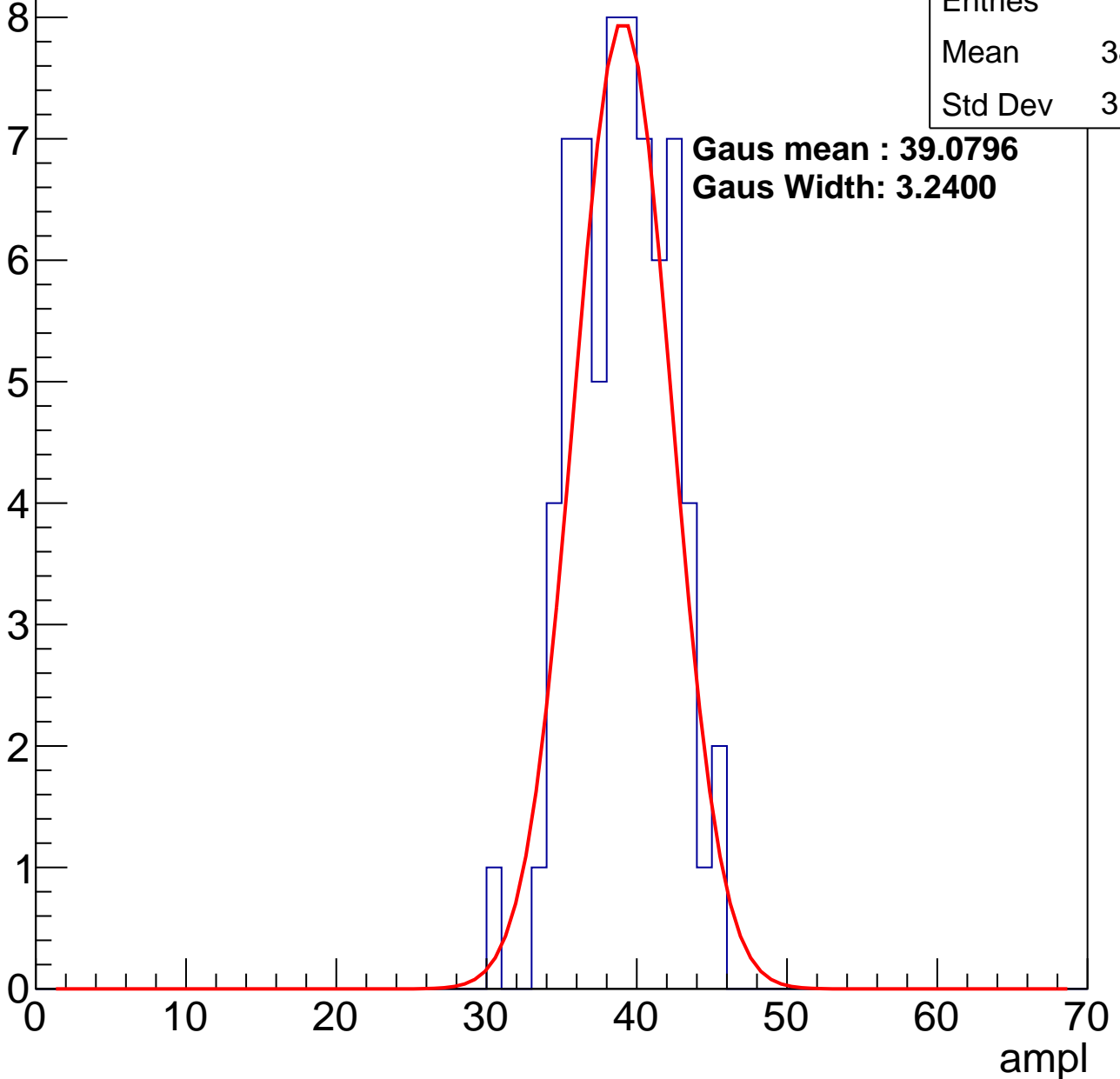
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.57
Std Dev	3.122

Gaus mean : 39.0796

Gaus Width: 3.2400



B0L001S, U22-ch67, adc2

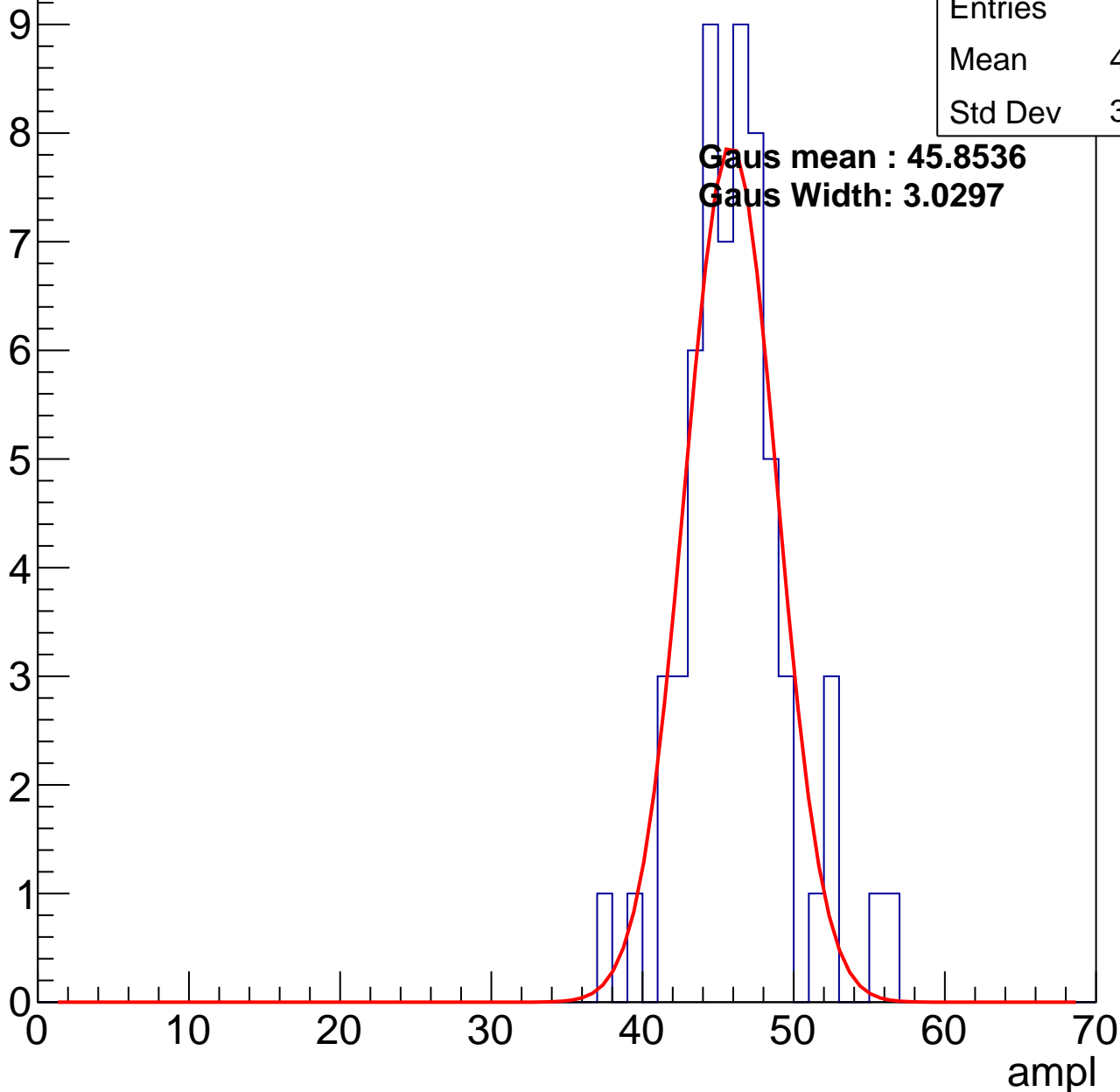
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	45.72
Std Dev	3.422

Gaus mean : 45.8536

Gaus Width: 3.0297

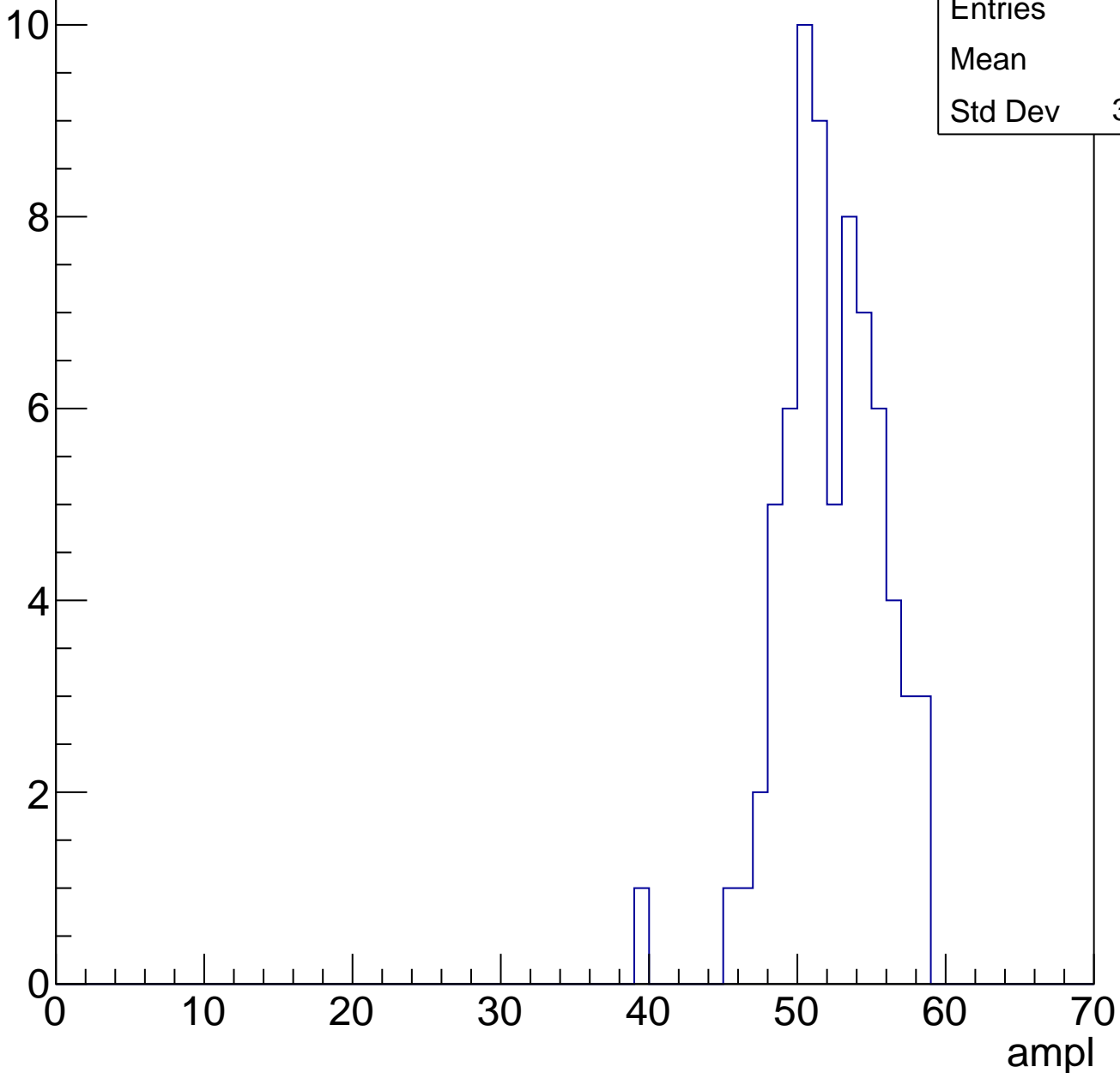


B0L001S, U22-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.8
Std Dev	3.401

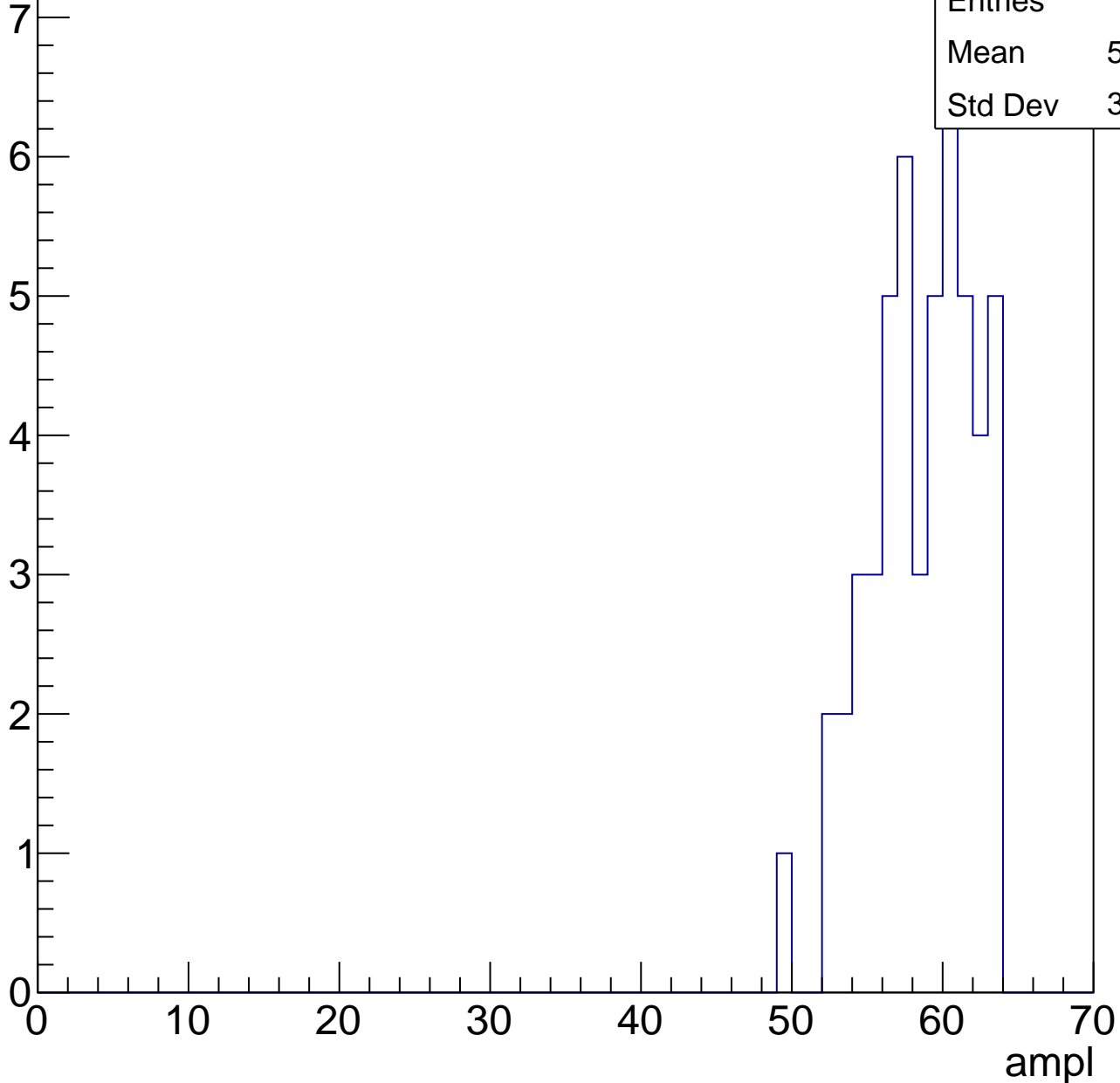


B0L001S, U22-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

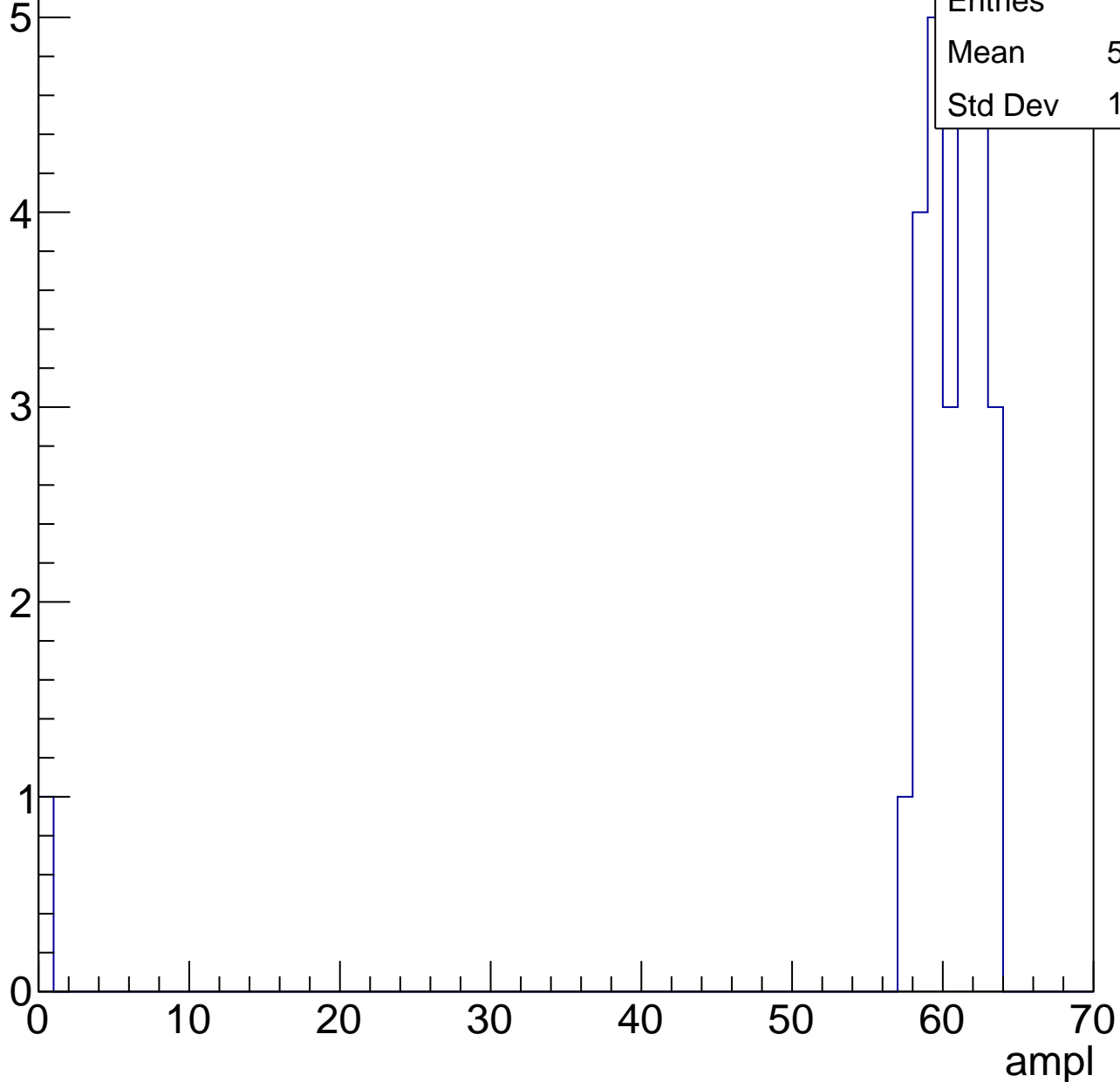
Entries	51
Mean	58.14
Std Dev	3.343



B0L001S, U22-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U22-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch68, adc0

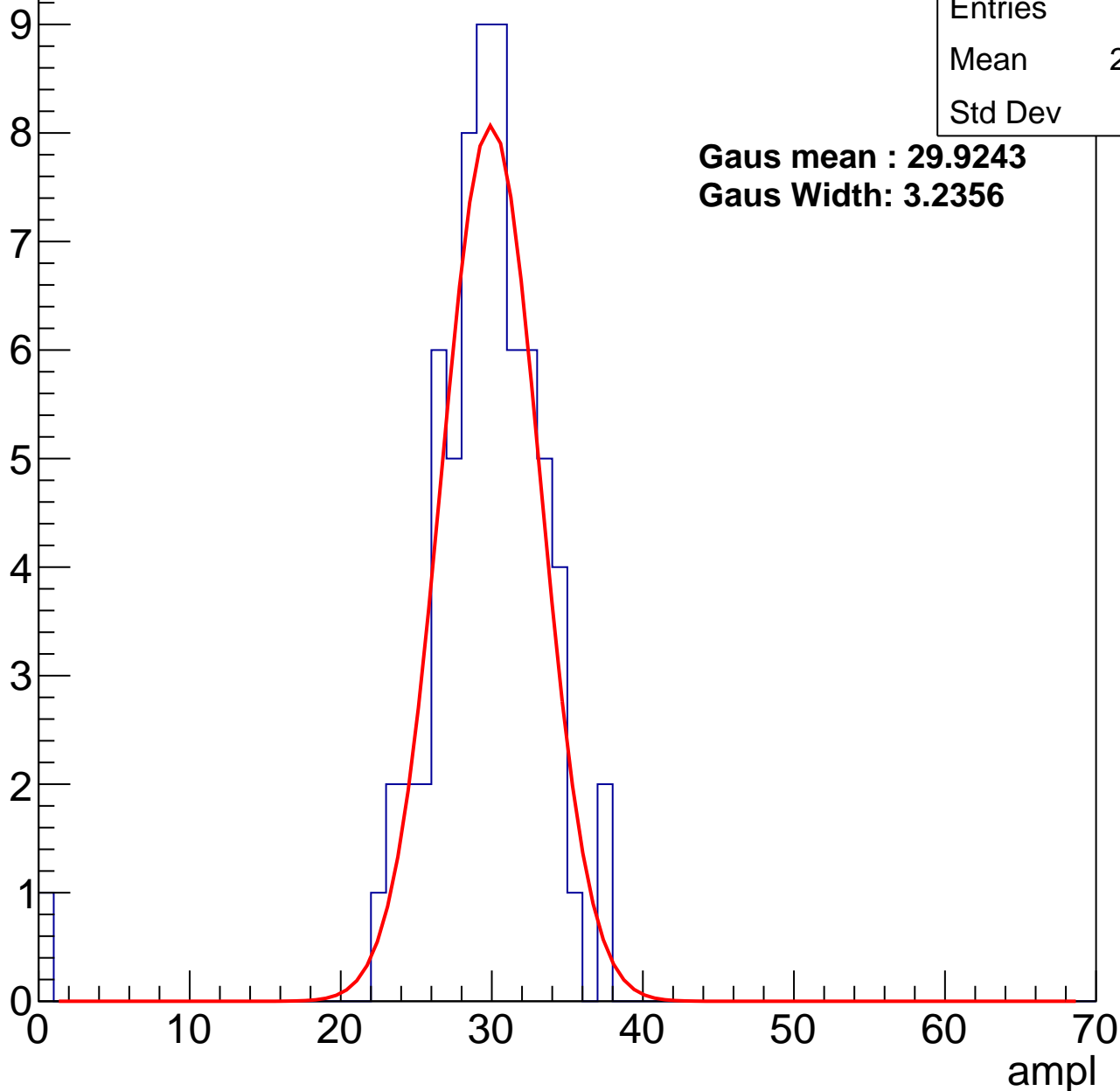
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	28.99
Std Dev	4.75

Gaus mean : 29.9243

Gaus Width: 3.2356



B0L001S, U22-ch68, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	37.32
Std Dev	3.825

Gaus mean : 37.5847

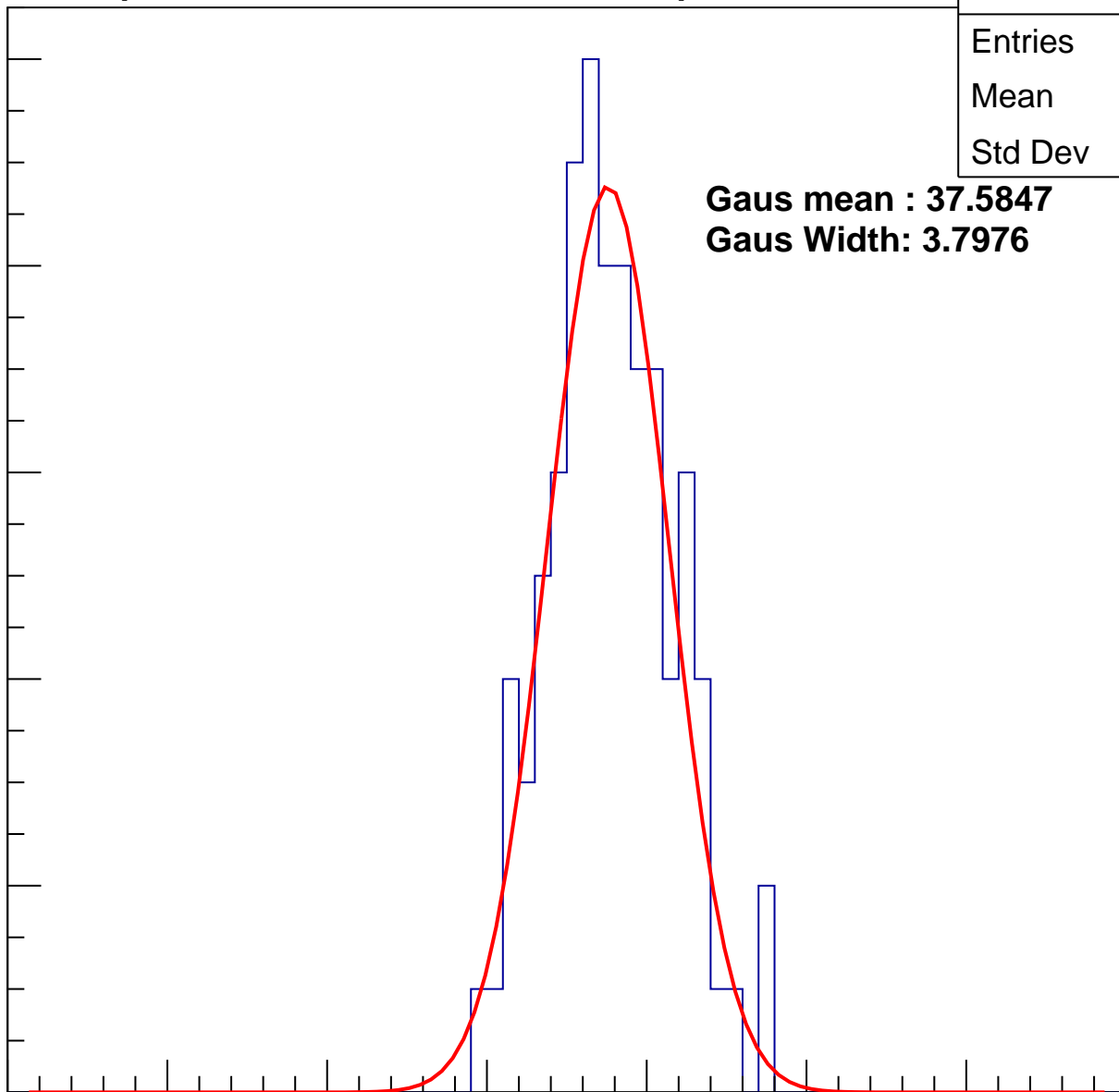
Gaus Width: 3.7976

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch68, adc2

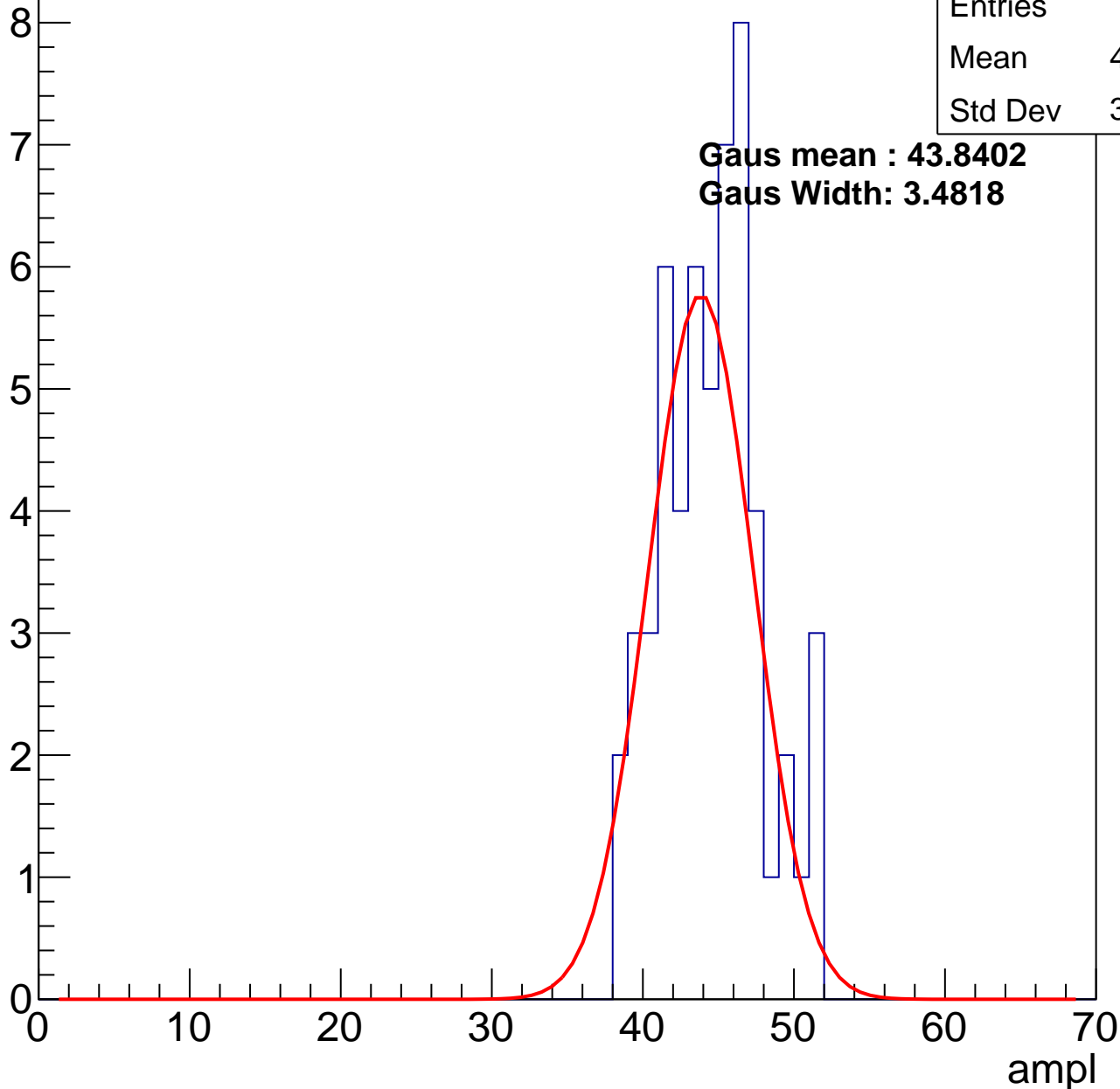
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.09
Std Dev	3.288

Gaus mean : 43.8402

Gaus Width: 3.4818

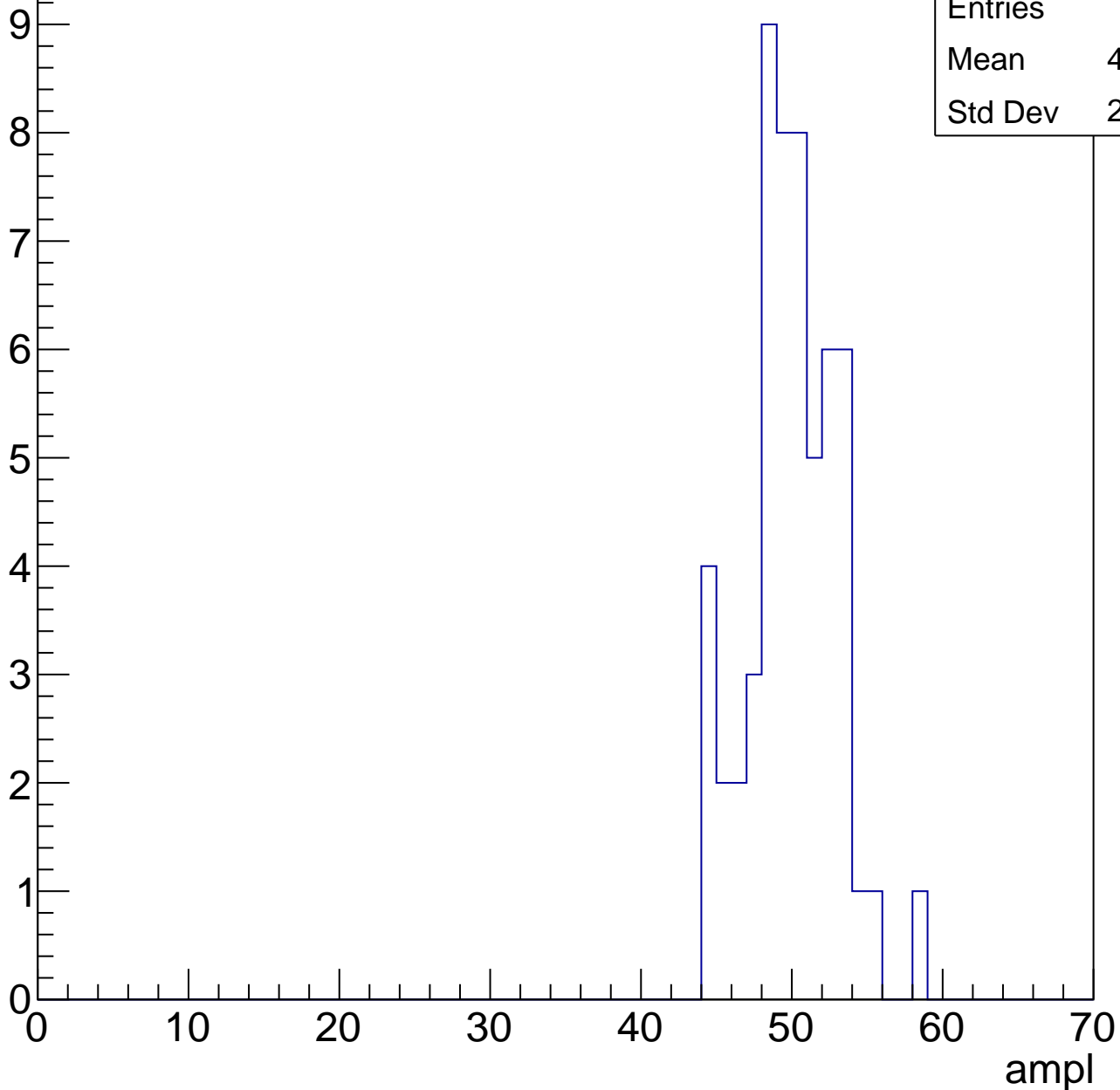


B0L001S, U22-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	49.55
Std Dev	2.909

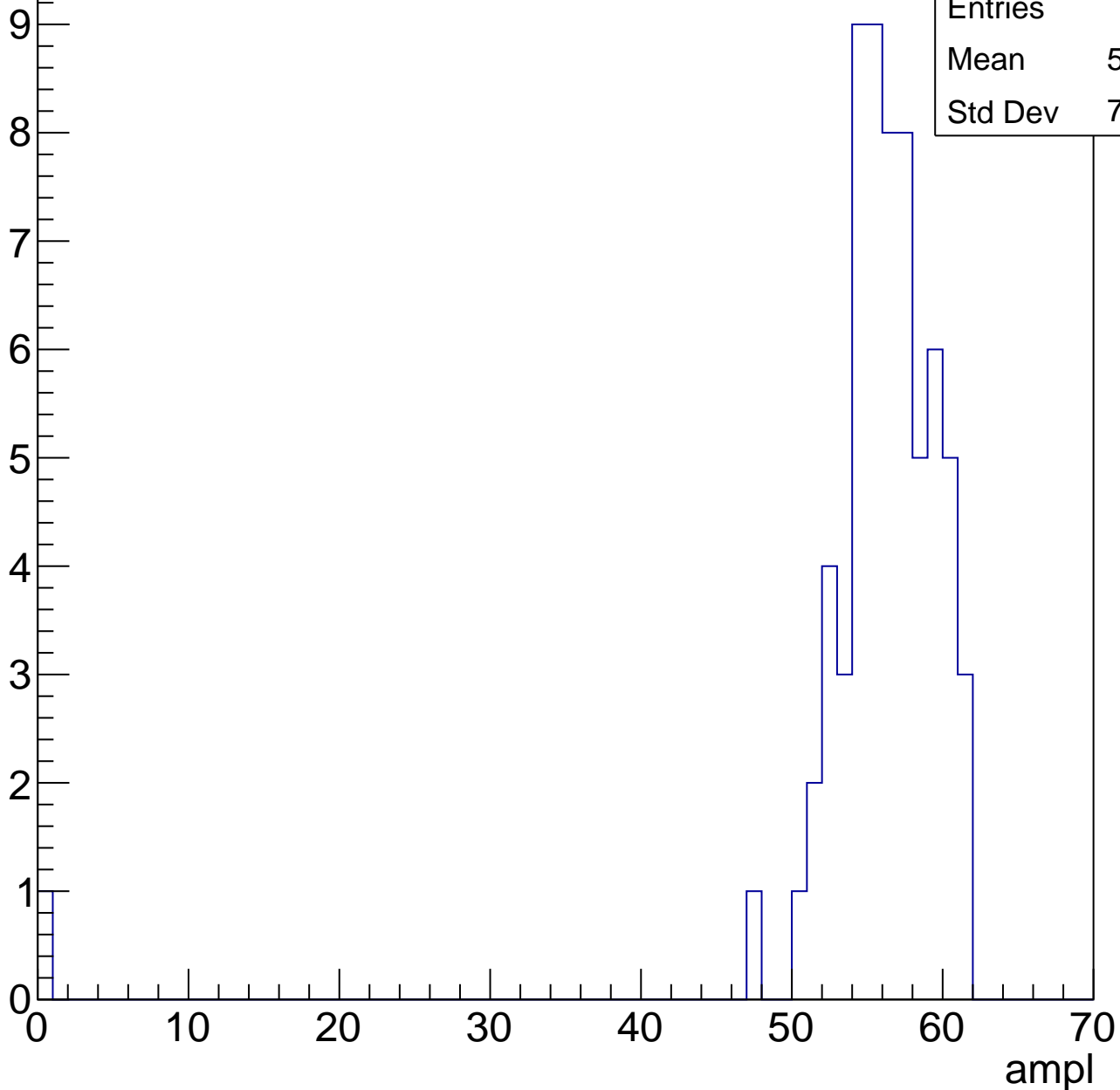


B0L001S, U22-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	55.05
Std Dev	7.459

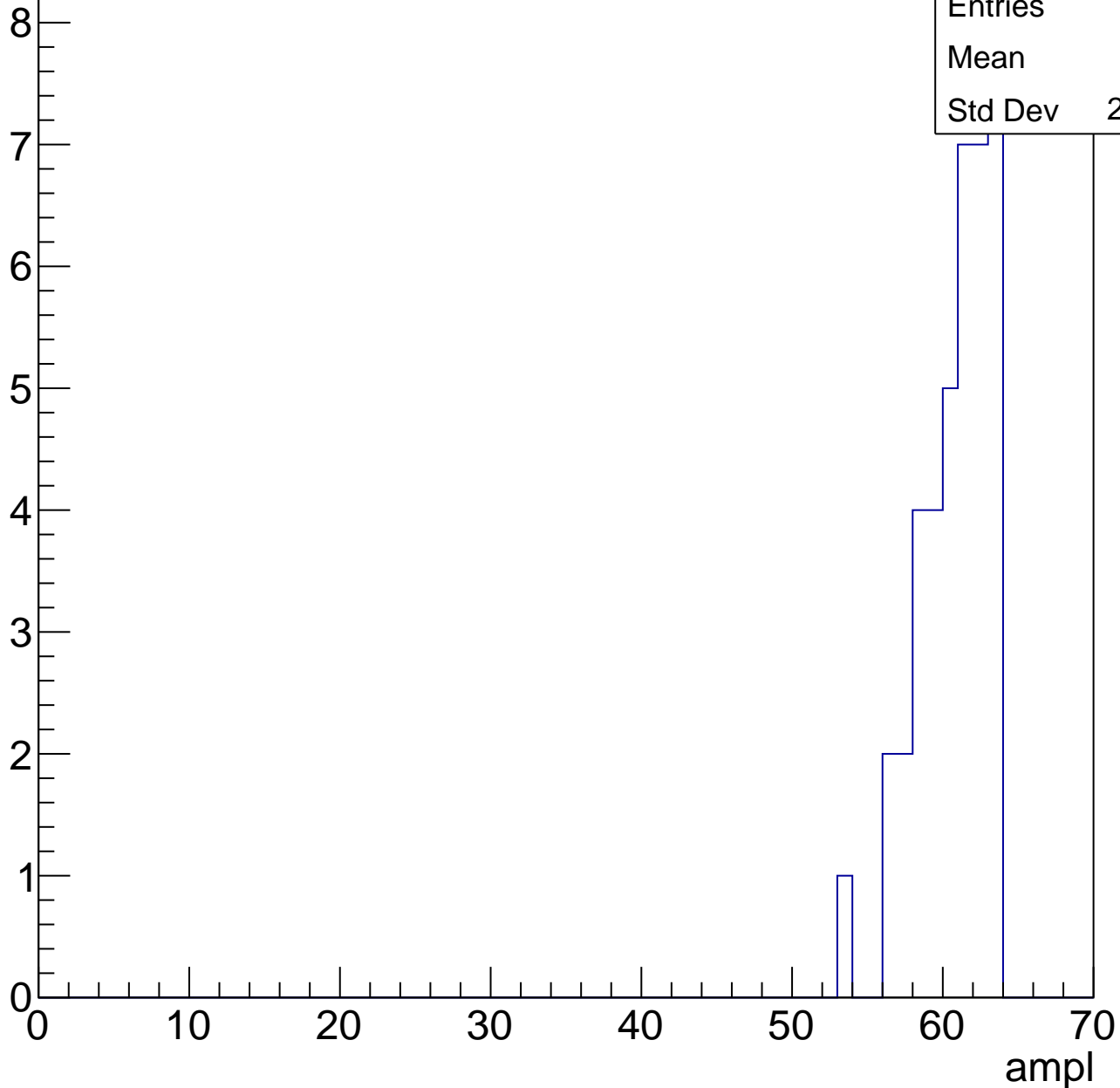


B0L001S, U22-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	60.3
Std Dev	2.358



B0L001S, U22-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch69, adc0

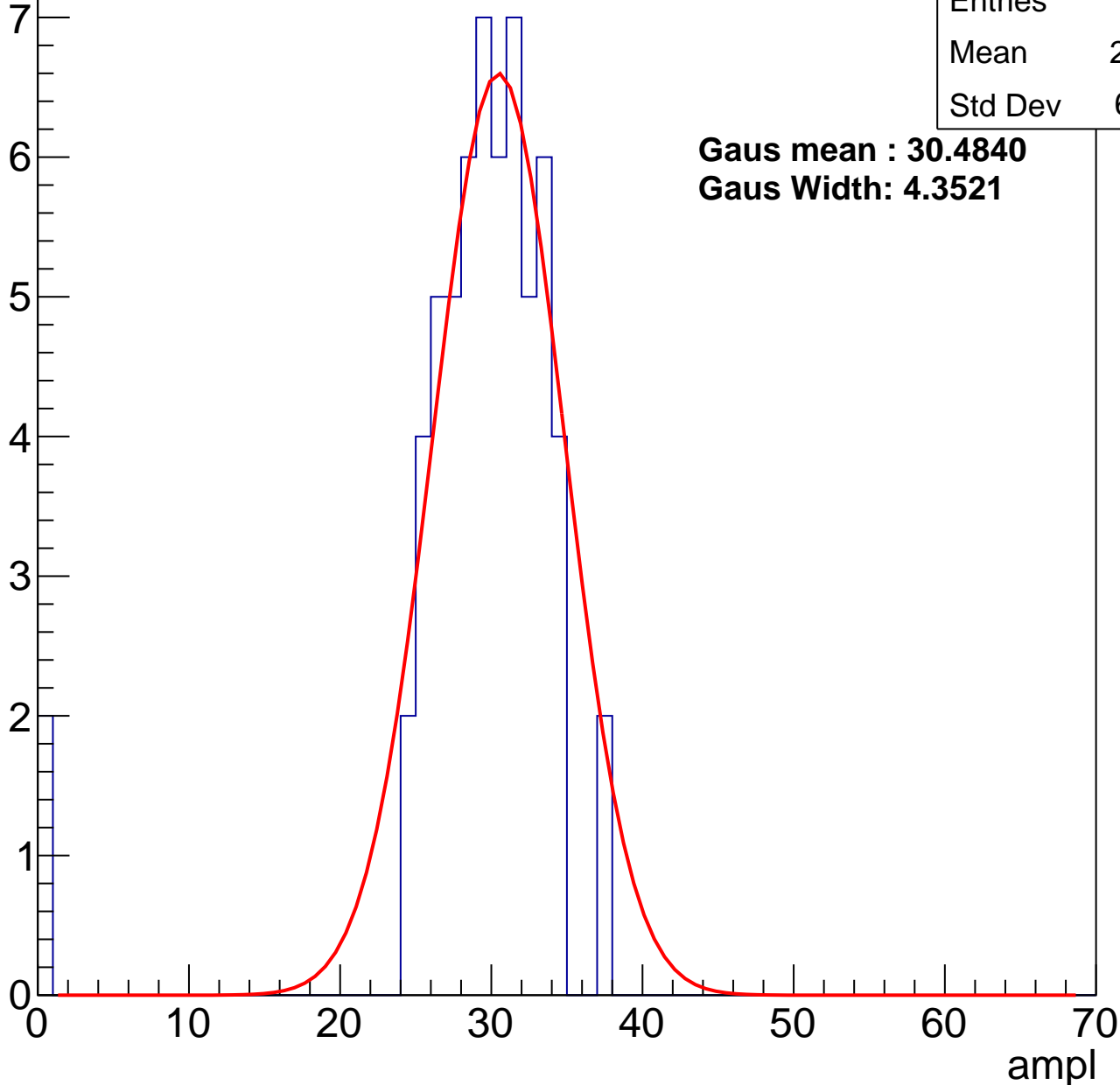
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.67
Std Dev	6.091

Gaus mean : 30.4840

Gaus Width: 4.3521



B0L001S, U22-ch69, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	36.64
Std Dev	3.836

Gaus mean : 37.1313

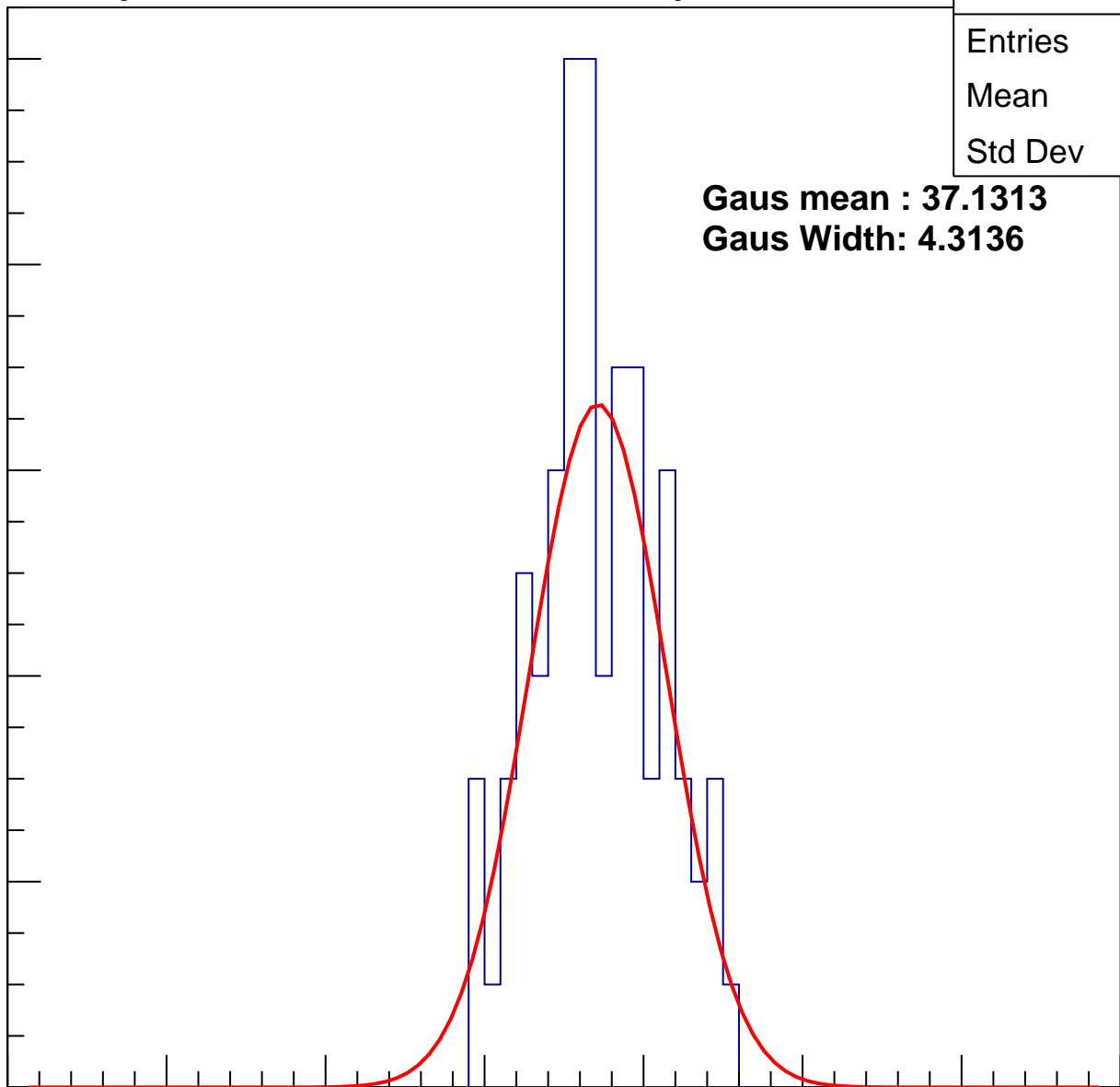
Gaus Width: 4.3136

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch69, adc2

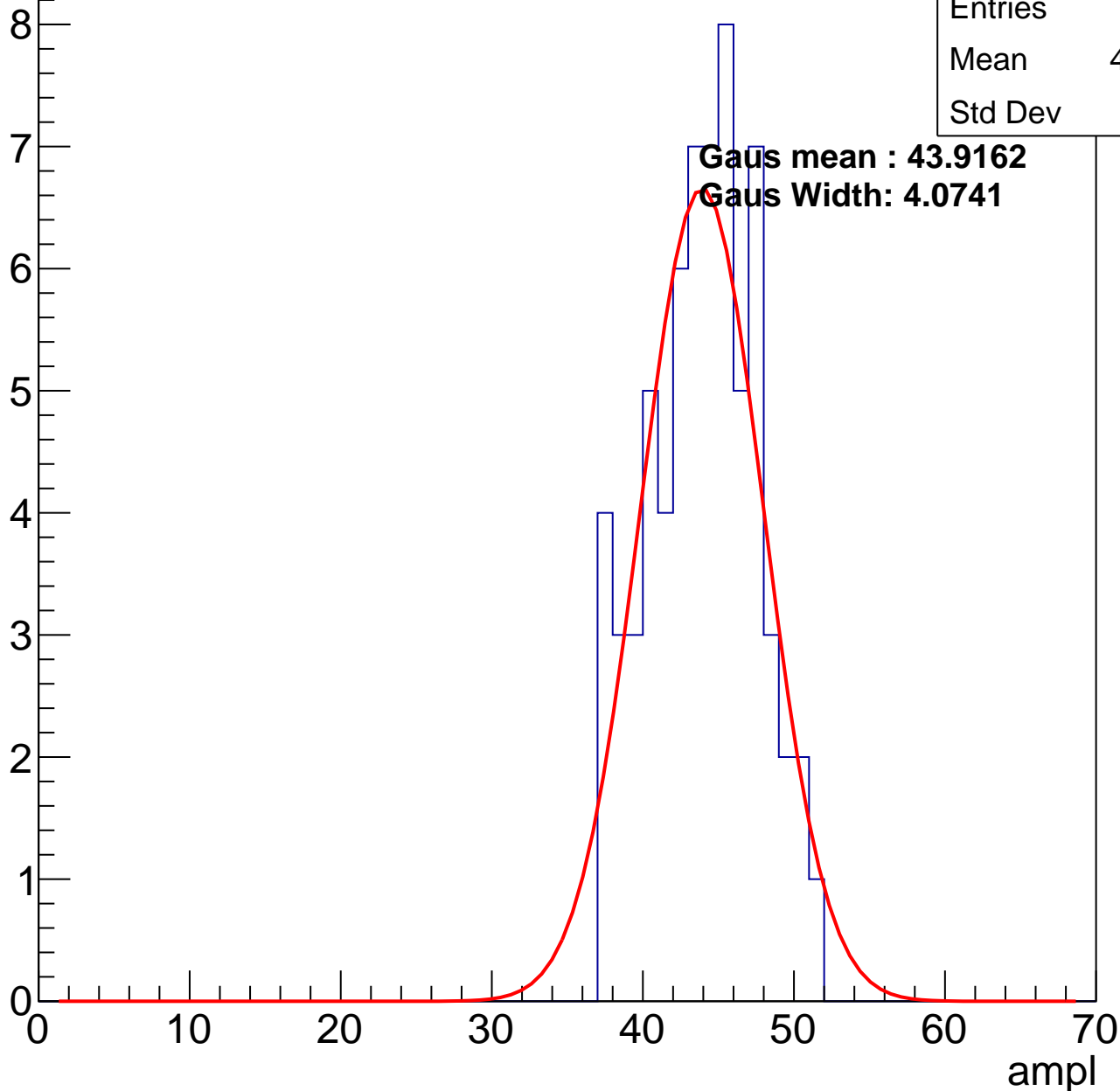
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.52
Std Dev	3.5

Gaus mean : 43.9162

Gaus Width: 4.0741

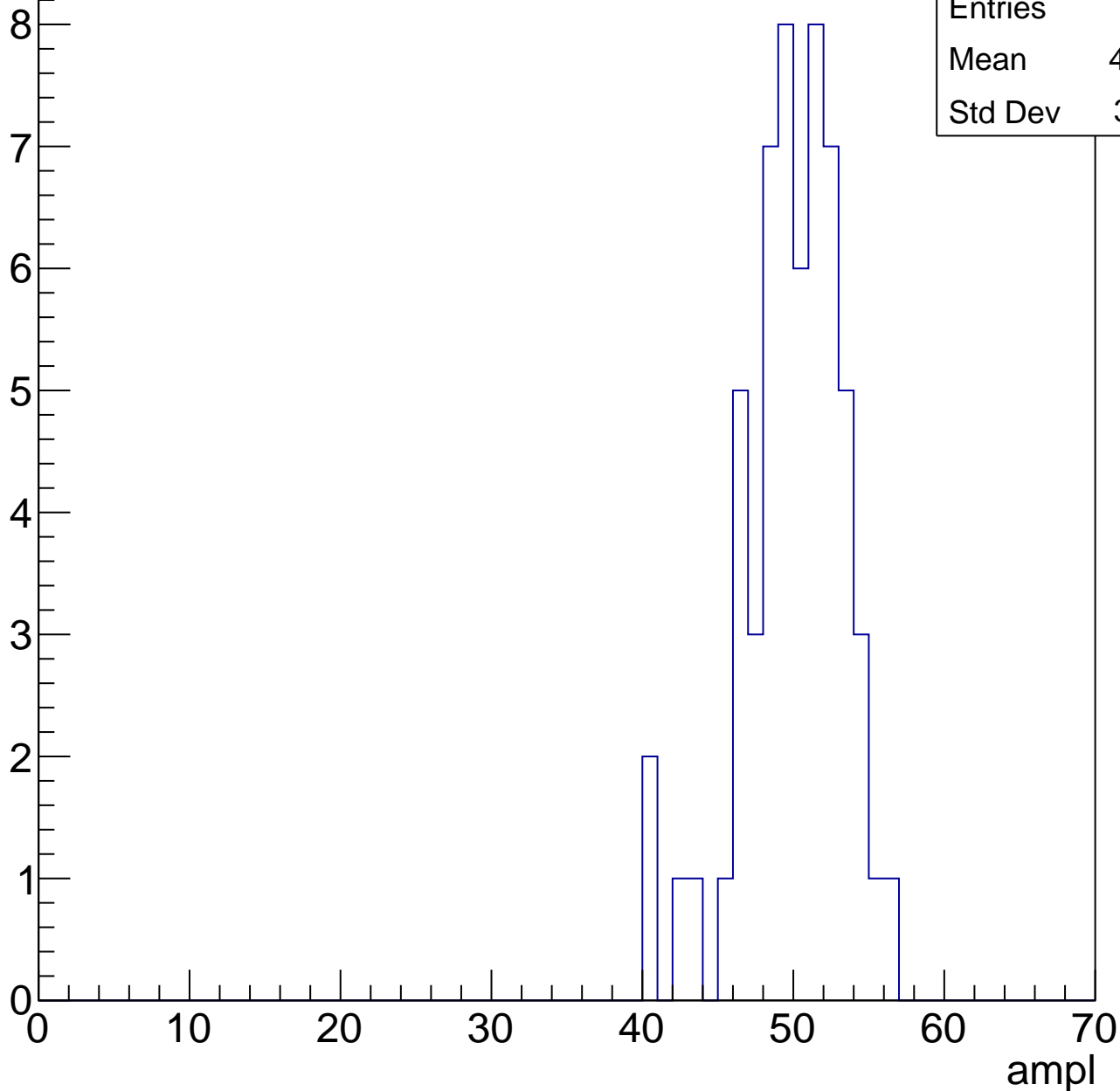


B0L001S, U22-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	49.47
Std Dev	3.331



B0L001S, U22-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	55.81
Std Dev	2.589

Entry

10

8

6

4

2

0

0

10

20

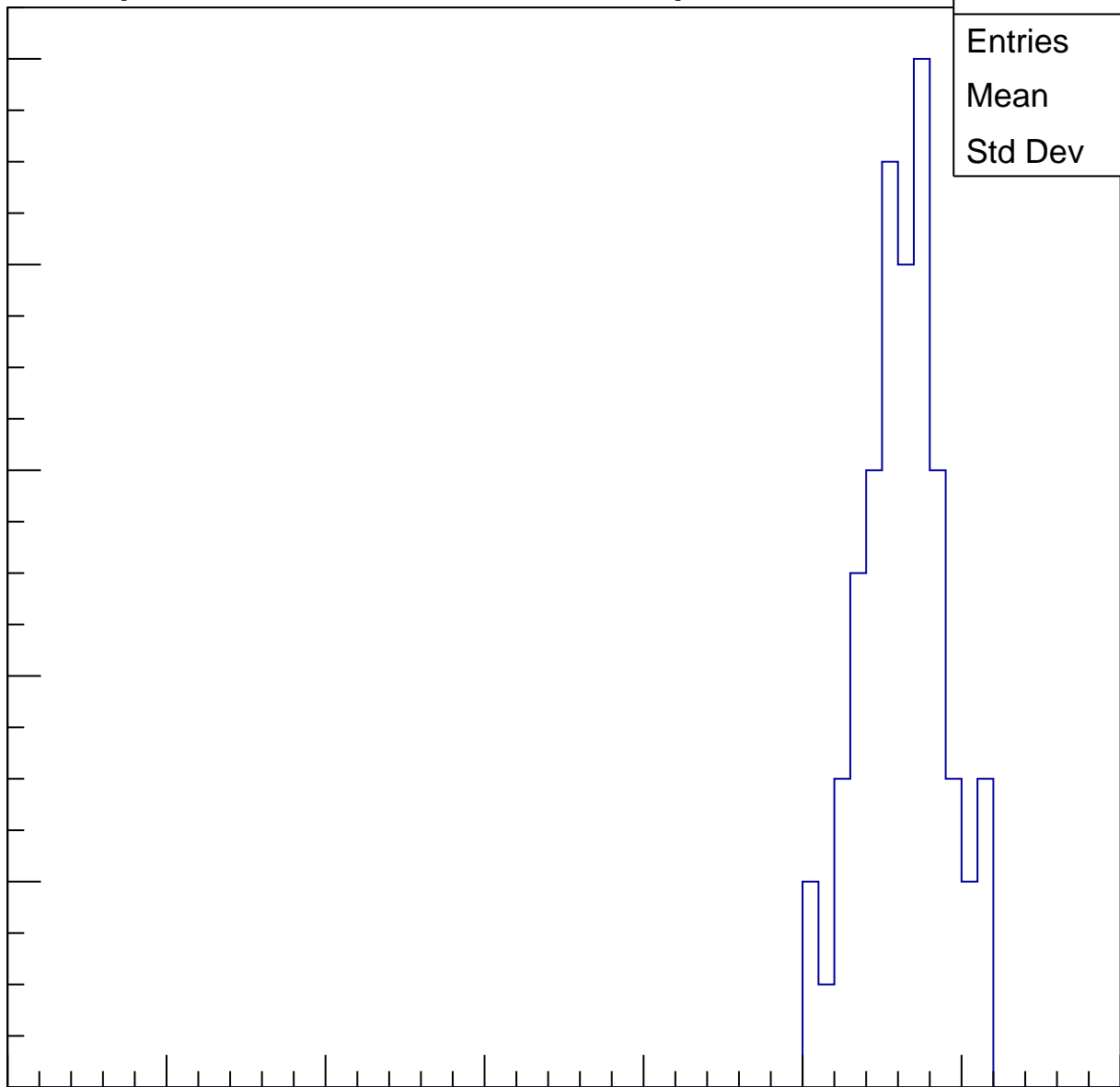
30

40

50

60

ampl

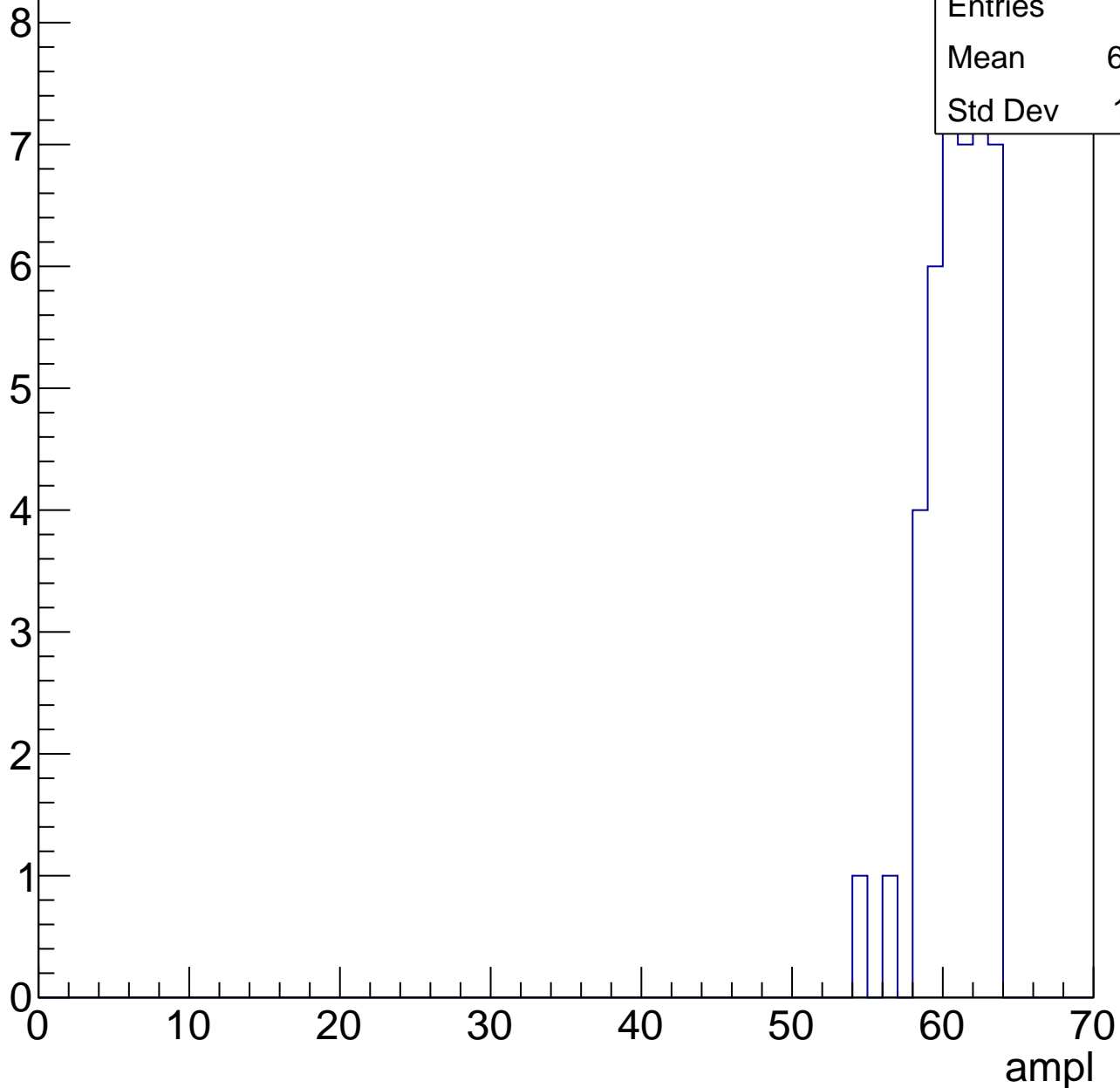


B0L001S, U22-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

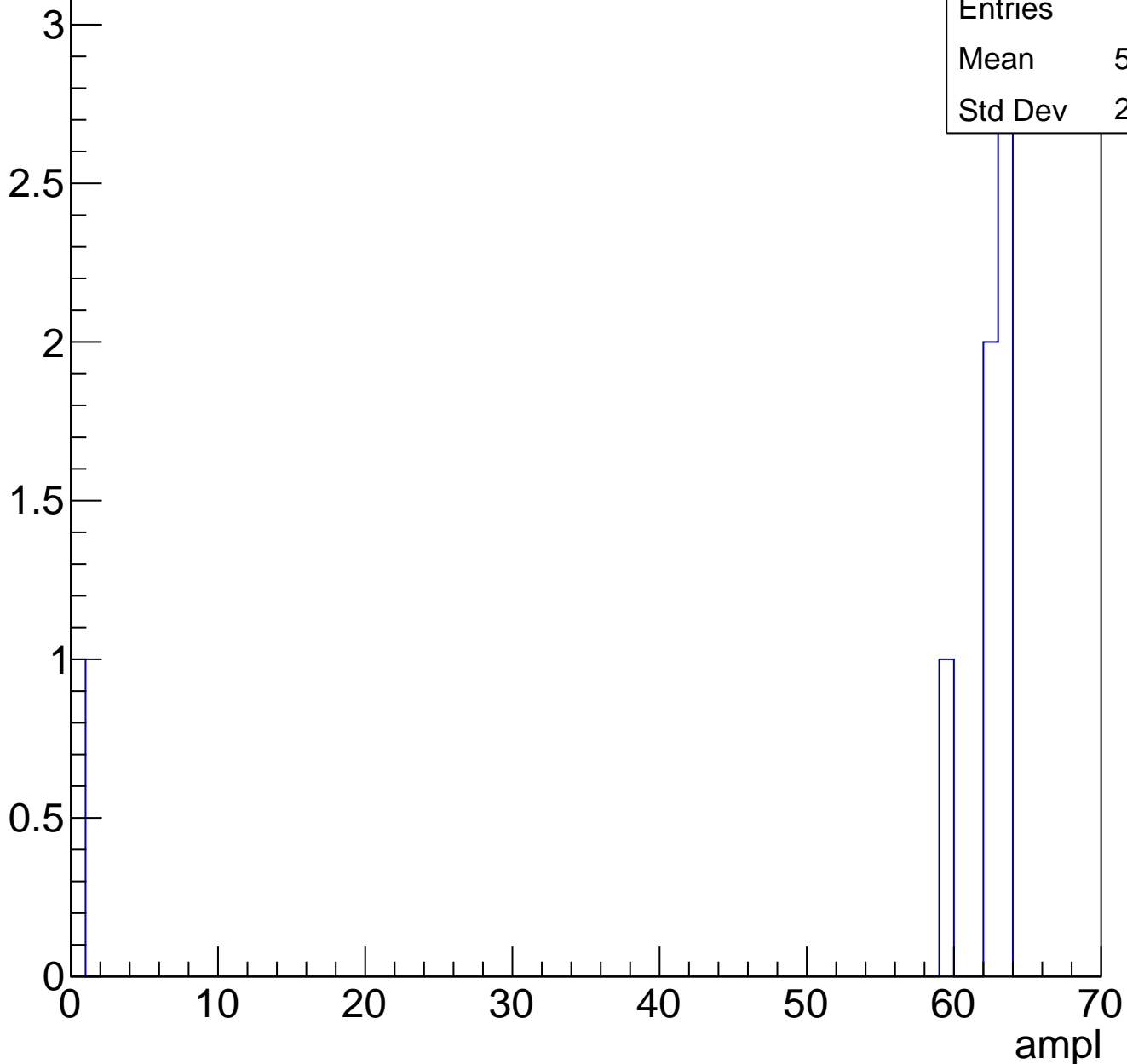
Entries	42
Mean	60.48
Std Dev	1.991



B0L001S, U22-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U22-ch70, adc0

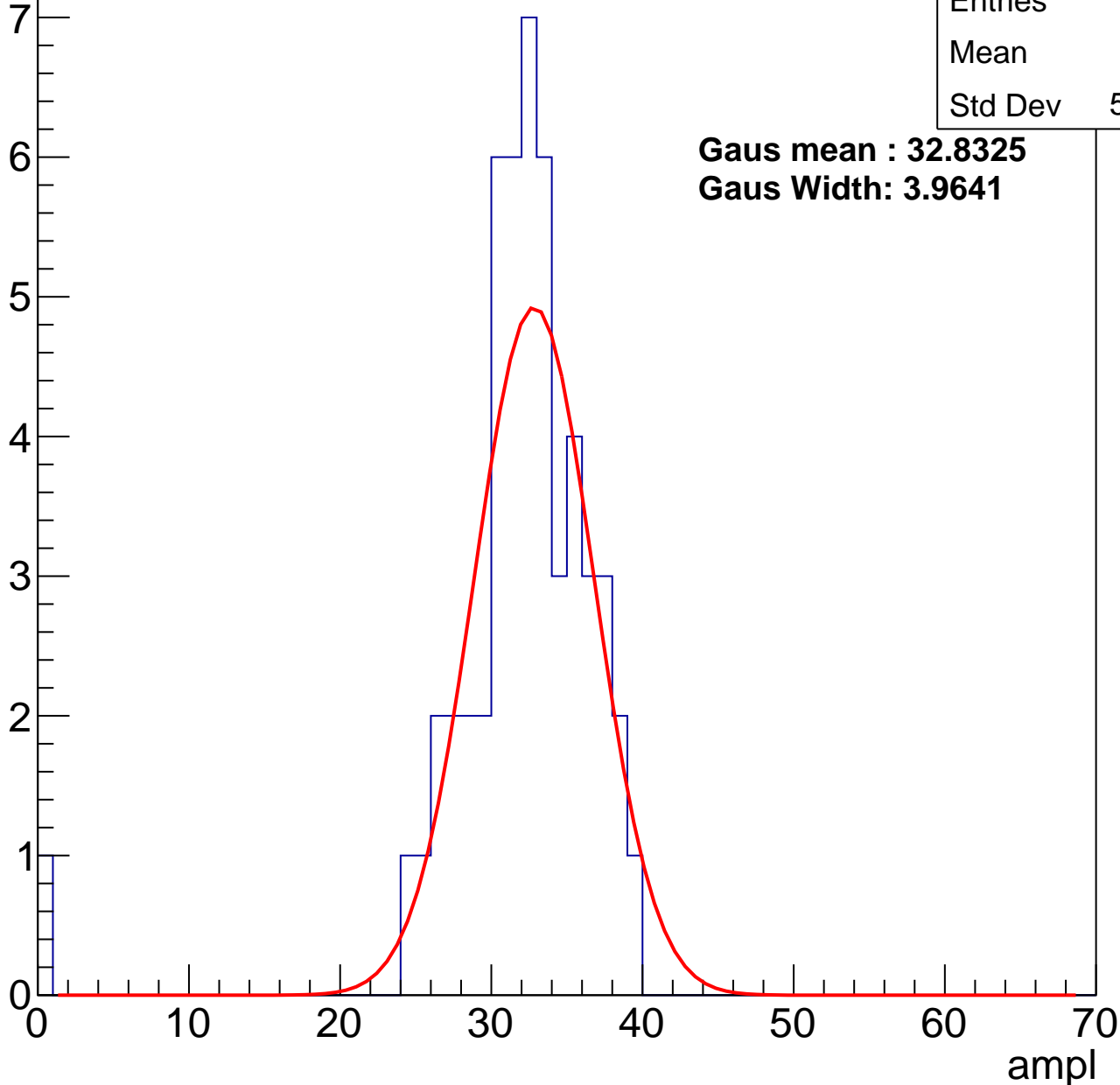
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	31.4
Std Dev	5.579

Gaus mean : 32.8325

Gaus Width: 3.9641



B0L001S, U22-ch70, adc1

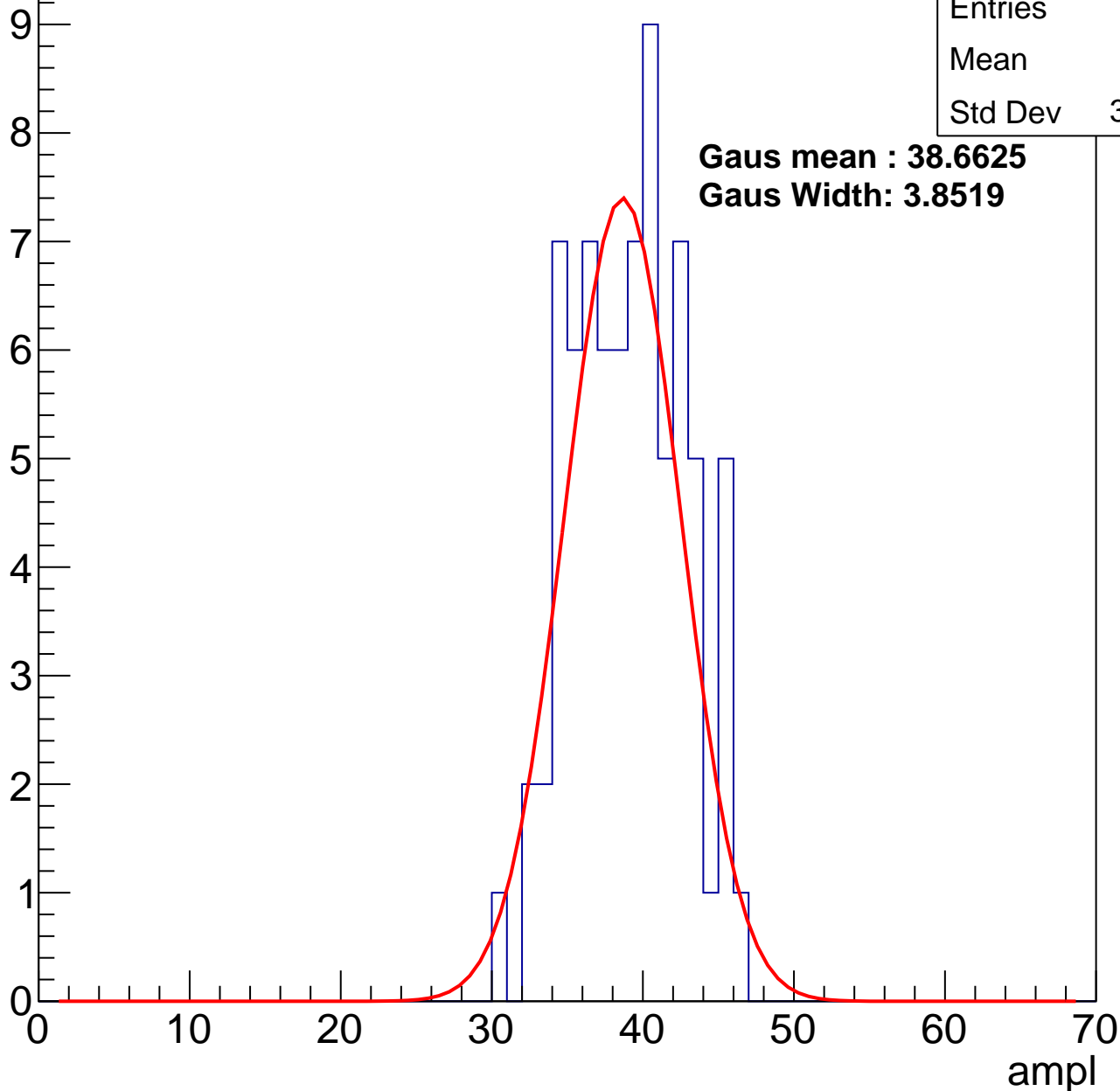
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	38.6
Std Dev	3.655

Gaus mean : 38.6625

Gaus Width: 3.8519



B0L001S, U22-ch70, adc2

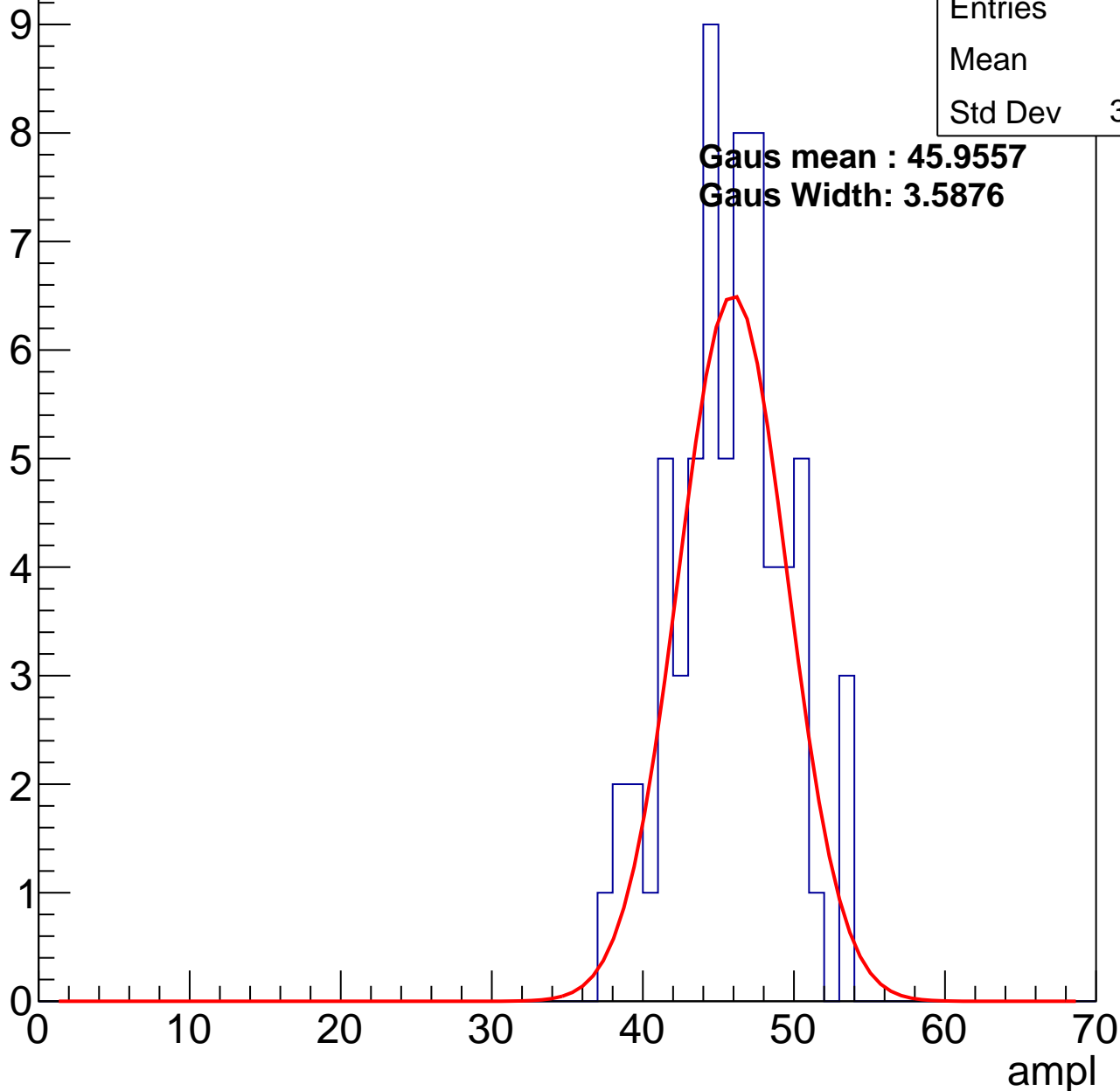
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.3
Std Dev	3.656

Gaus mean : 45.9557

Gaus Width: 3.5876

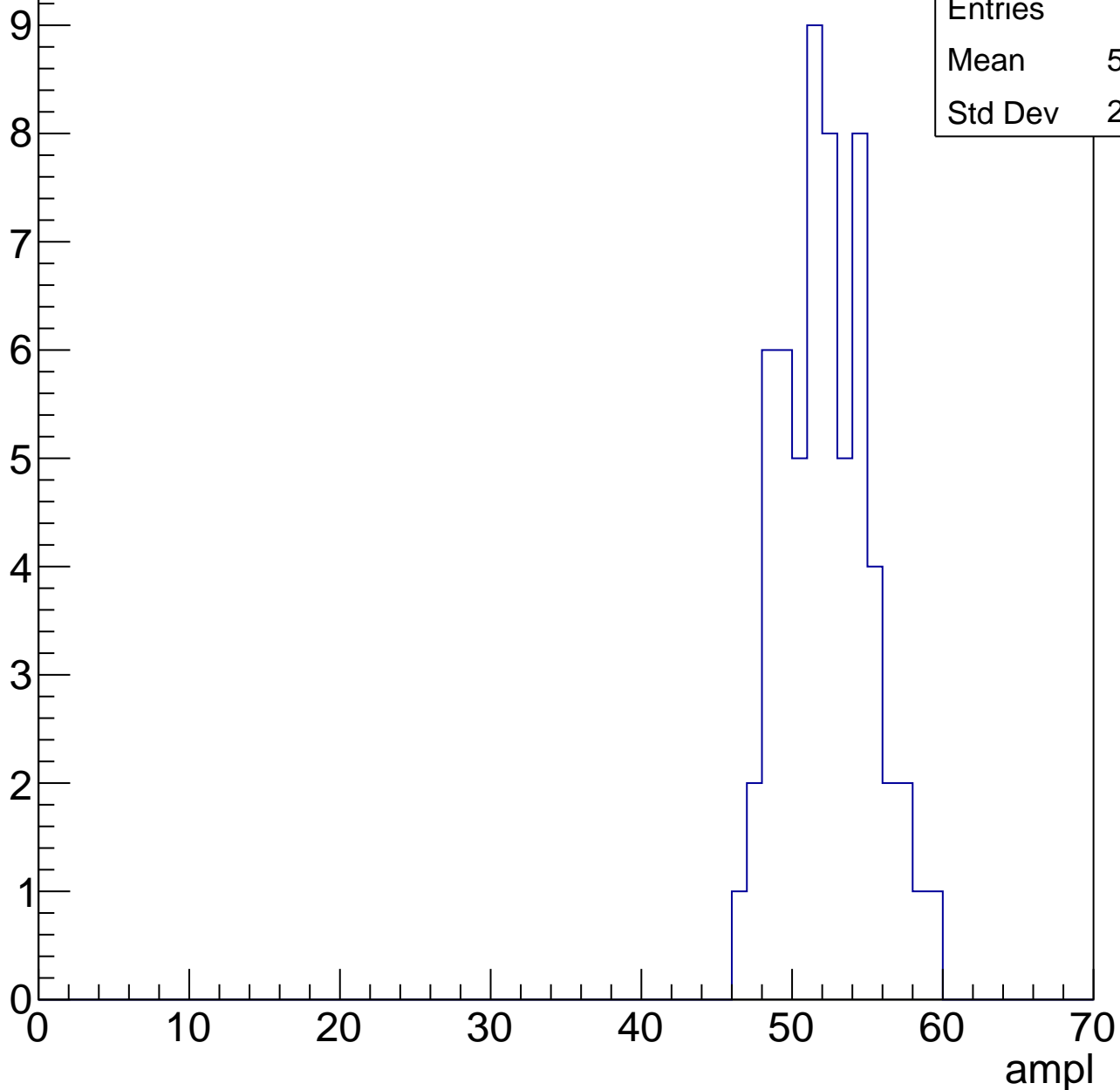


B0L001S, U22-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	51.78
Std Dev	2.893

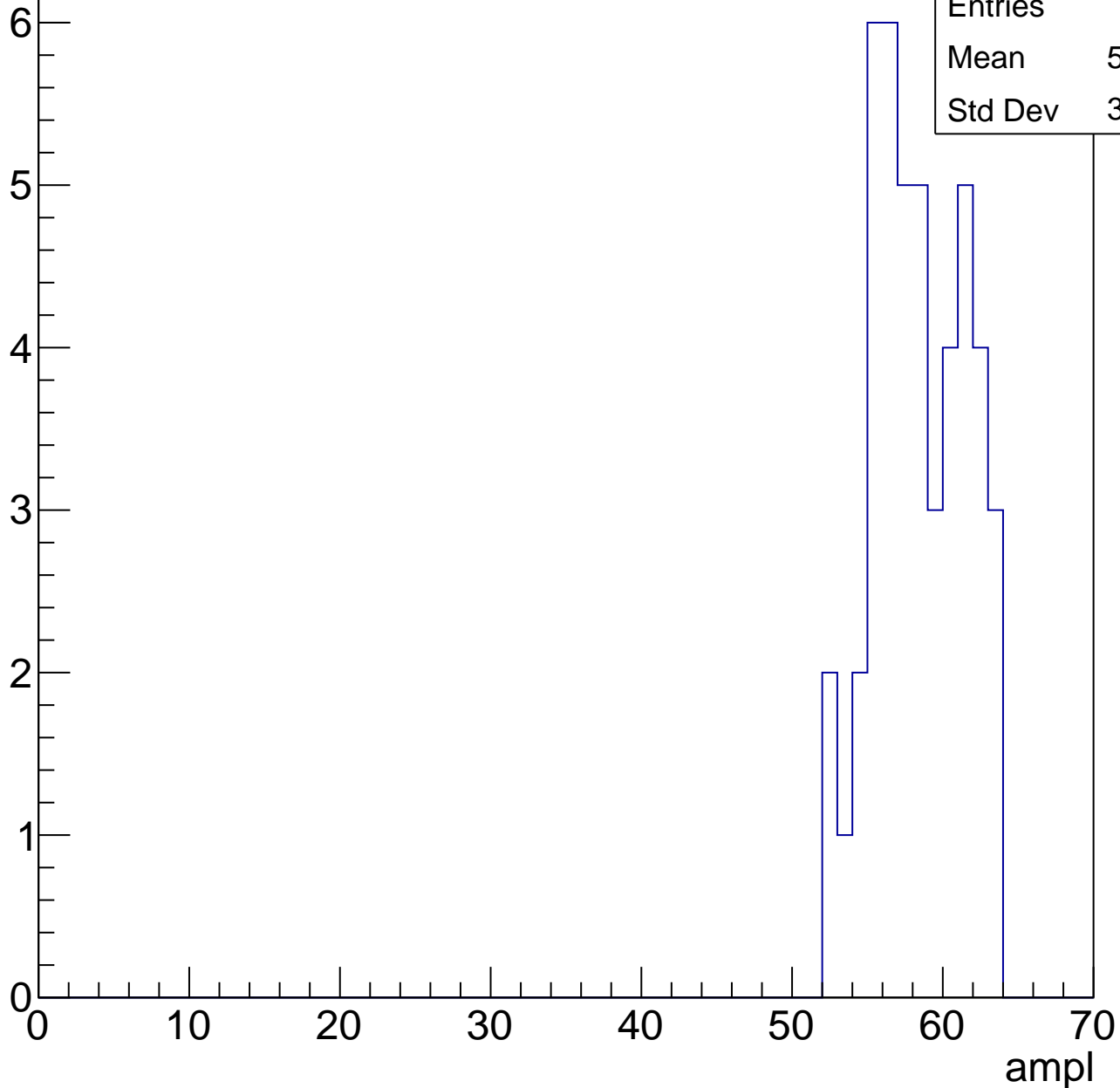


B0L001S, U22-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.93
Std Dev	3.003

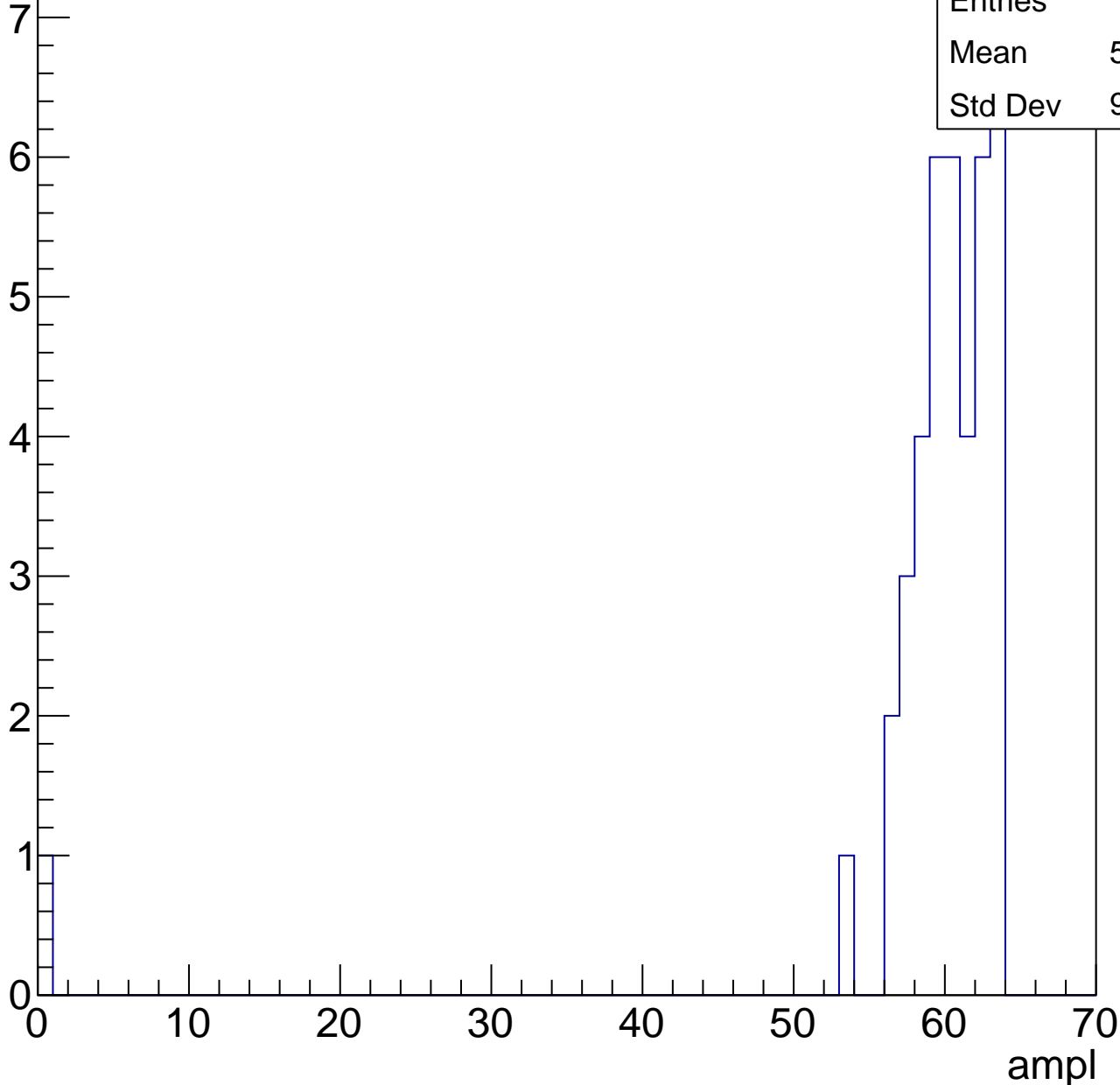


B0L001S, U22-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	58.48
Std Dev	9.654



B0L001S, U22-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch71, adc0

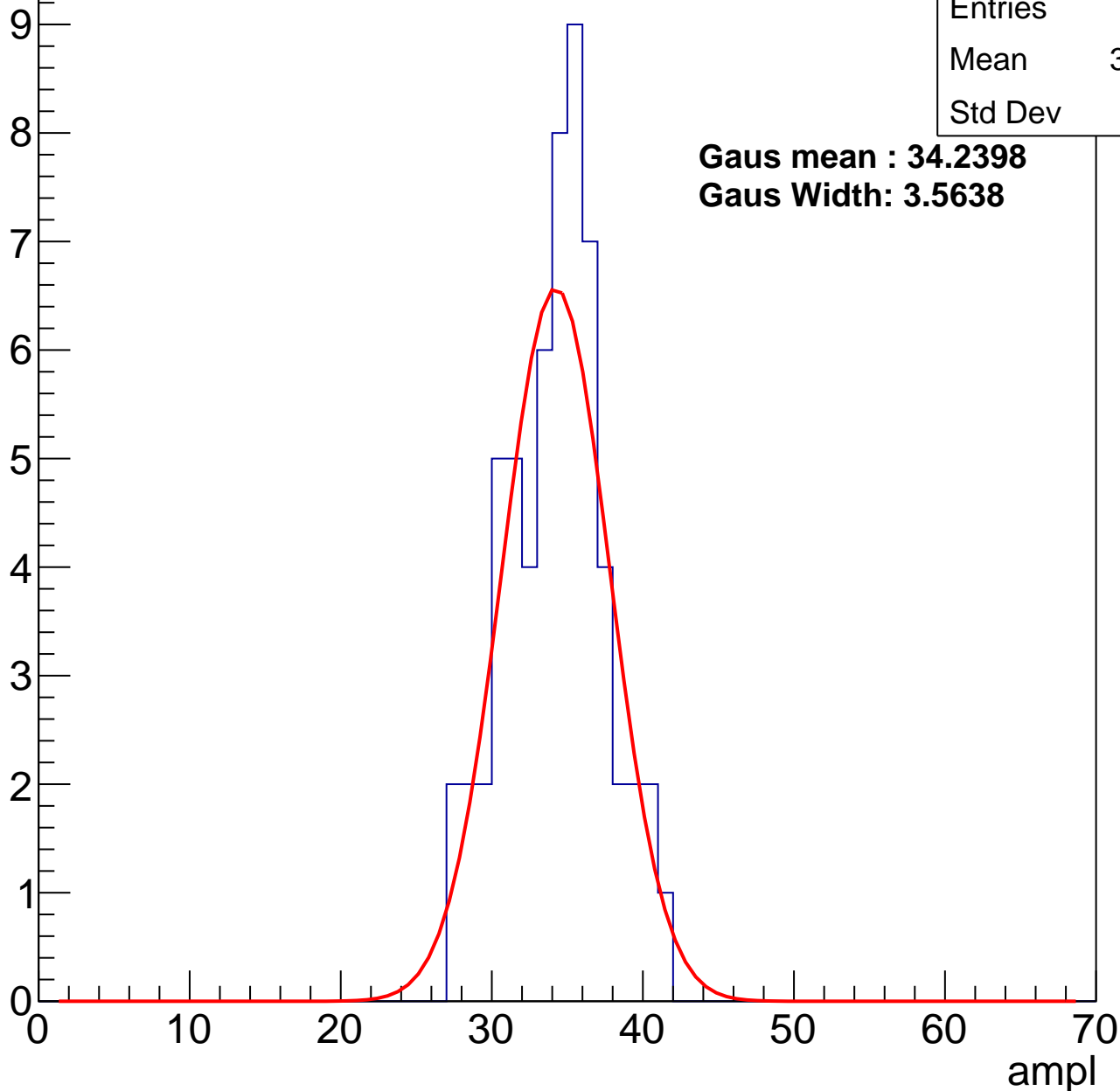
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	33.79
Std Dev	3.24

Gaus mean : 34.2398

Gaus Width: 3.5638



B0L001S, U22-ch71, adc1

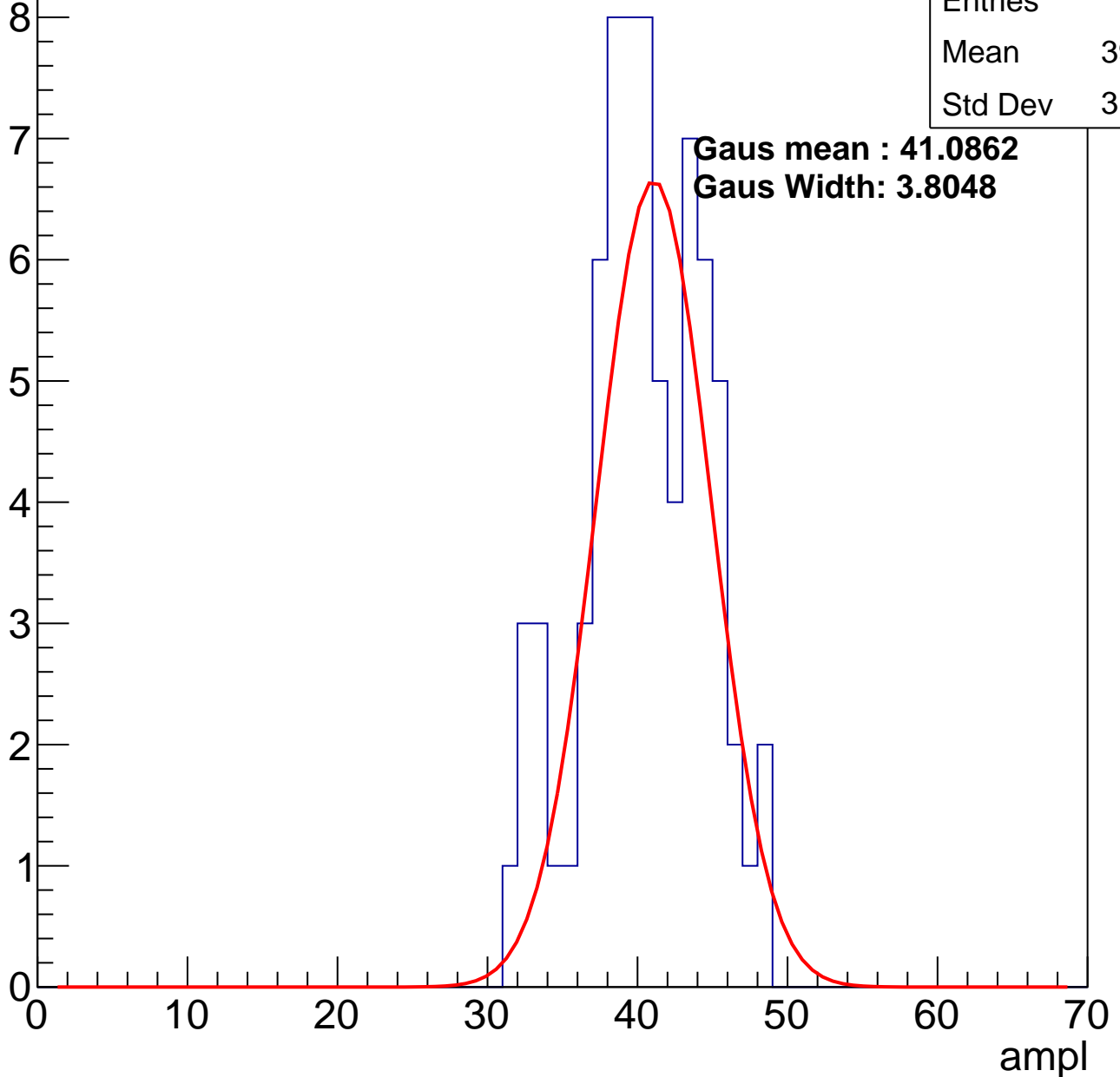
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	39.99
Std Dev	3.988

Gaus mean : 41.0862

Gaus Width: 3.8048



B0L001S, U22-ch71, adc2

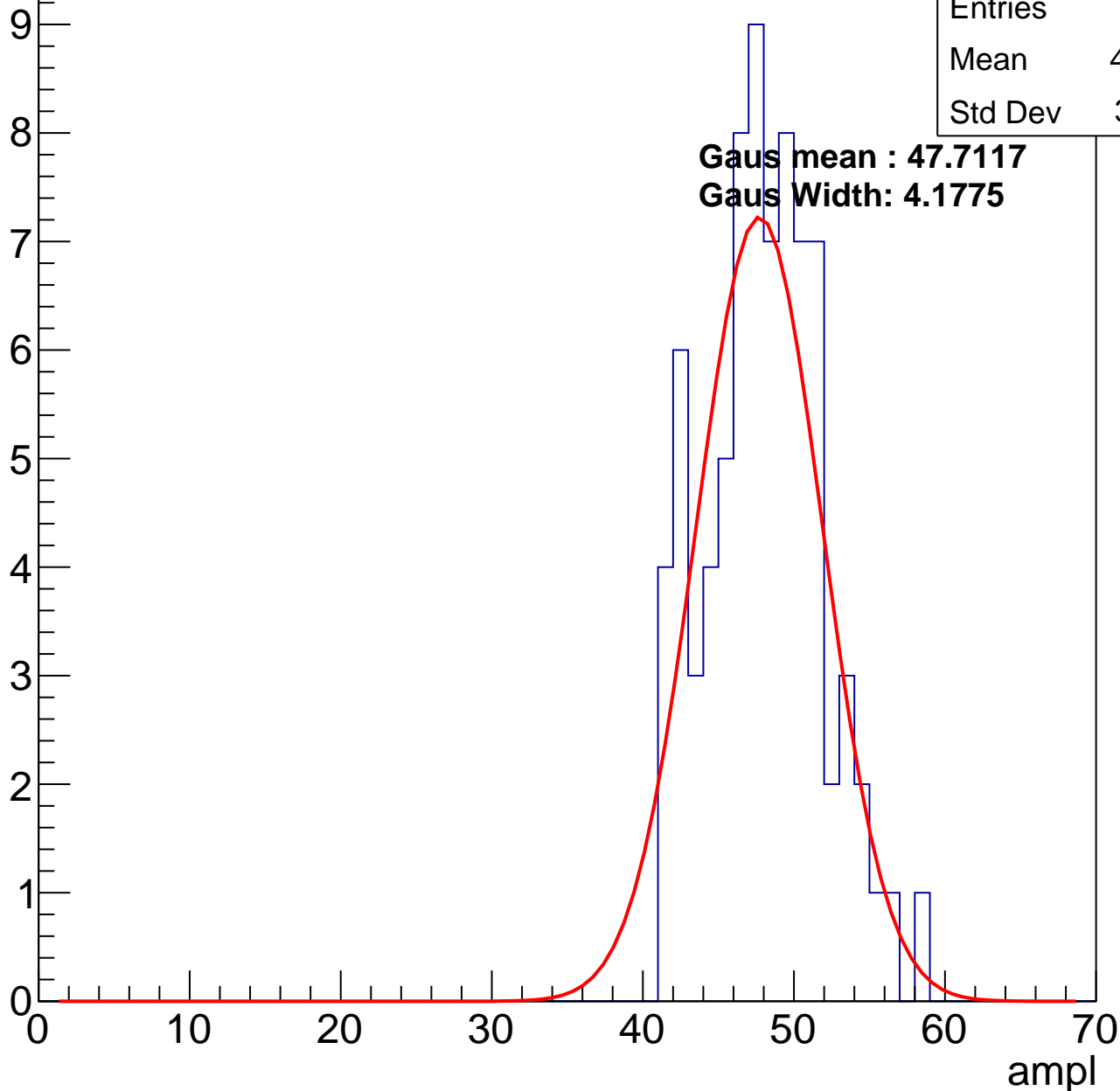
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	47.59
Std Dev	3.781

Gaus mean : 47.7117

Gaus Width: 4.1775

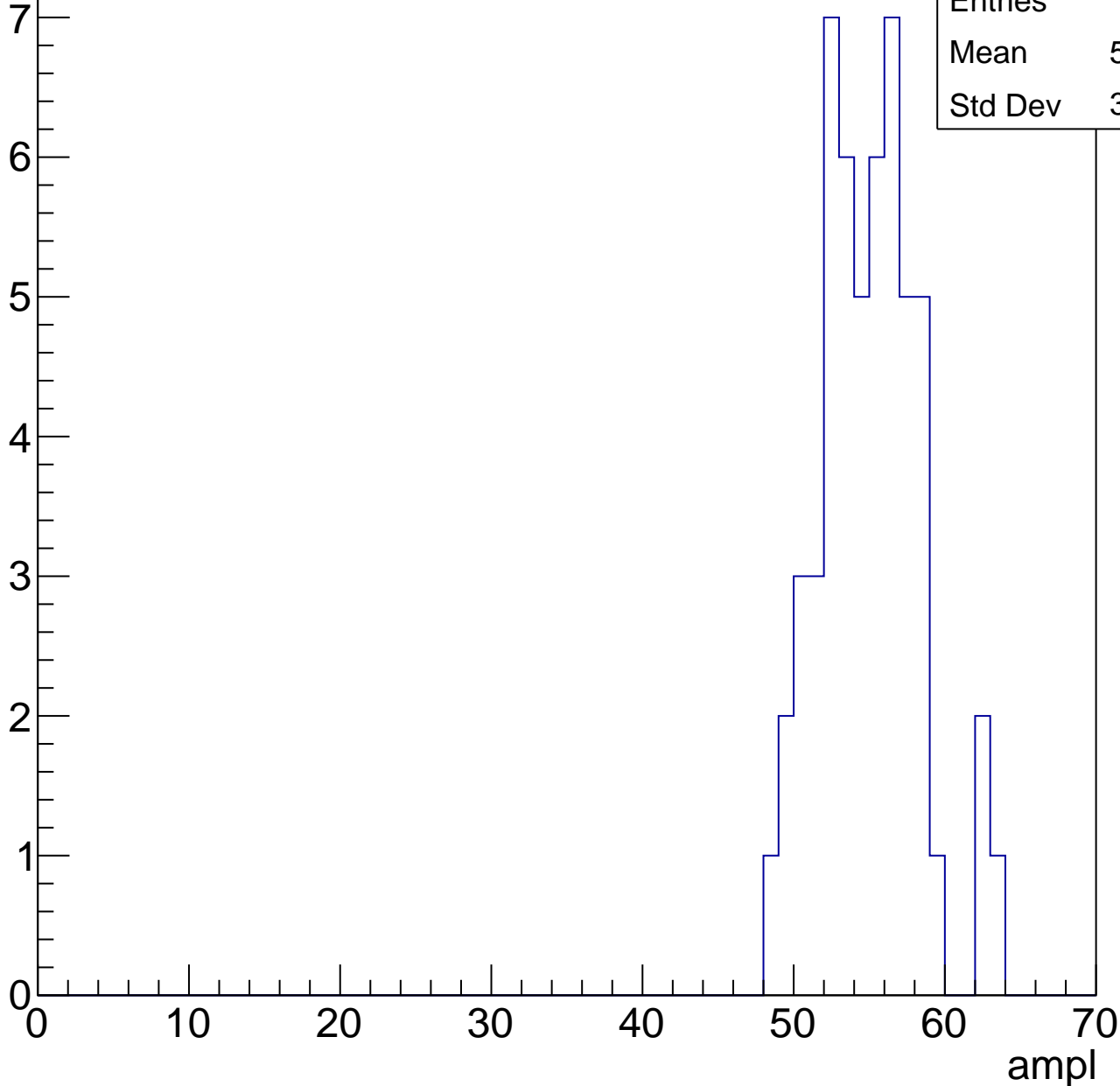


B0L001S, U22-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	54.52
Std Dev	3.259

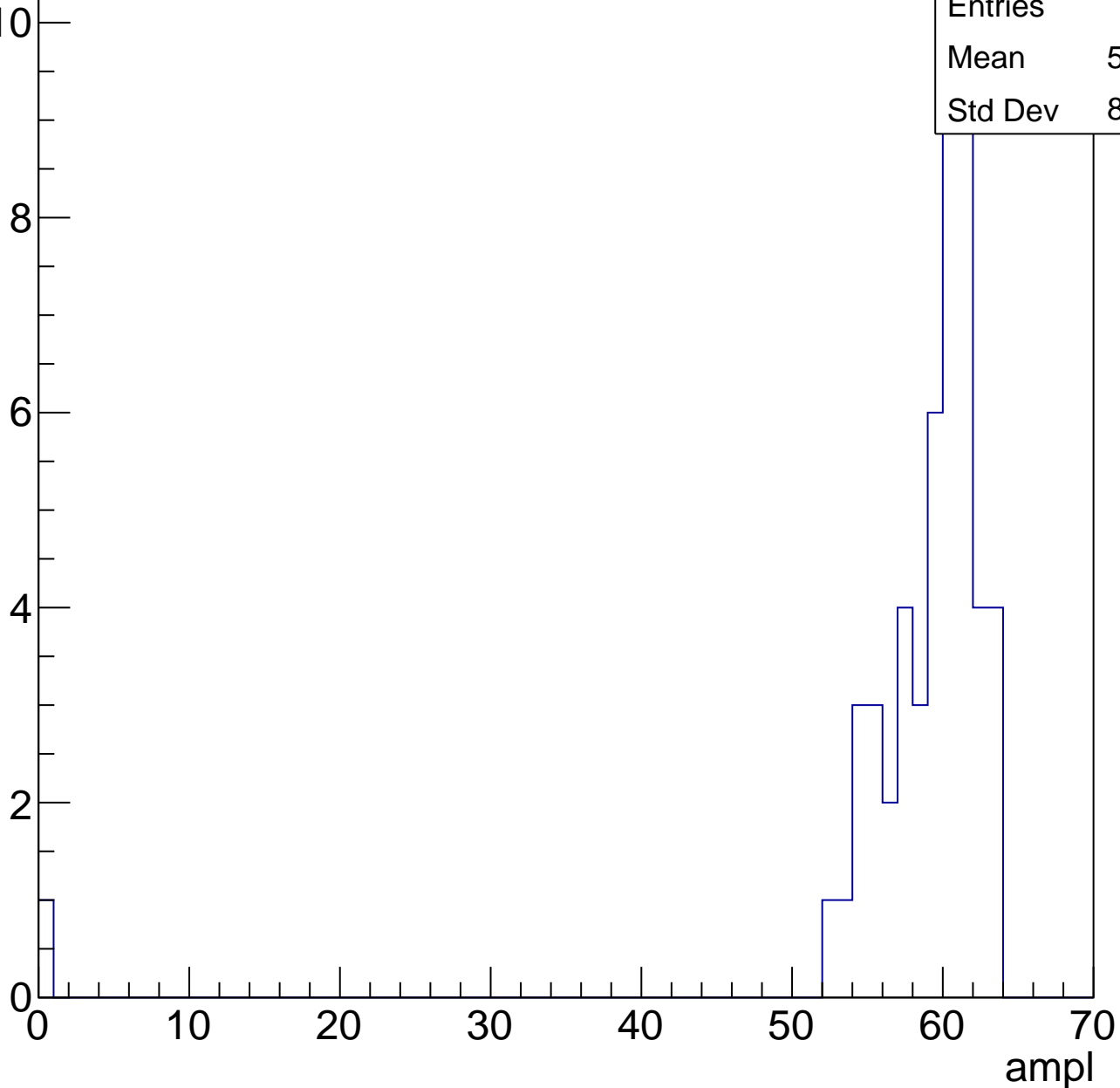


B0L001S, U22-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

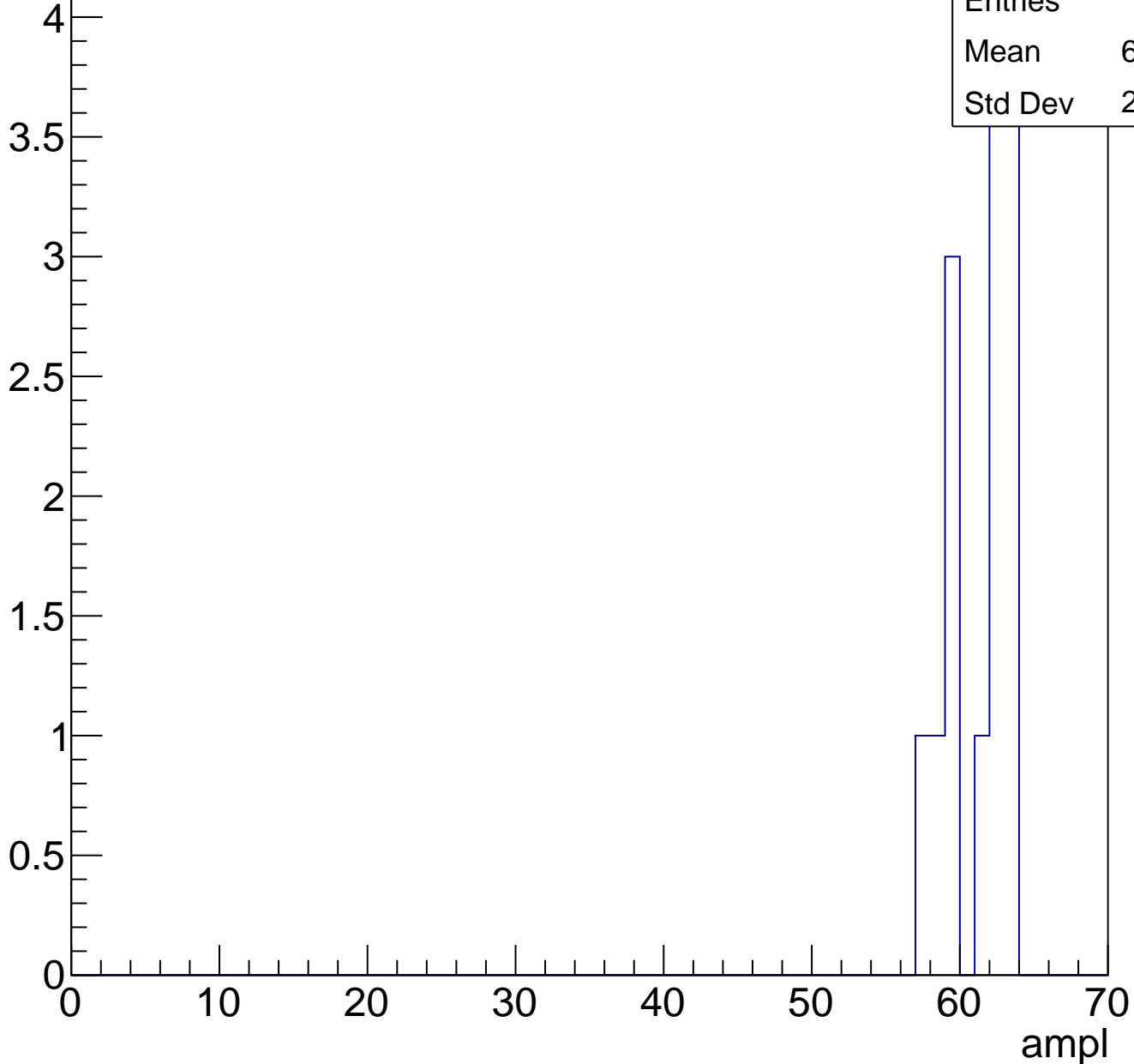
Entries	51
Mean	57.82
Std Dev	8.634



B0L001S, U22-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch72, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	31.28
Std Dev	4.929

Gaus mean : 32.4054

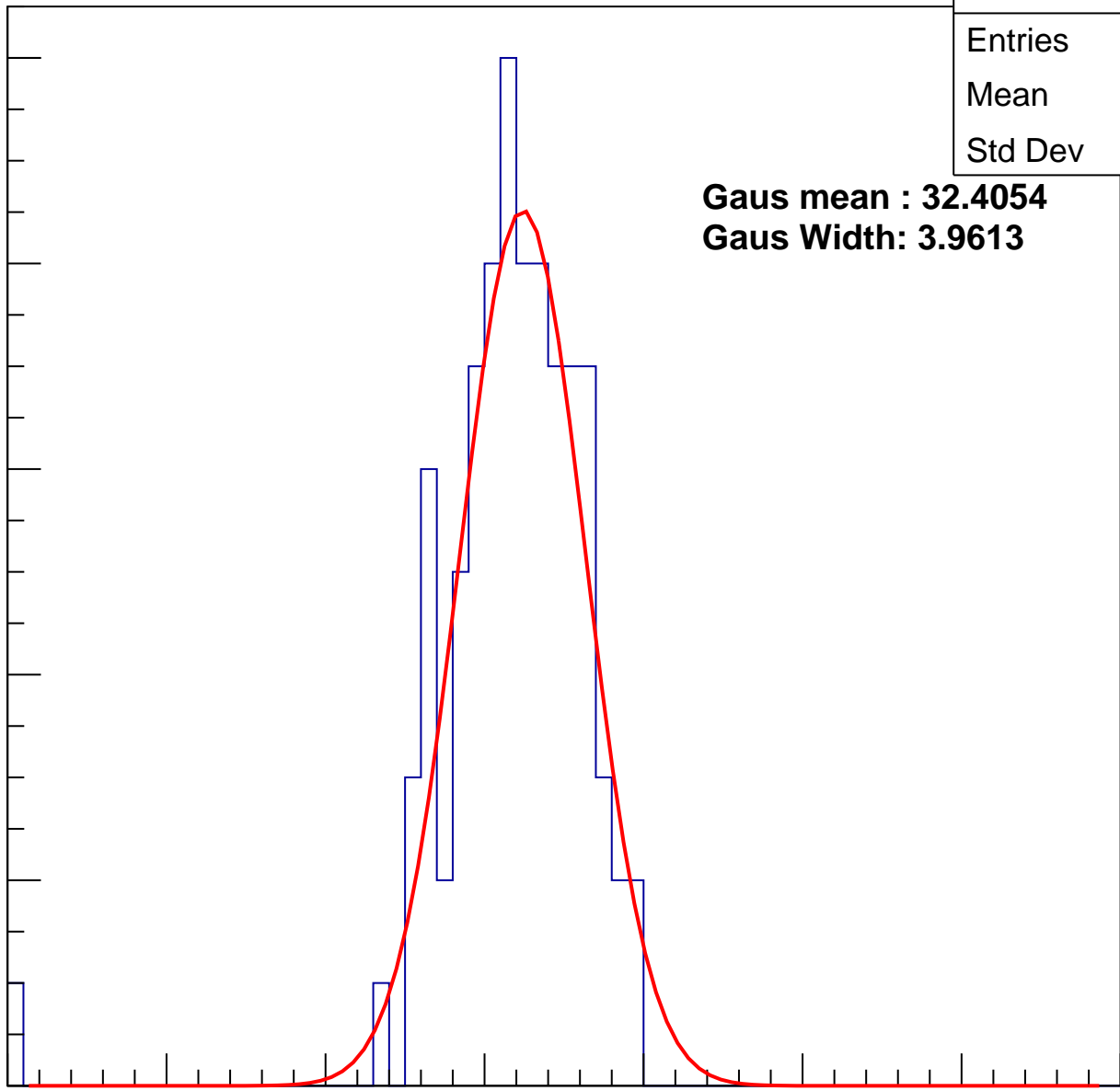
Gaus Width: 3.9613

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



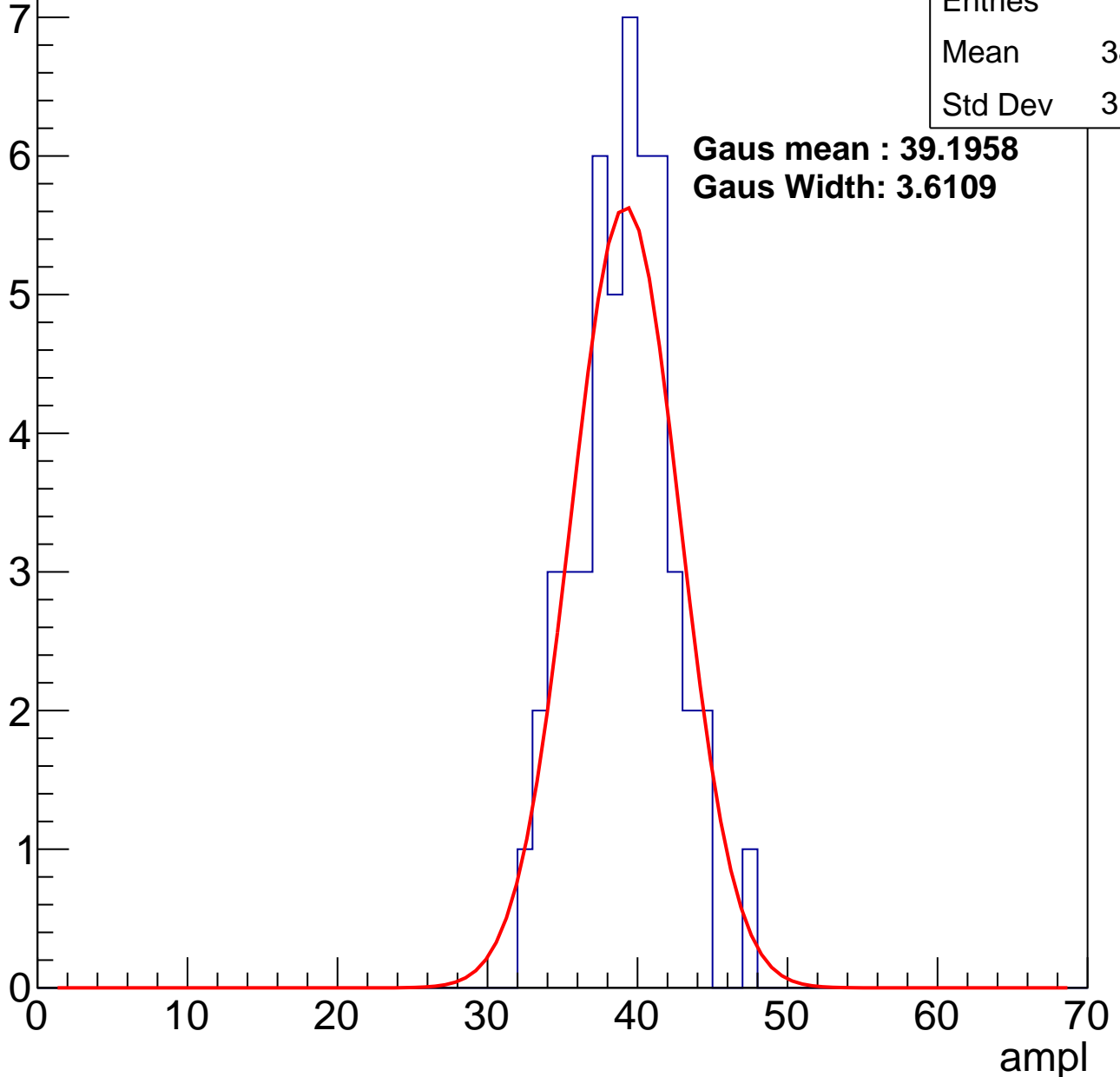
B0L001S, U22-ch72, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	38.62
Std Dev	3.149

Gaus mean : 39.1958
Gaus Width: 3.6109



B0L001S, U22-ch72, adc2

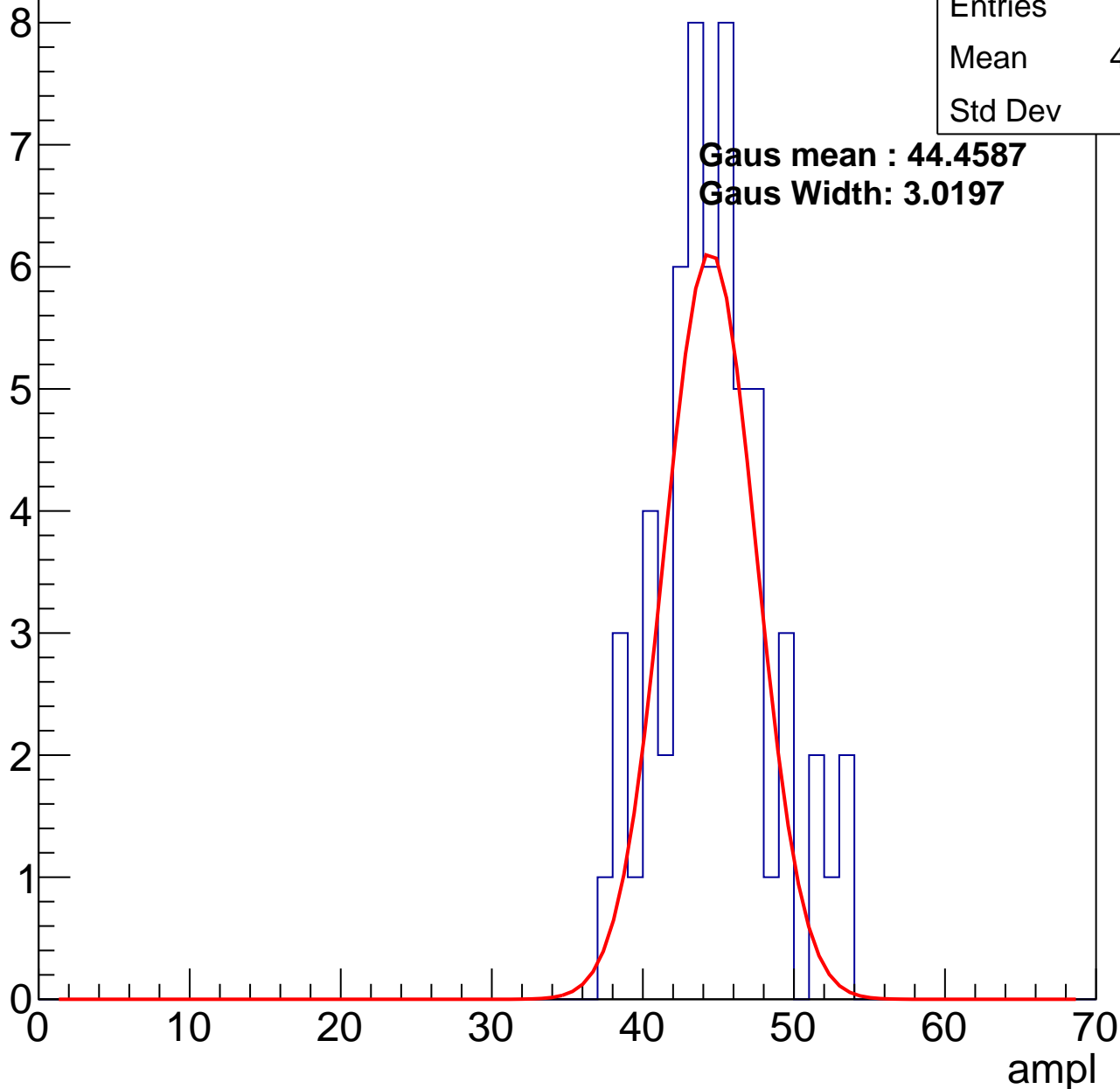
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.34
Std Dev	3.66

Gaus mean : 44.4587

Gaus Width: 3.0197

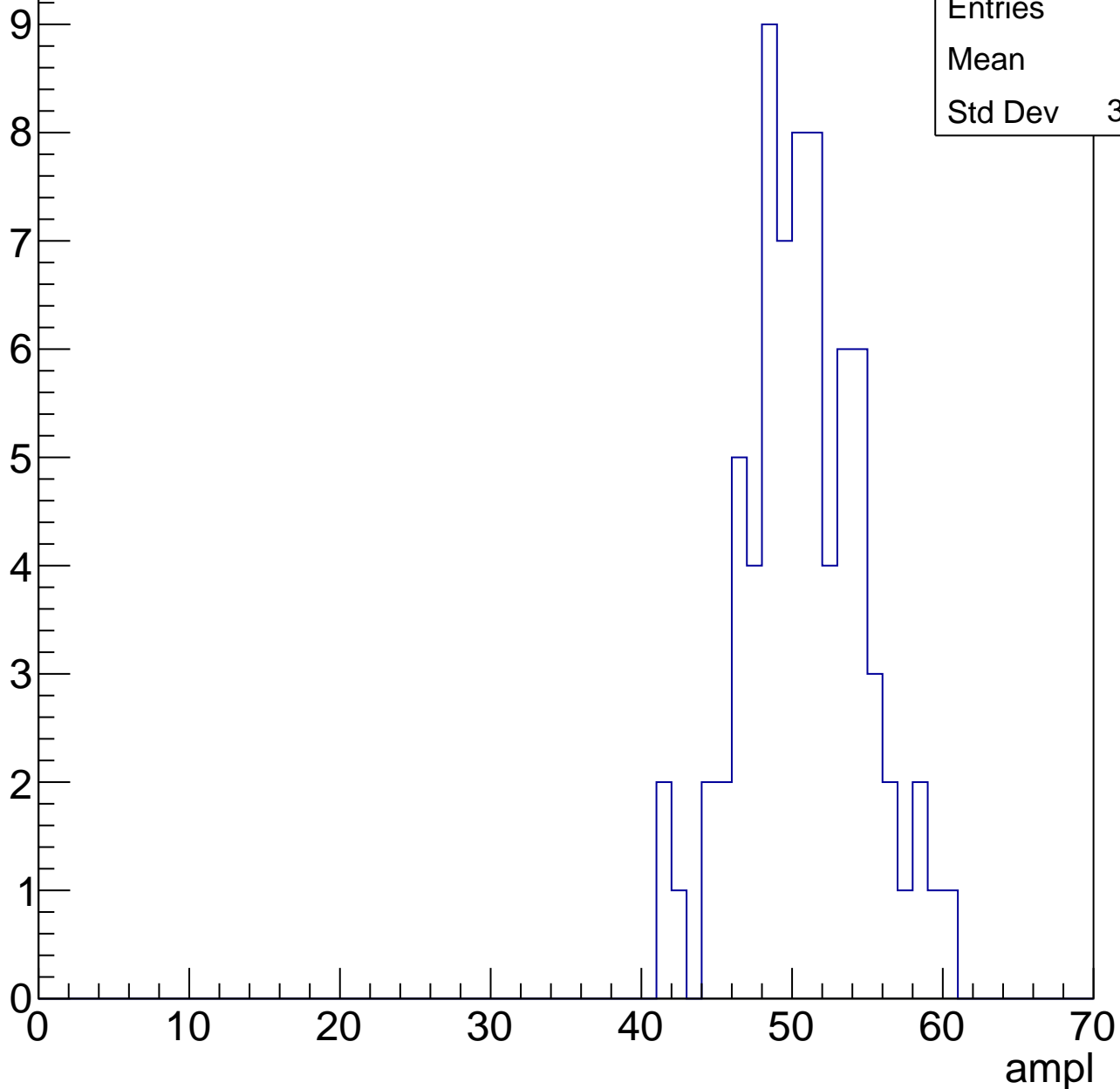


B0L001S, U22-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

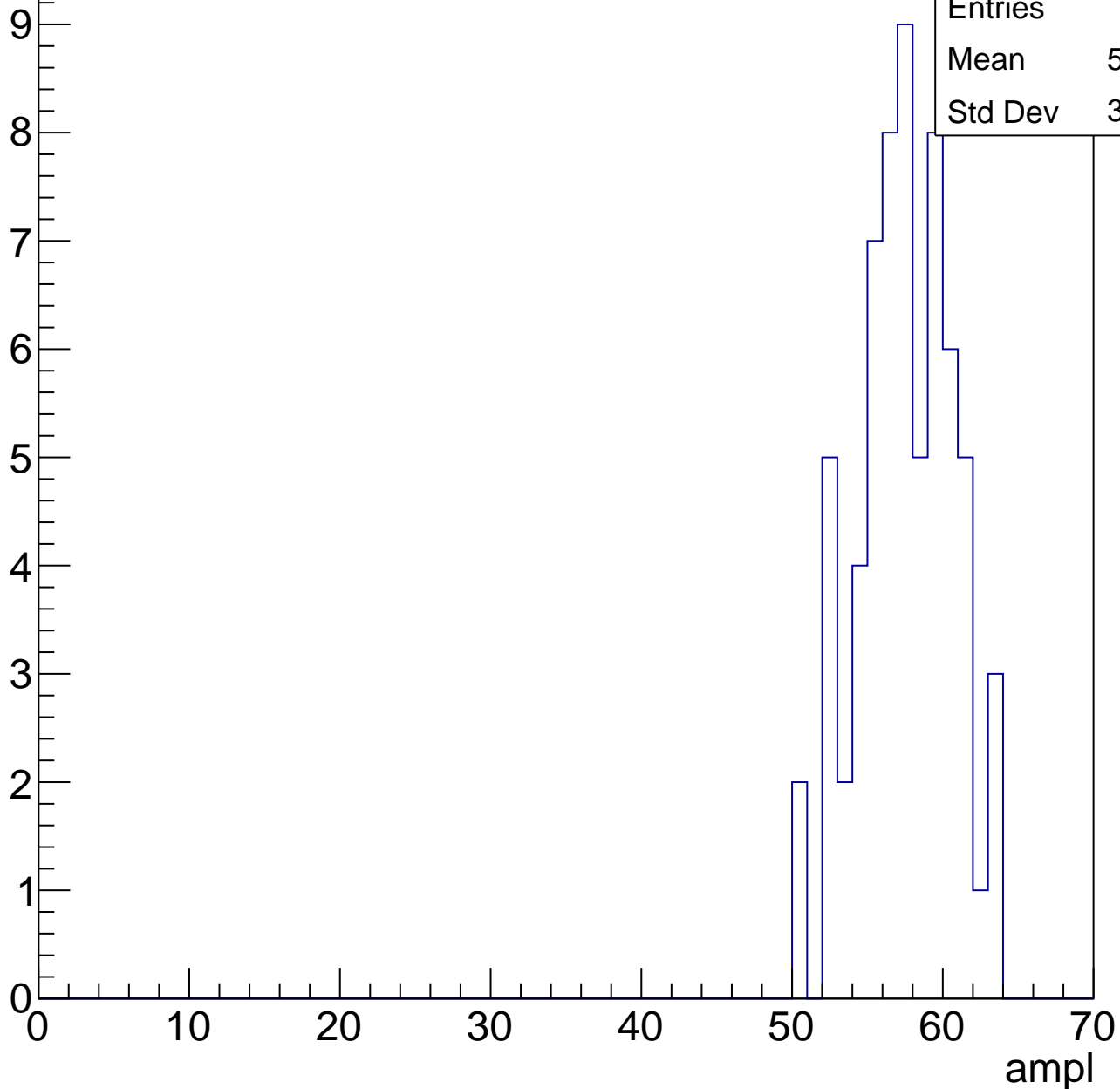
Entries	74
Mean	50.3
Std Dev	3.992



B0L001S, U22-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

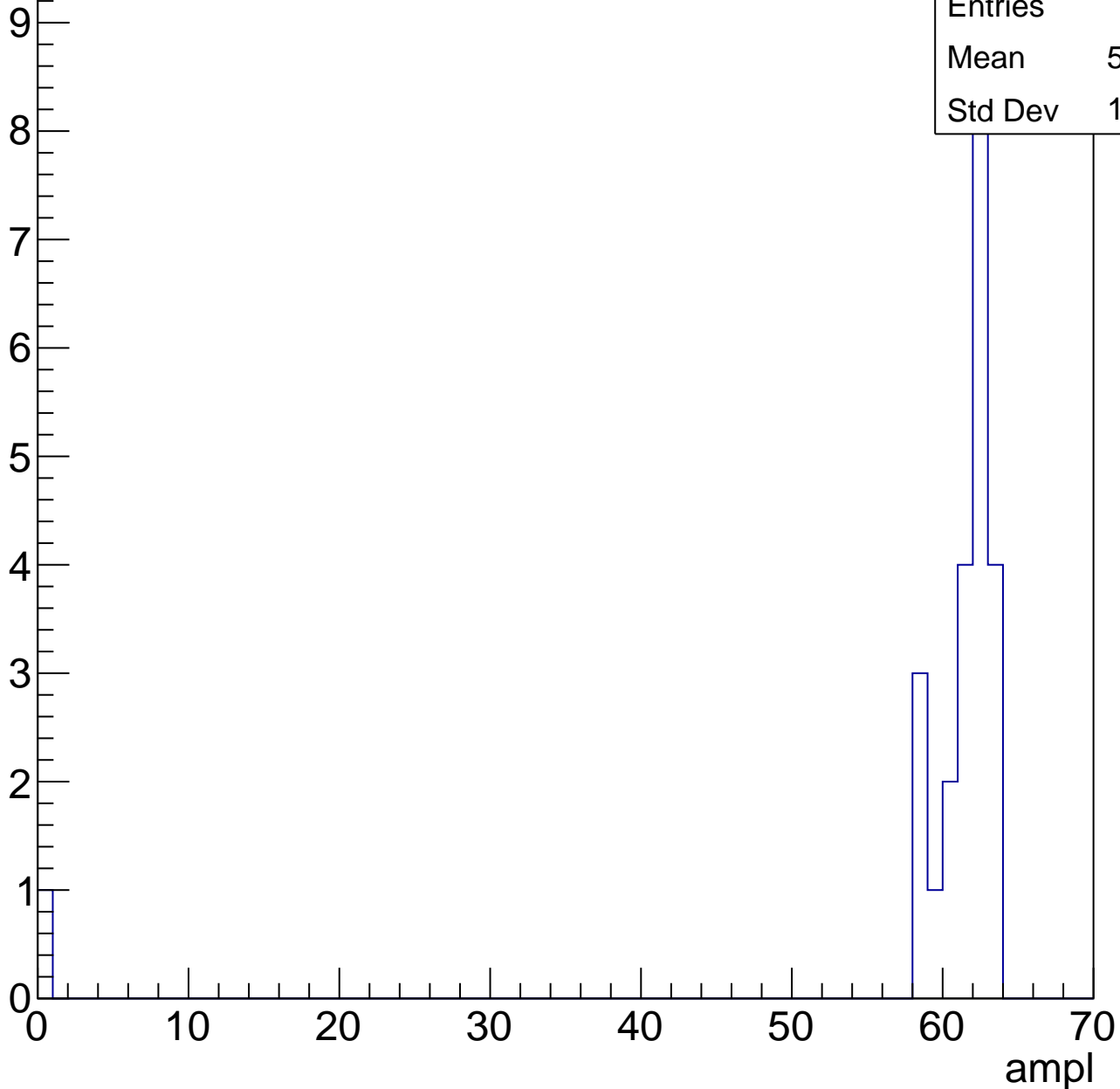


B0L001S, U22-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

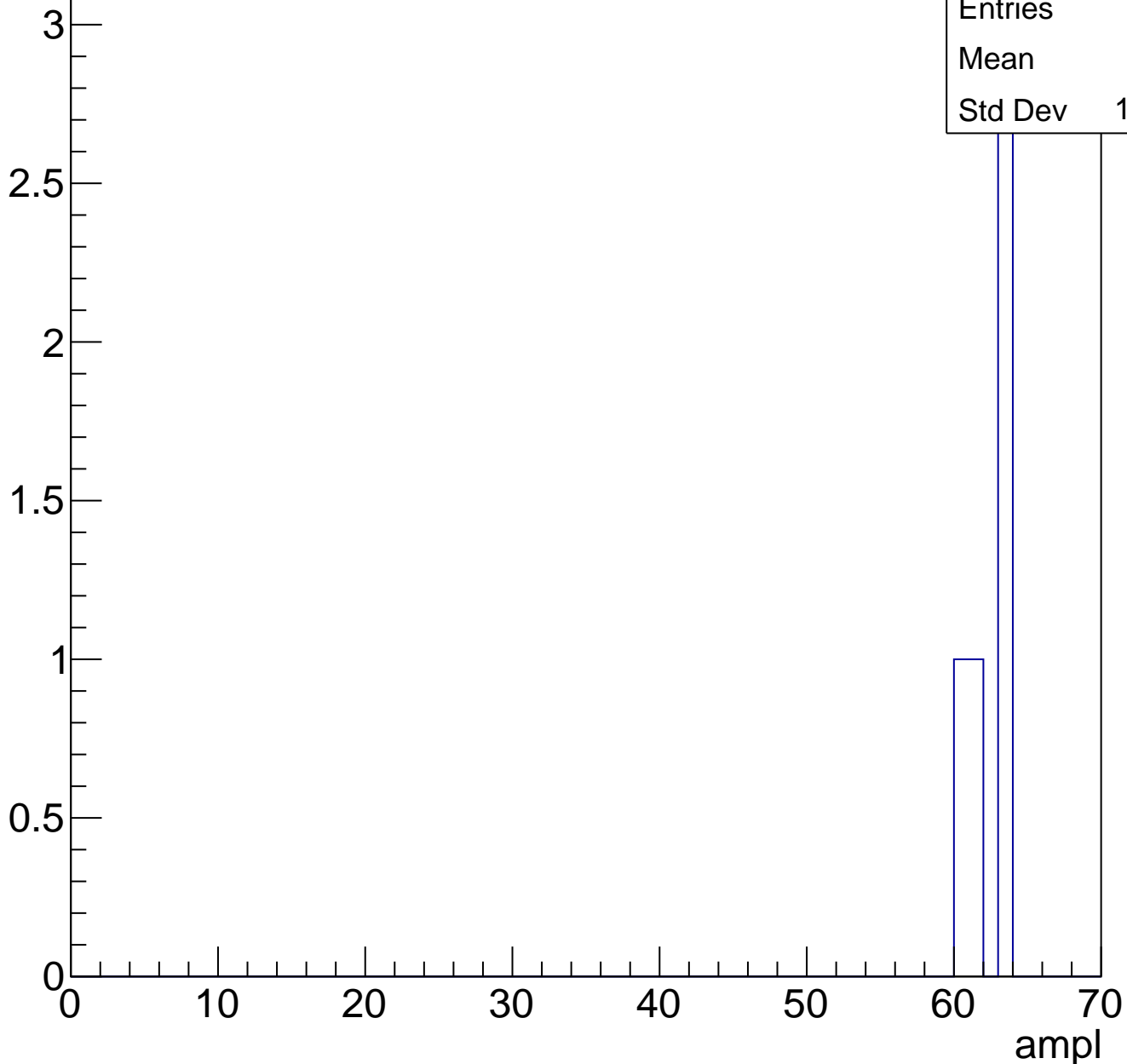
Entries	24
Mean	58.62
Std Dev	12.32



B0L001S, U22-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch73, adc0

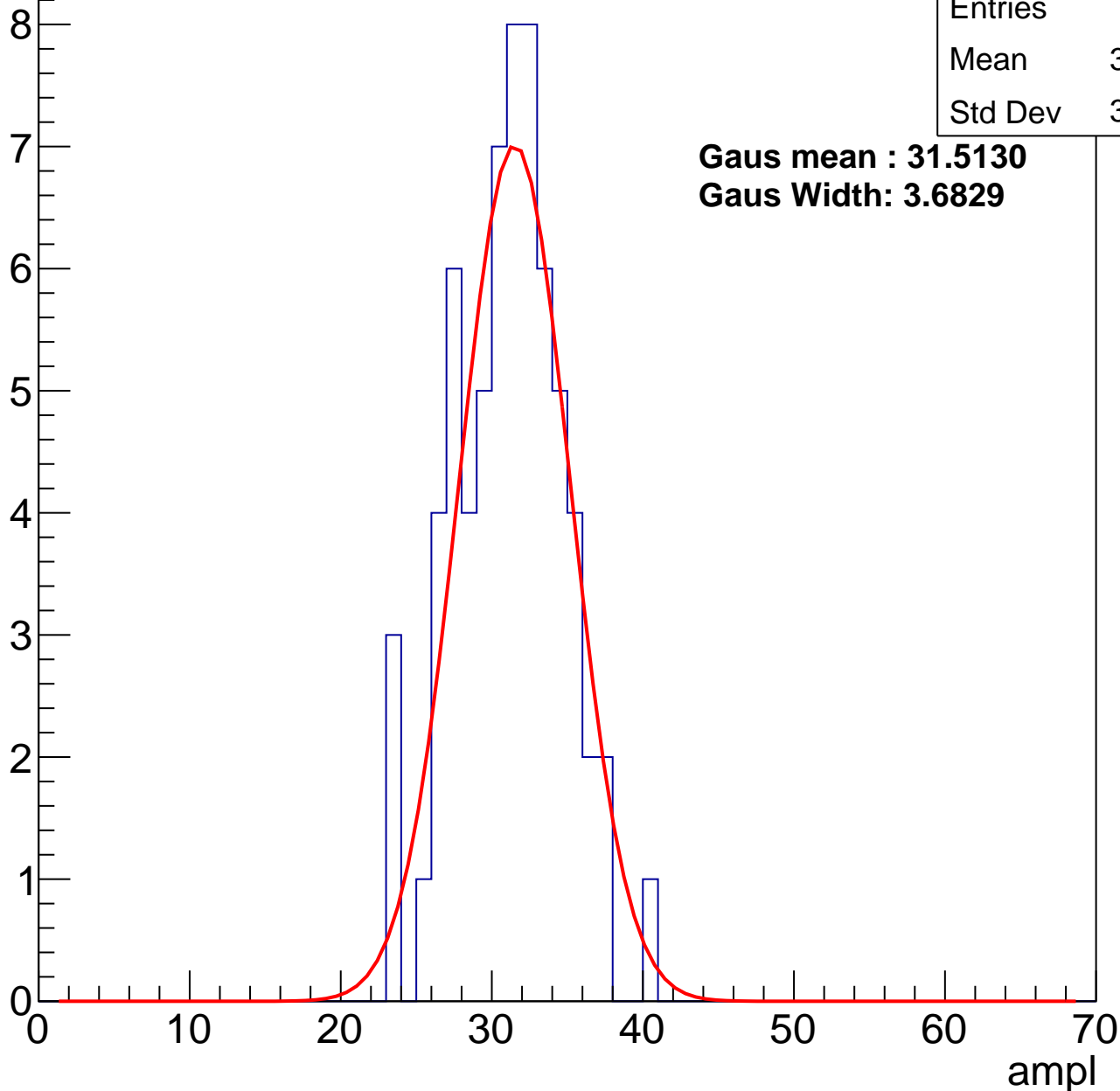
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.68
Std Dev	3.534

Gaus mean : 31.5130

Gaus Width: 3.6829



B0L001S, U22-ch73, adc1

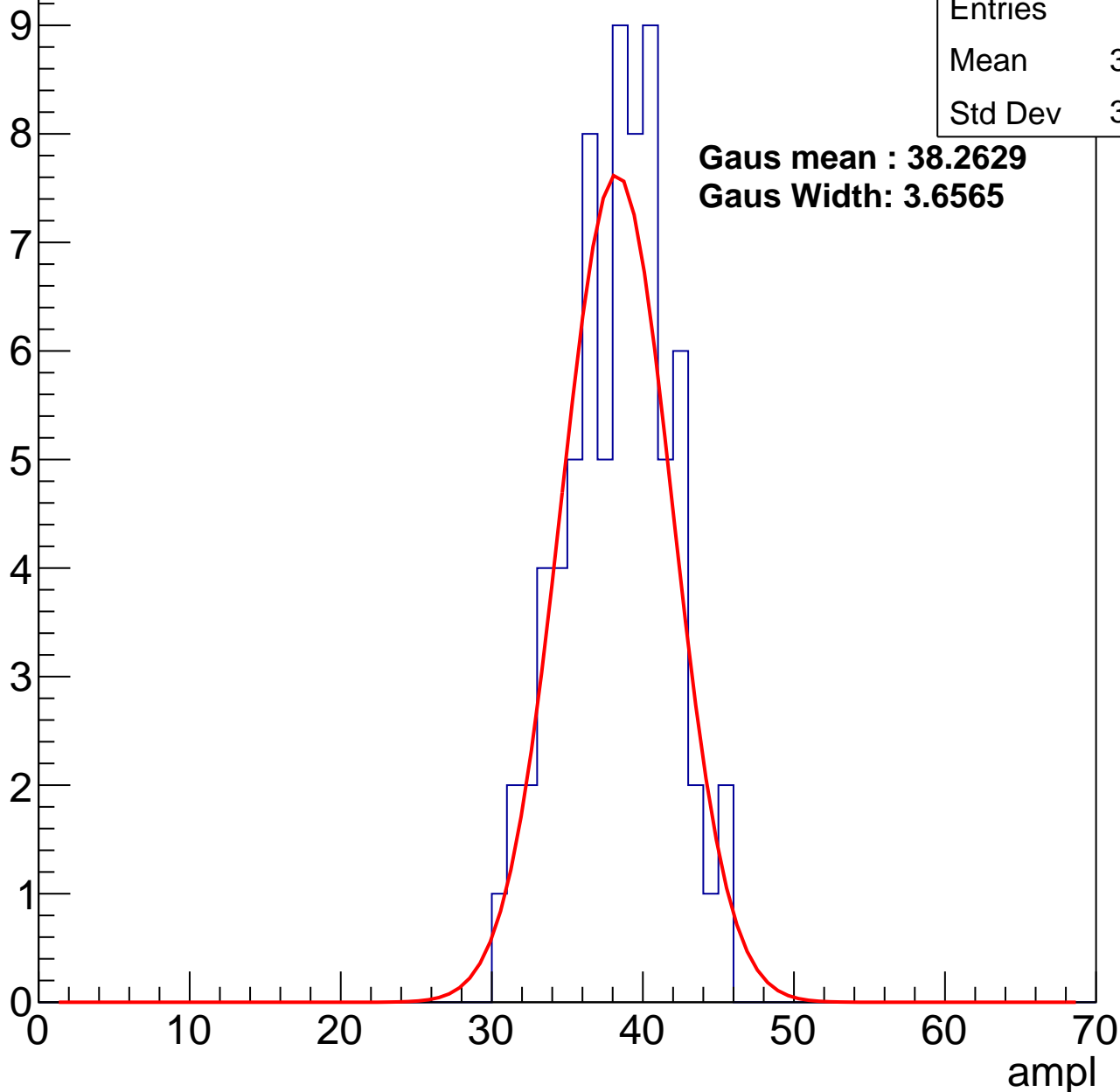
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.85
Std Dev	3.407

Gaus mean : 38.2629

Gaus Width: 3.6565



B0L001S, U22-ch73, adc2

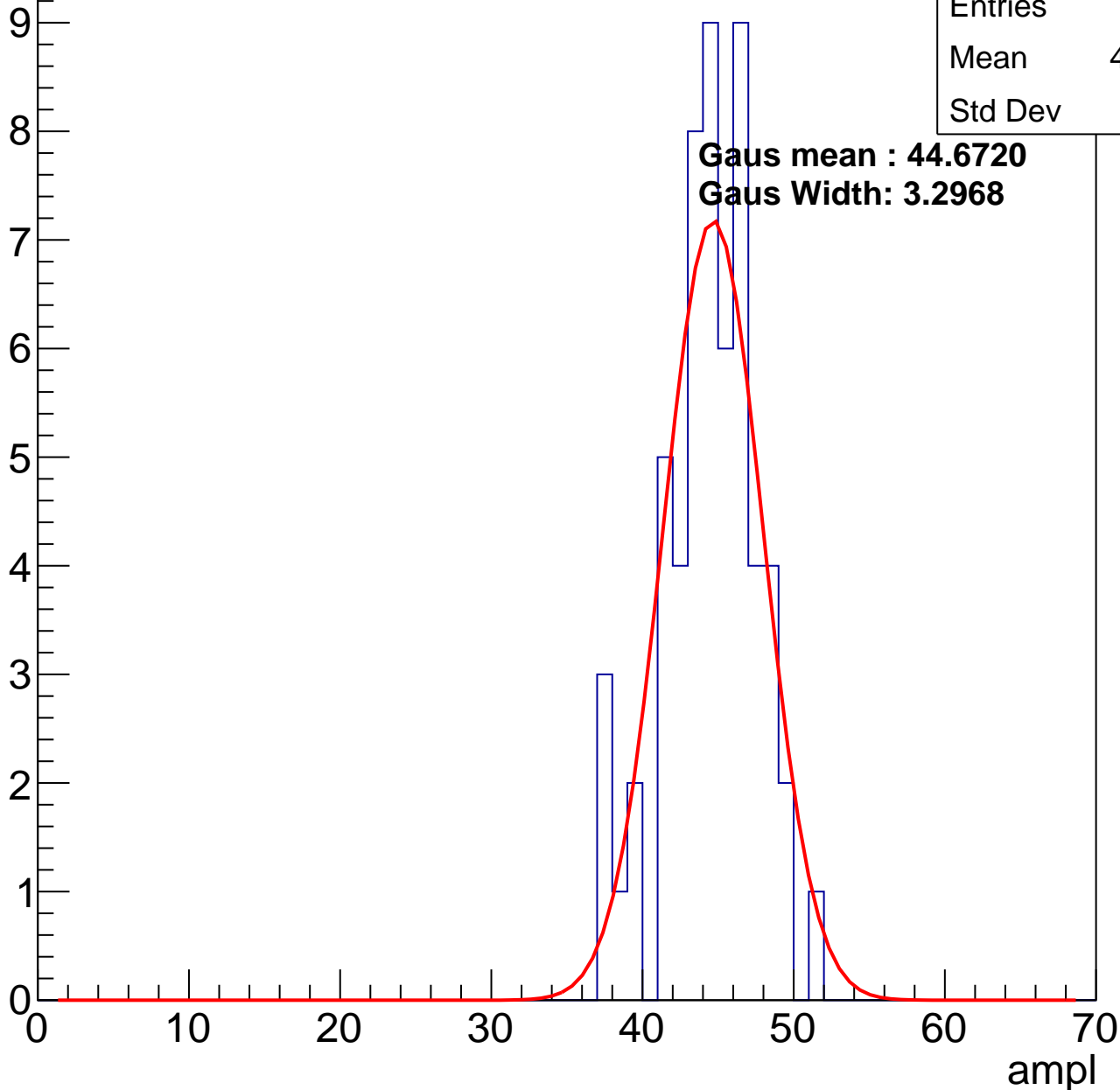
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.02
Std Dev	3.06

Gaus mean : 44.6720

Gaus Width: 3.2968

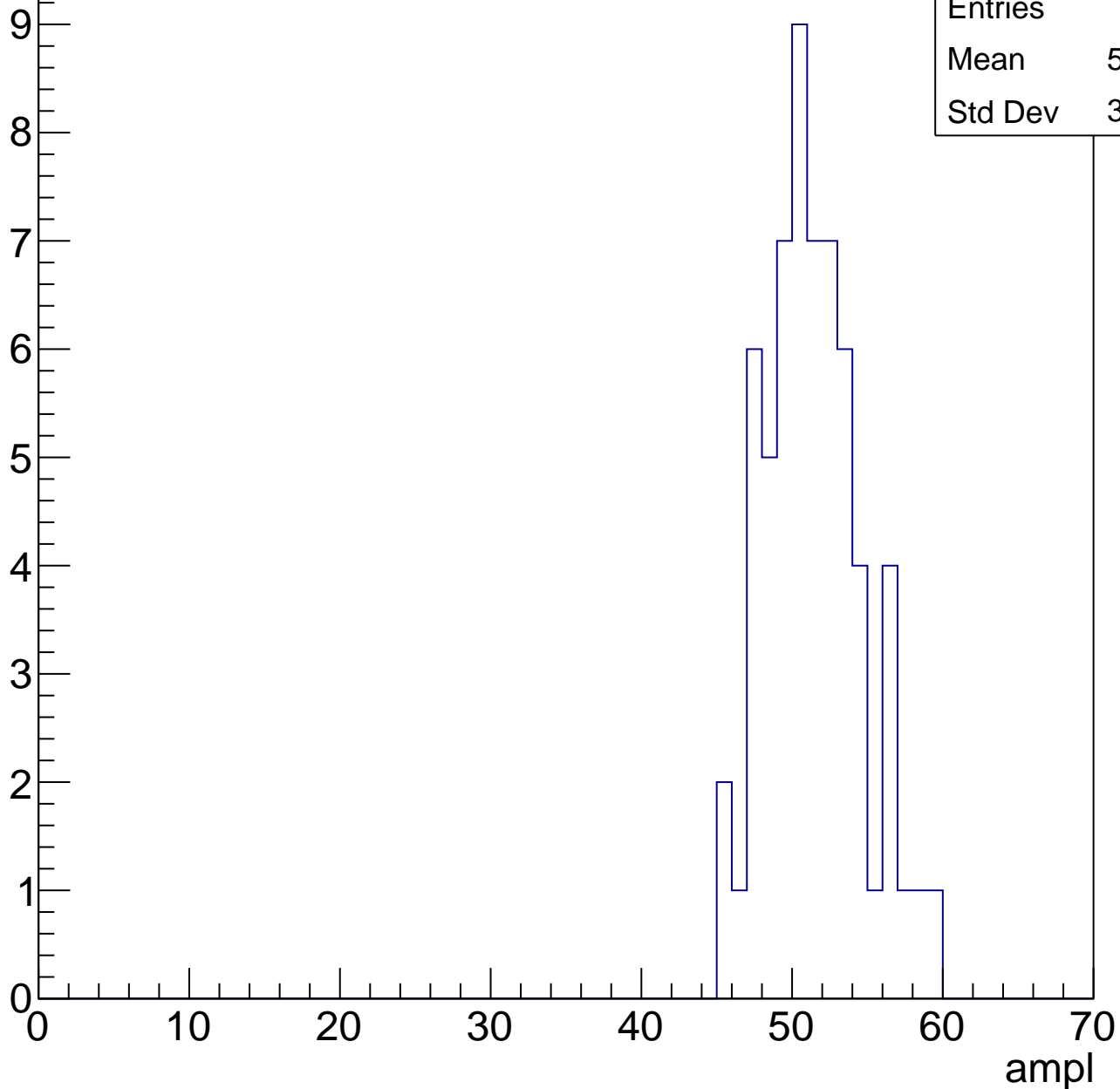


B0L001S, U22-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.95
Std Dev	3.129

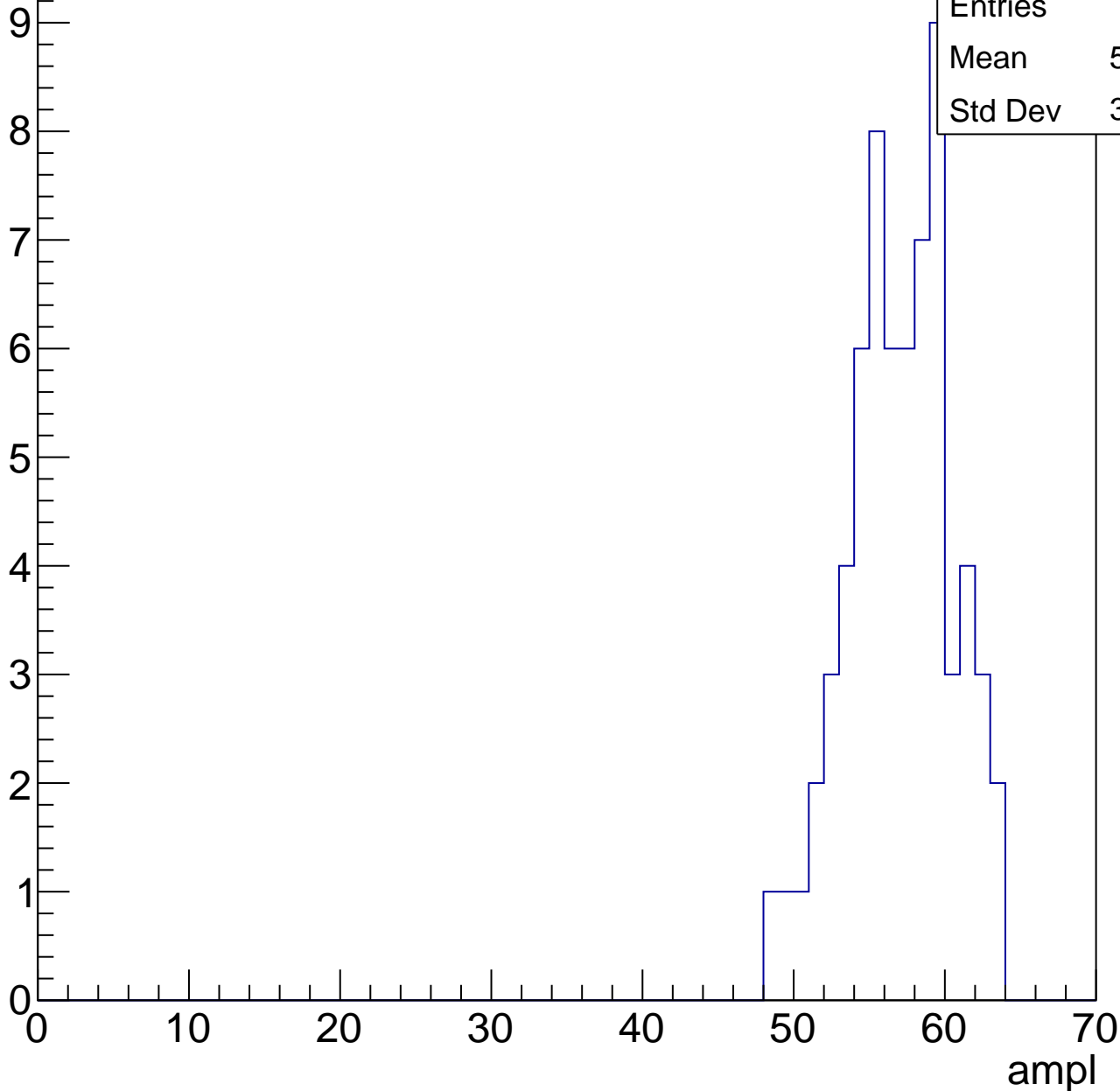


B0L001S, U22-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.55
Std Dev	3.403



B0L001S, U22-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	30
Mean	60.53
Std Dev	2.093

10

20

30

40

50

60

70

ampl

B0L001S, U22-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch74, adc0

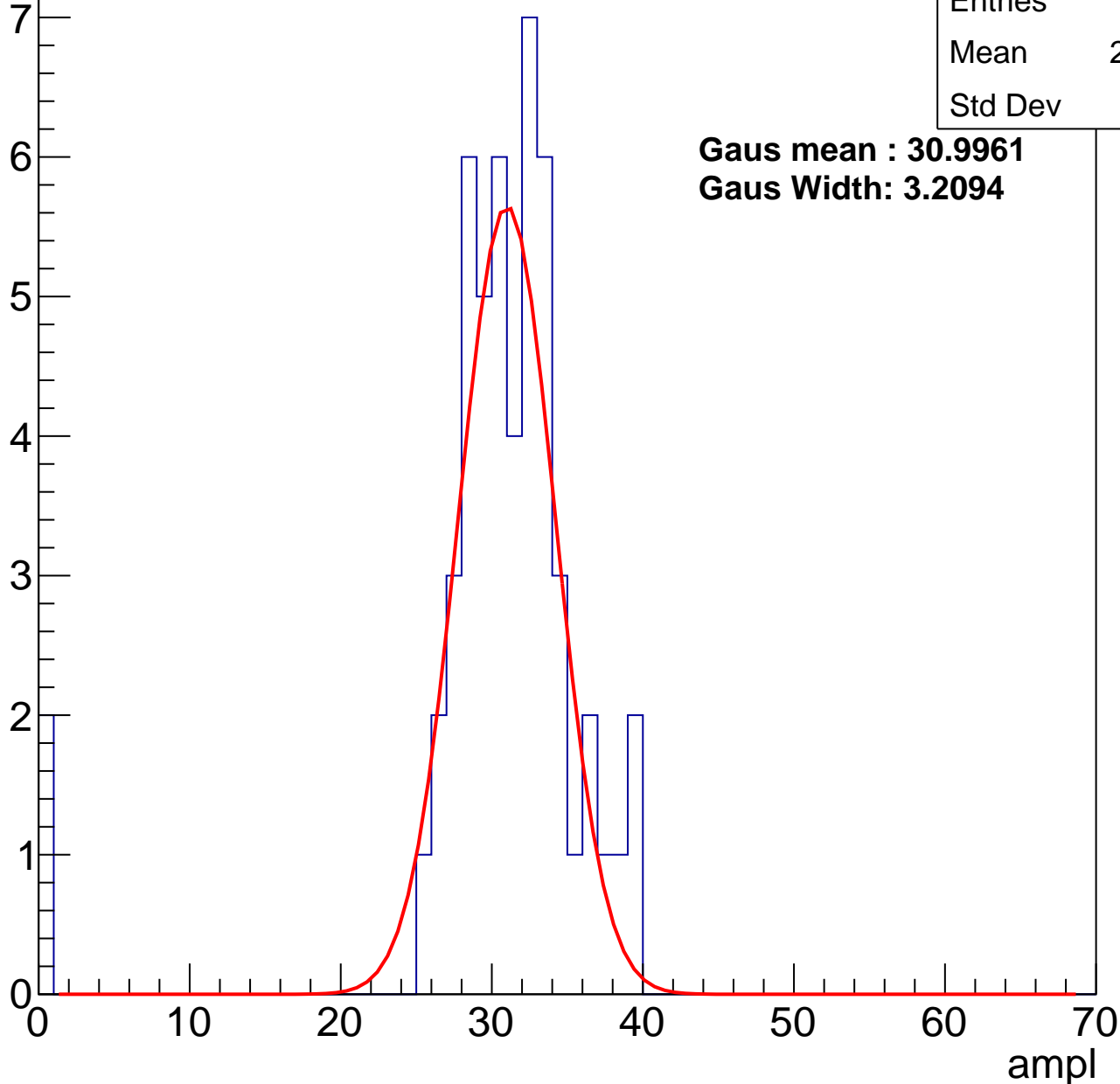
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.98
Std Dev	6.82

Gaus mean : 30.9961

Gaus Width: 3.2094



B0L001S, U22-ch74, adc1

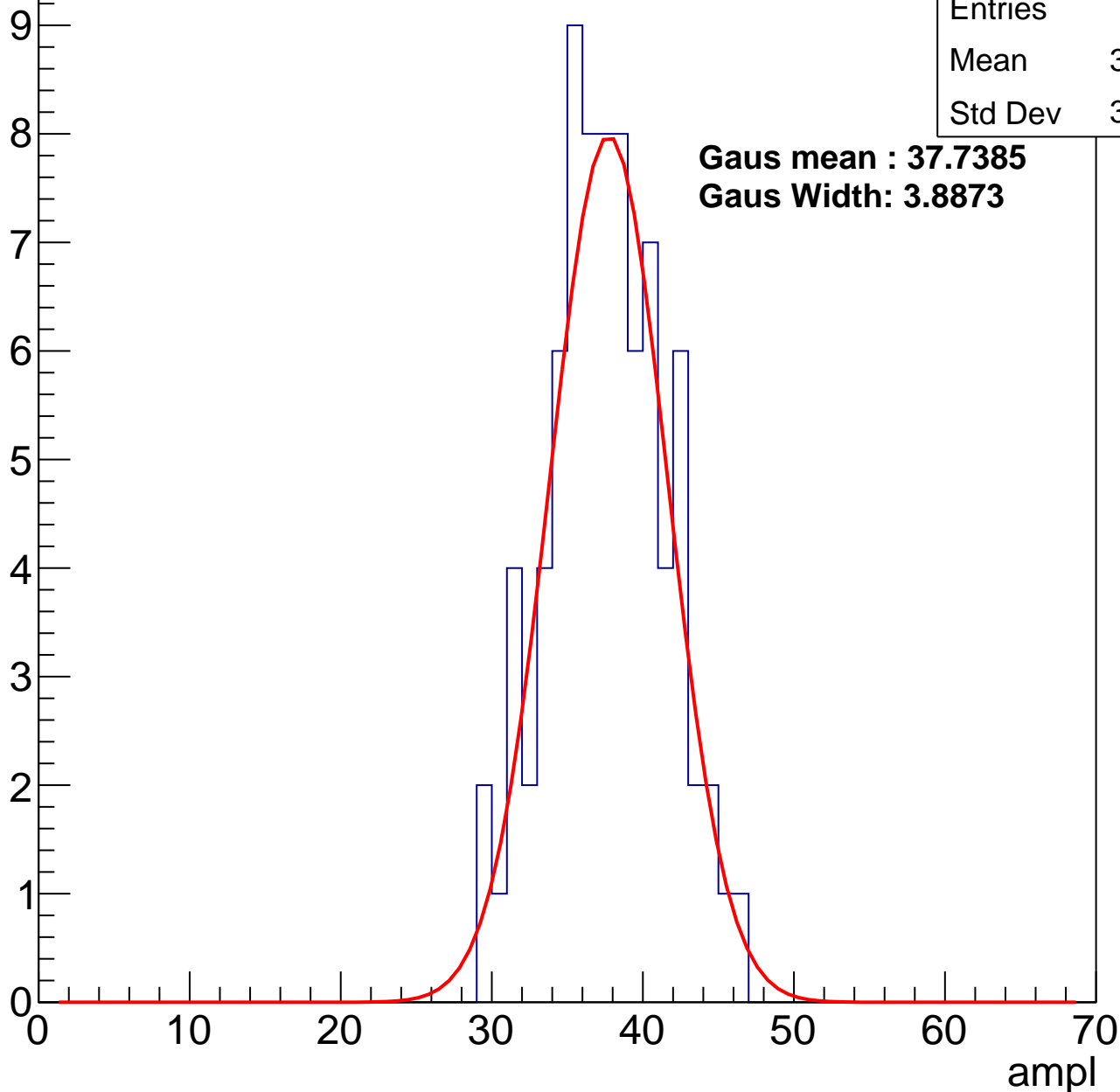
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.16
Std Dev	3.776

Gaus mean : 37.7385

Gaus Width: 3.8873



B0L001S, U22-ch74, adc2

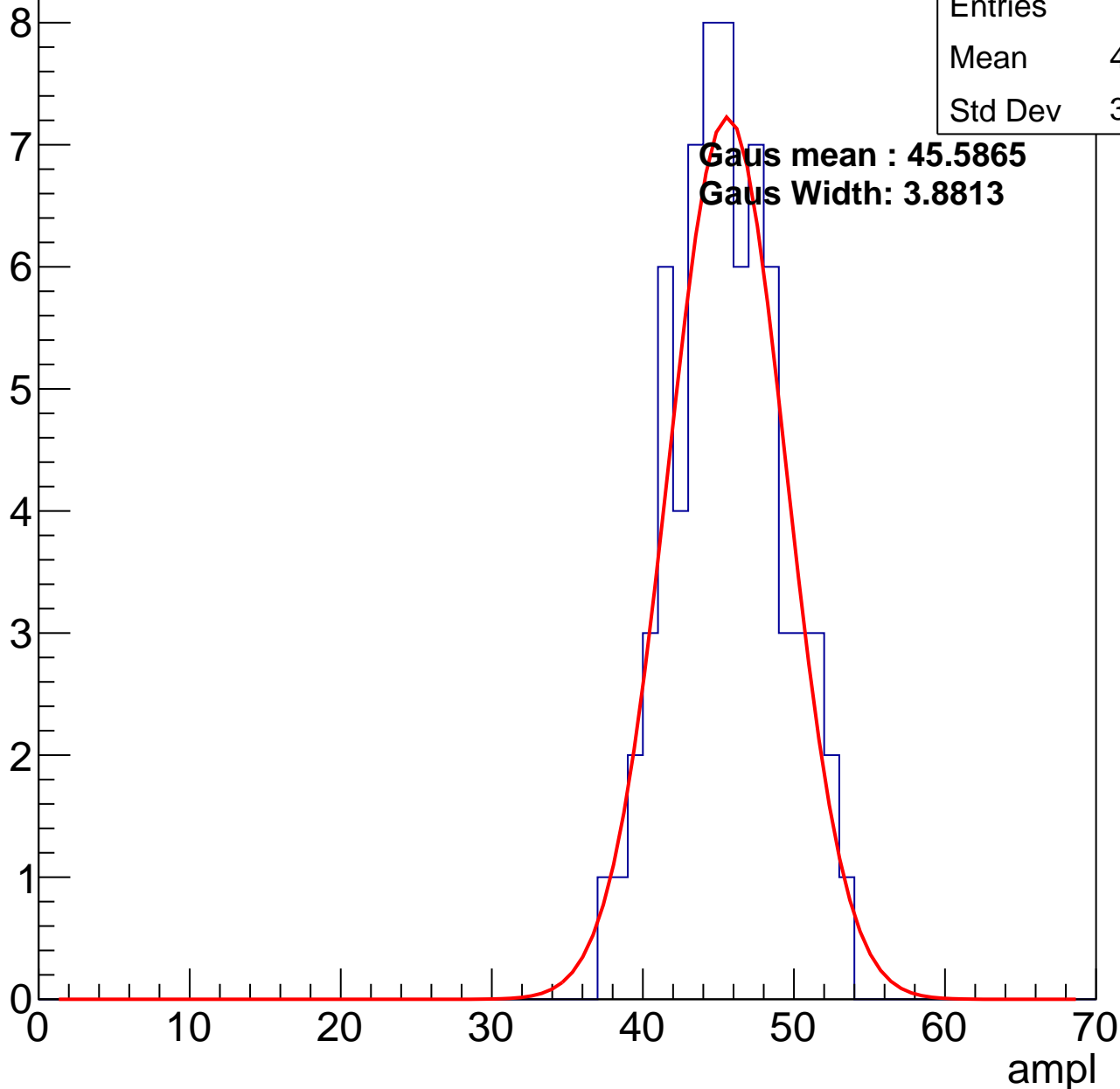
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	45.07
Std Dev	3.577

Gaus mean : 45.5865

Gaus Width: 3.8813

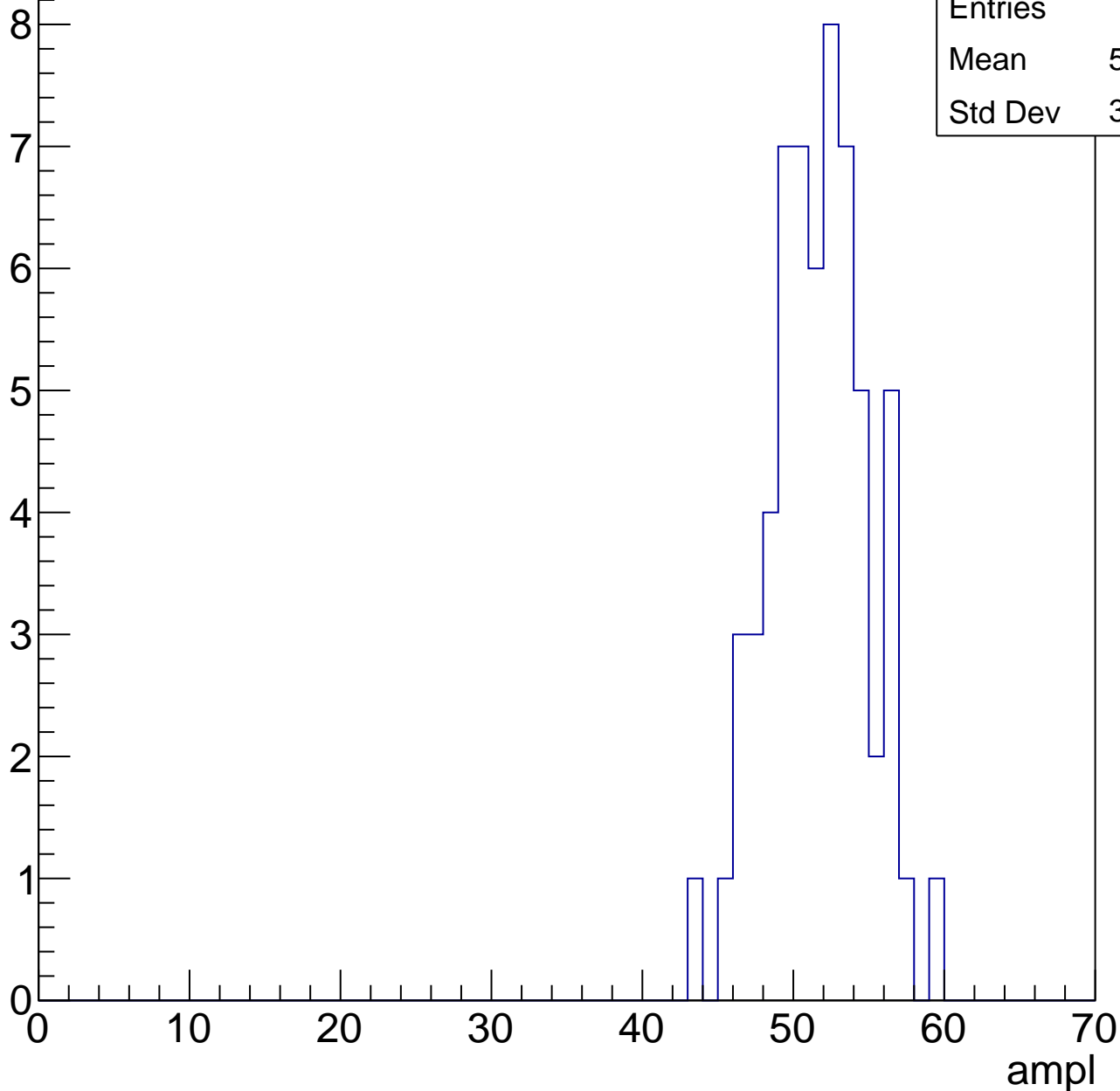


B0L001S, U22-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

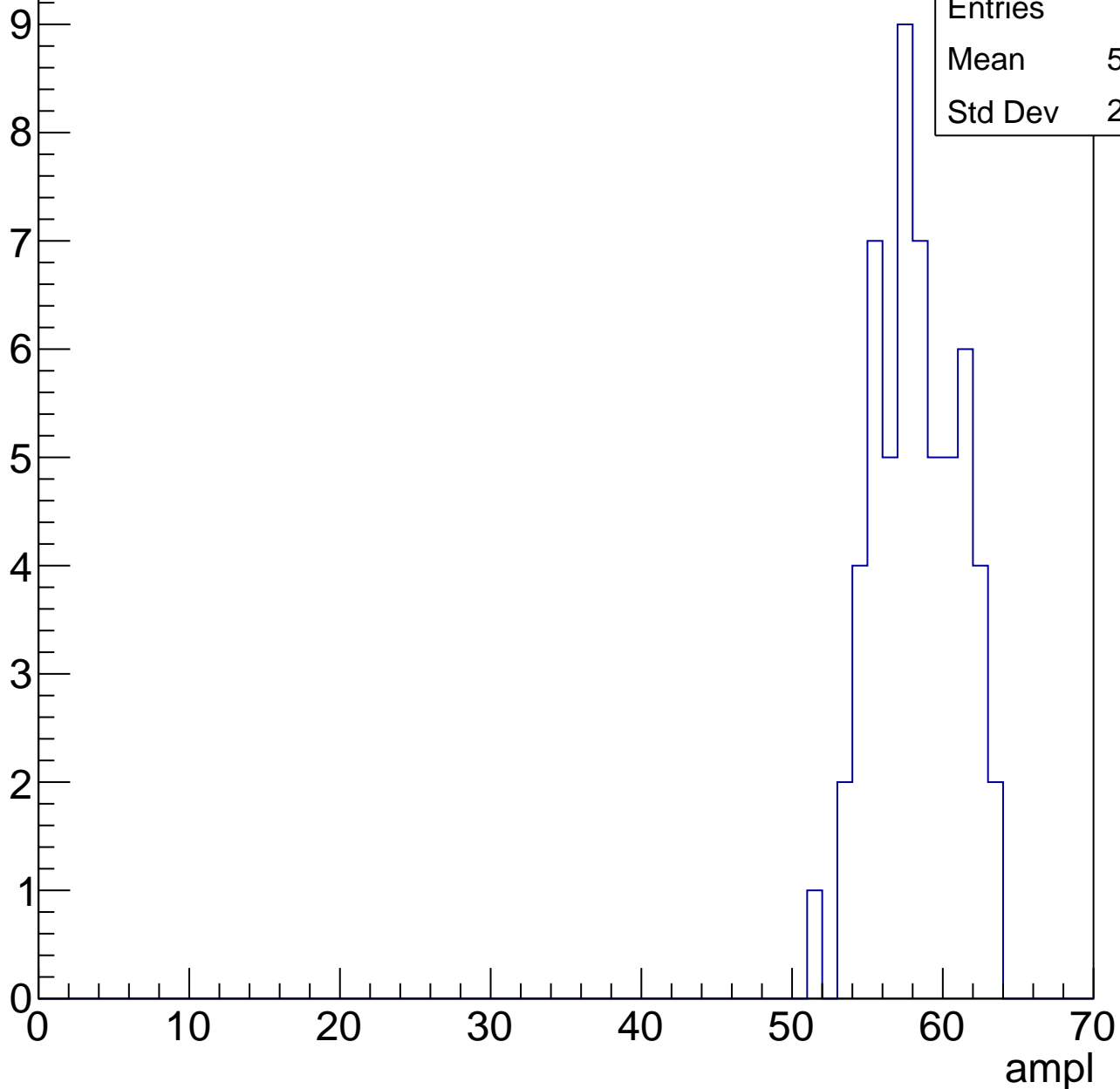
Entries	61
Mean	51.16
Std Dev	3.215



B0L001S, U22-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

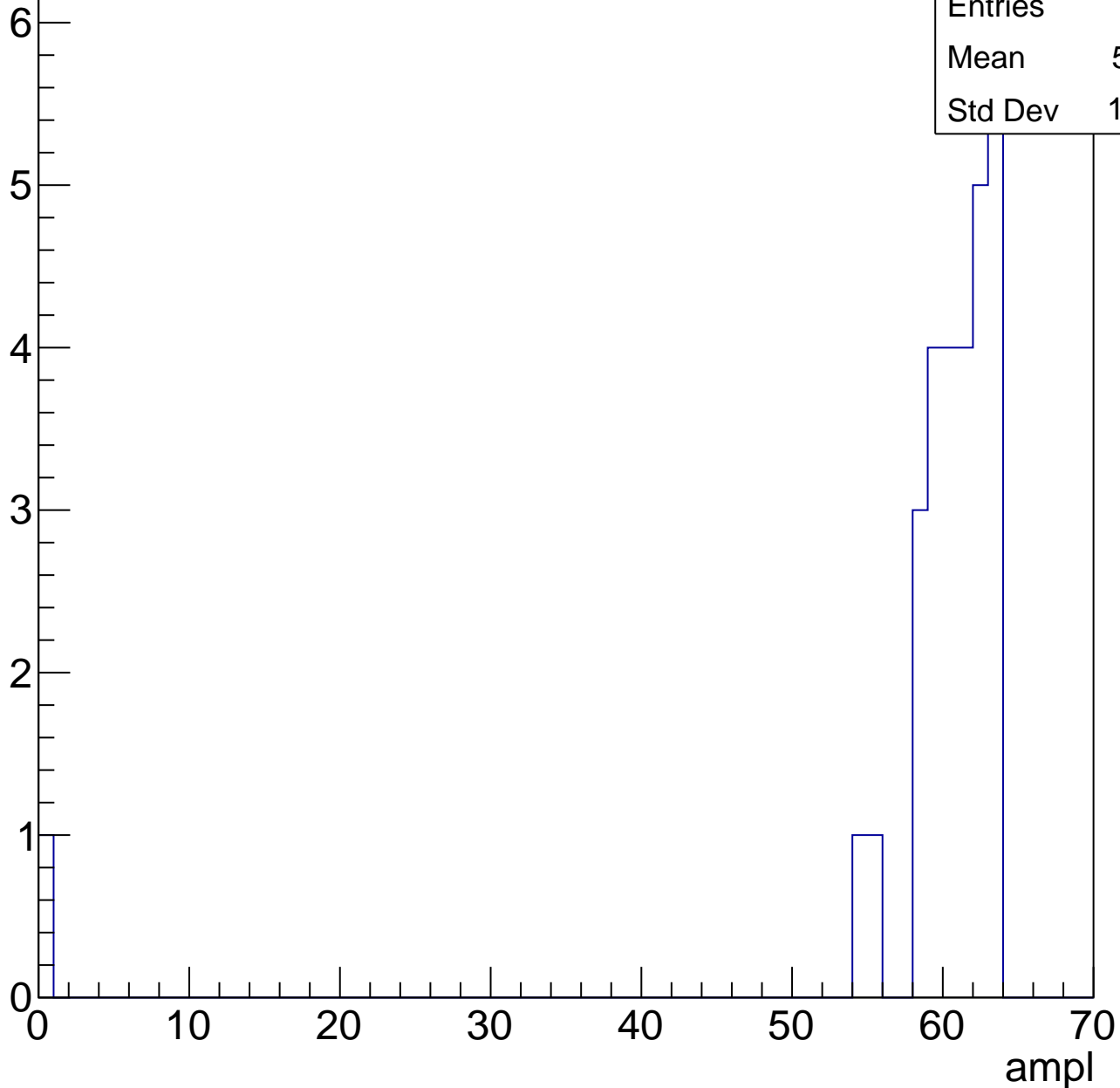


B0L001S, U22-ch74, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.31
Std Dev	11.25



B0L001S, U22-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch75, adc0

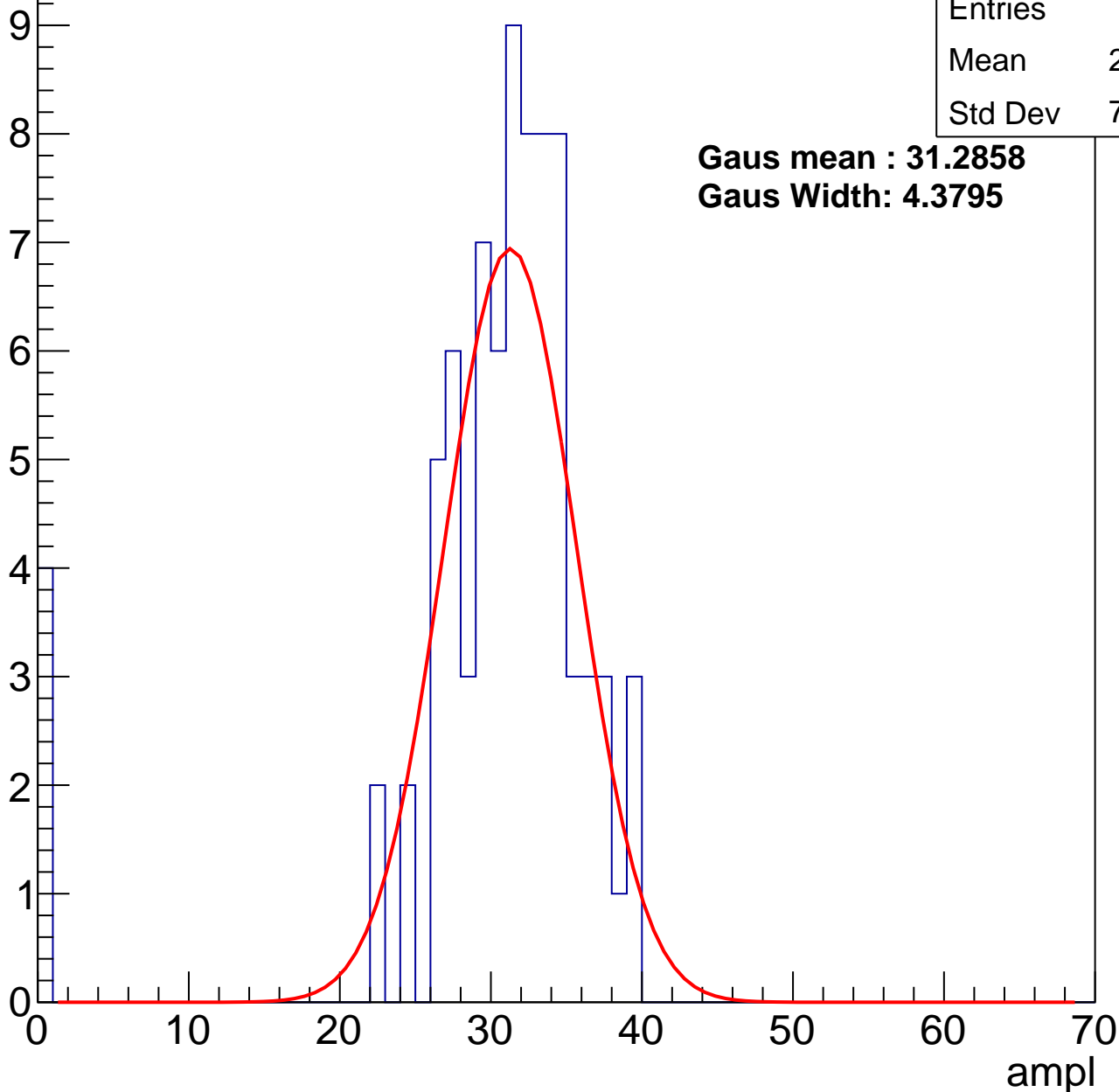
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	29.64
Std Dev	7.715

Gaus mean : 31.2858

Gaus Width: 4.3795



B0L001S, U22-ch75, adc1

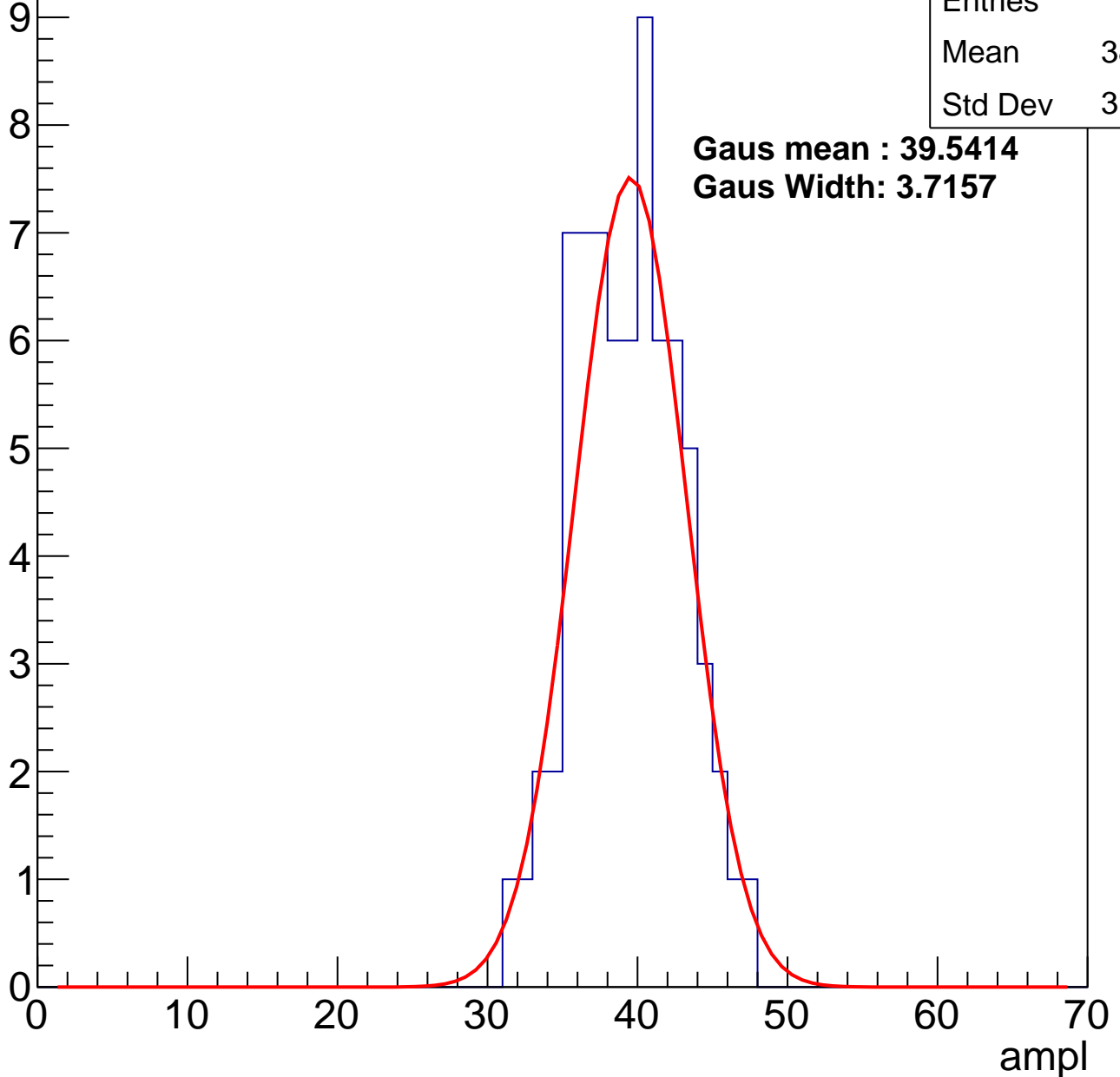
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.93
Std Dev	3.477

Gaus mean : 39.5414

Gaus Width: 3.7157



B0L001S, U22-ch75, adc2

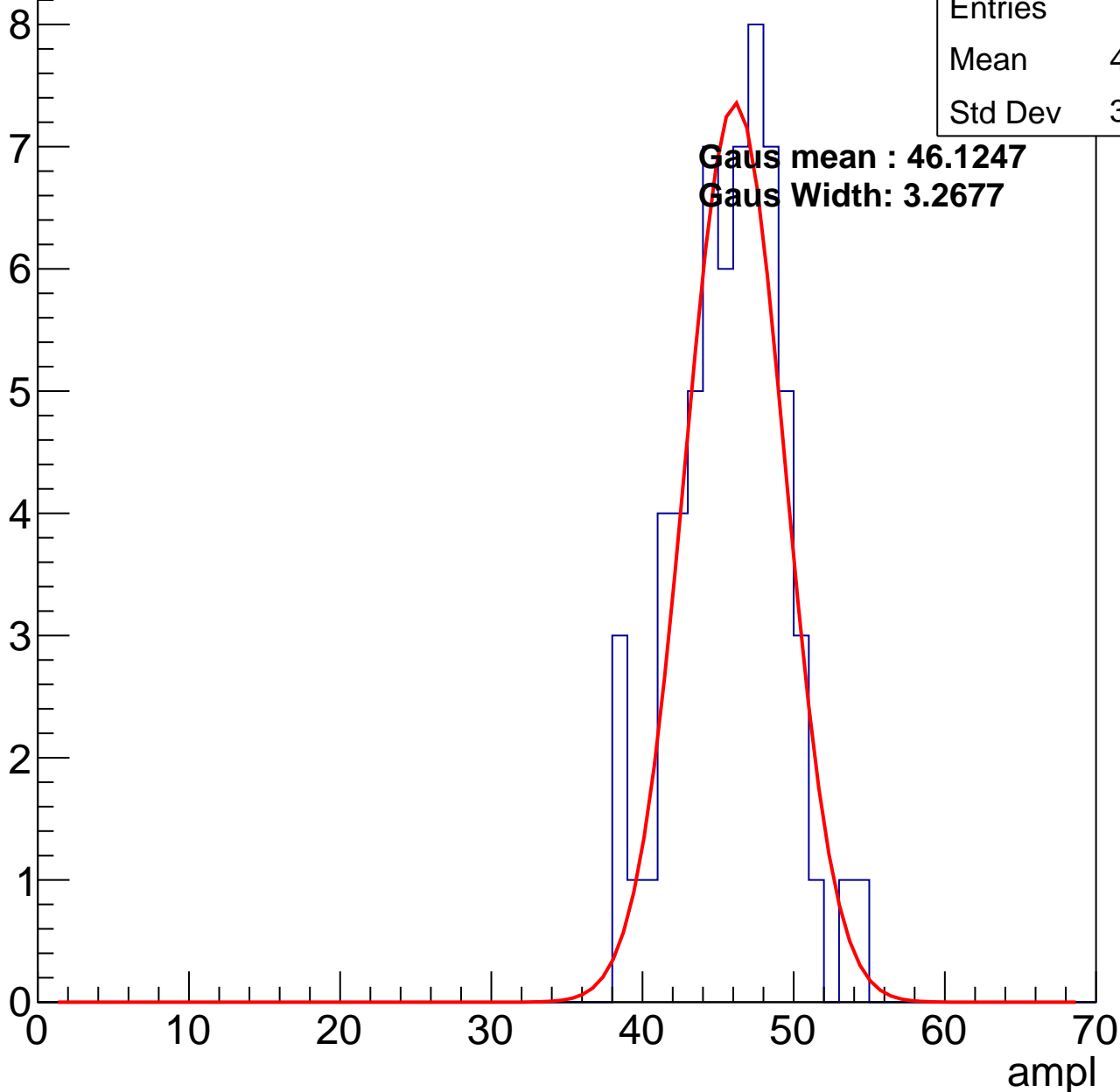
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.39
Std Dev	3.453

Gaus mean : 46.1247

Gaus Width: 3.2677

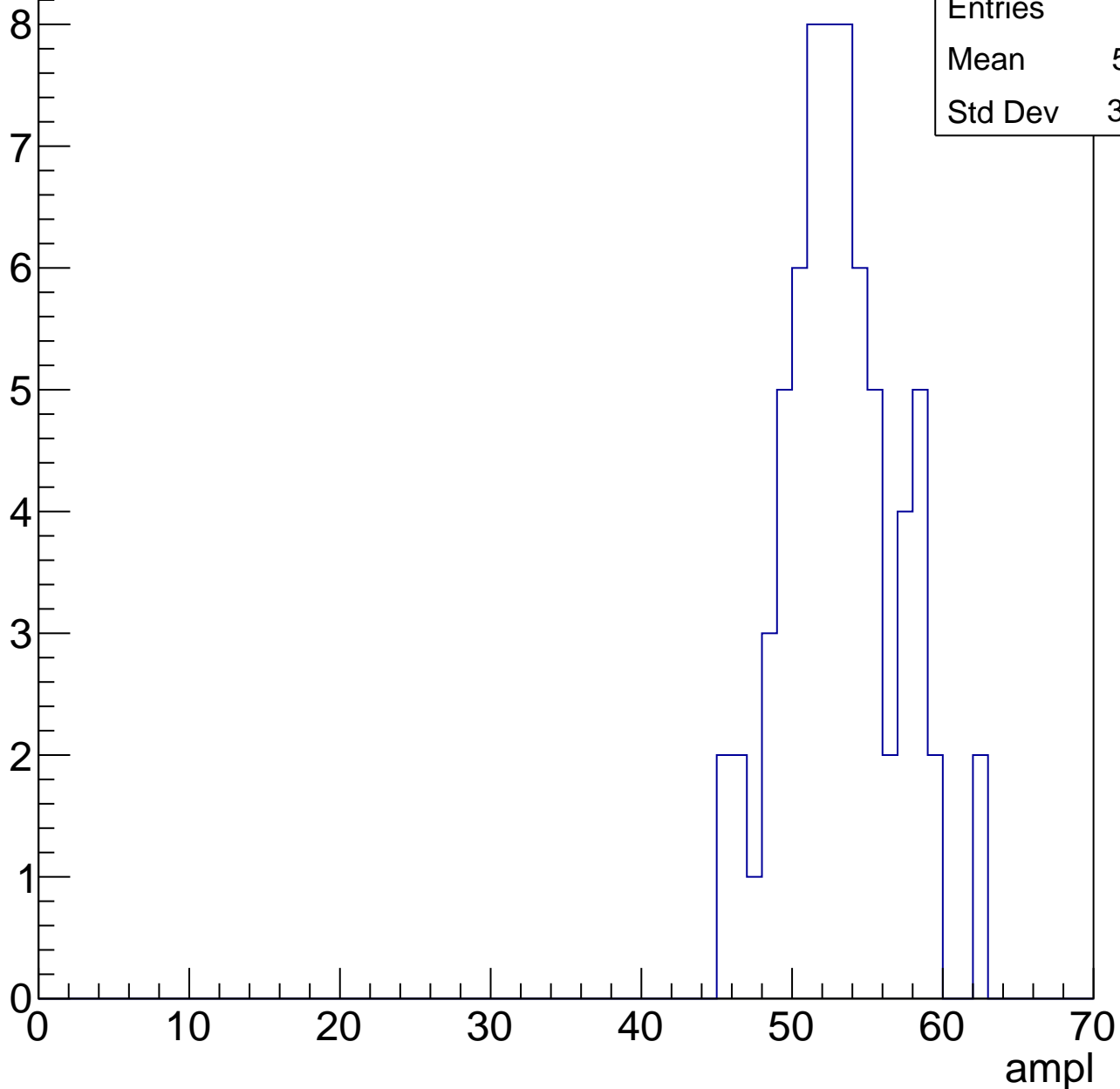


B0L001S, U22-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	52.71
Std Dev	3.754

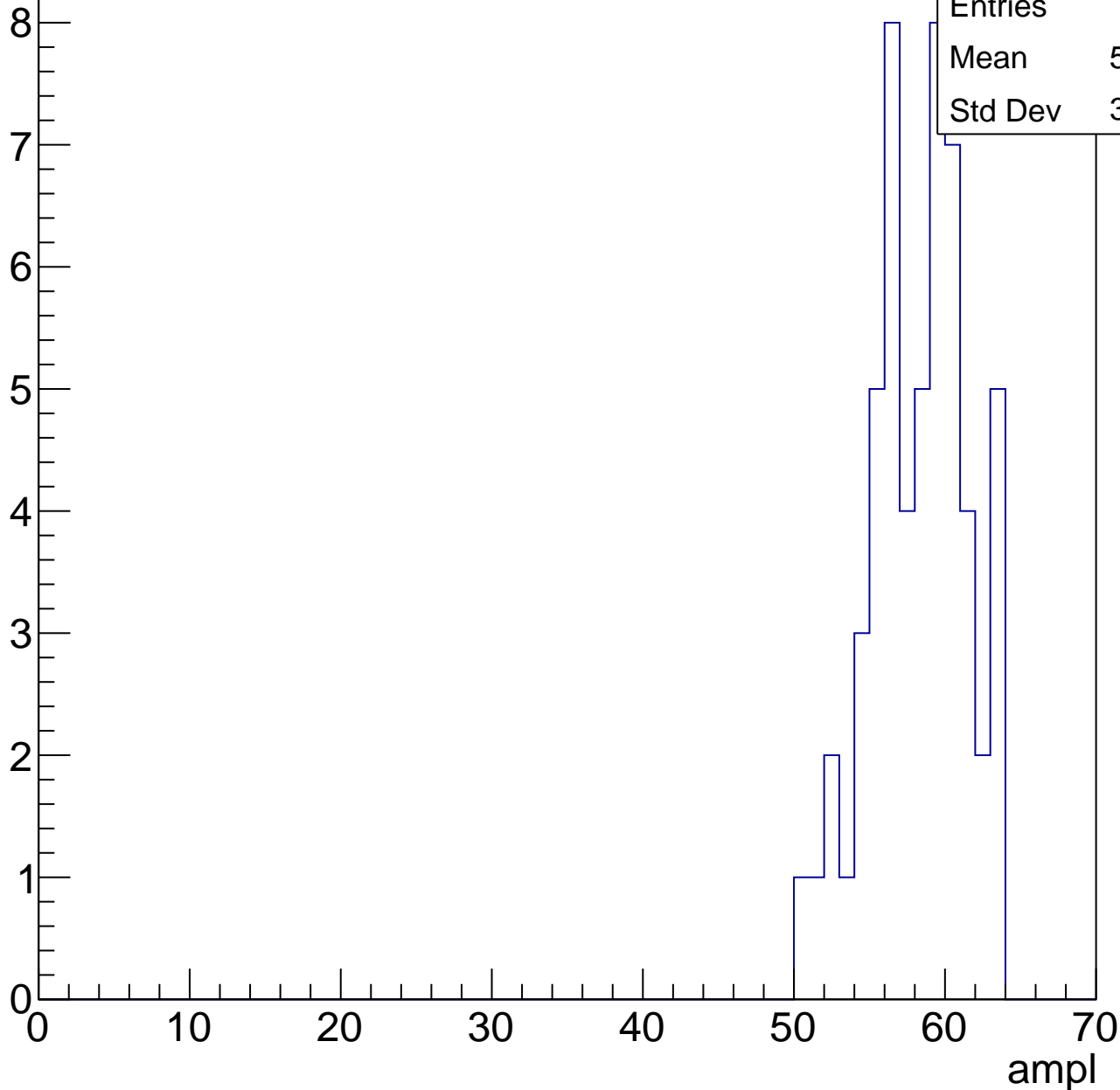


B0L001S, U22-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.79
Std Dev	3.178

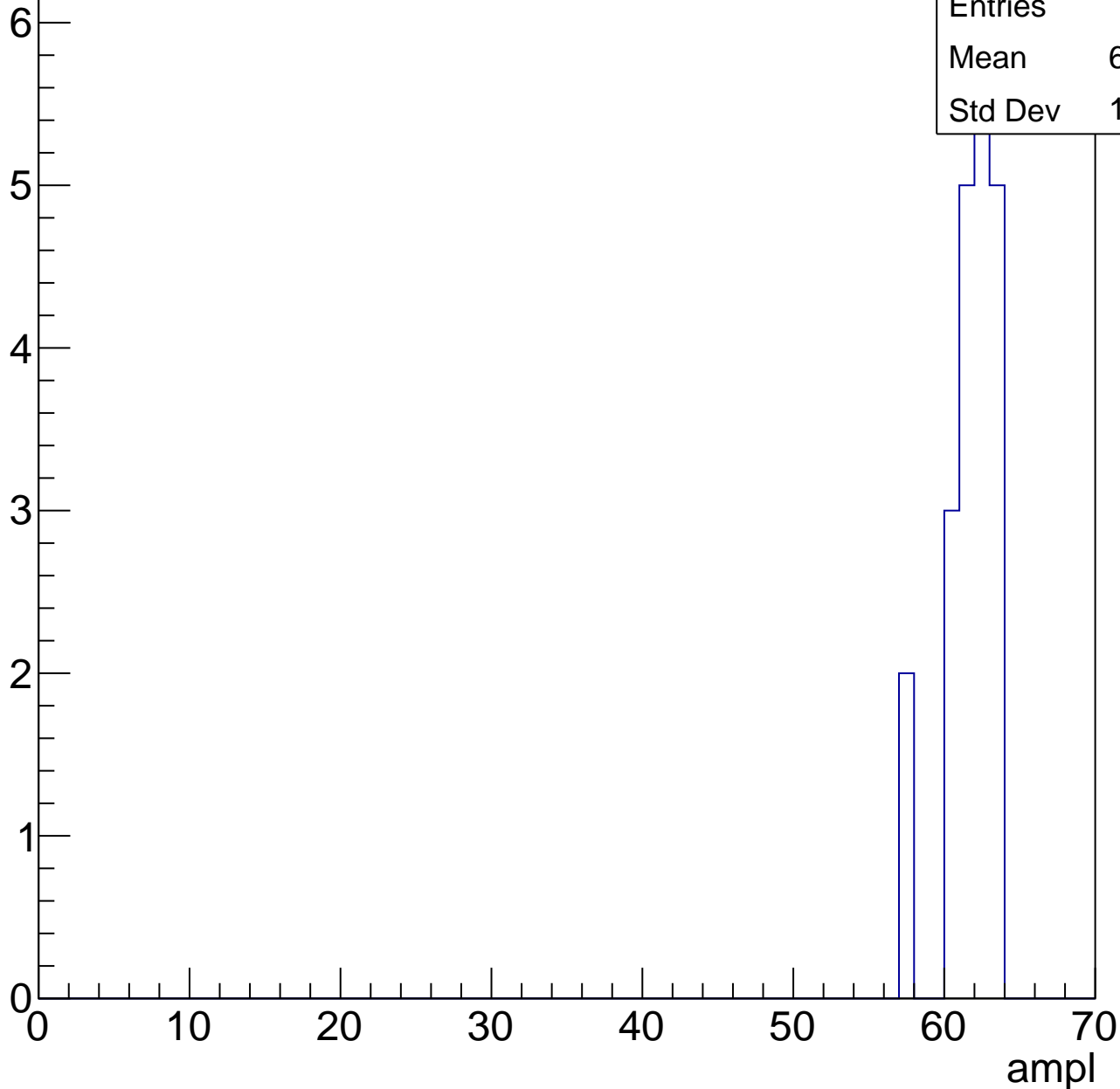


B0L001S, U22-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

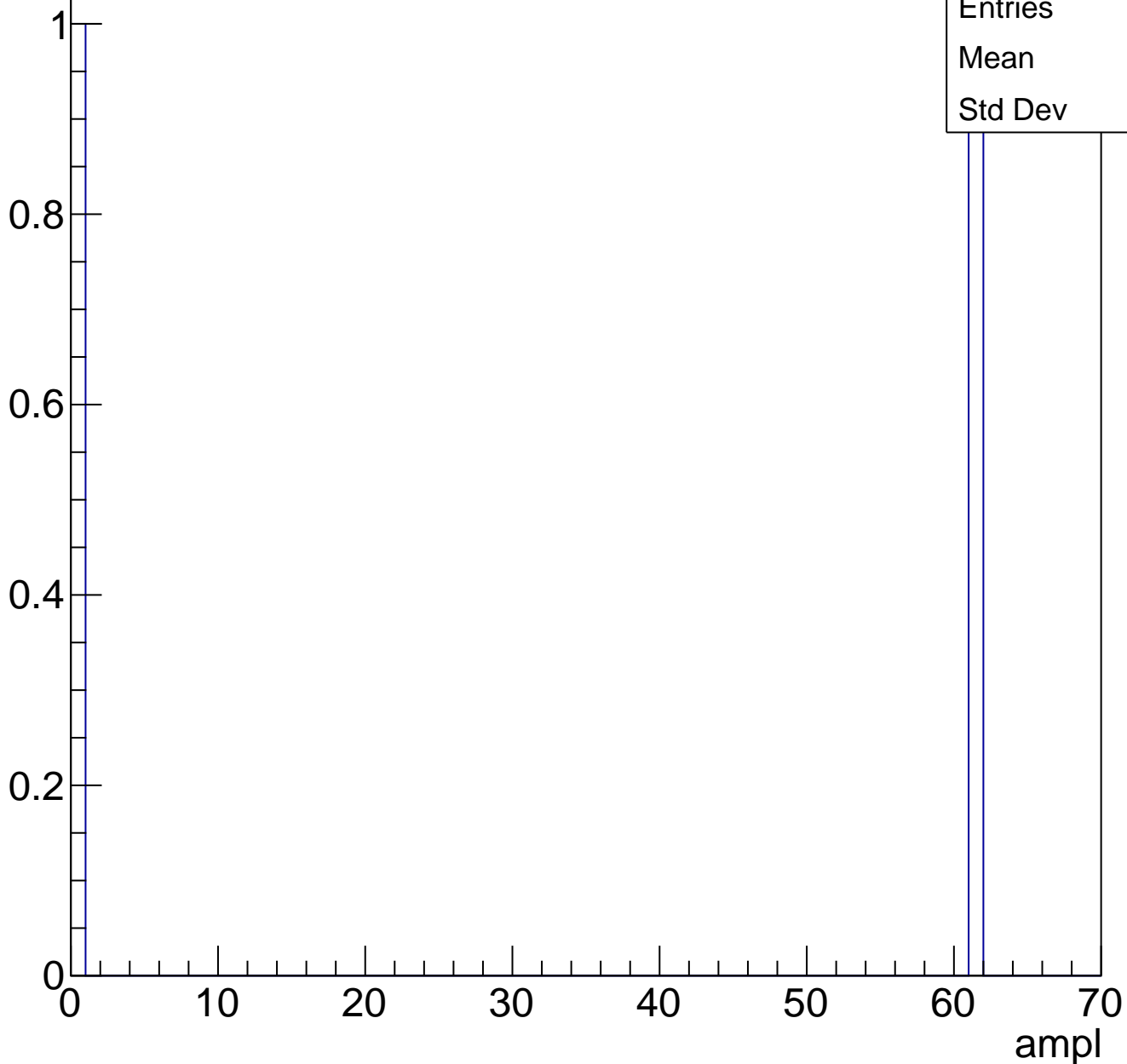
Entries	21
Mean	61.24
Std Dev	1.688



B0L001S, U22-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	30.5
Std Dev	30.5

B0L001S, U22-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch76, adc0

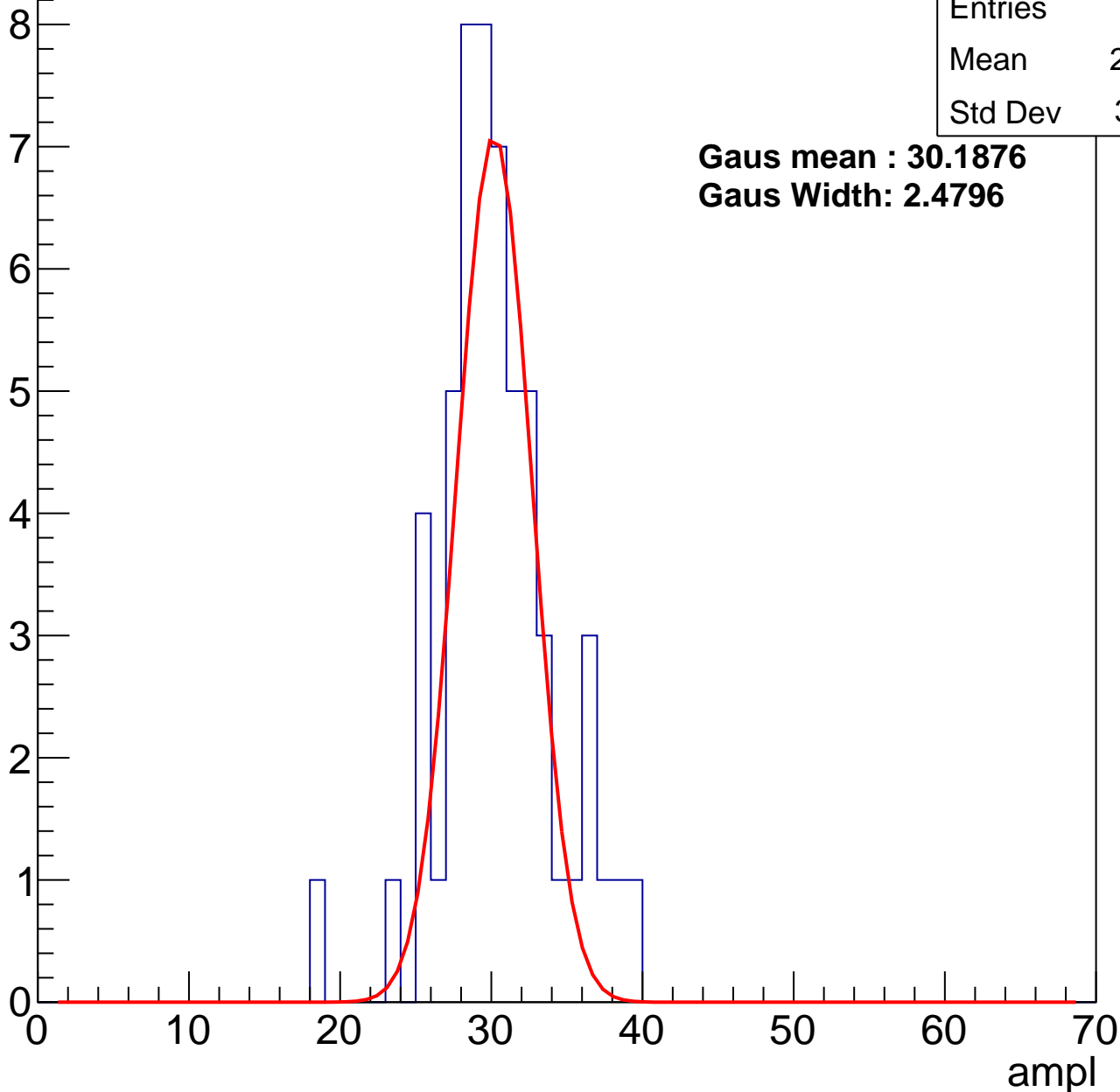
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.88
Std Dev	3.751

Gaus mean : 30.1876

Gaus Width: 2.4796



B0L001S, U22-ch76, adc1

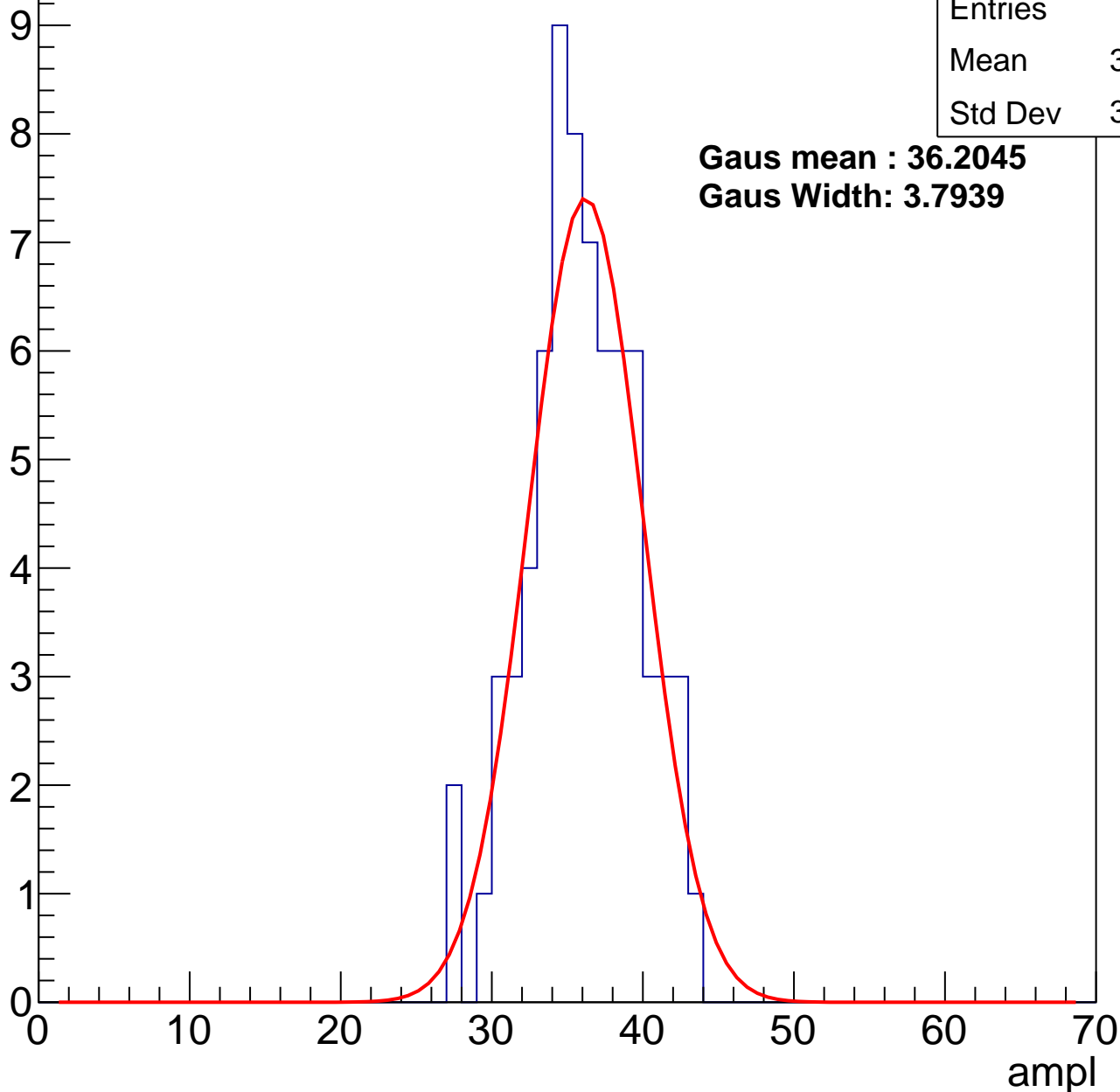
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	35.58
Std Dev	3.575

Gaus mean : 36.2045

Gaus Width: 3.7939



B0L001S, U22-ch76, adc2

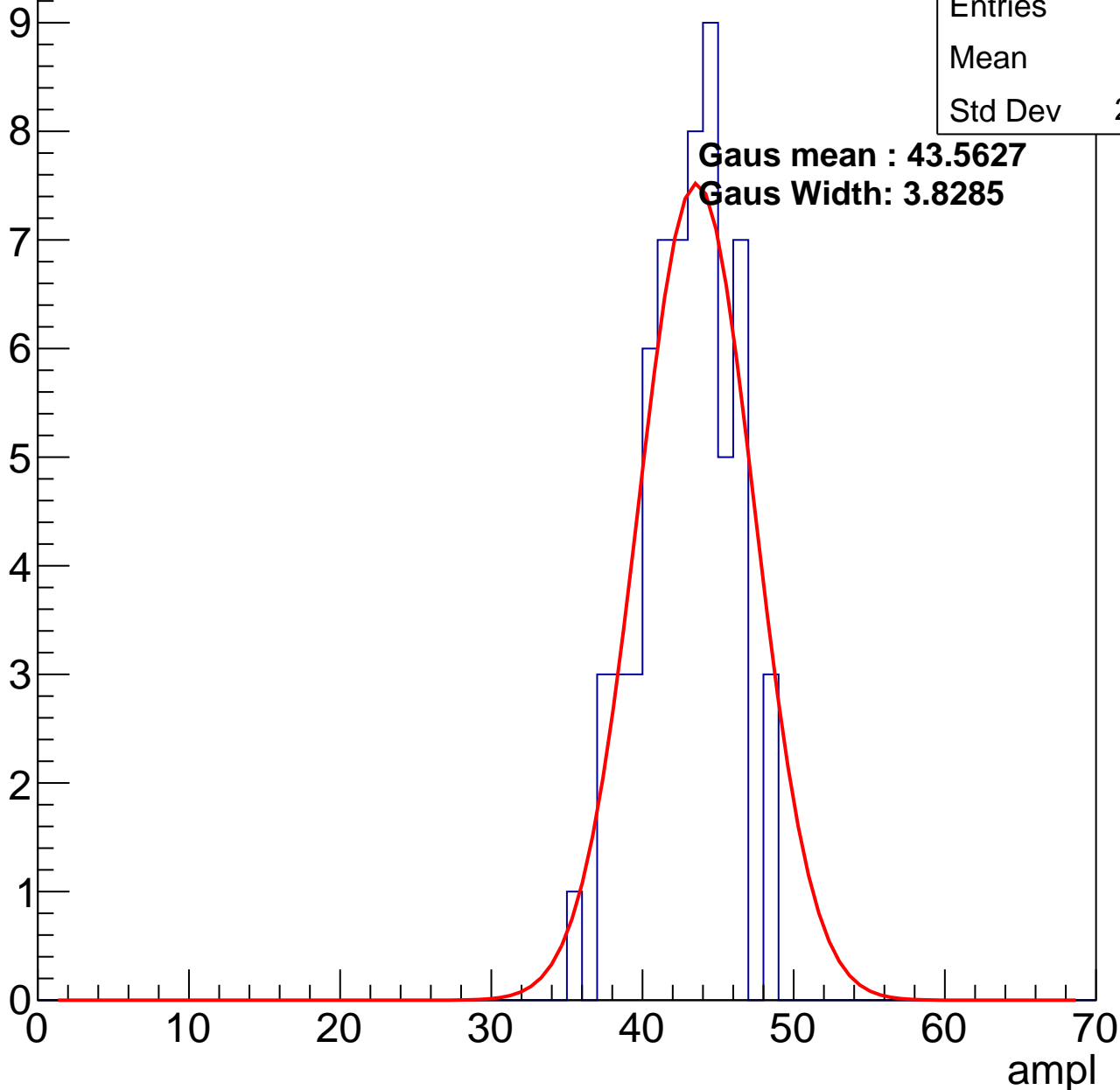
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	42.4
Std Dev	2.921

Gaus mean : 43.5627

Gaus Width: 3.8285



B0L001S, U22-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

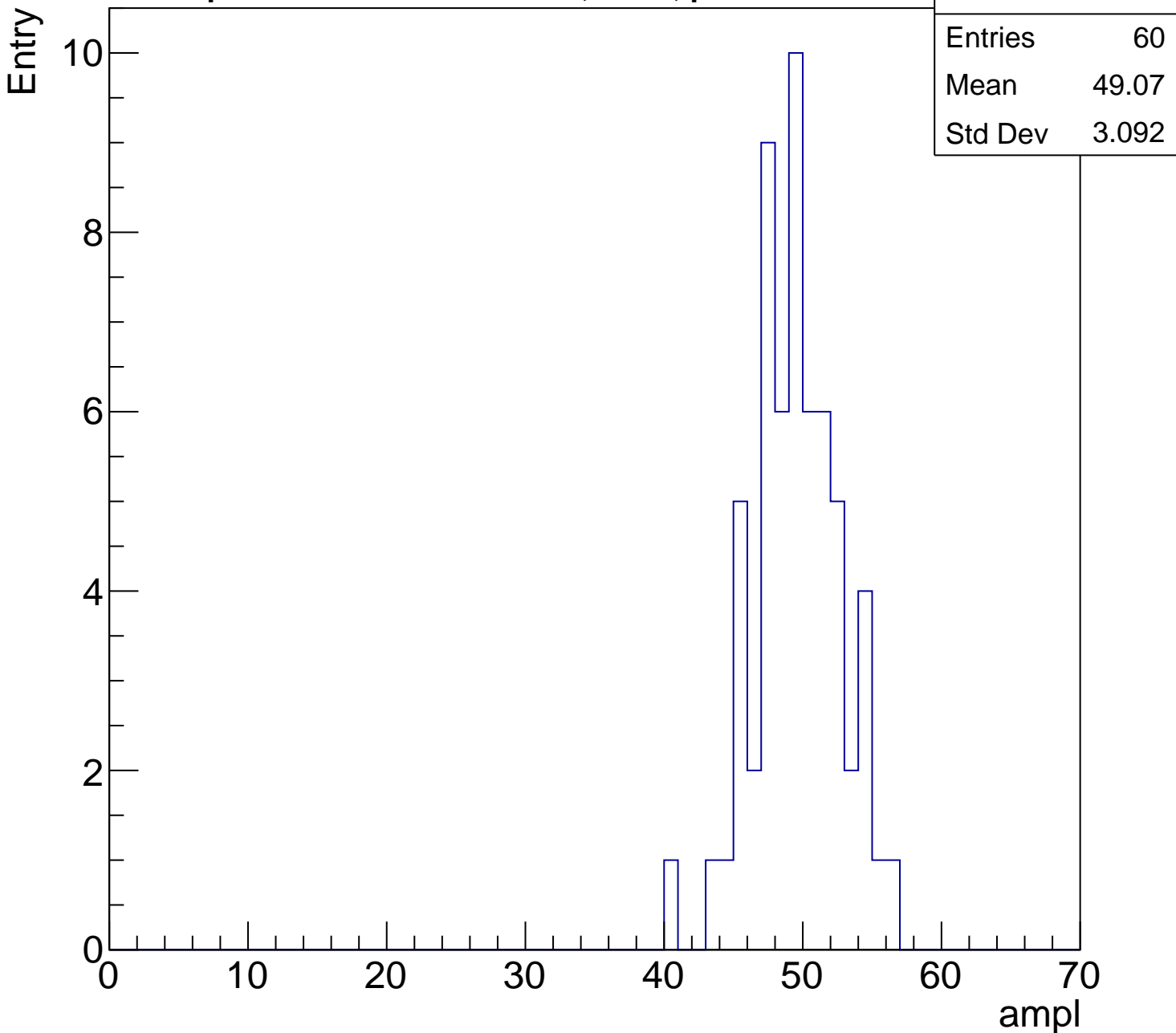
Entries	60
Mean	49.07
Std Dev	3.092

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

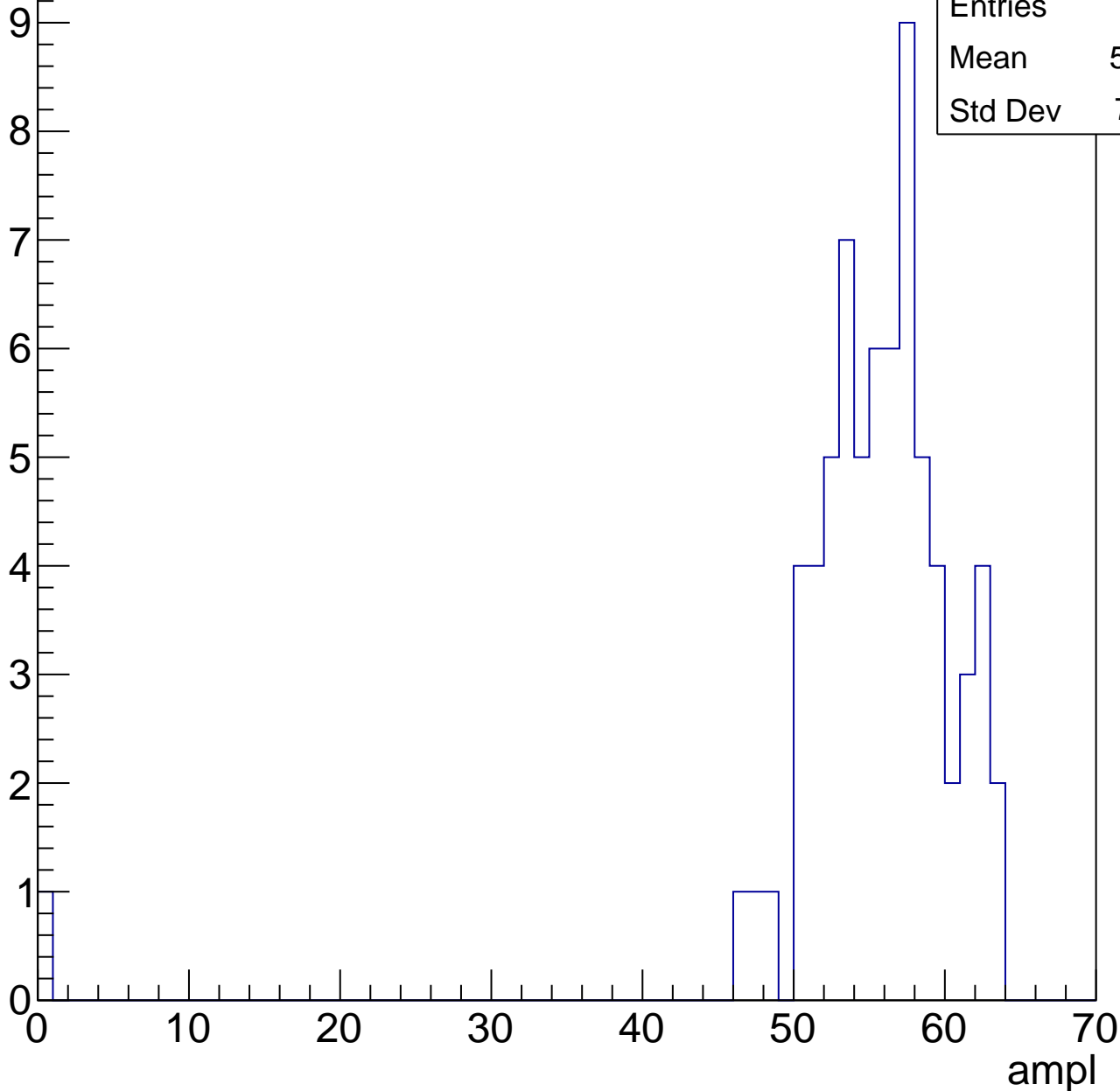


B0L001S, U22-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	54.69
Std Dev	7.641

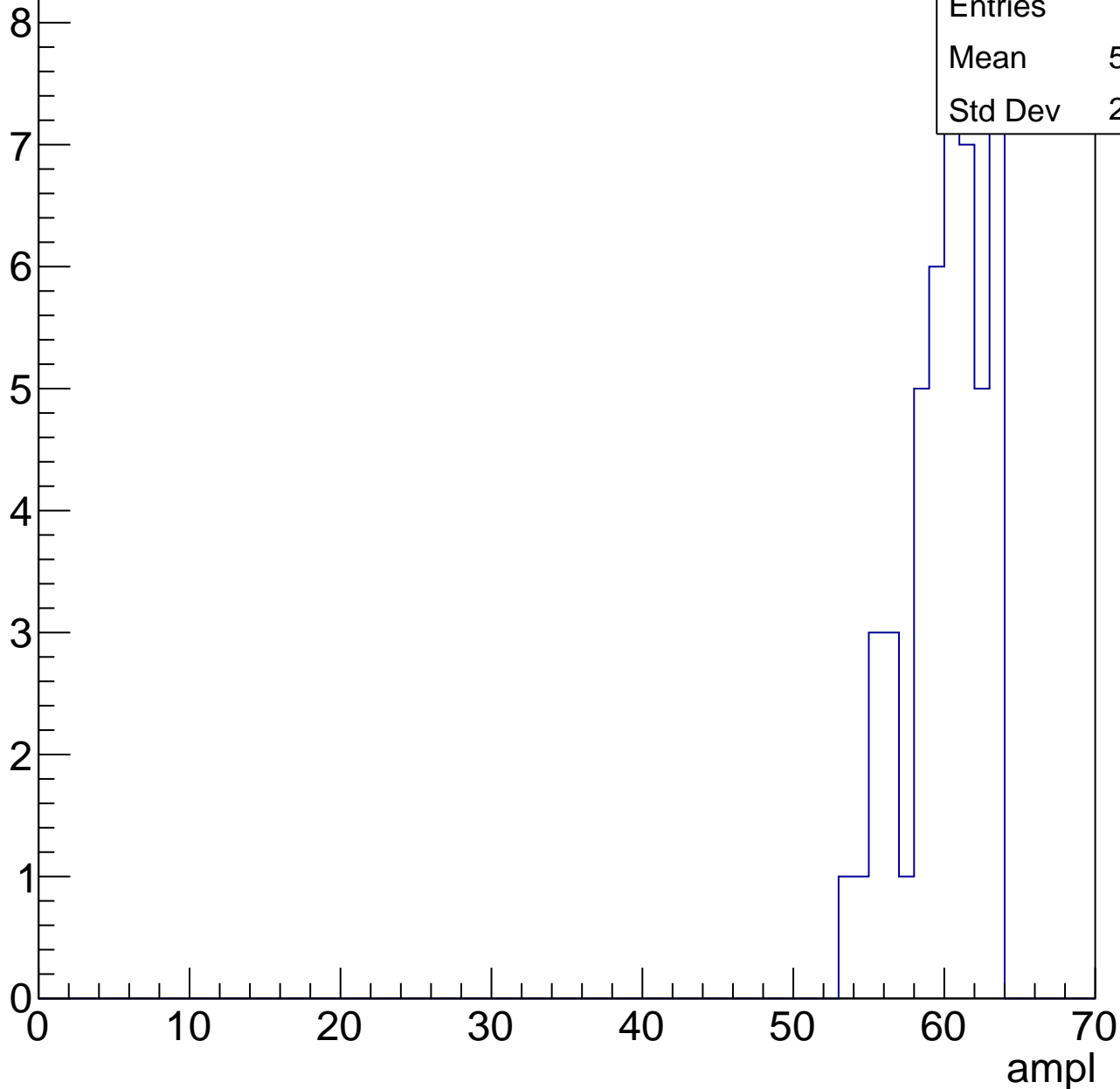


B0L001S, U22-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	59.62
Std Dev	2.643



B0L001S, U22-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch77, adc0

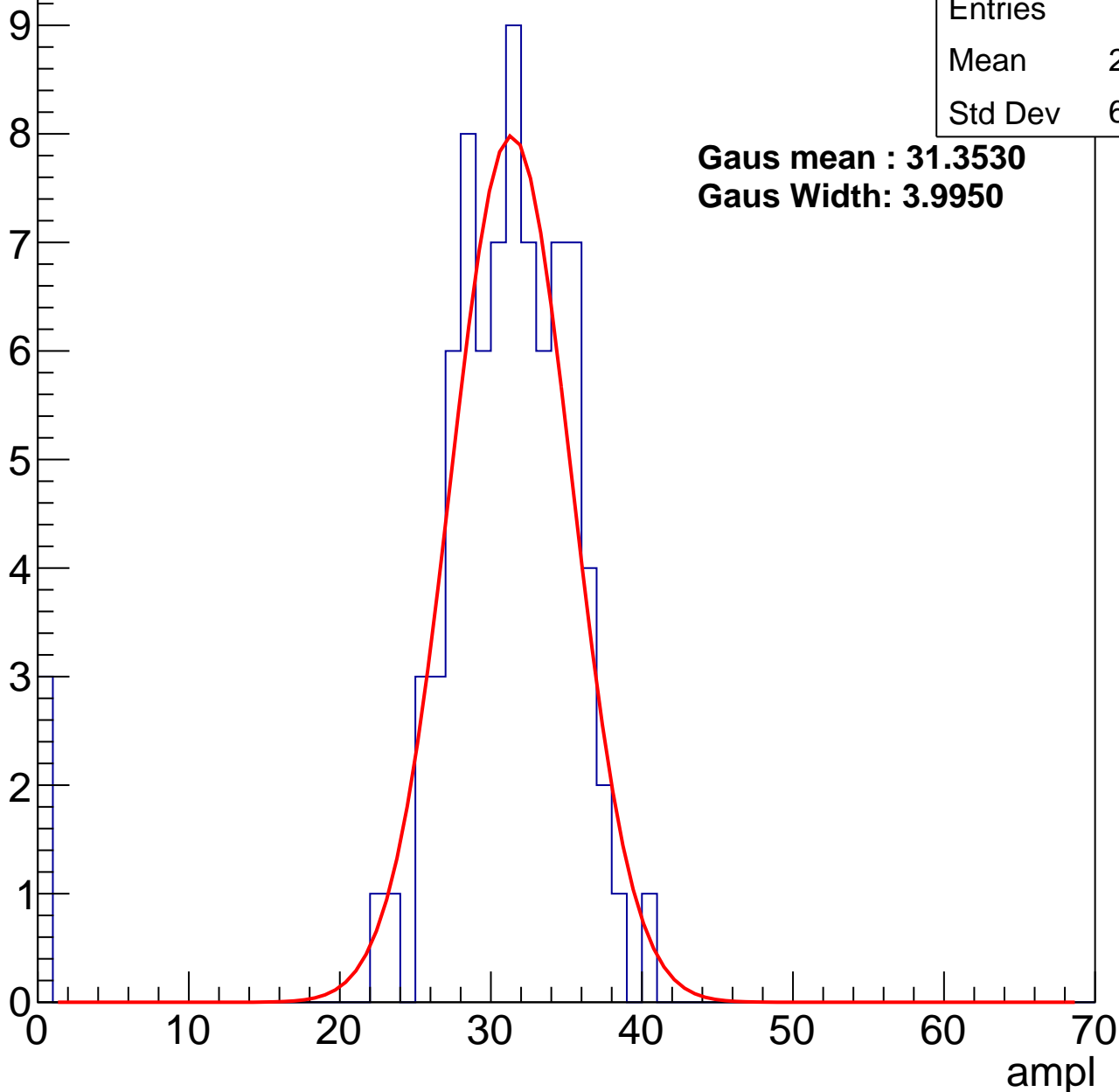
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	29.85
Std Dev	6.817

Gaus mean : 31.3530

Gaus Width: 3.9950



B0L001S, U22-ch77, adc1

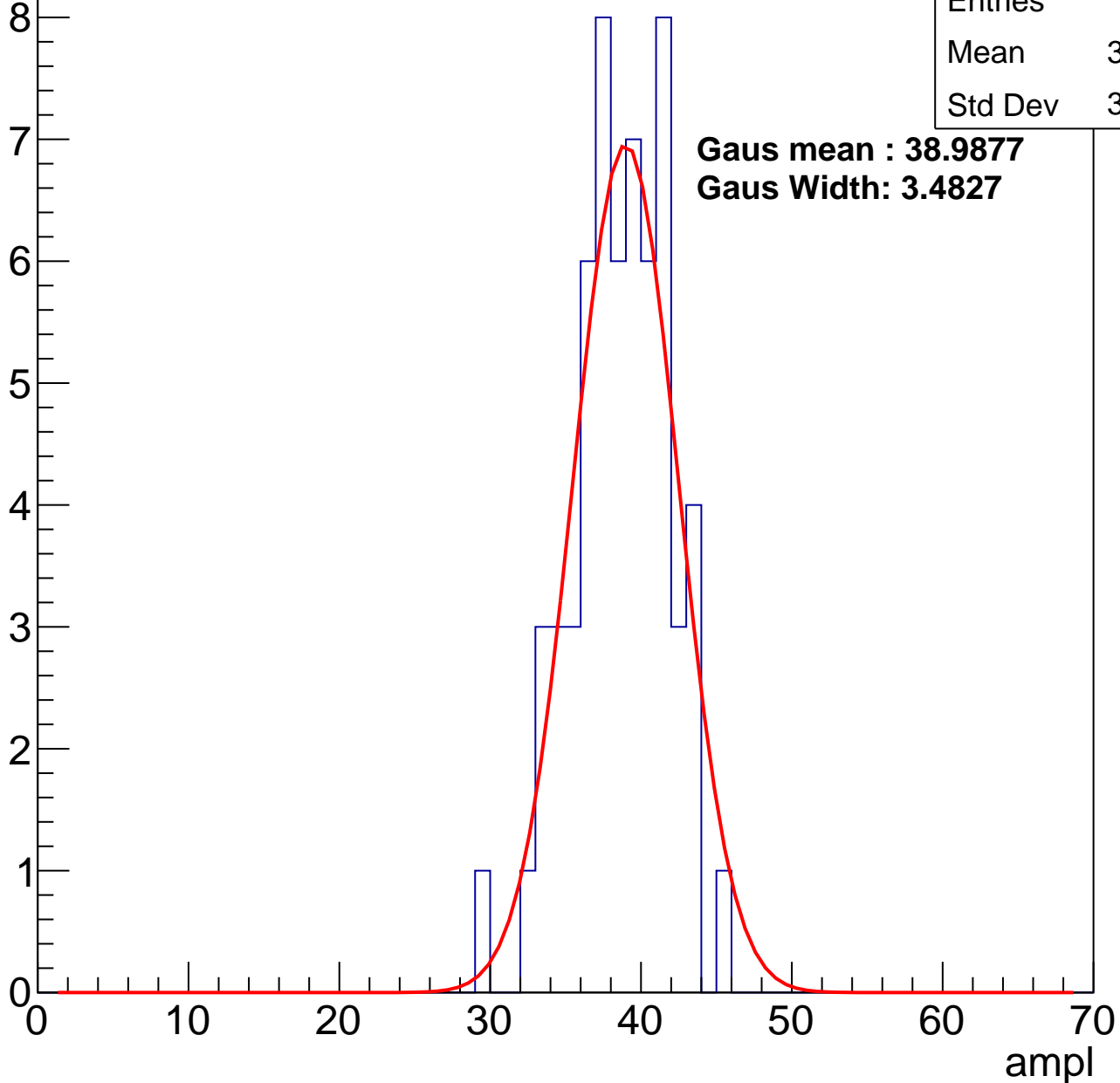
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.18
Std Dev	3.154

Gaus mean : 38.9877

Gaus Width: 3.4827



B0L001S, U22-ch77, adc2

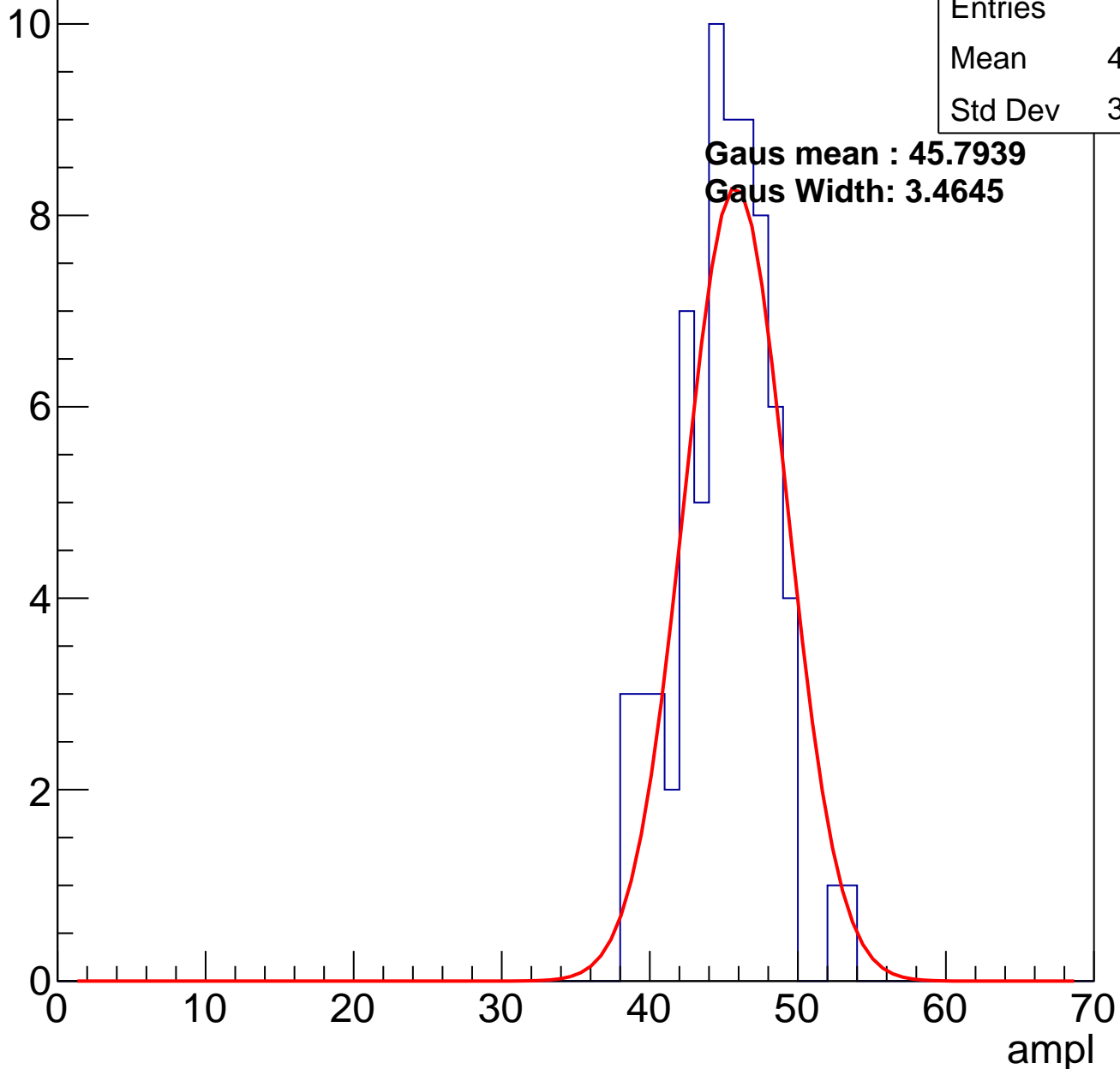
calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	44.59
Std Dev	3.174

Gaus mean : 45.7939

Gaus Width: 3.4645

Entry

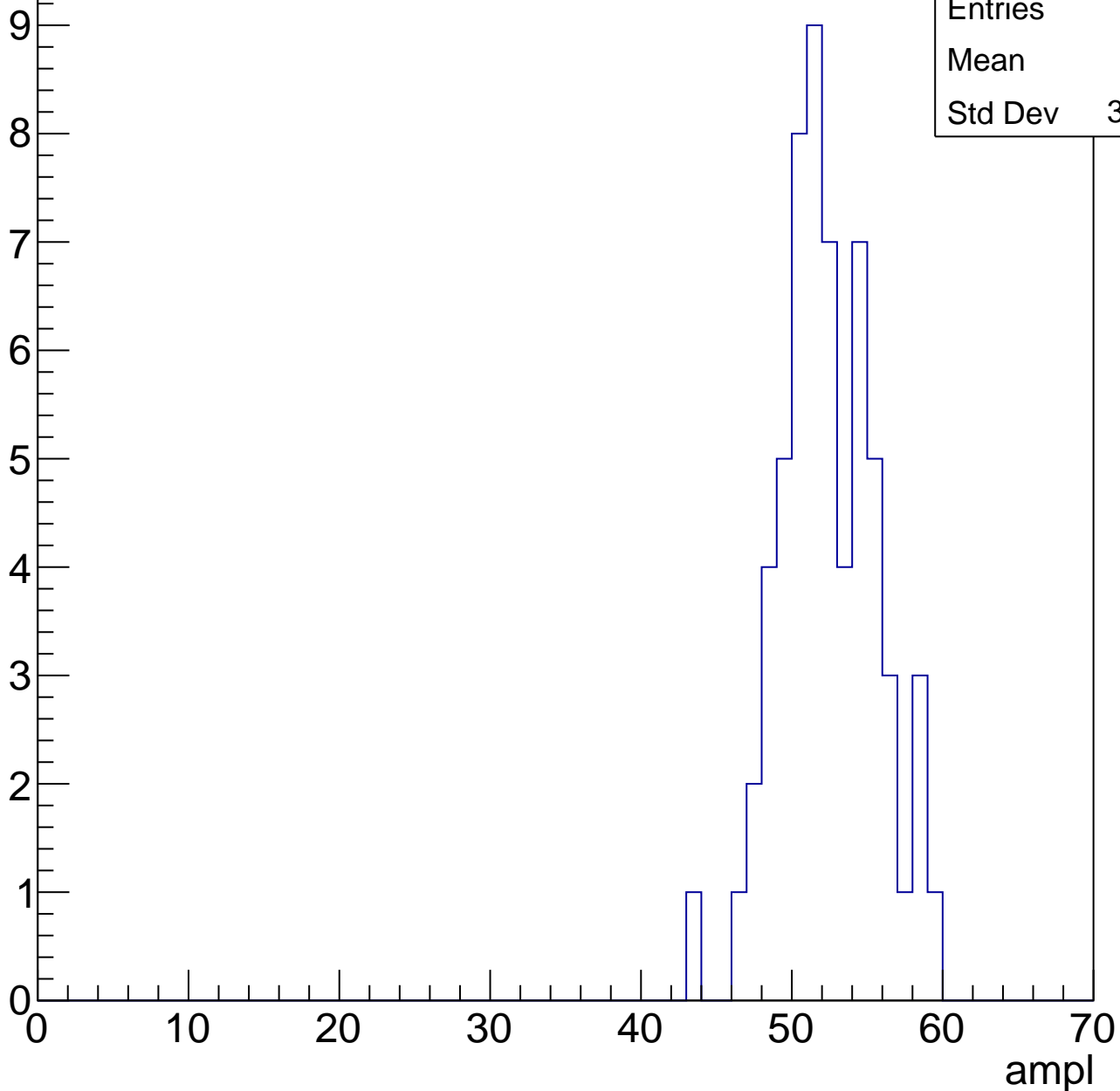


B0L001S, U22-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	51.9
Std Dev	3.197



B0L001S, U22-ch77, adc4

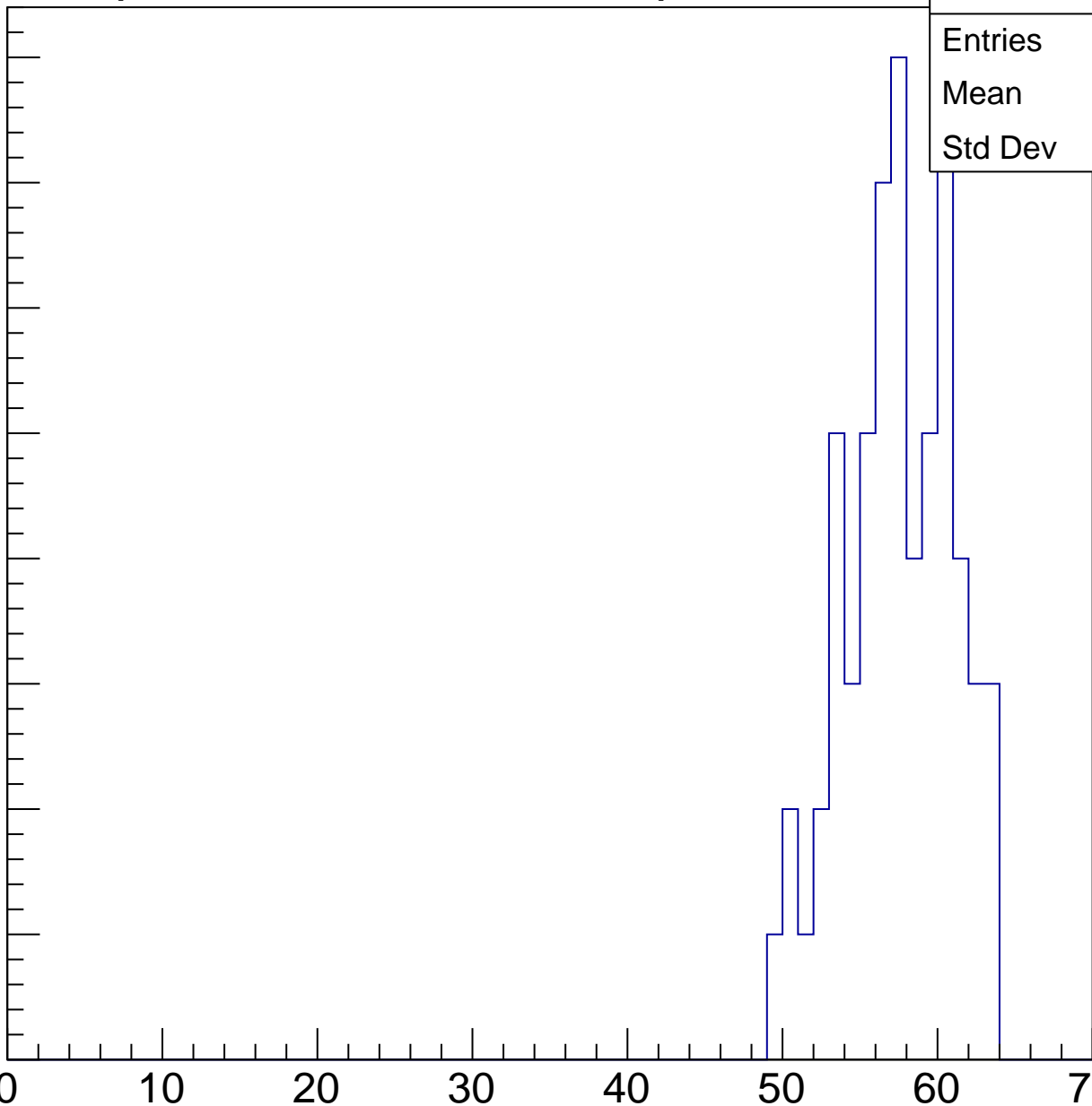
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	57.05
Std Dev	3.447

ampl

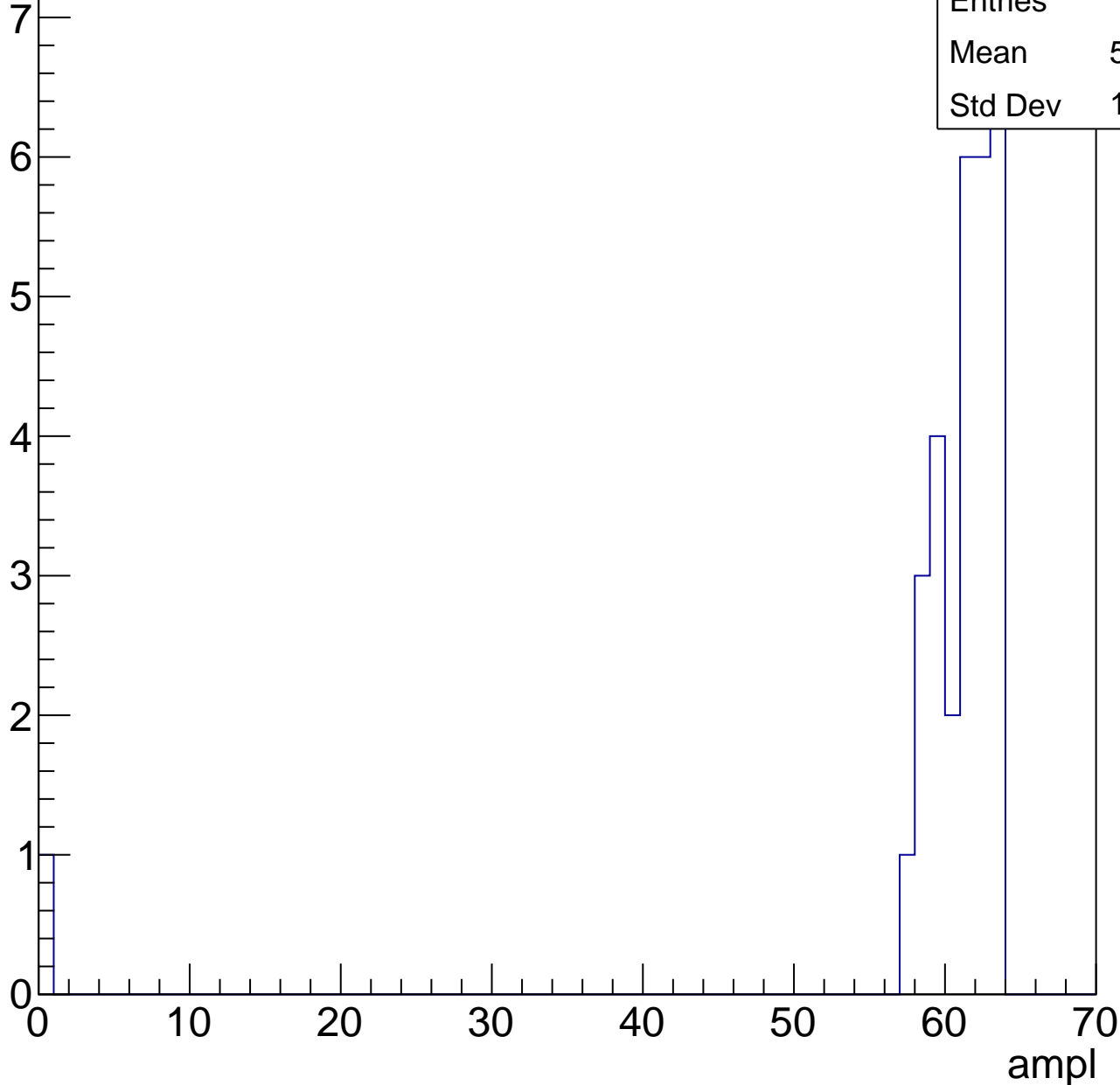


B0L001S, U22-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	58.87
Std Dev	11.07



B0L001S, U22-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch78, adc0

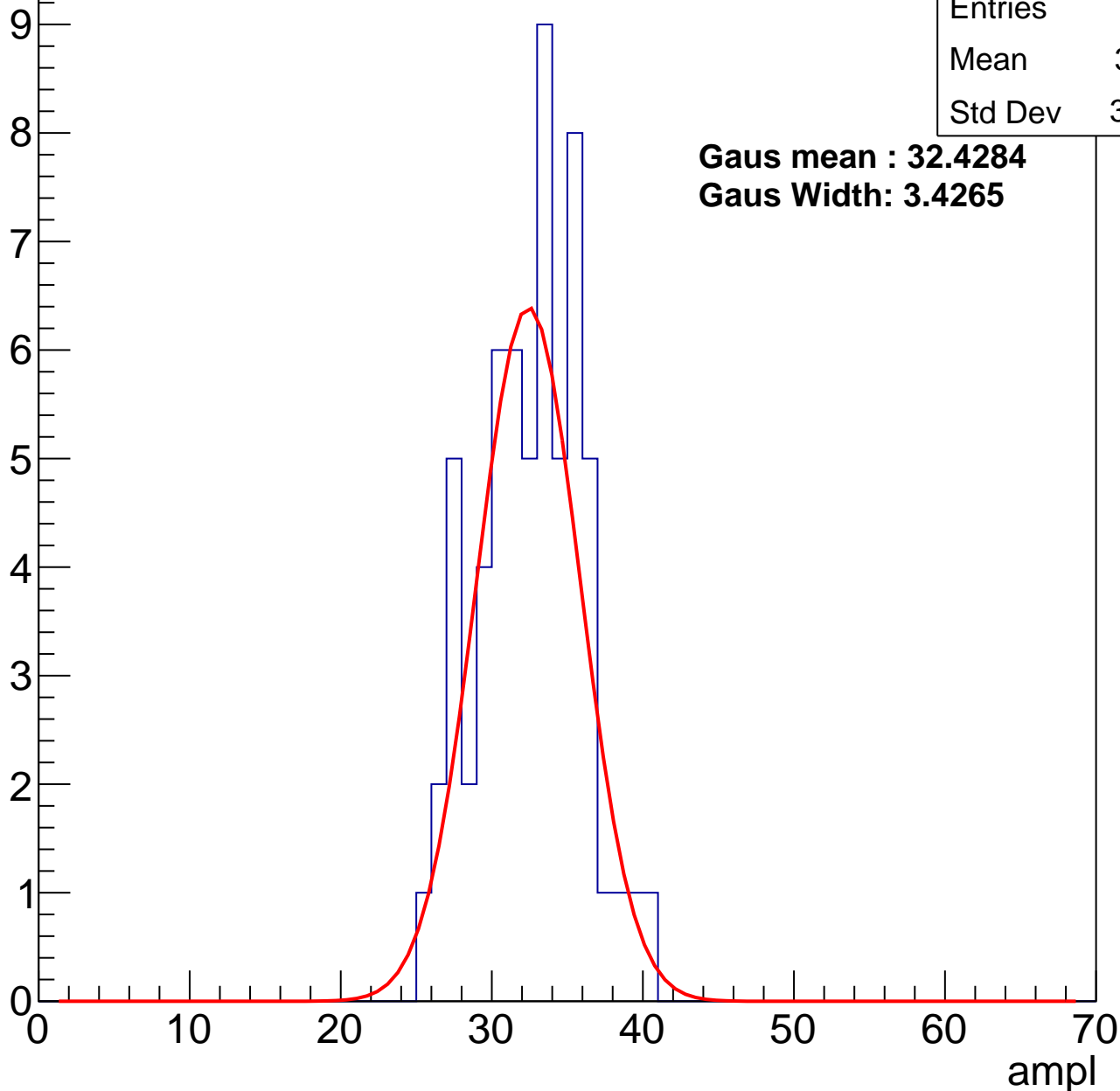
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	32.11
Std Dev	3.356

Gaus mean : 32.4284

Gaus Width: 3.4265



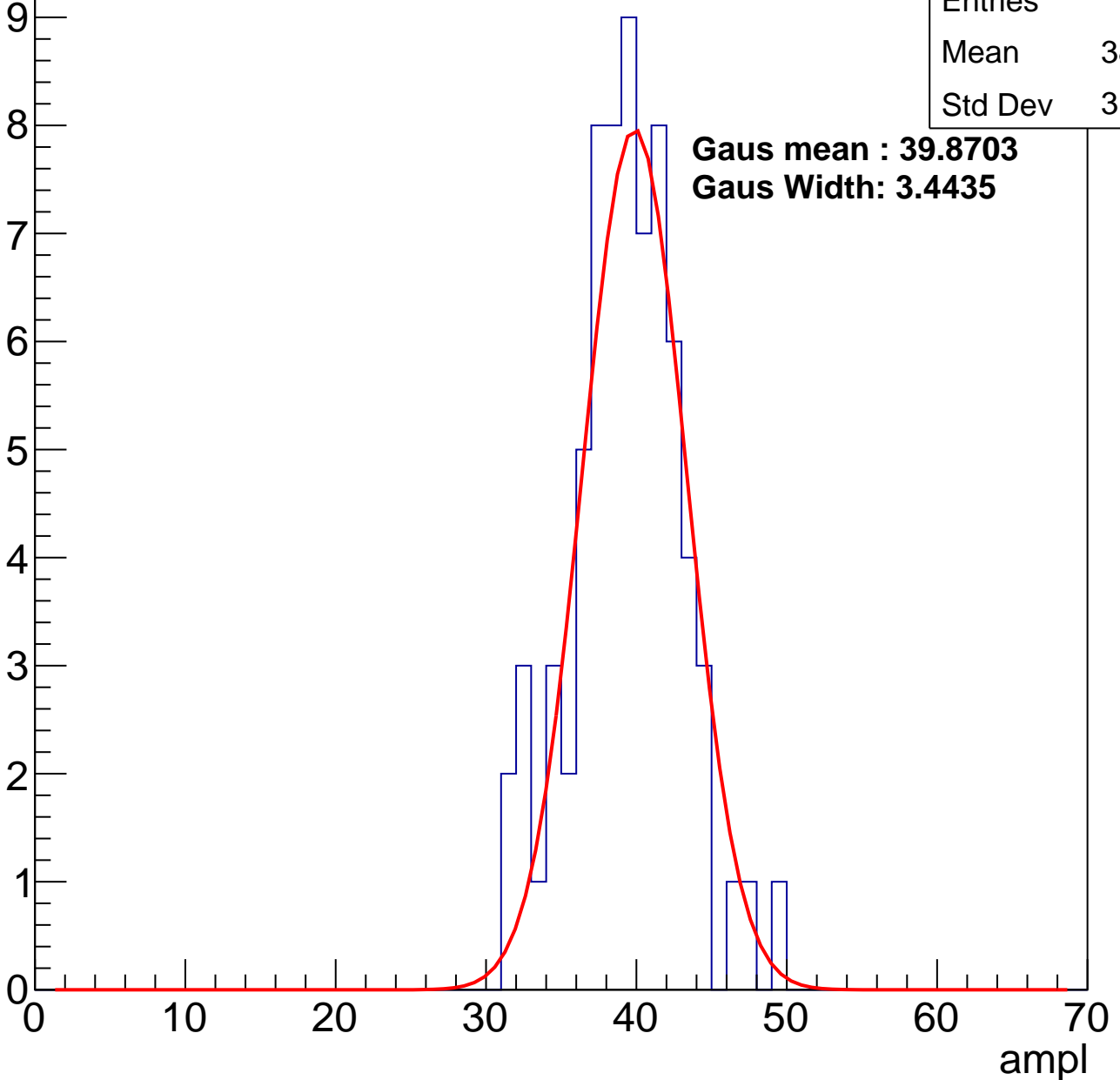
B0L001S, U22-ch78, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.89
Std Dev	3.627

Gaus mean : 39.8703
Gaus Width: 3.4435



B0L001S, U22-ch78, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	45.57
Std Dev	3.14

Gaus mean : 46.0842

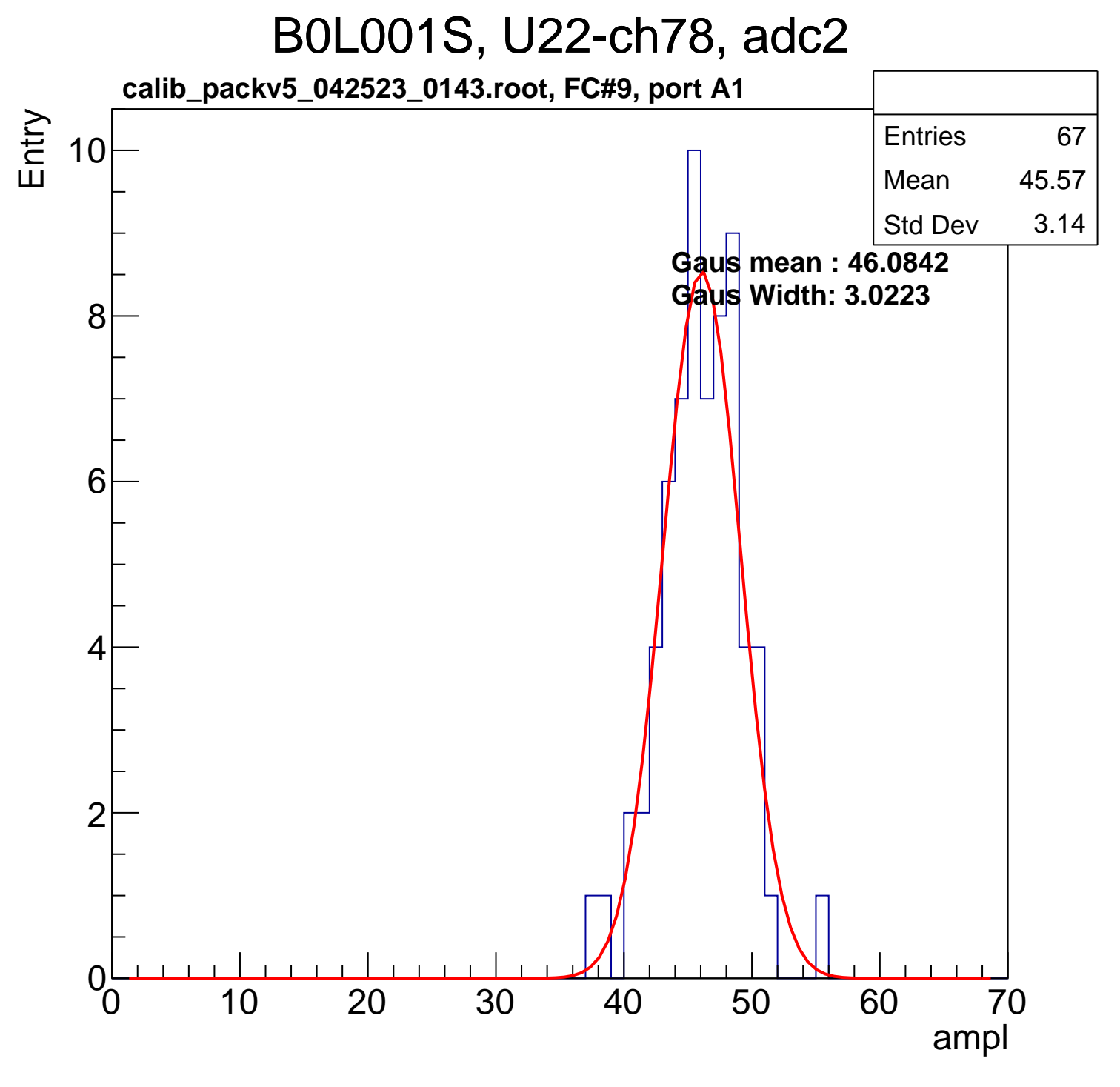
Gaus Width: 3.0223

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

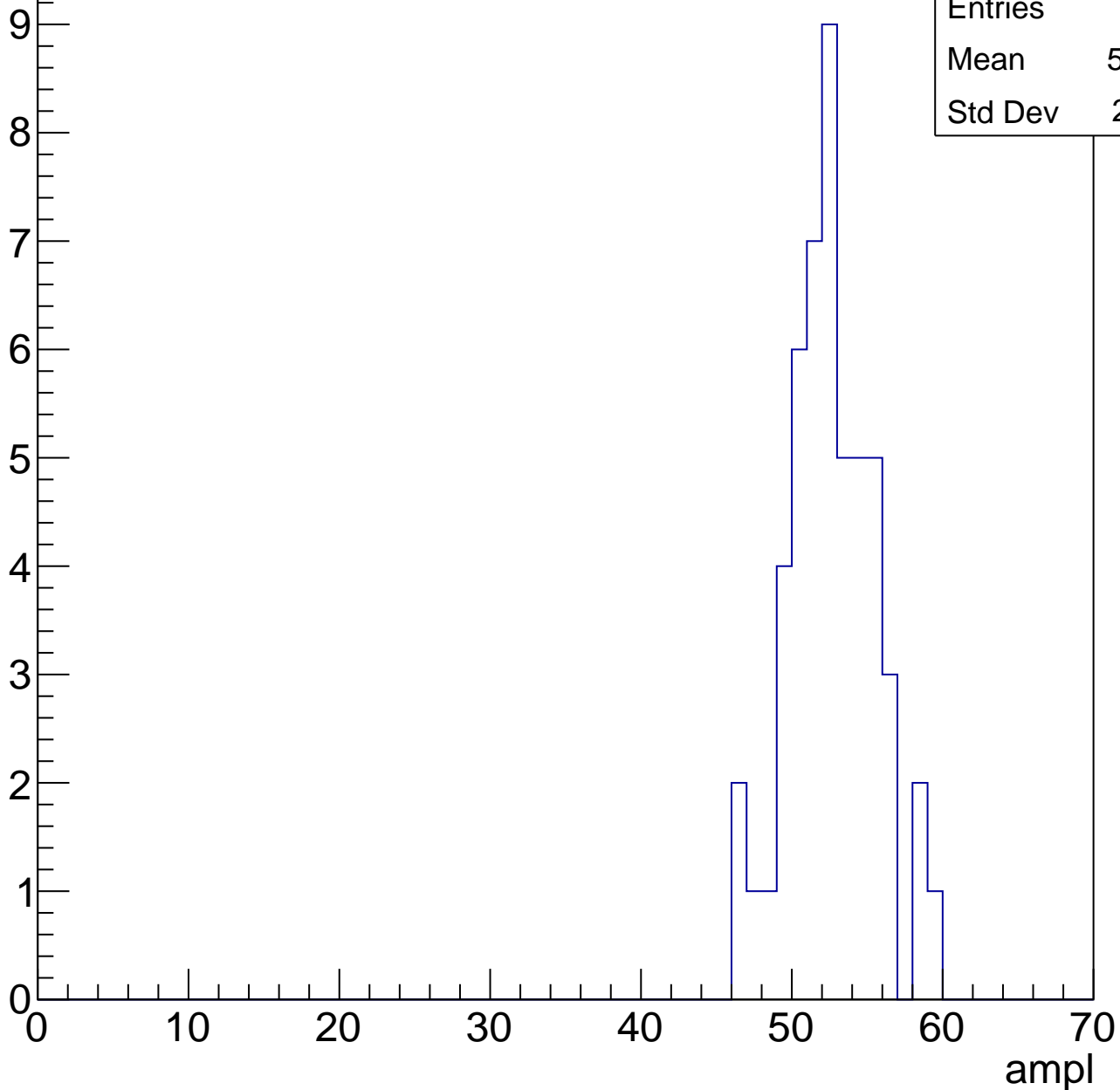


B0L001S, U22-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	52.18
Std Dev	2.861



B0L001S, U22-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	57.64
Std Dev	3.365

Entry

10

8

6

4

2

0

0

10

20

30

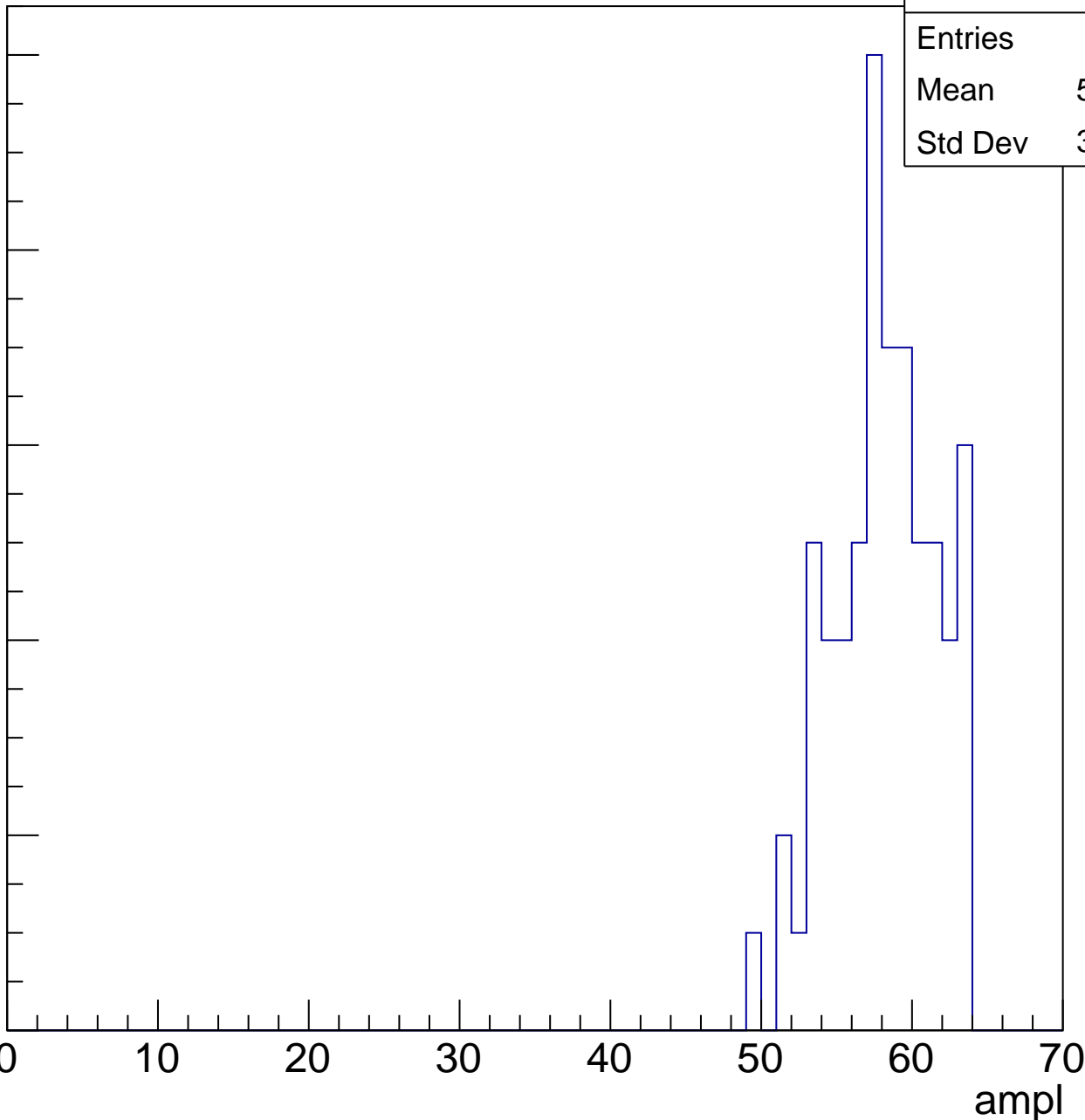
40

50

60

70

ampl

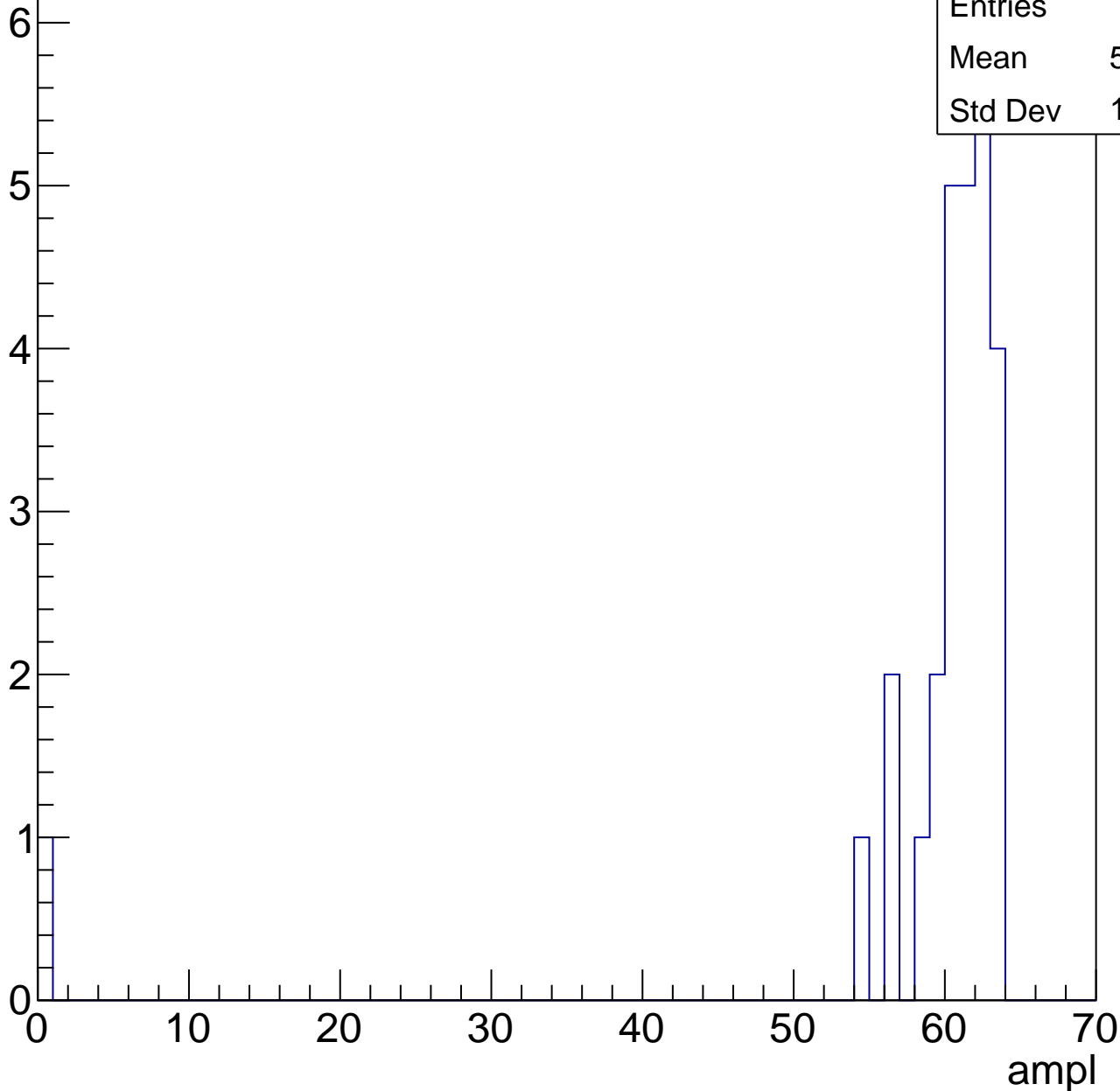


B0L001S, U22-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.19
Std Dev	11.63



B0L001S, U22-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch79, adc0

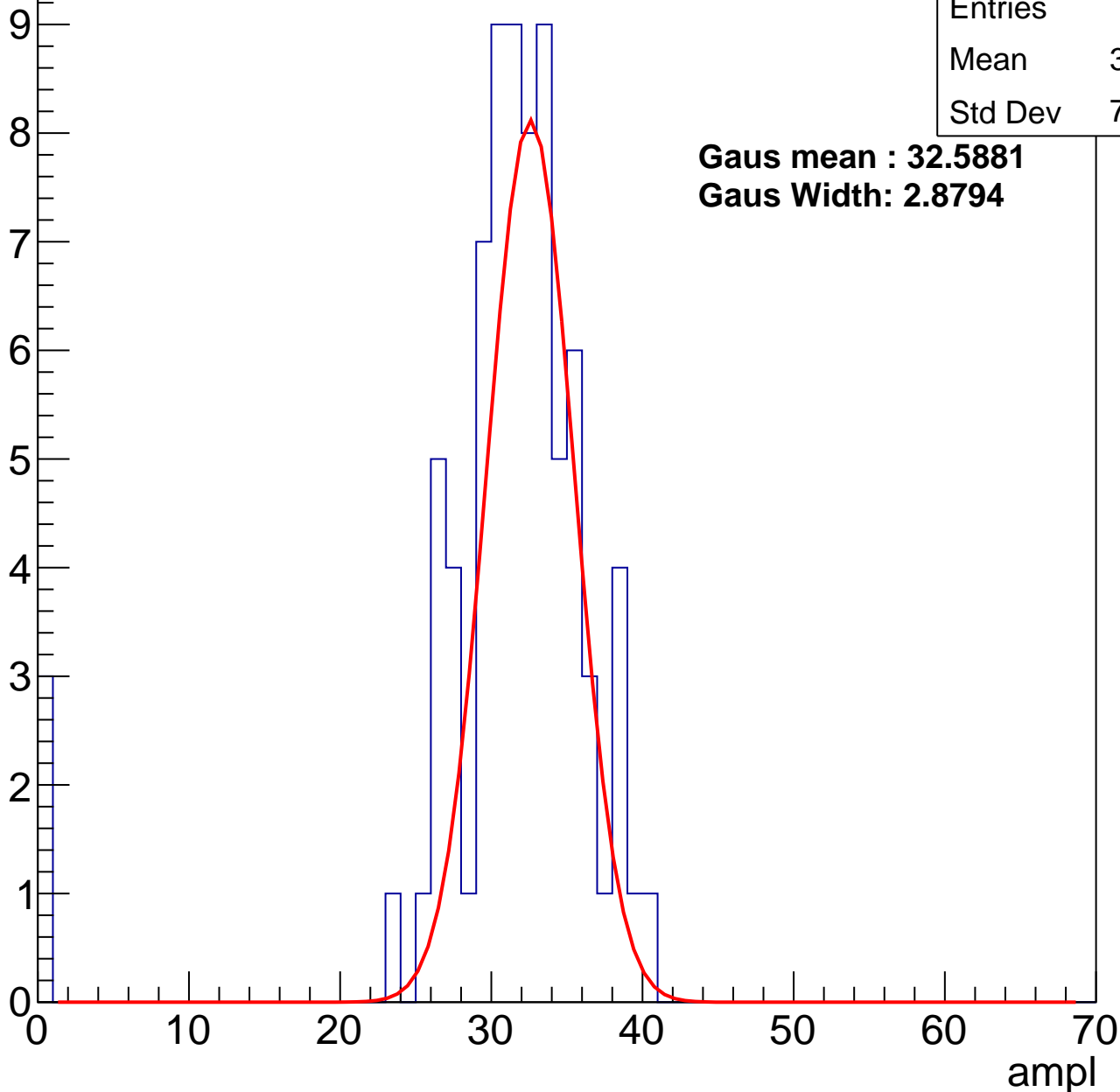
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	30.45
Std Dev	7.009

Gaus mean : 32.5881

Gaus Width: 2.8794



B0L001S, U22-ch79, adc1

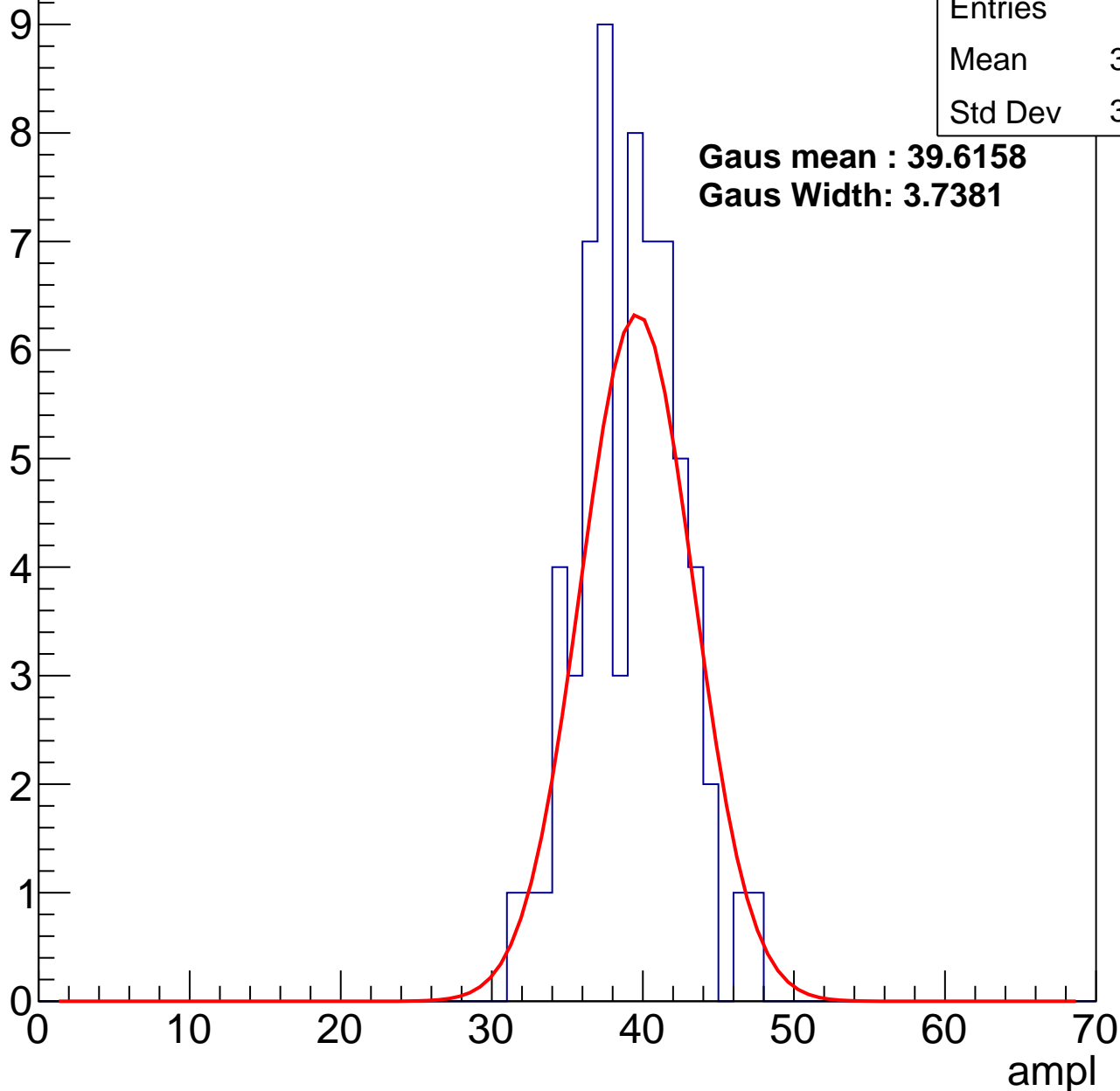
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	38.72
Std Dev	3.309

Gaus mean : 39.6158

Gaus Width: 3.7381



B0L001S, U22-ch79, adc2

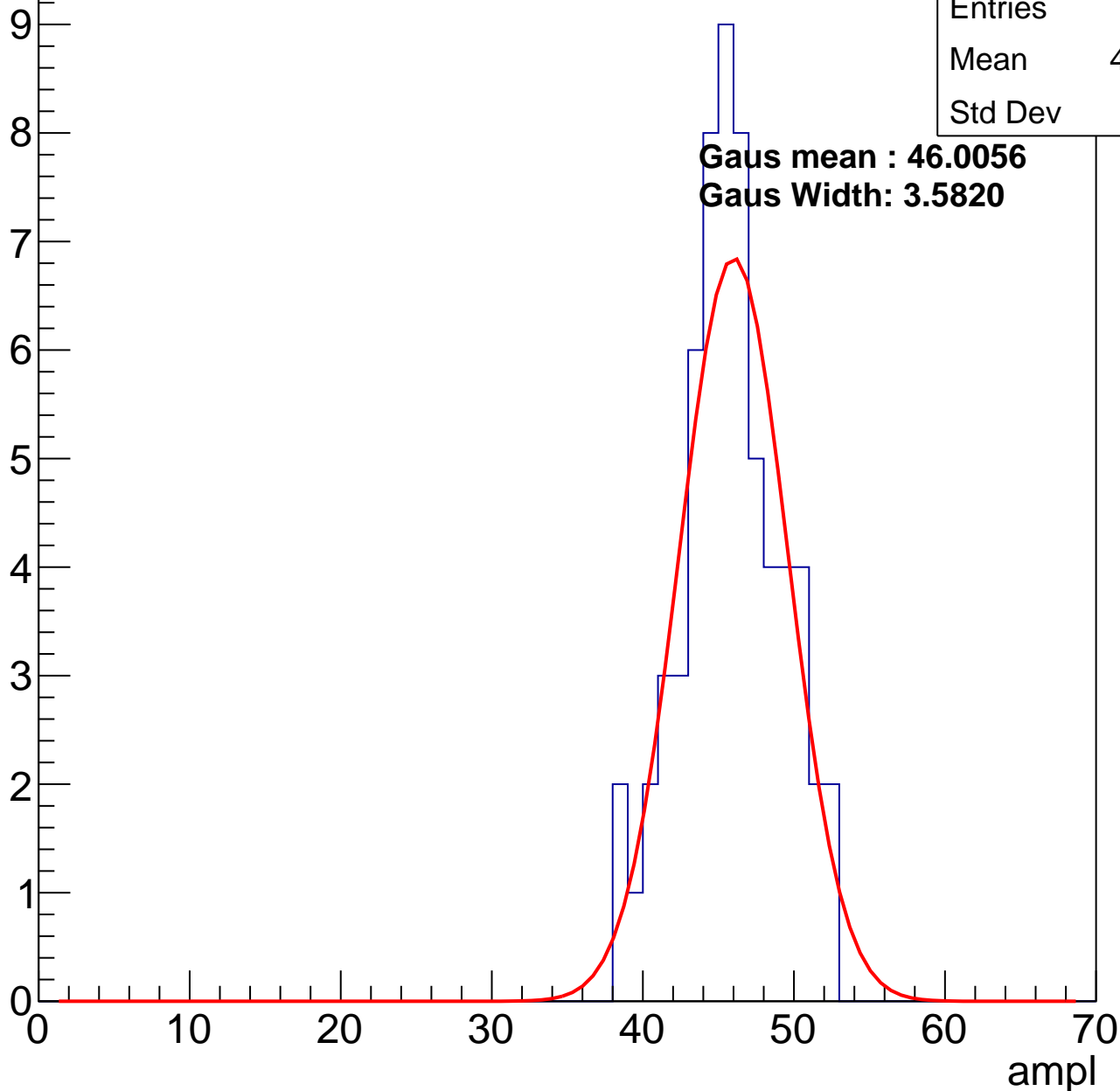
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	45.33
Std Dev	3.29

Gaus mean : 46.0056

Gaus Width: 3.5820

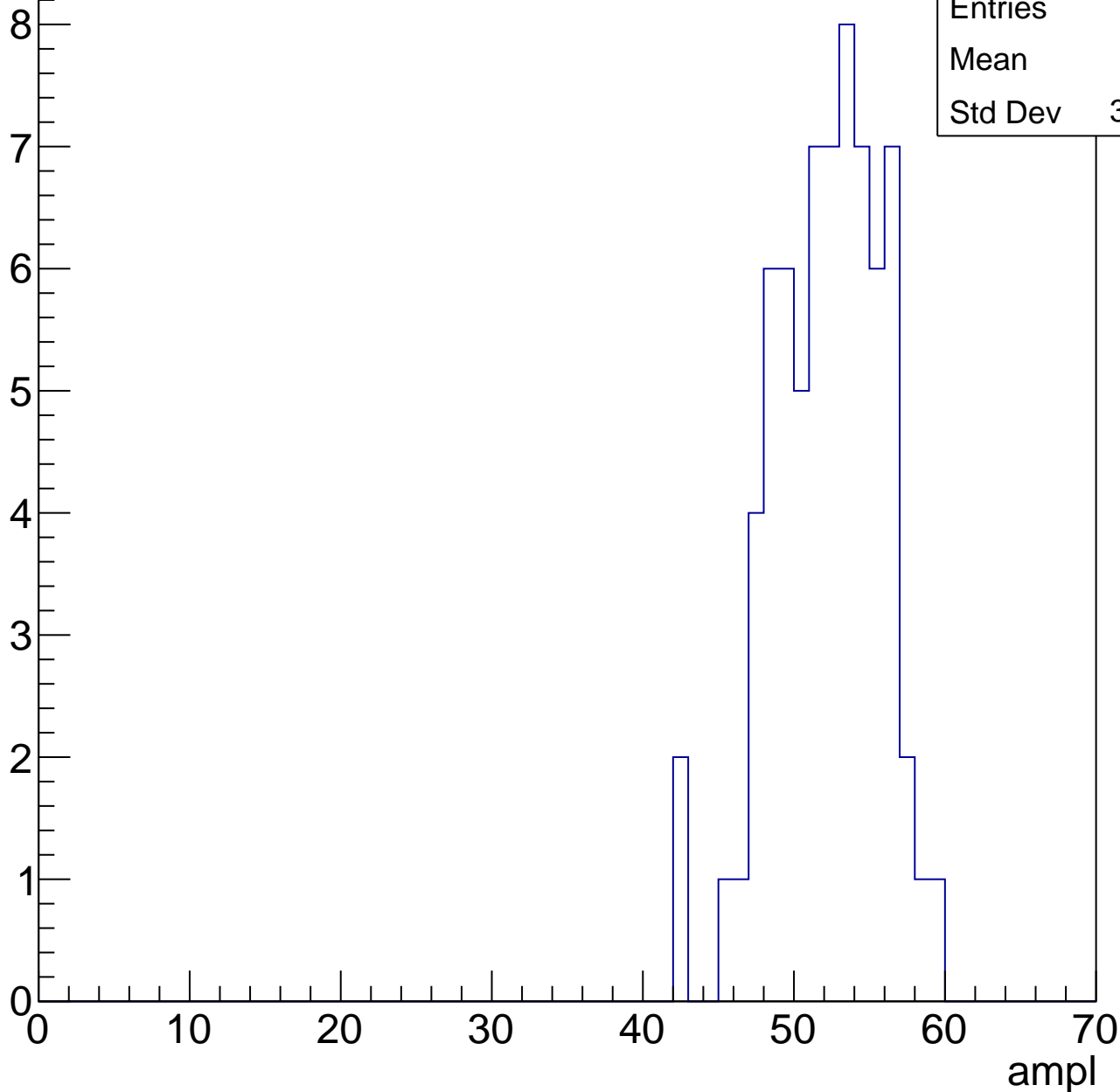


B0L001S, U22-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

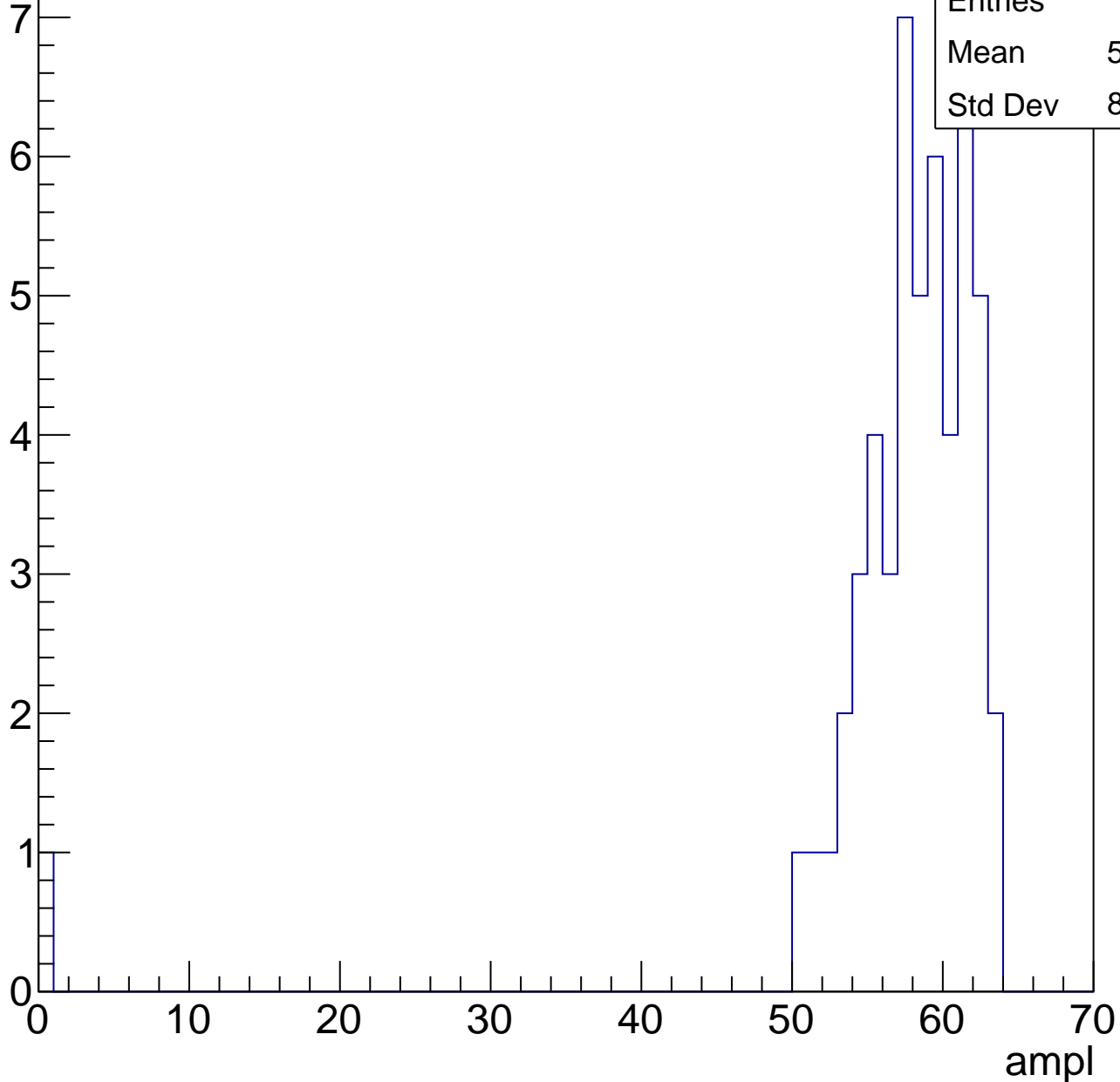
Entries	71
Mean	51.7
Std Dev	3.546



B0L001S, U22-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

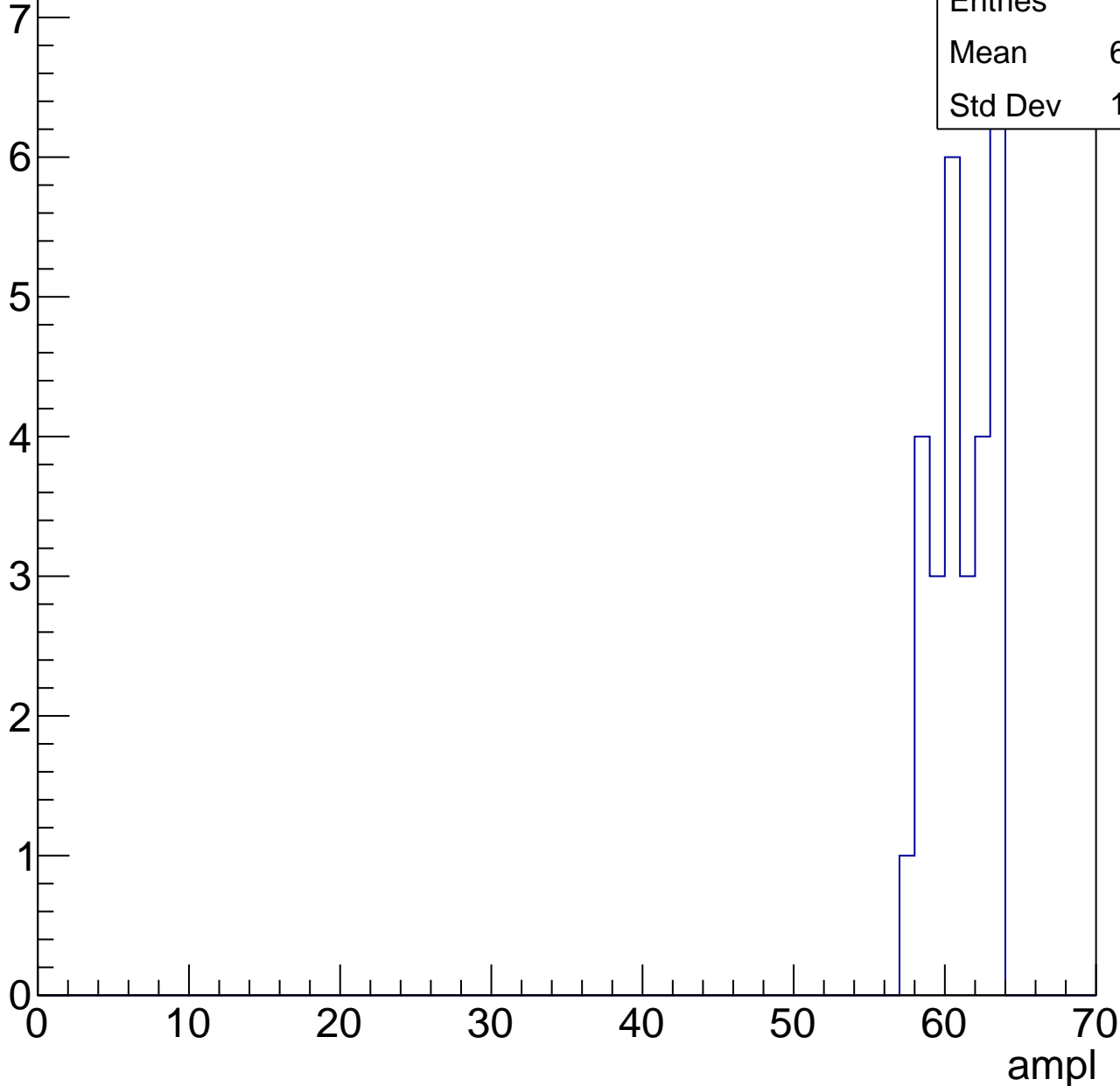


B0L001S, U22-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	60.64
Std Dev	1.875



B0L001S, U22-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch80, adc0

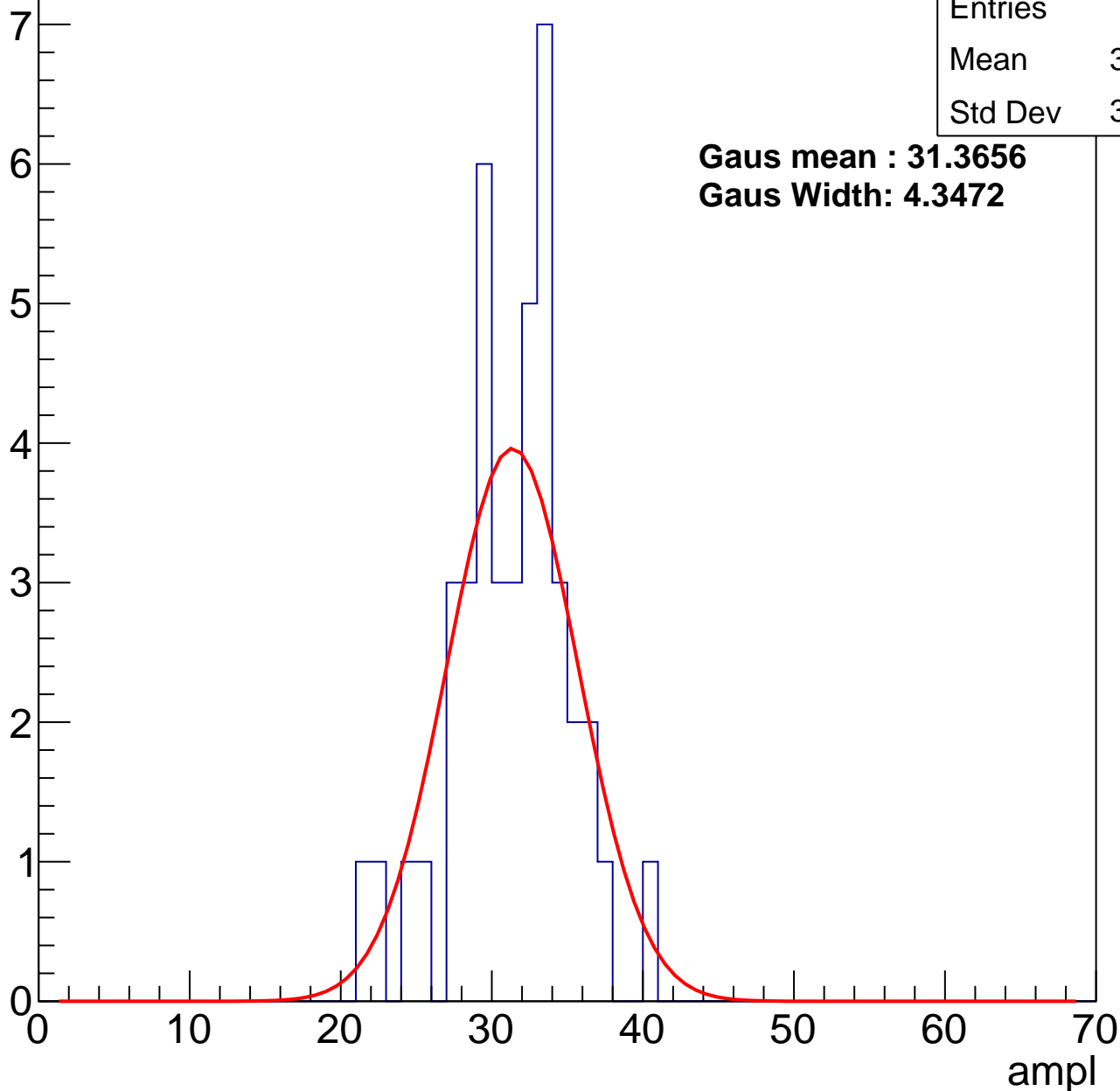
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	30.84
Std Dev	3.815

Gaus mean : 31.3656

Gaus Width: 4.3472



B0L001S, U22-ch80, adc1

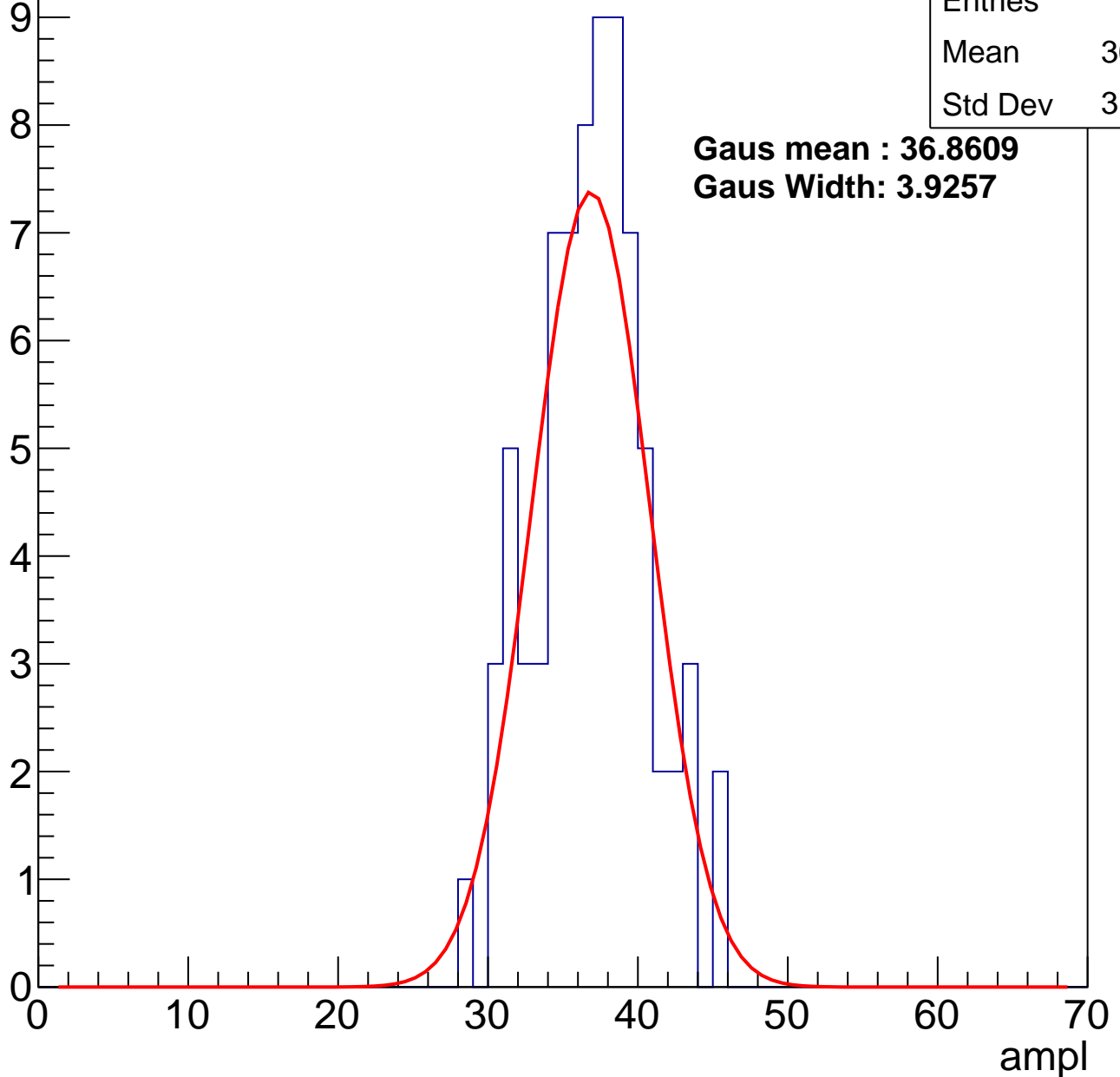
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	36.47
Std Dev	3.644

Gaus mean : 36.8609

Gaus Width: 3.9257



B0L001S, U22-ch80, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	44.67
Std Dev	3.732

Gaus mean : 45.3719

Gaus Width: 4.0755

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

0

2

4

6

8

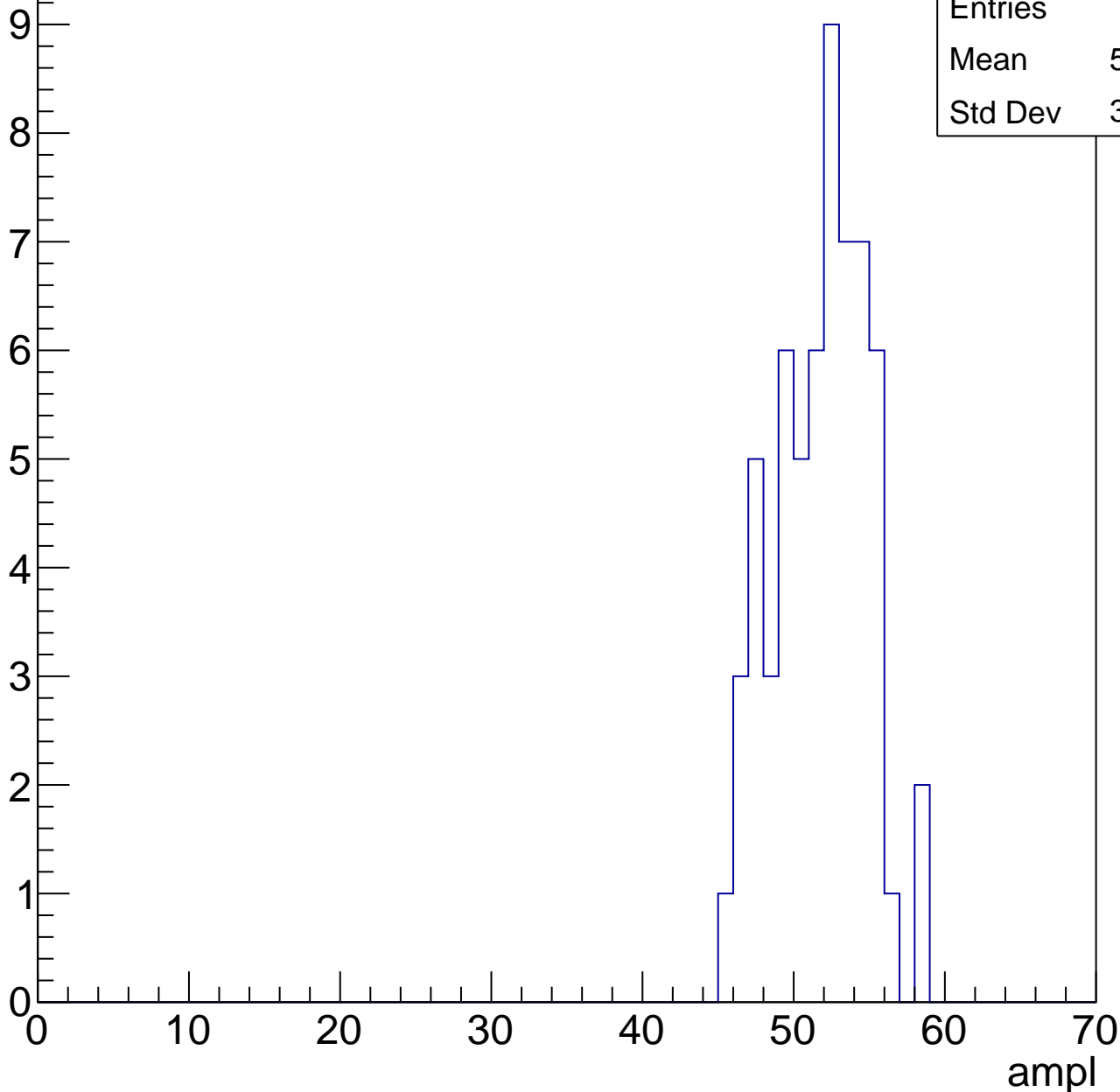
10

B0L001S, U22-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	51.33
Std Dev	3.028

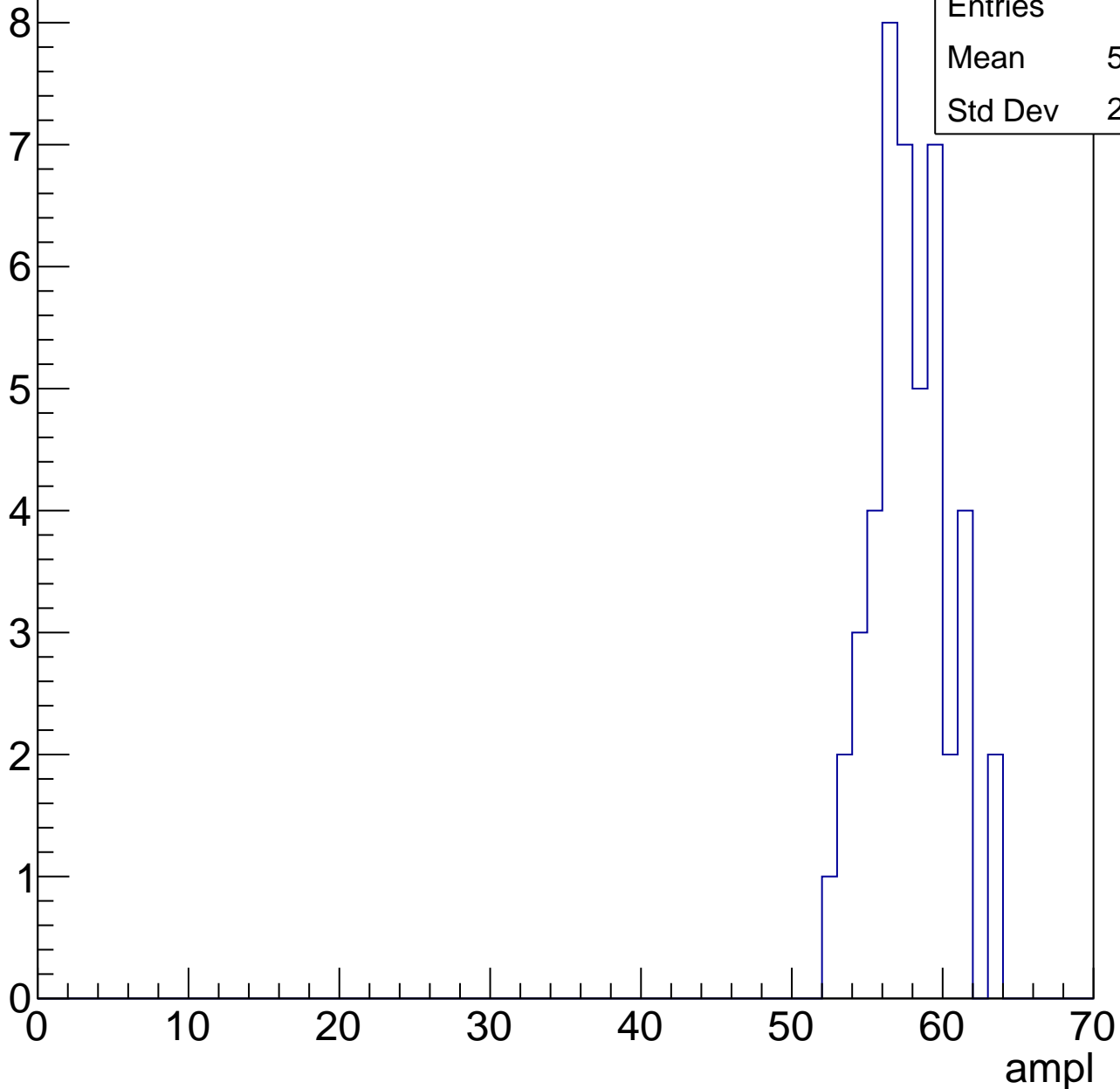


B0L001S, U22-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	57.33
Std Dev	2.539

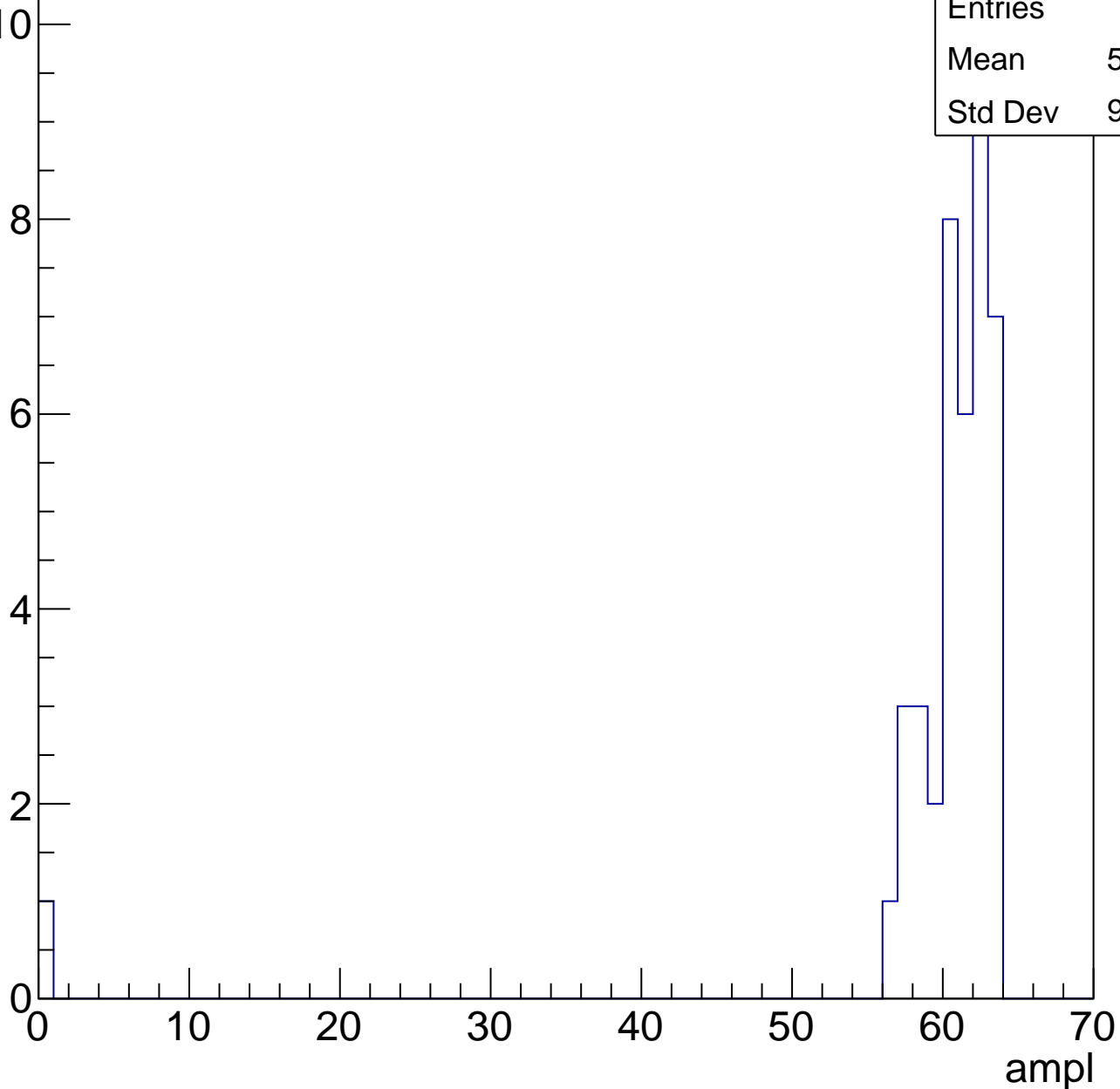


B0L001S, U22-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	59.17
Std Dev	9.548



B0L001S, U22-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch81, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	32.37
Std Dev	3.404

Gaus mean : 33.0369

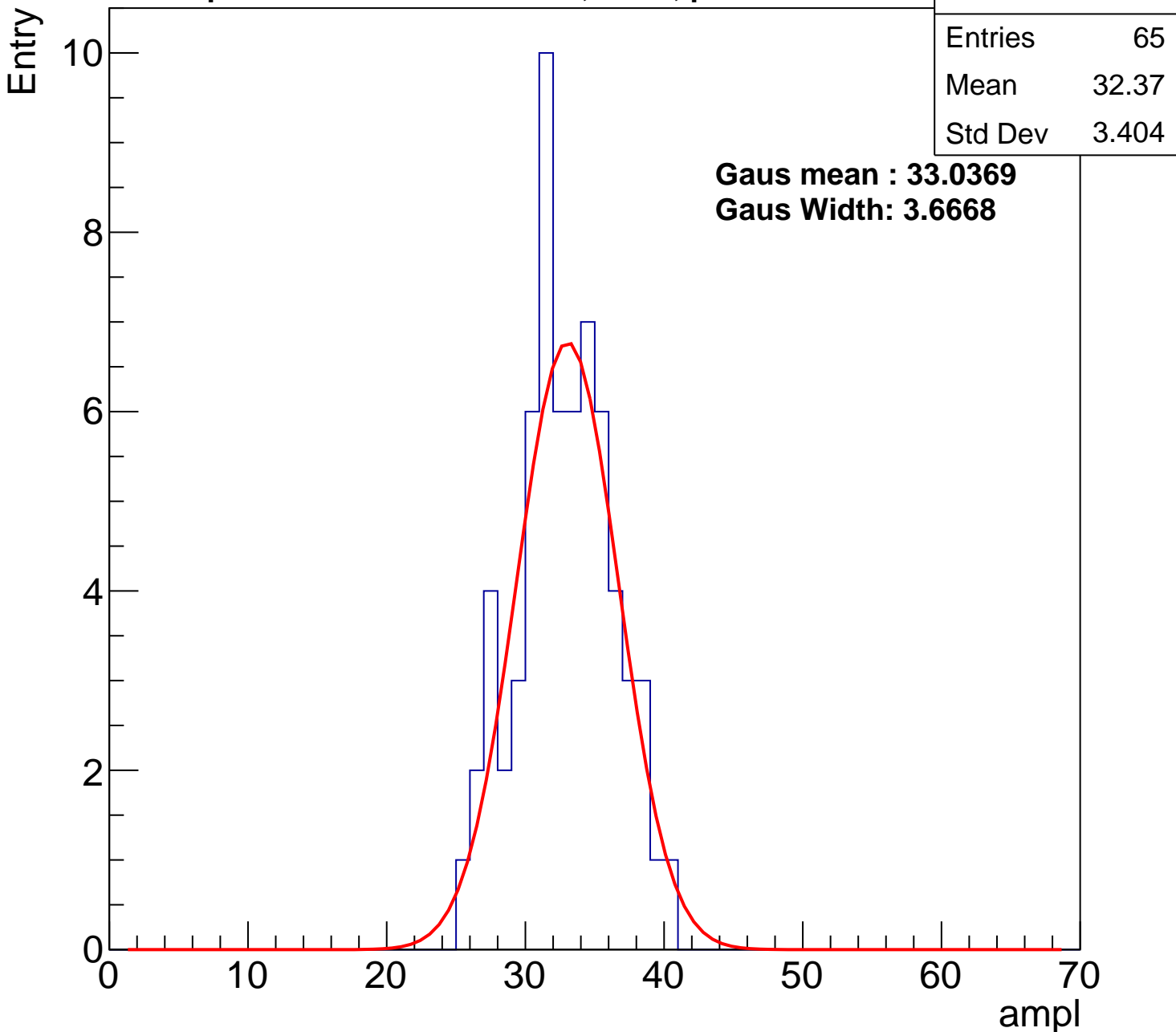
Gaus Width: 3.6668

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch81, adc1

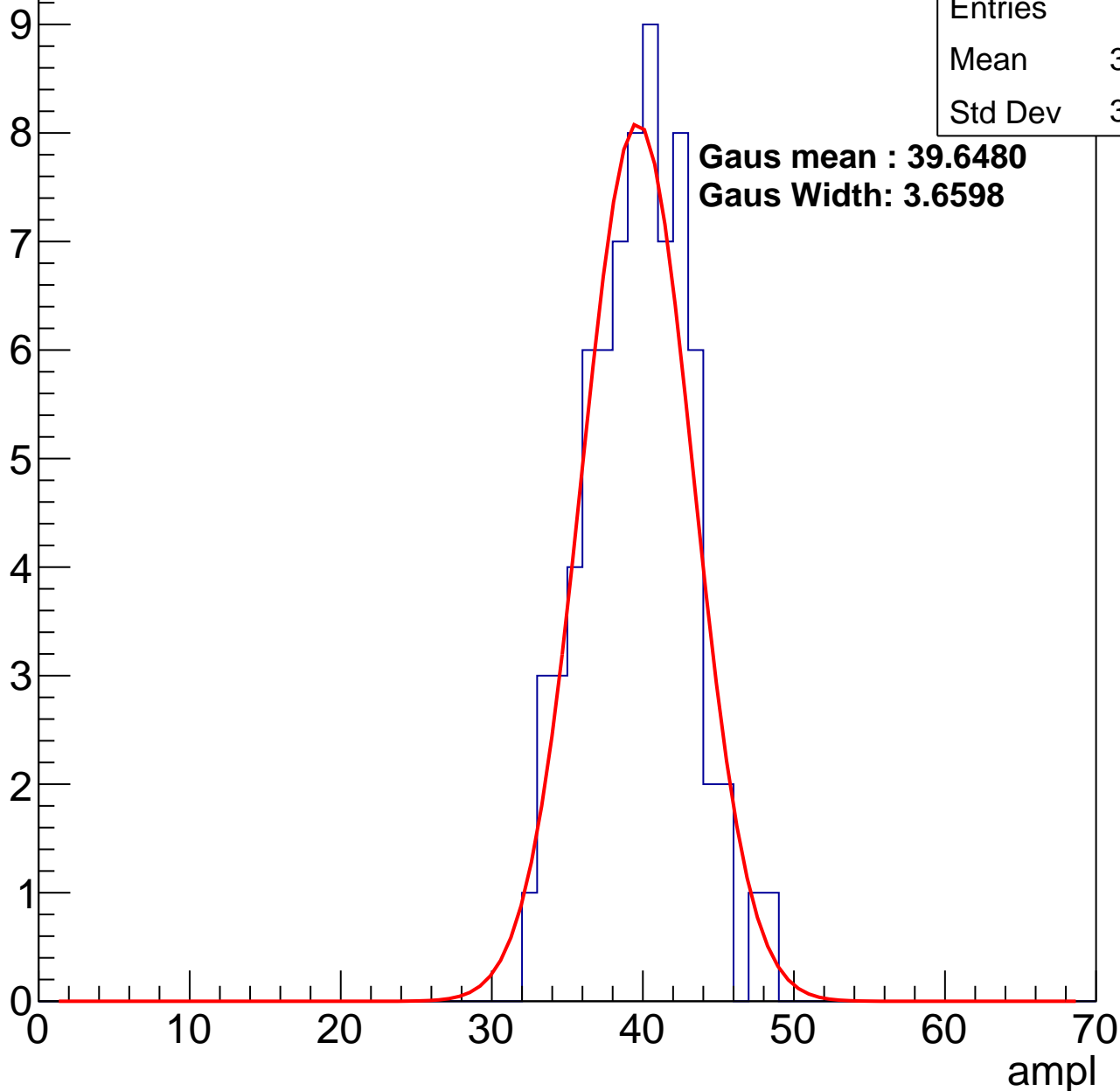
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	39.23
Std Dev	3.387

Gaus mean : 39.6480

Gaus Width: 3.6598



B0L001S, U22-ch81, adc2

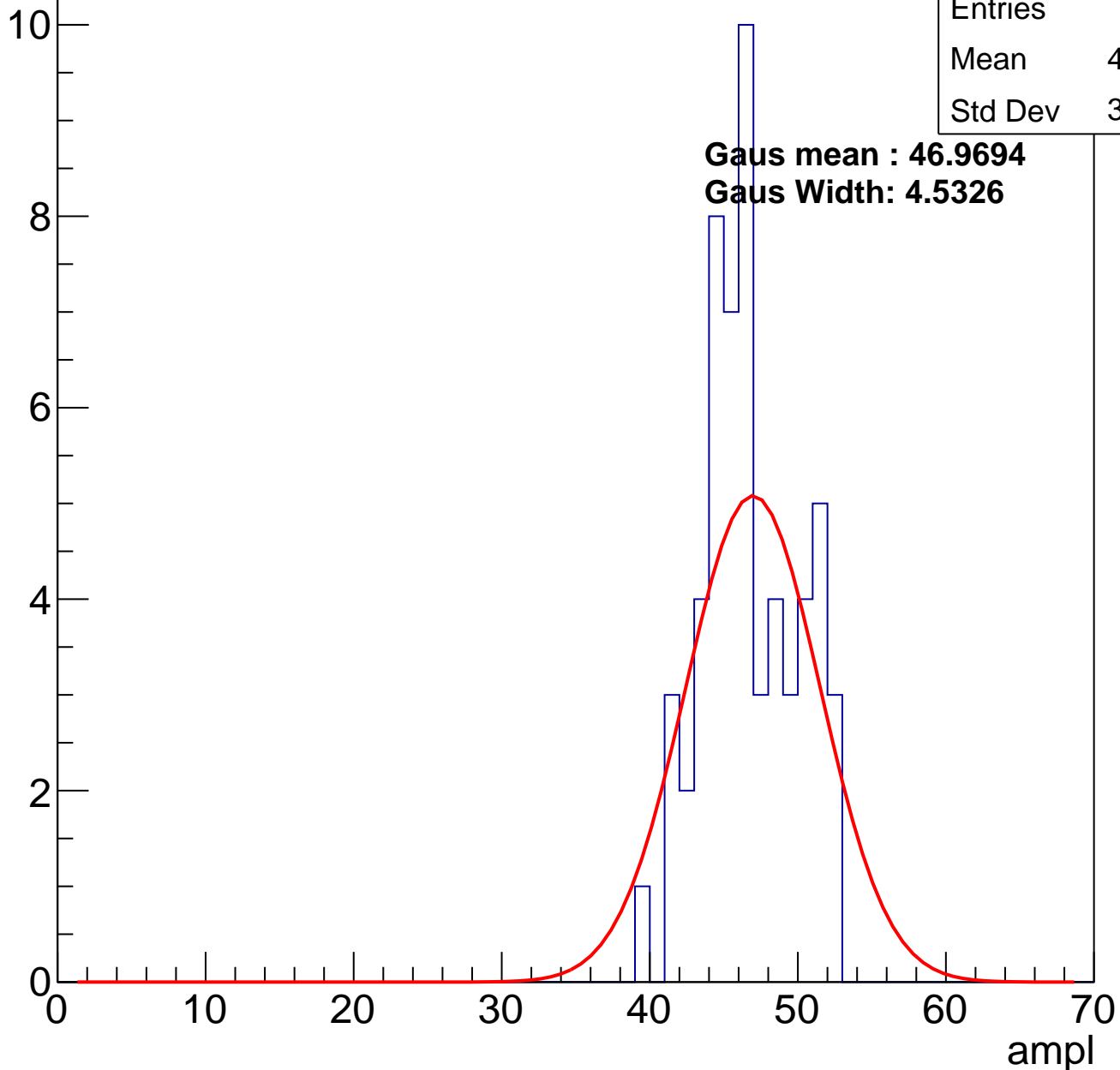
calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	46.25
Std Dev	3.164

Gaus mean : 46.9694

Gaus Width: 4.5326

Entry



B0L001S, U22-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	52.96
Std Dev	3.785

Entry

10

8

6

4

2

0

0

10

20

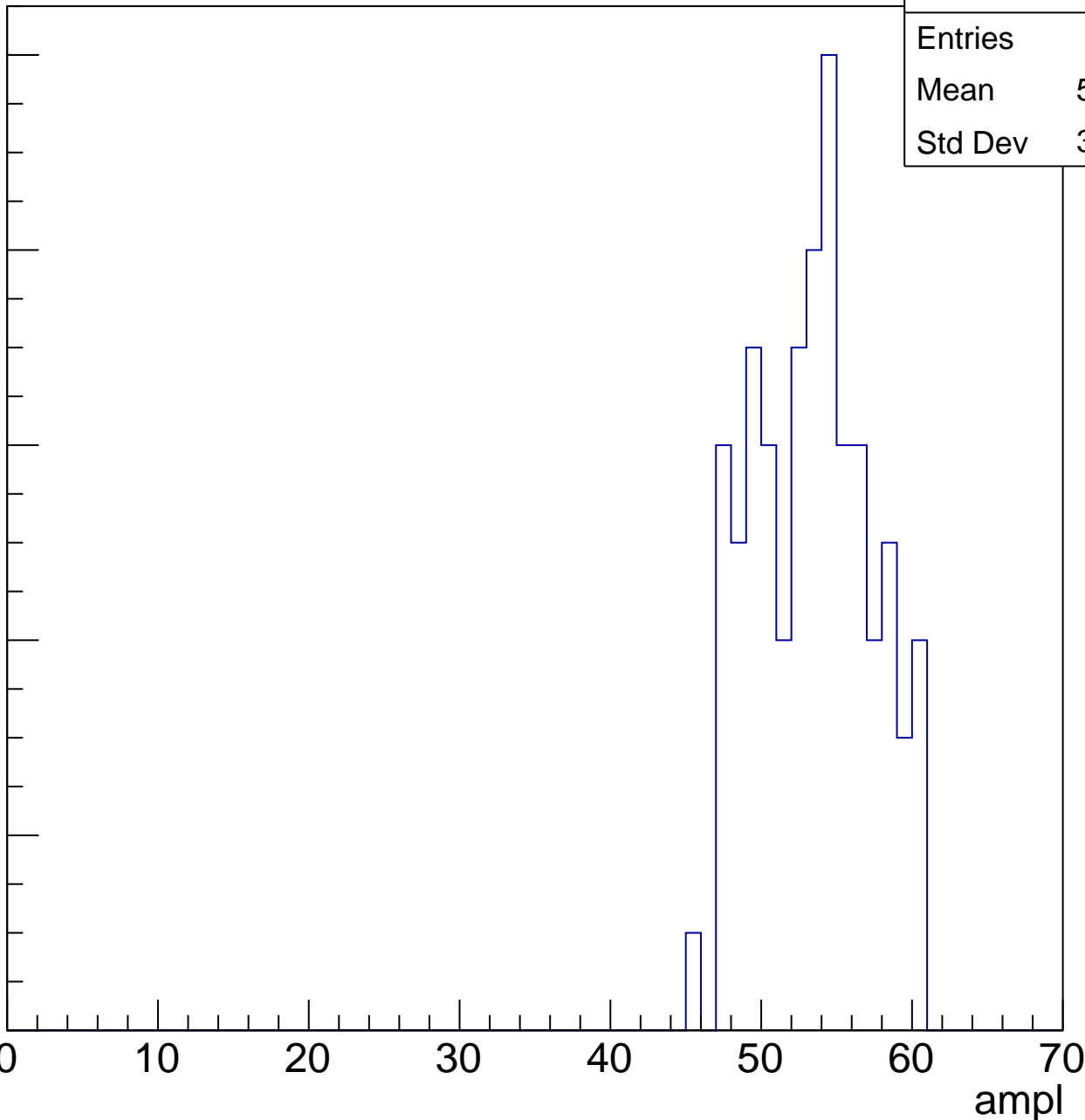
30

40

50

60

ampl

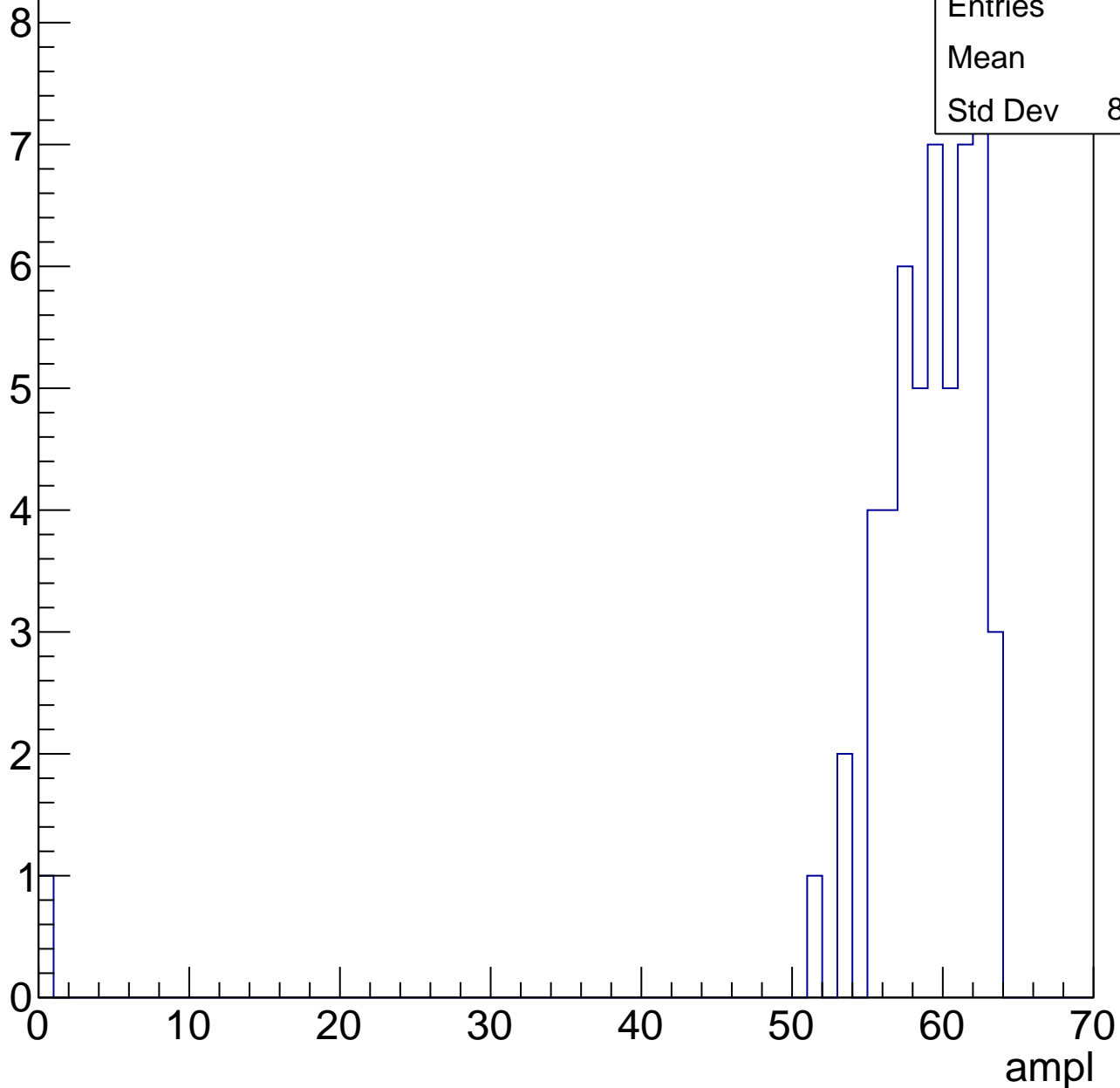


B0L001S, U22-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.7
Std Dev	8.478

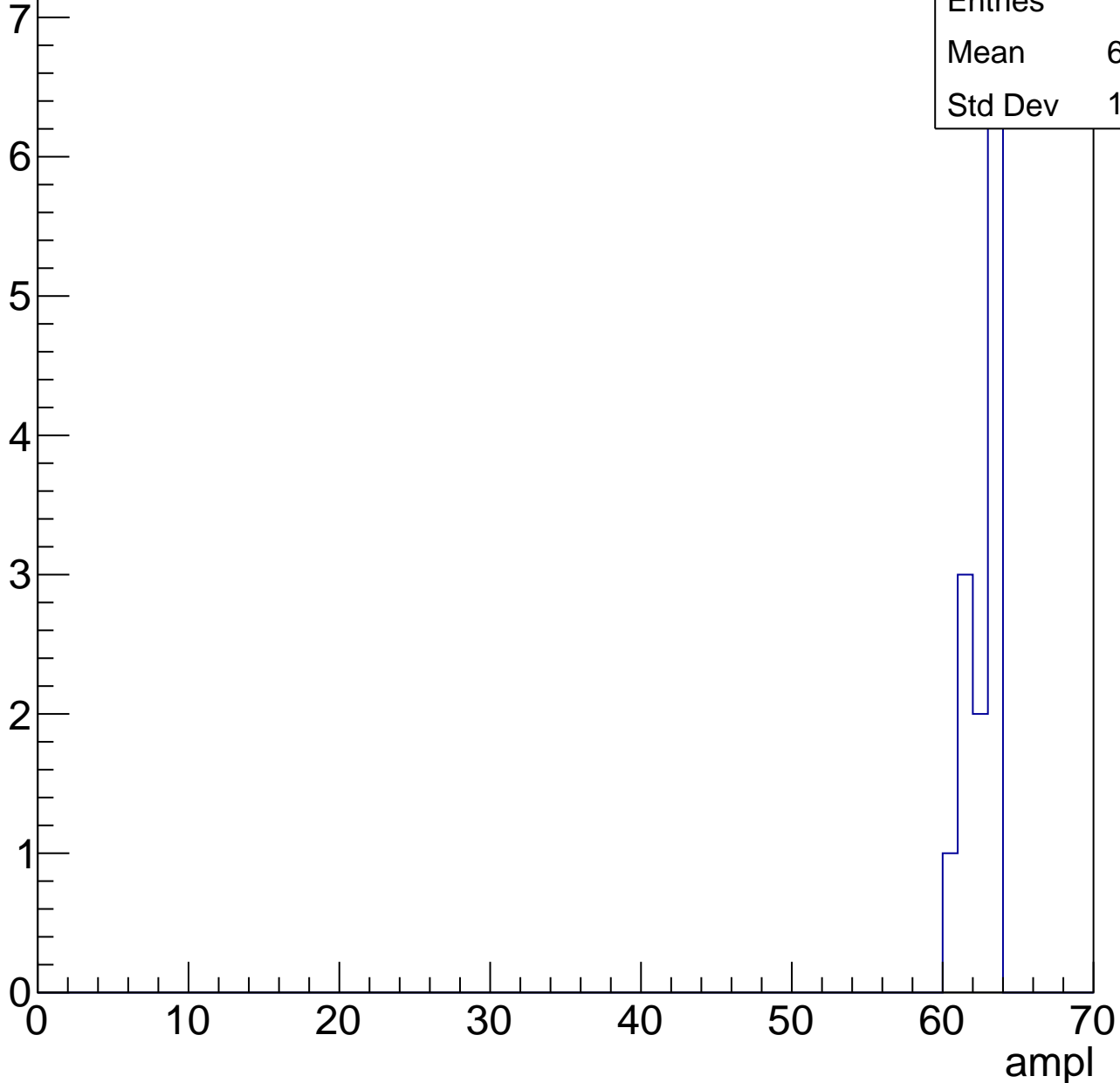


B0L001S, U22-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	13
Mean	62.15
Std Dev	1.026



B0L001S, U22-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B0L001S, U22-ch82, adc0

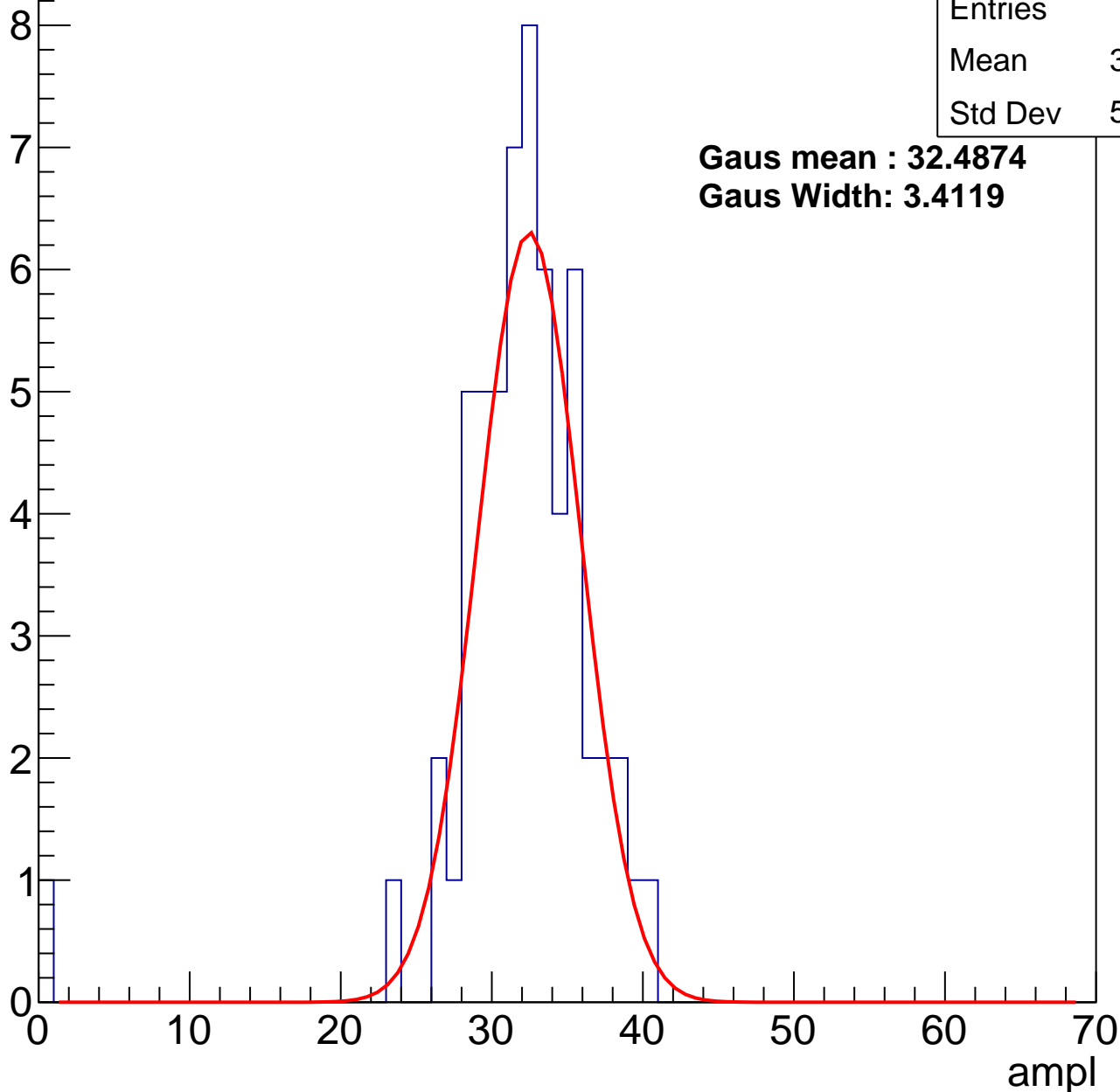
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	31.44
Std Dev	5.334

Gaus mean : 32.4874

Gaus Width: 3.4119



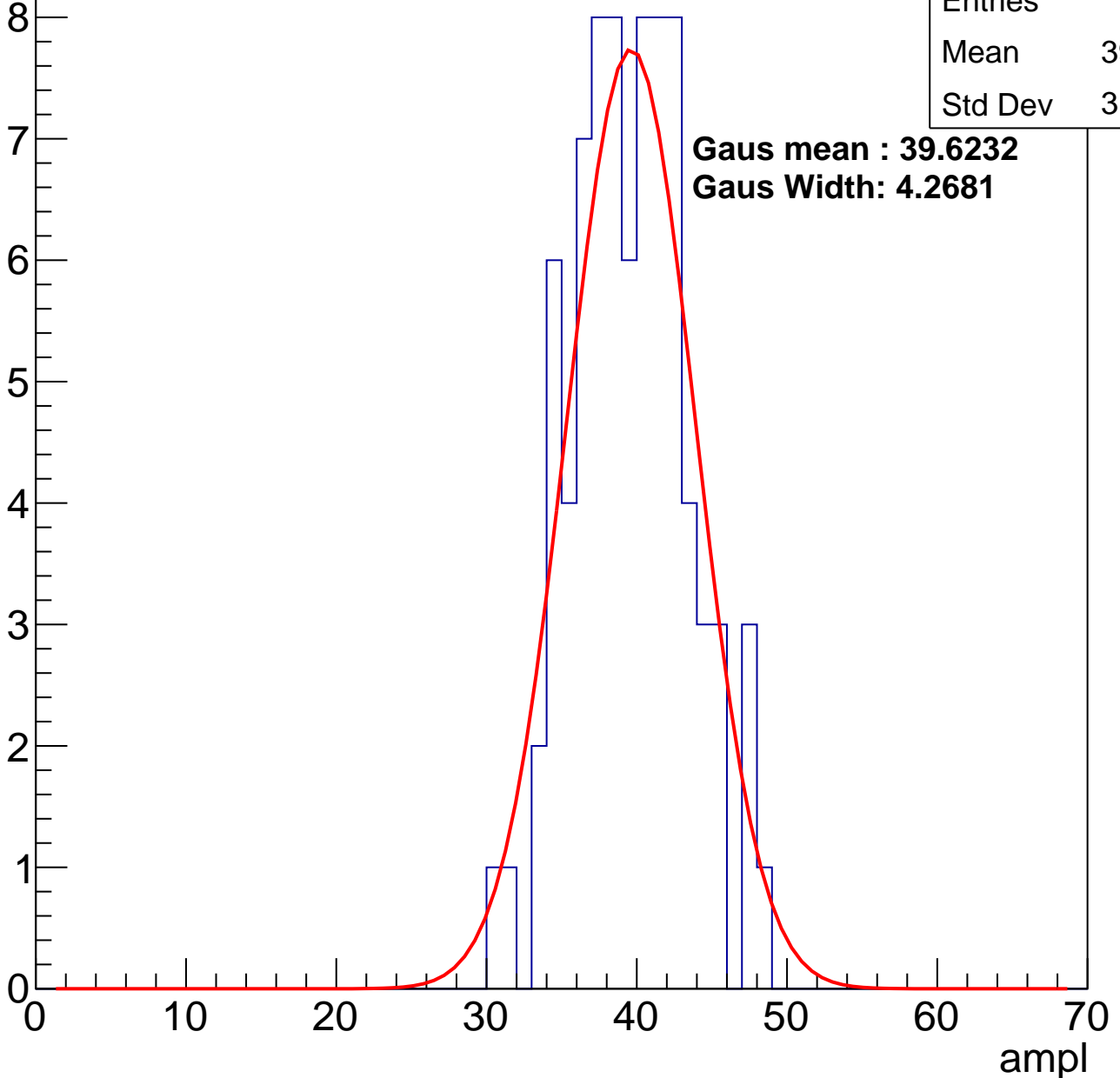
B0L001S, U22-ch82, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	39.12
Std Dev	3.792

Gaus mean : 39.6232
Gaus Width: 4.2681



B0L001S, U22-ch82, adc2

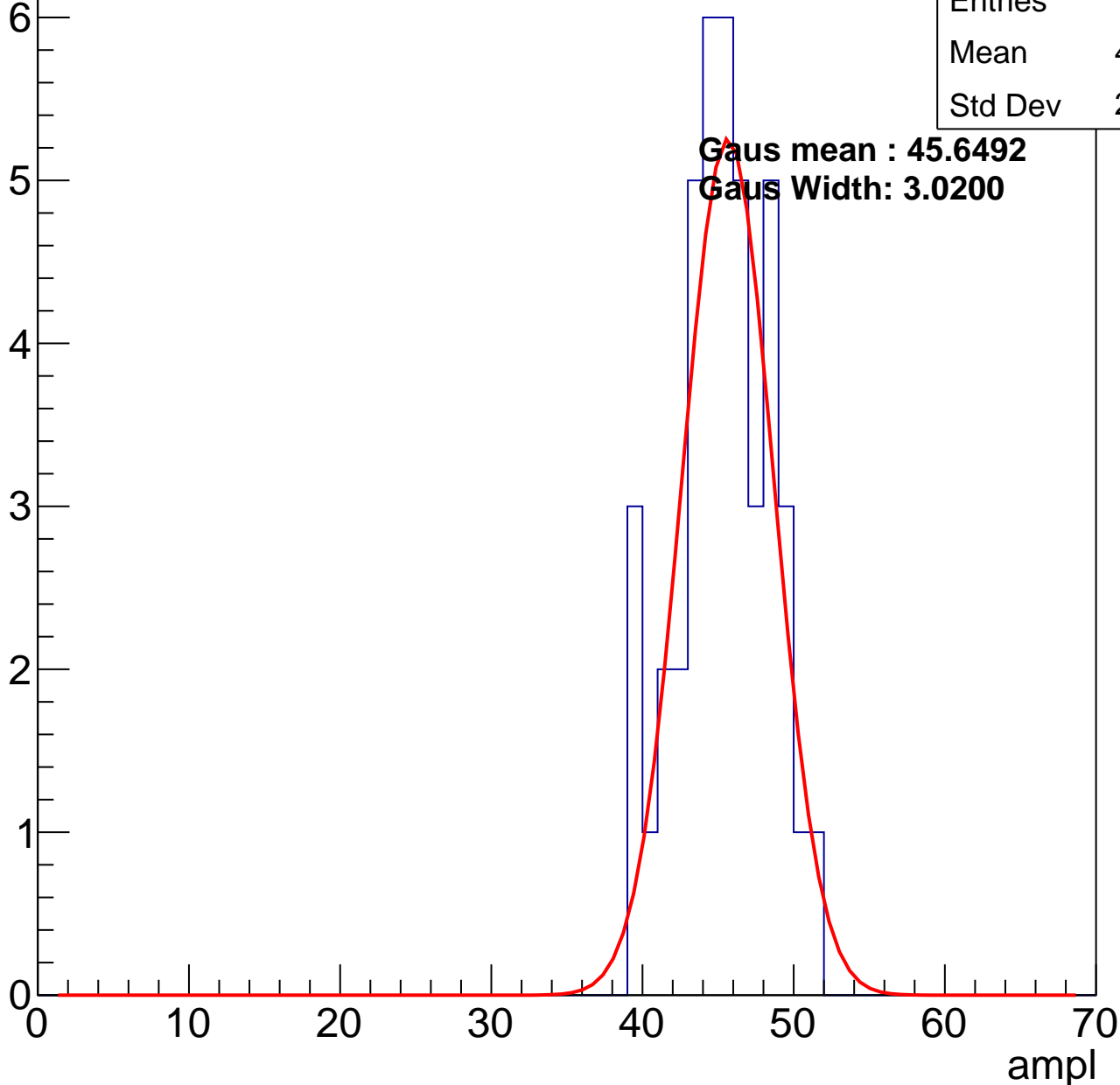
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	44.91
Std Dev	2.971

Gaus mean : 45.6492

Gaus Width: 3.0200



B0L001S, U22-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

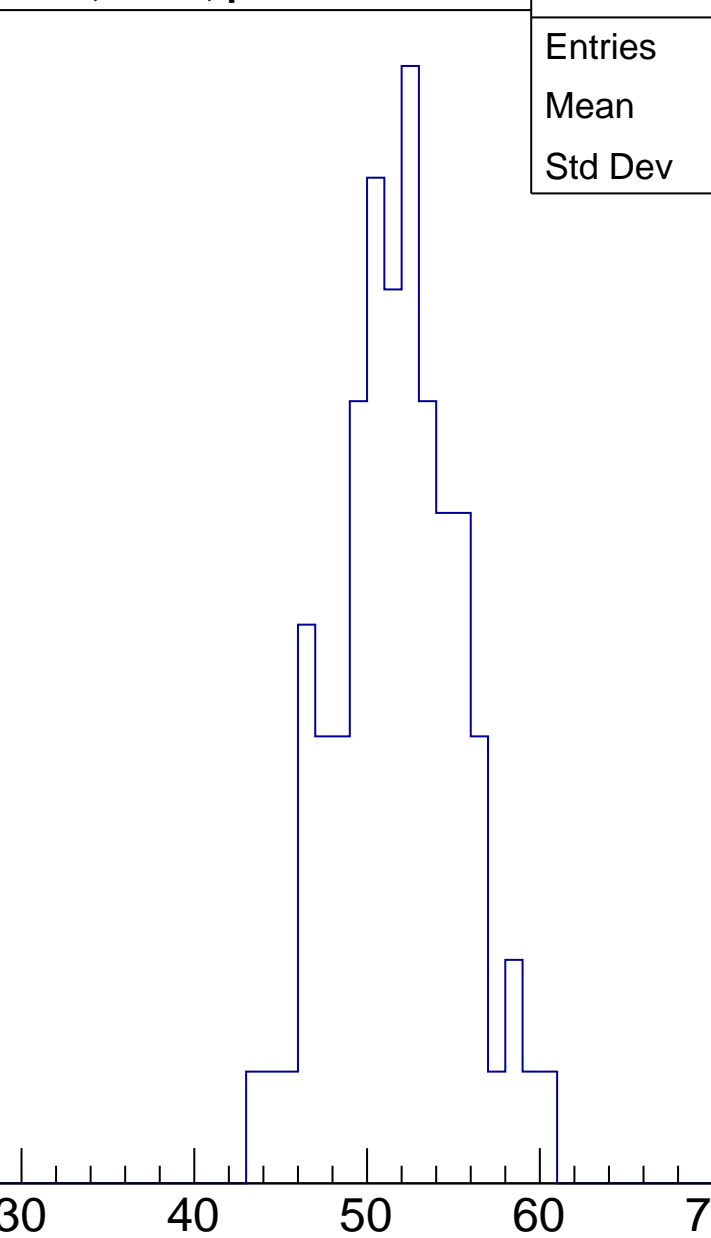
Entries	78
Mean	51.33
Std Dev	3.525

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

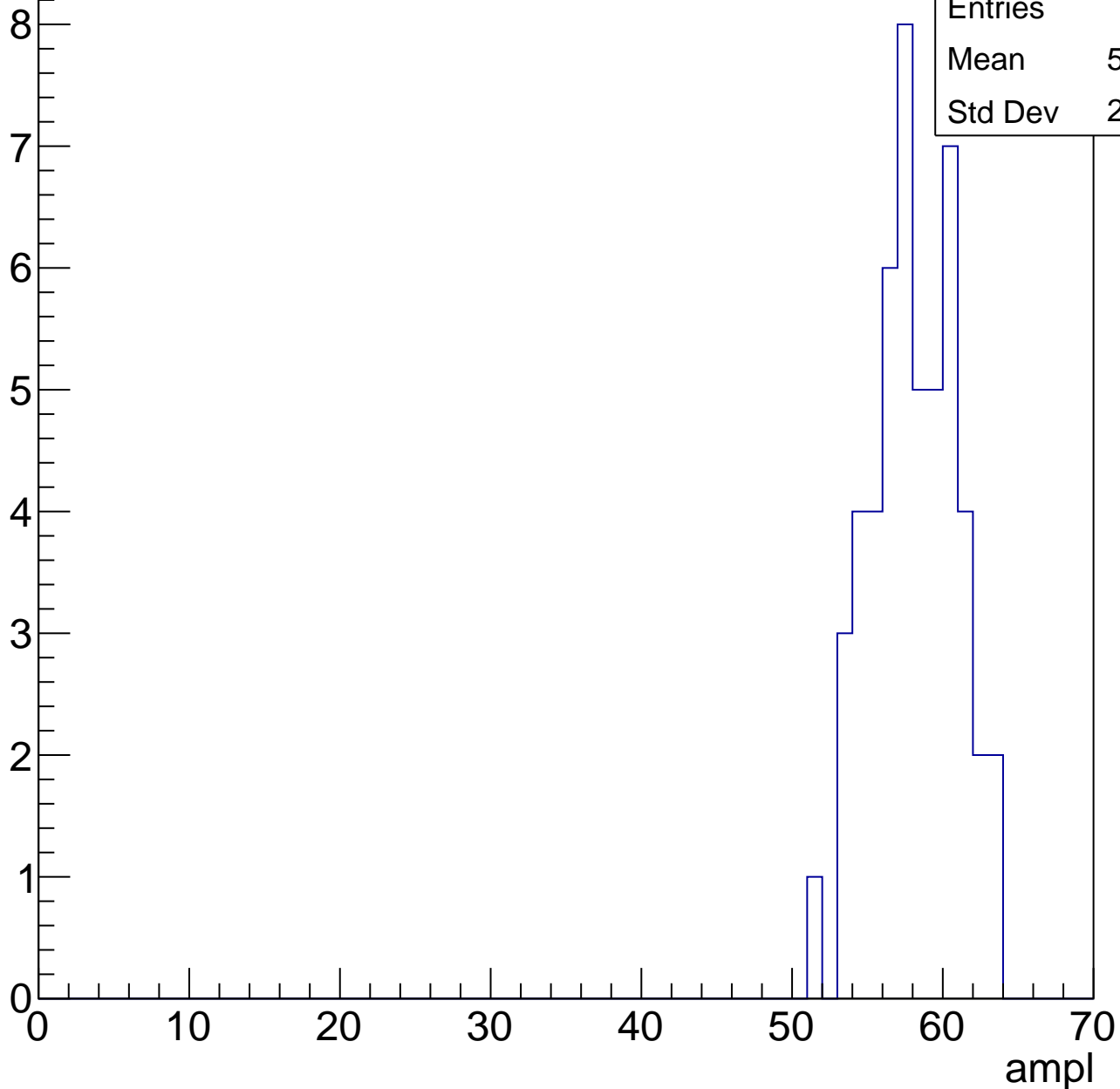
ampl



B0L001S, U22-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



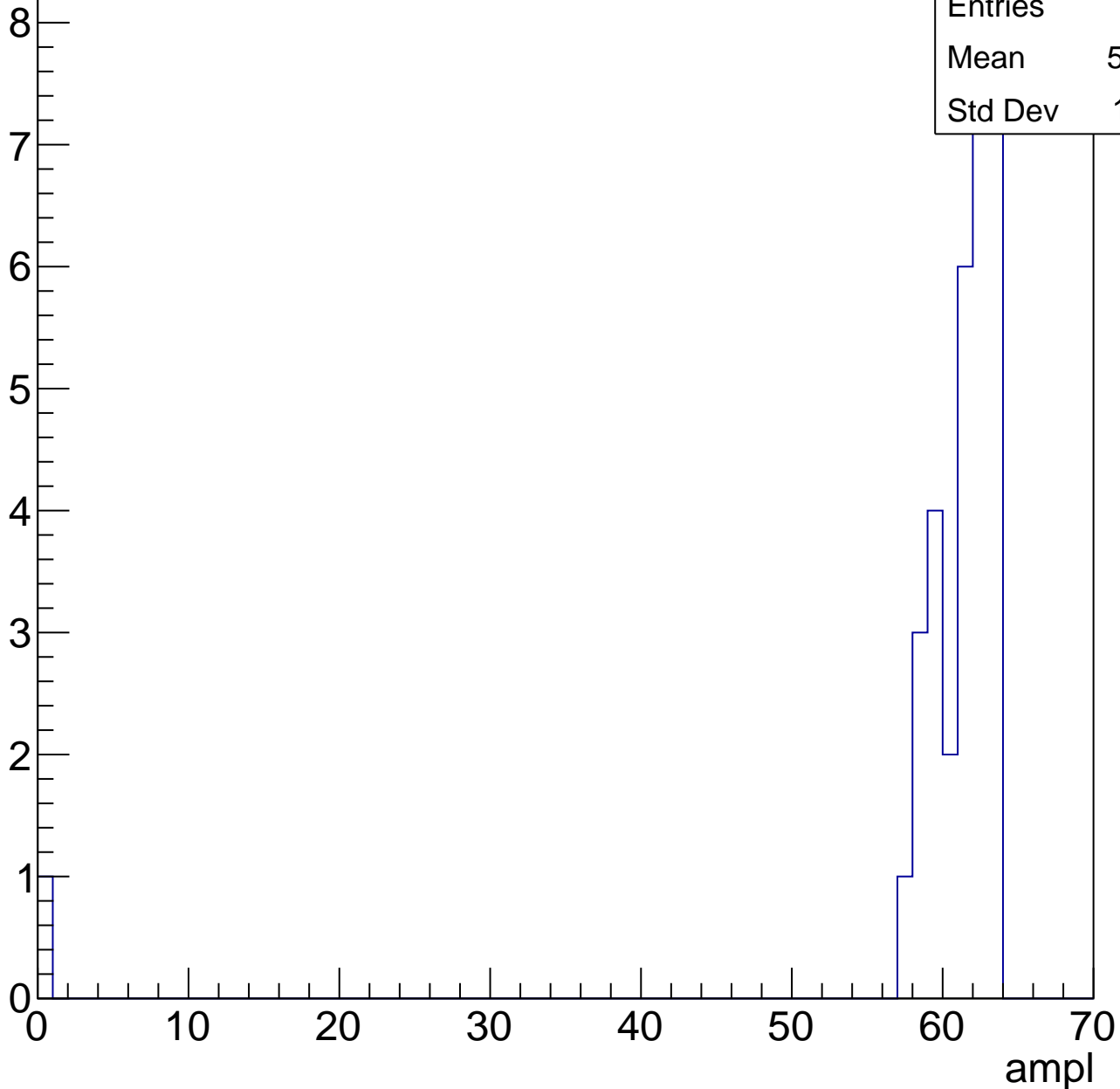
Entries	51
Mean	57.59
Std Dev	2.795

B0L001S, U22-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	59.18
Std Dev	10.61



B0L001S, U22-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch83, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.89
Std Dev	4.692

Gaus mean : 31.8169

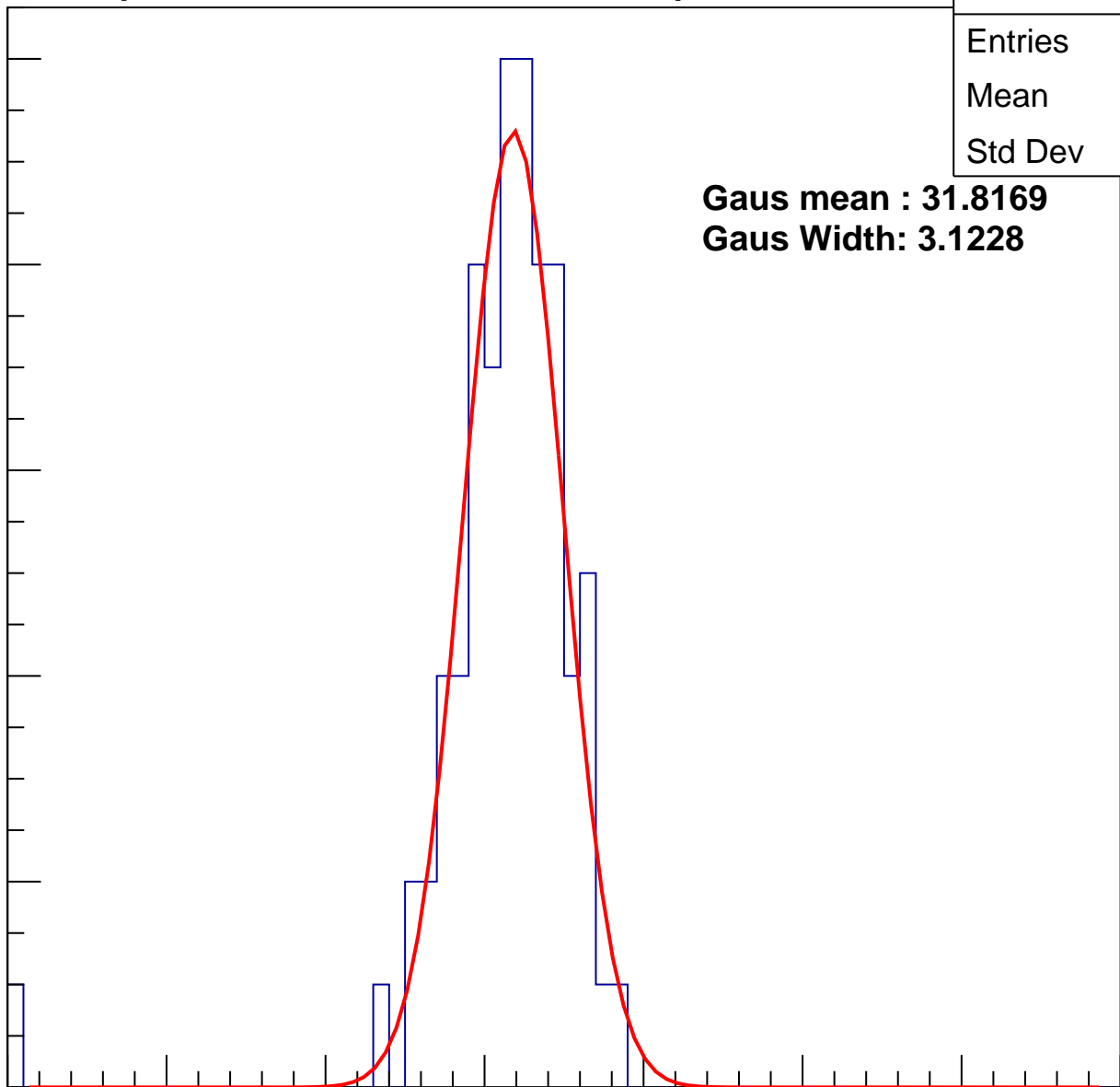
Gaus Width: 3.1228

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch83, adc1

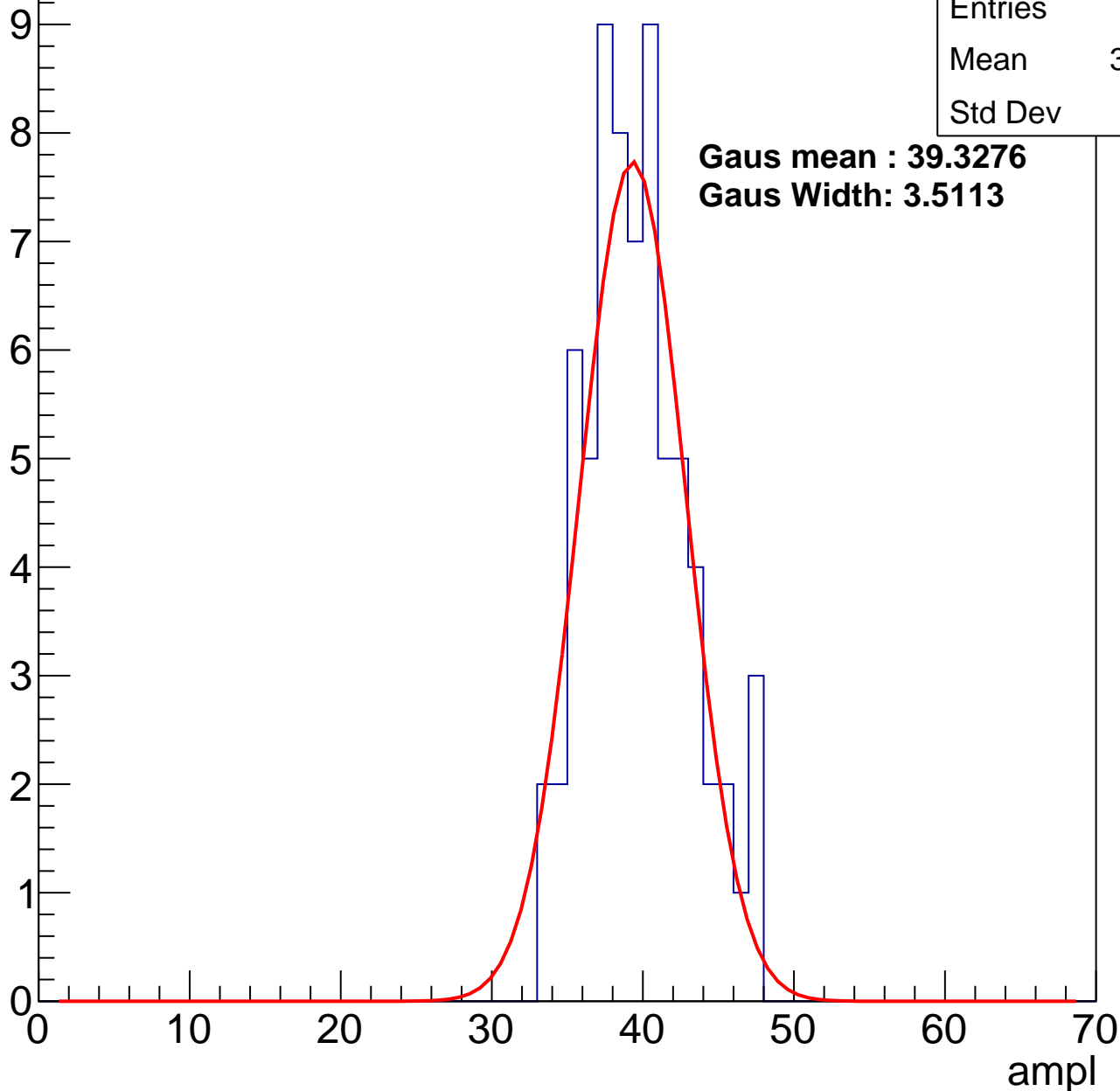
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	39.23
Std Dev	3.39

Gaus mean : 39.3276

Gaus Width: 3.5113



B0L001S, U22-ch83, adc2

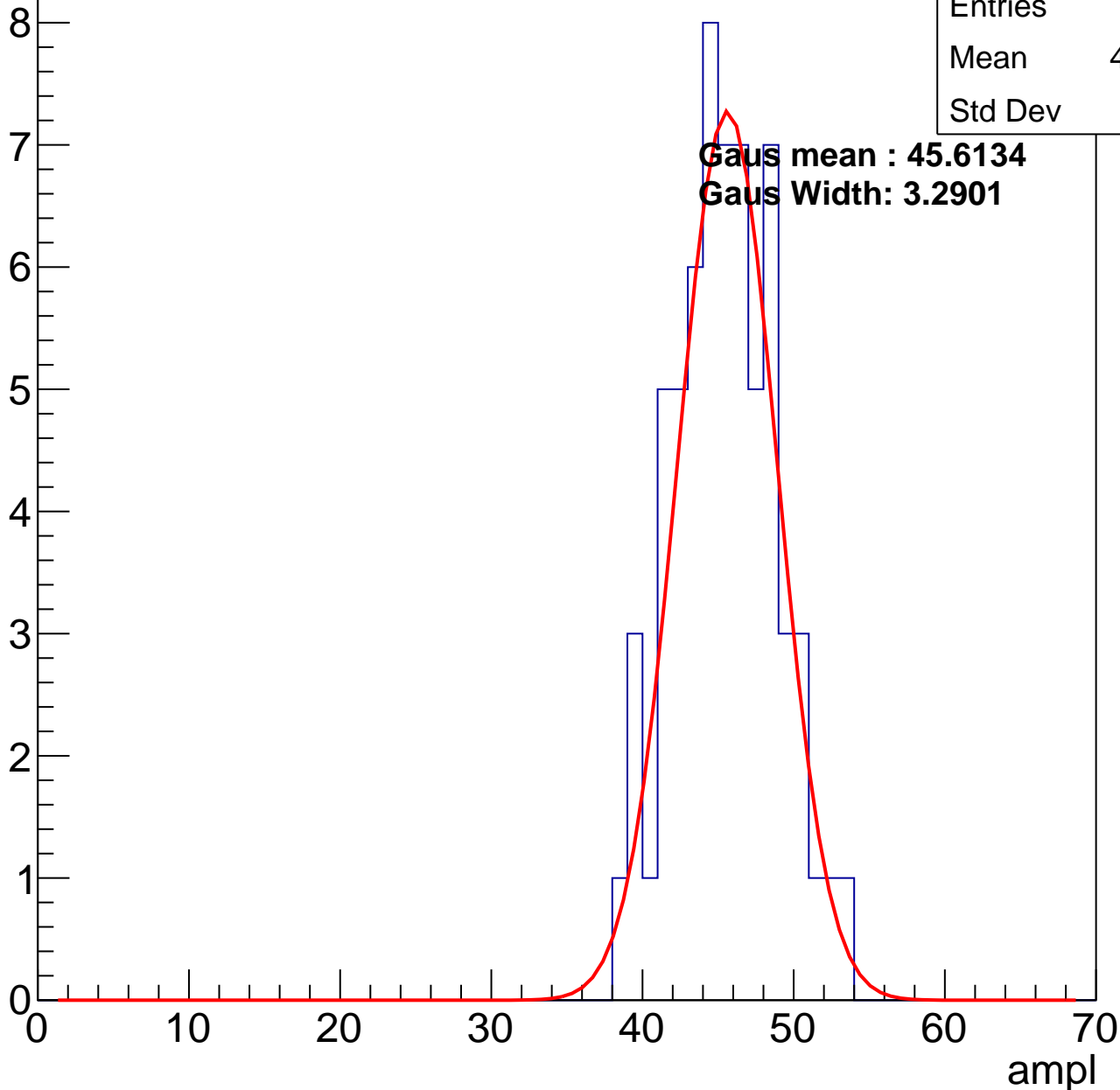
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.02
Std Dev	3.31

Gaus mean : 45.6134

Gaus Width: 3.2901

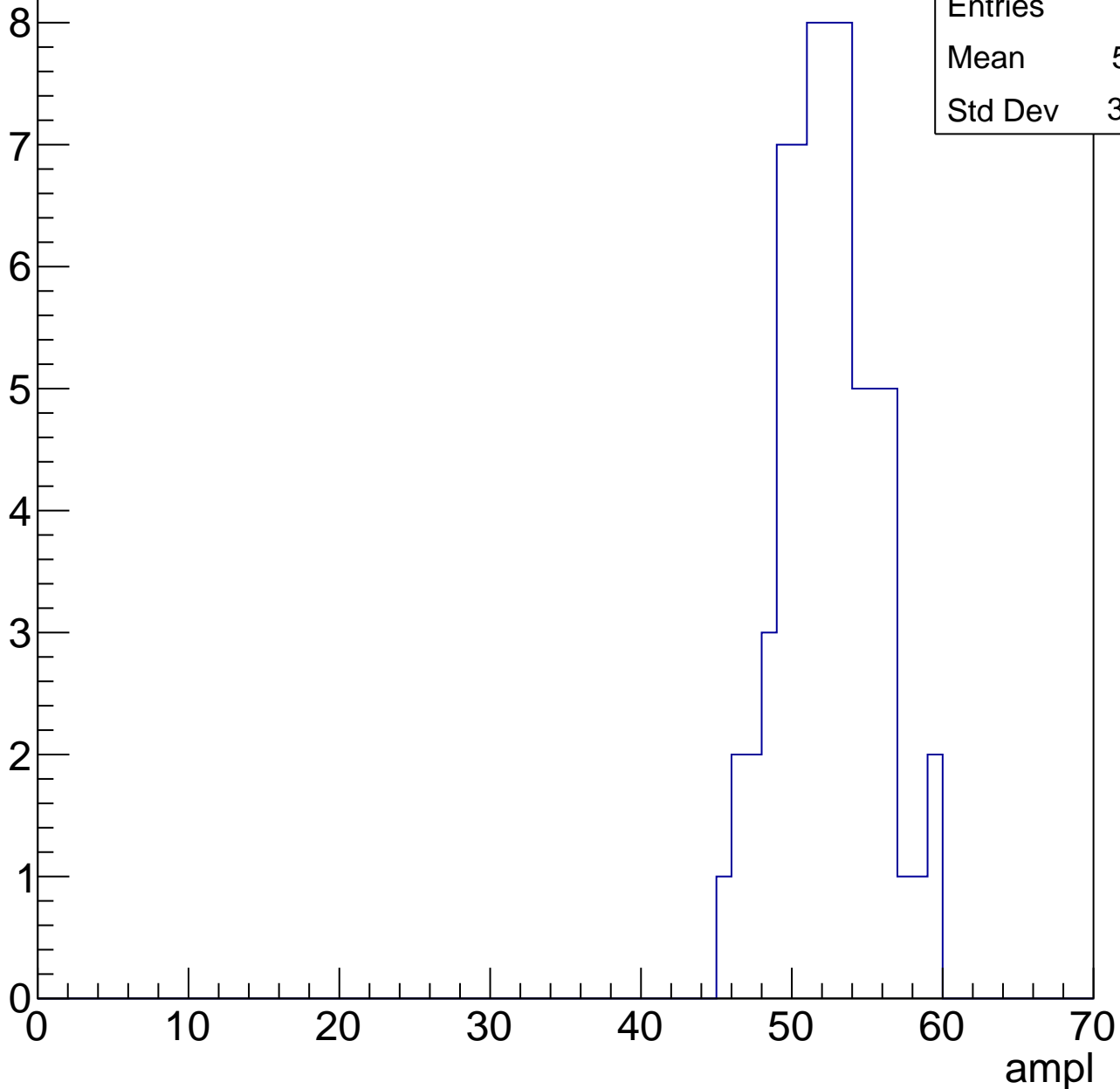


B0L001S, U22-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	51.91
Std Dev	3.112

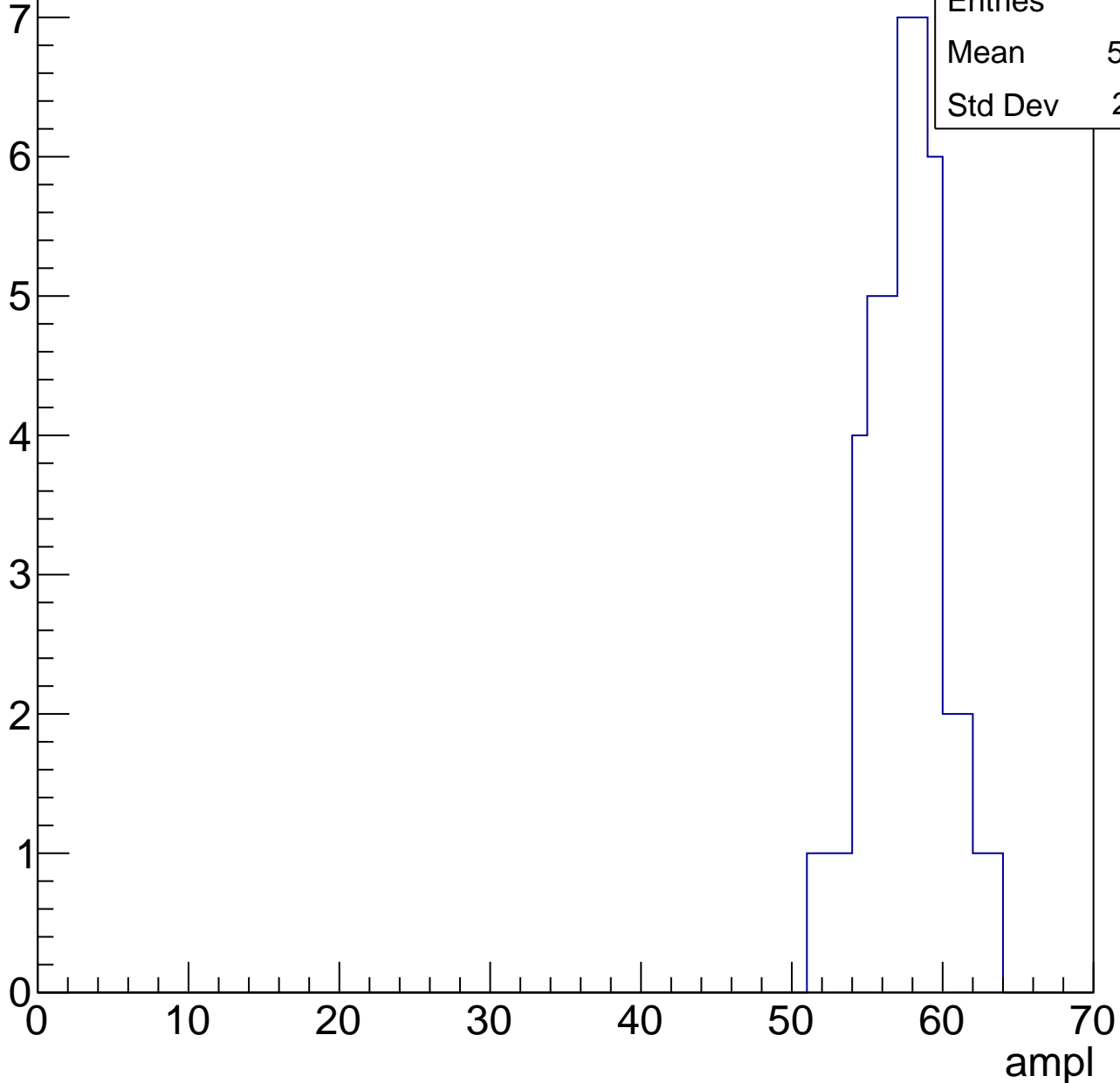


B0L001S, U22-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	57.05
Std Dev	2.551



B0L001S, U22-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.25
Std Dev	9.687

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

B0L001S, U22-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch84, adc0

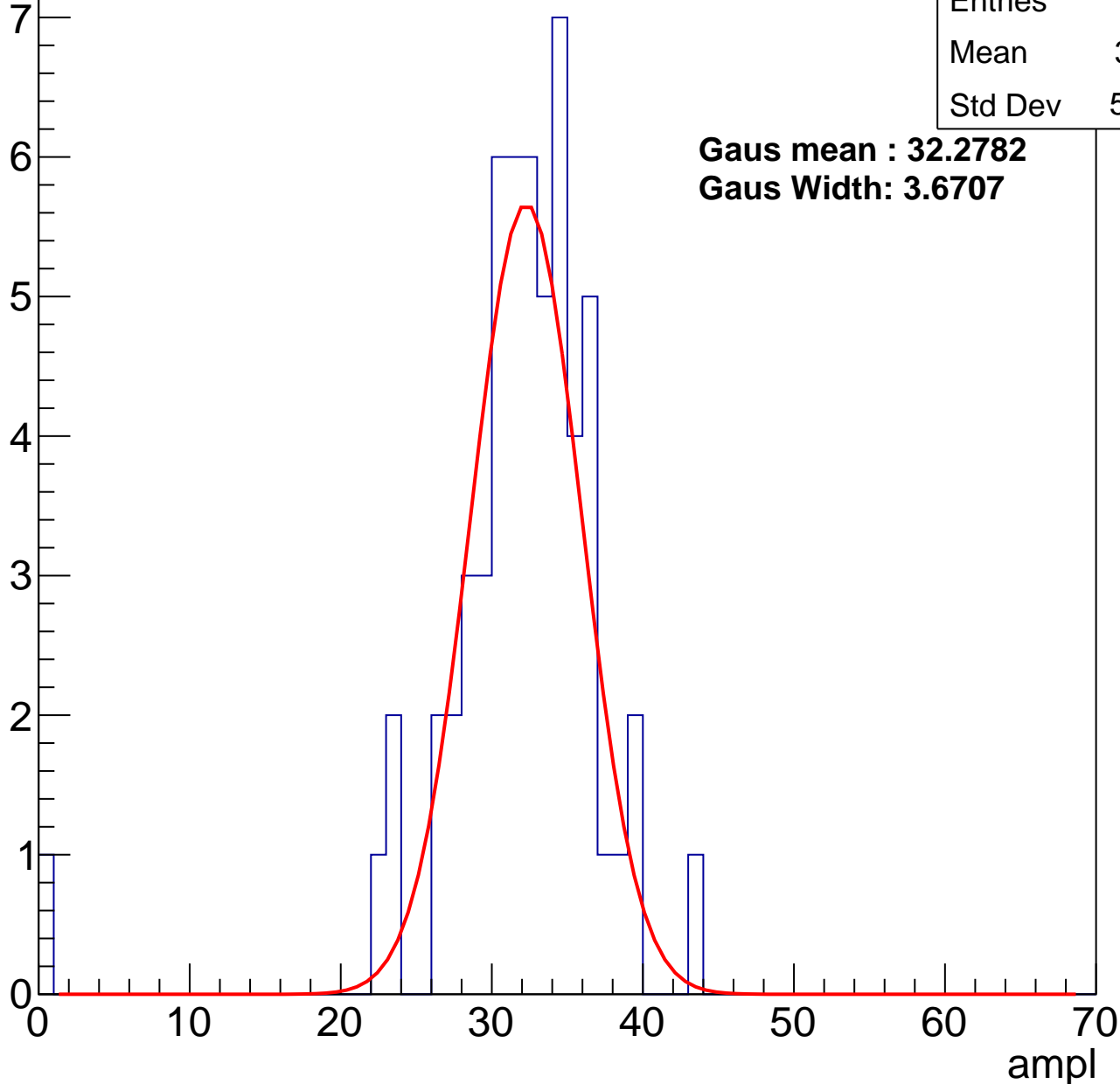
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	31.41
Std Dev	5.757

Gaus mean : 32.2782

Gaus Width: 3.6707



B0L001S, U22-ch84, adc1

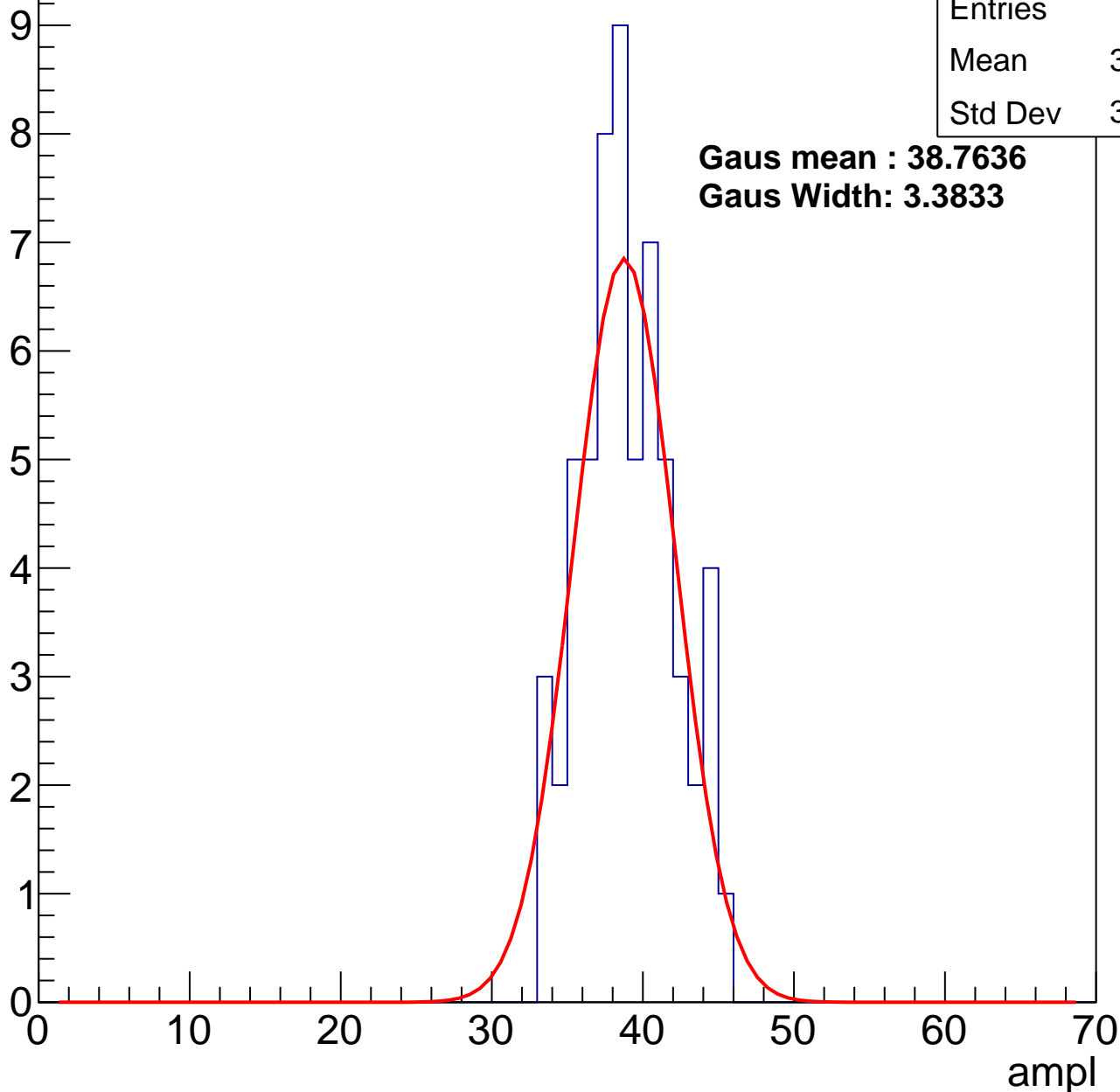
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	38.53
Std Dev	3.005

Gaus mean : 38.7636

Gaus Width: 3.3833



B0L001S, U22-ch84, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	45.12
Std Dev	3.51

Gaus mean : 45.2890

Gaus Width: 3.8160

Entry

10

8

6

4

2

0

0

10

20

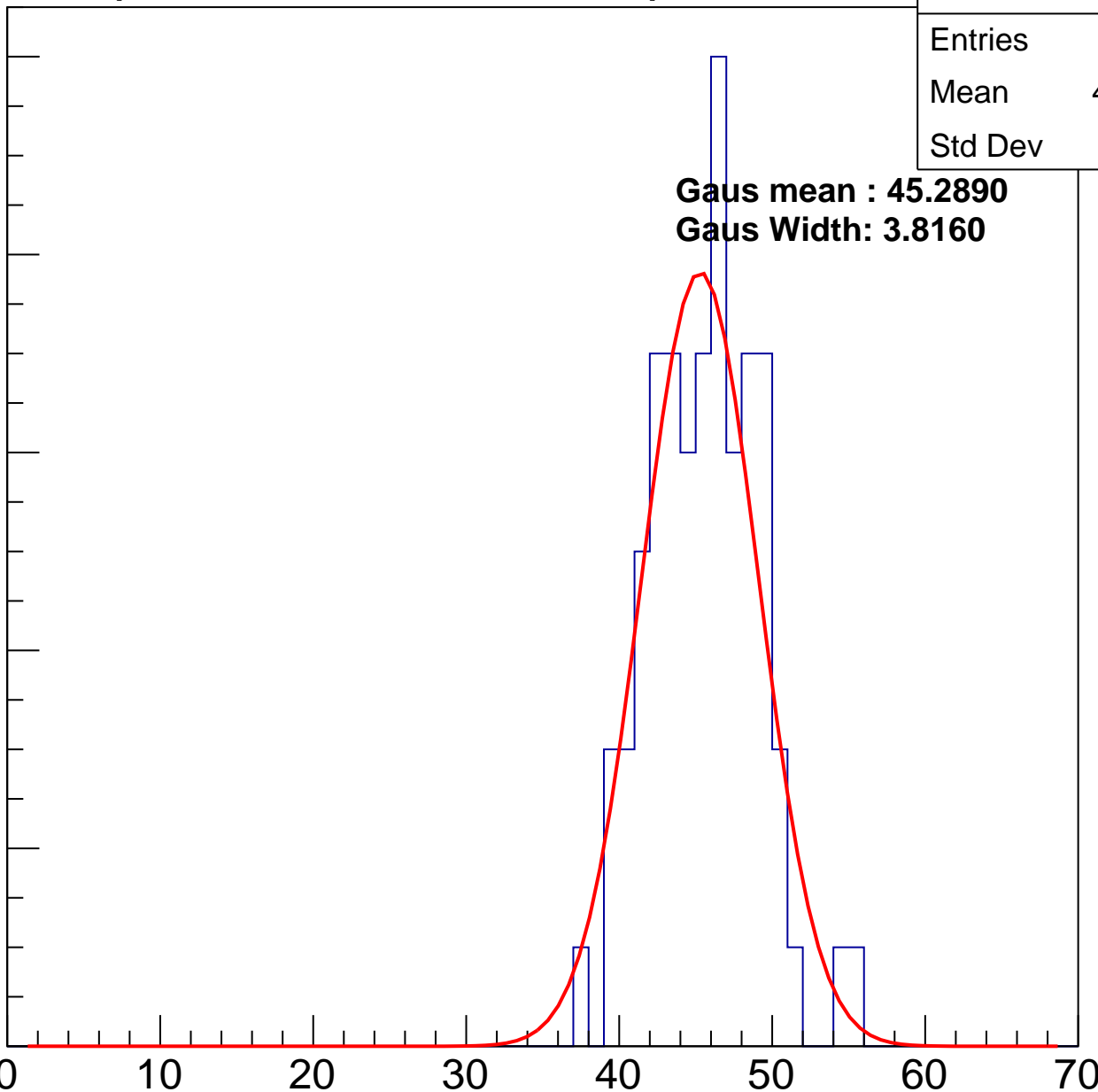
30

40

50

60

ampl

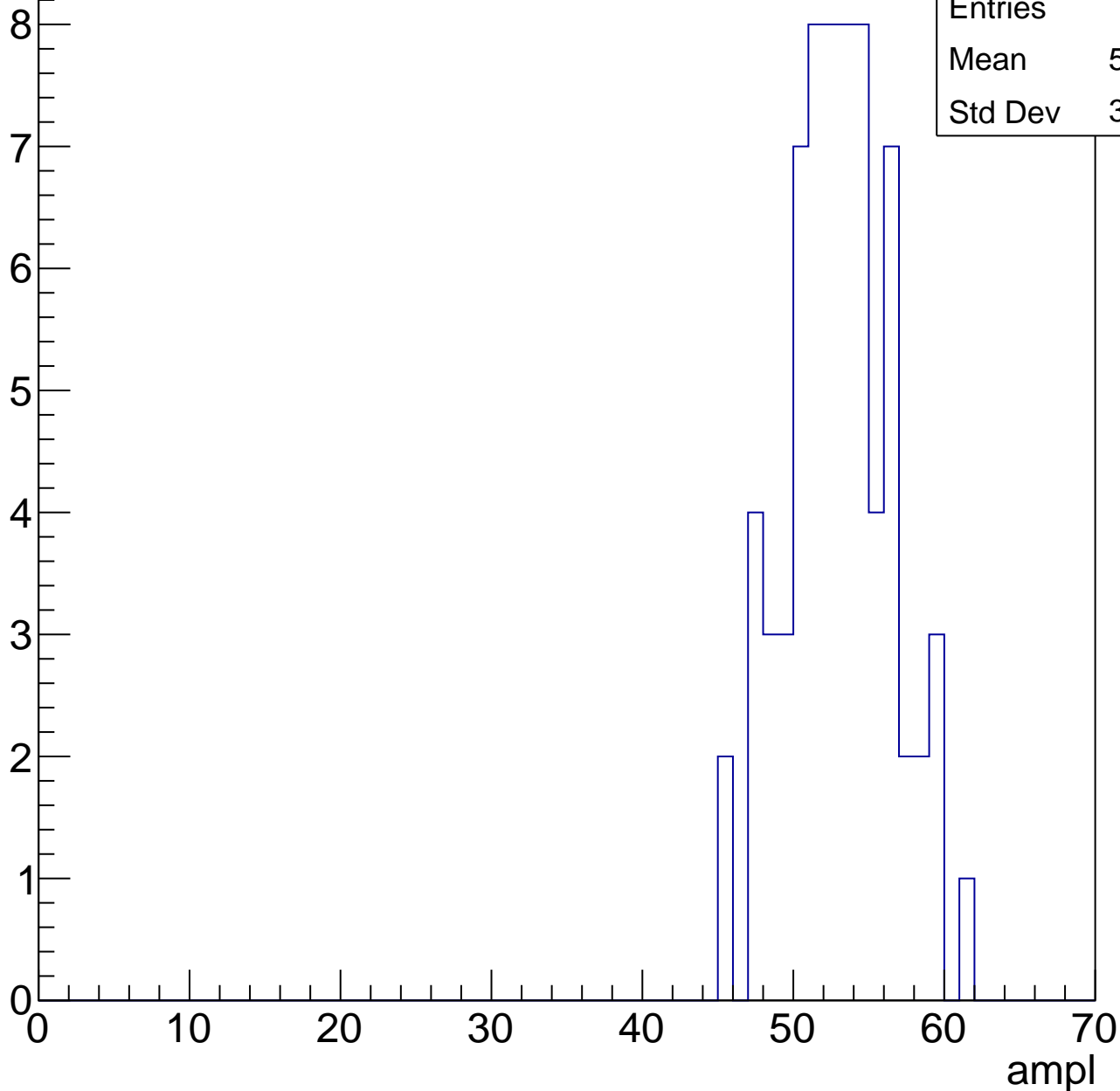


B0L001S, U22-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	52.56
Std Dev	3.434



B0L001S, U22-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

Entries	49
Mean	58.12
Std Dev	3.014

ampl

10

20

30

40

50

60

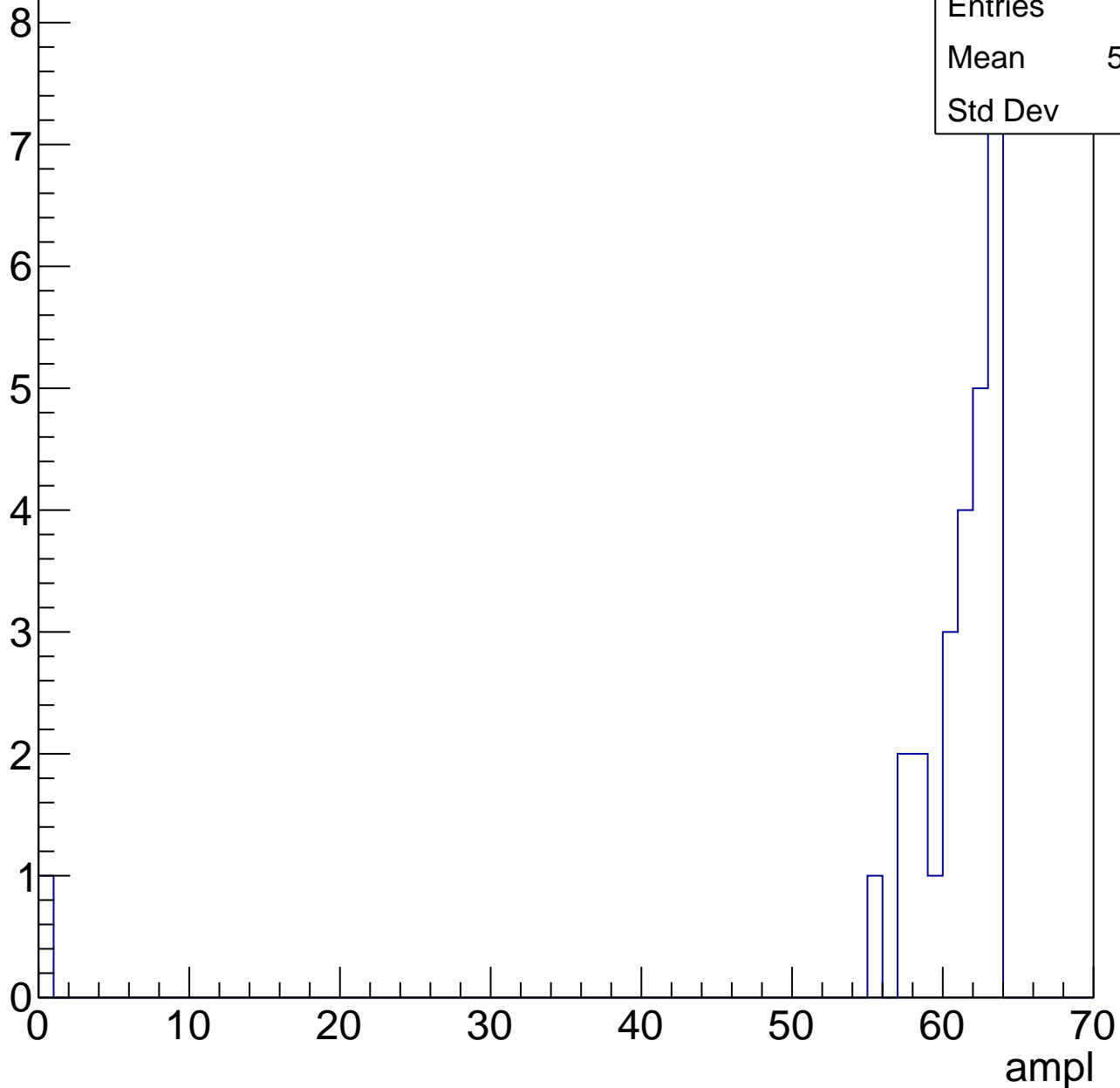
70

B0L001S, U22-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	27
Mean	58.59
Std Dev	11.7



B0L001S, U22-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

60

Std Dev

0

B0L001S, U22-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch85, adc0

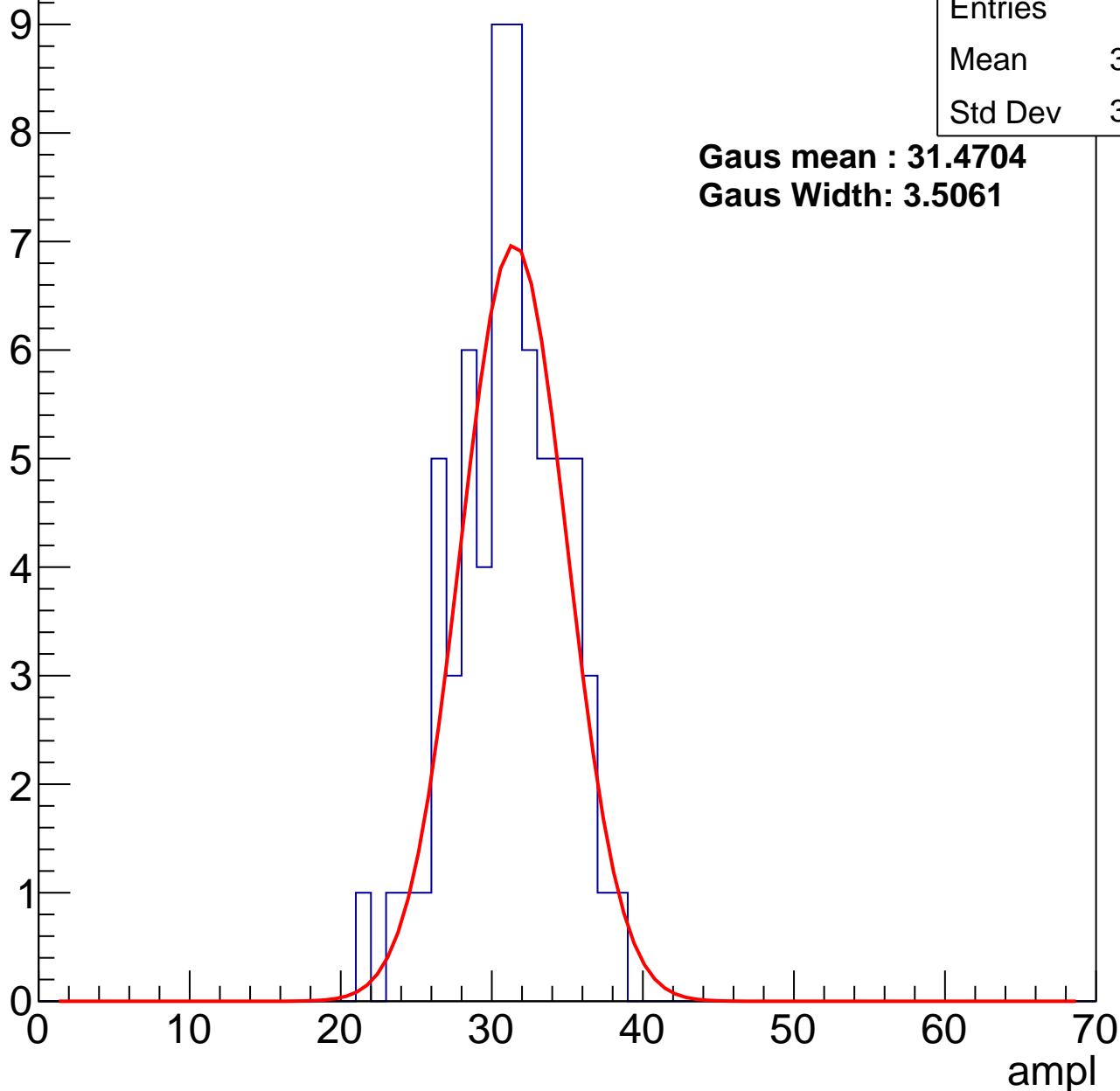
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.64
Std Dev	3.489

Gaus mean : 31.4704

Gaus Width: 3.5061



B0L001S, U22-ch85, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	88
Mean	37.99
Std Dev	3.973

Gaus mean : 38.6822

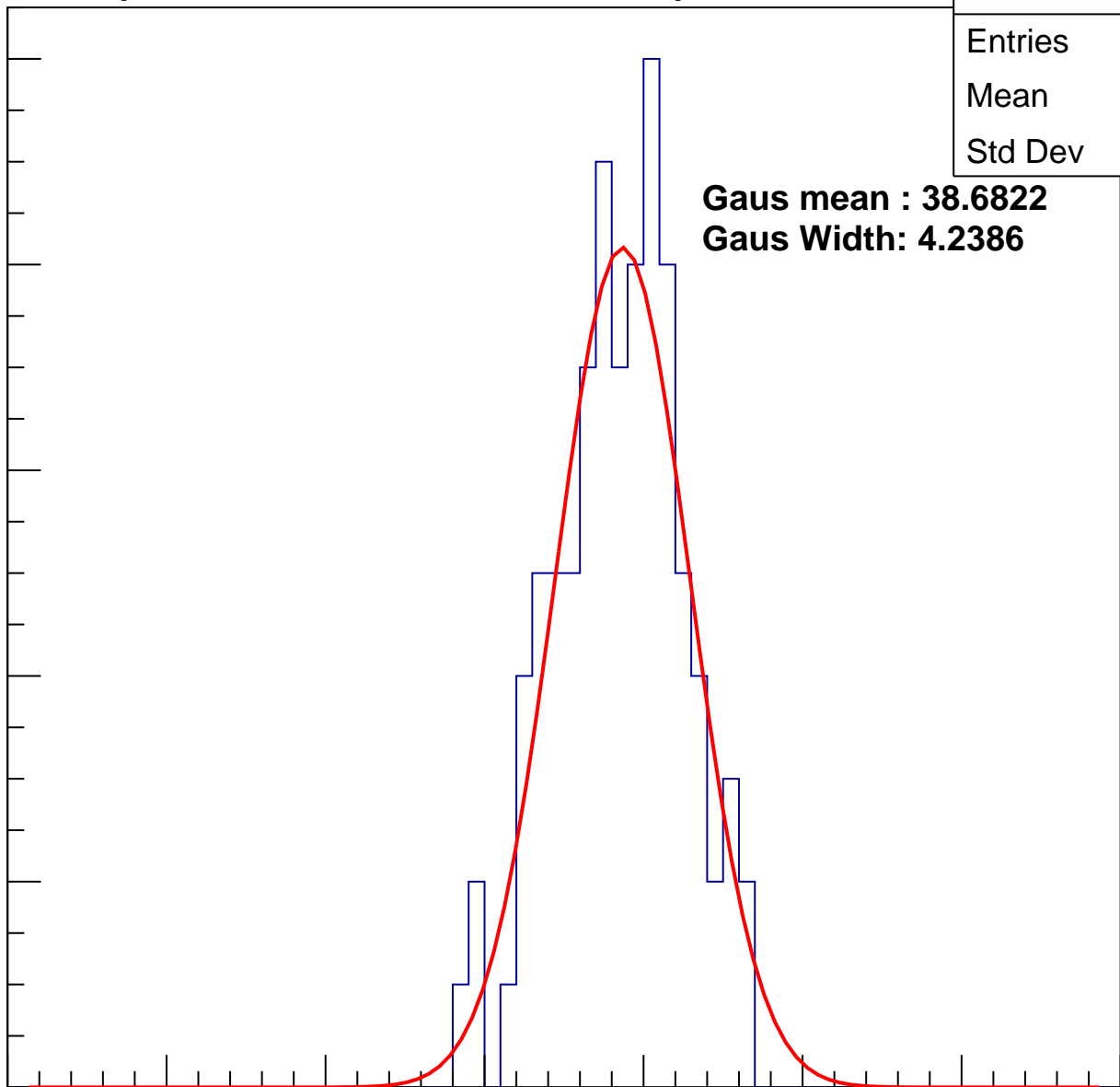
Gaus Width: 4.2386

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch85, adc2

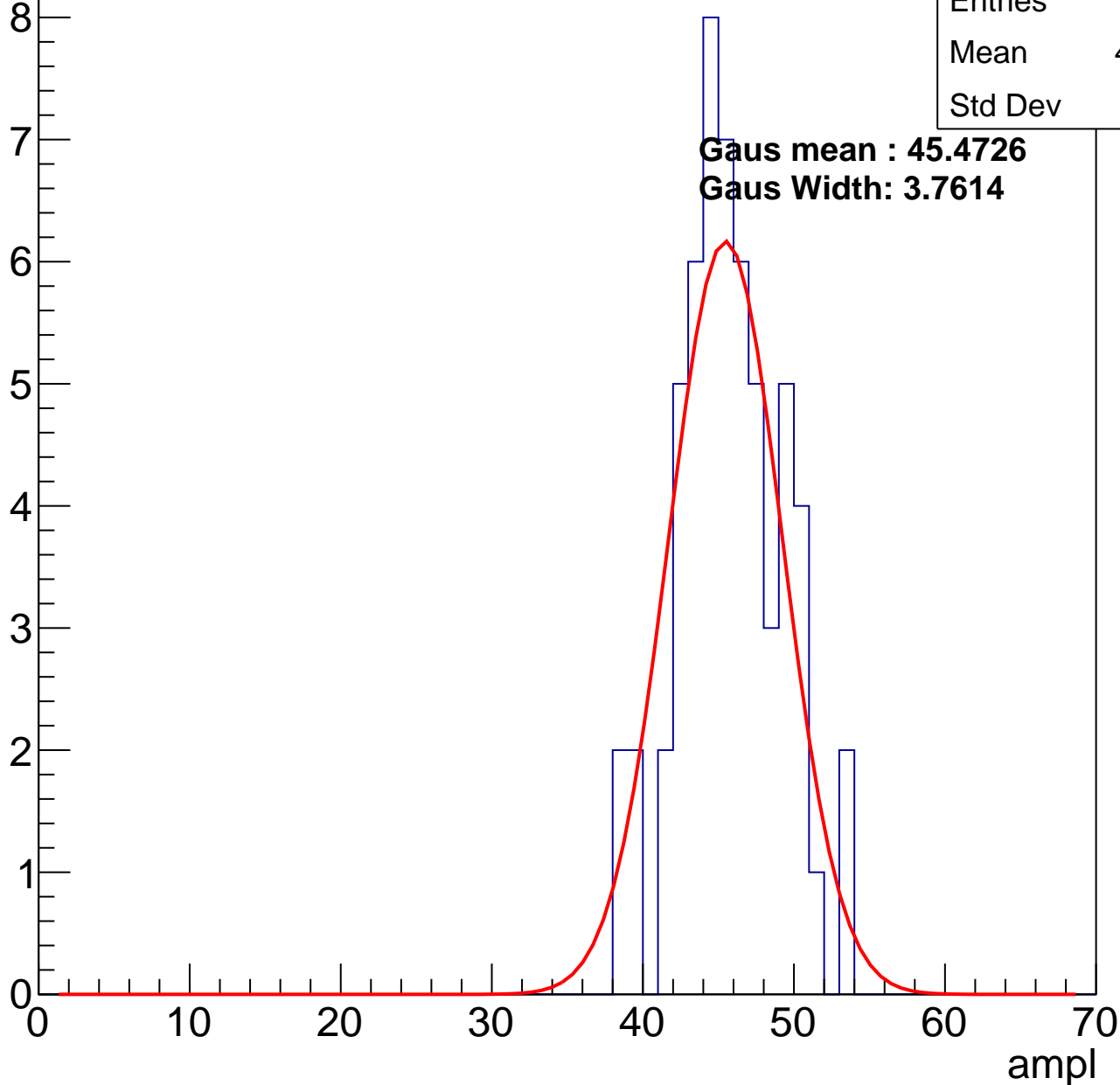
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	45.31
Std Dev	3.4

Gaus mean : 45.4726

Gaus Width: 3.7614

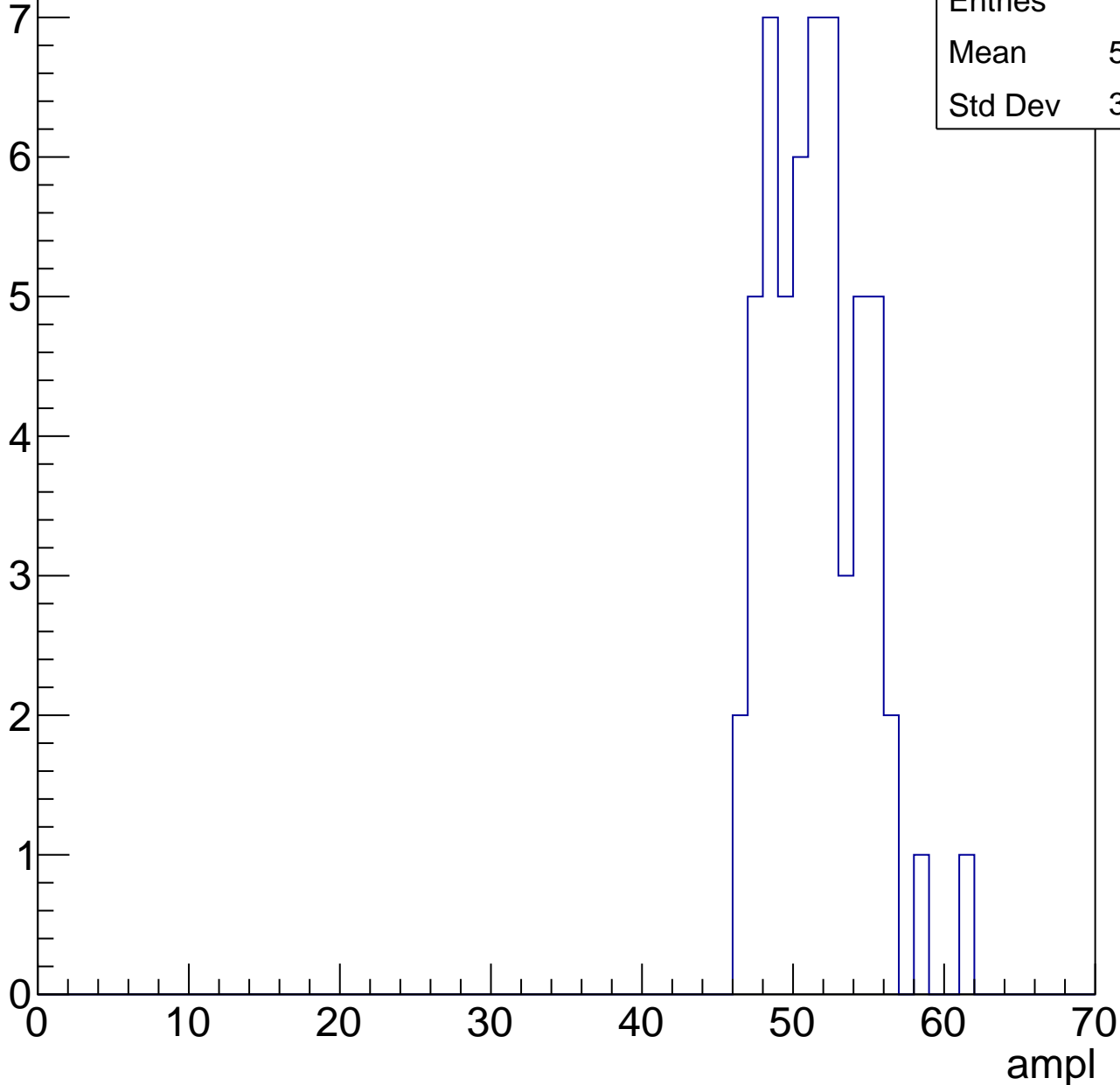


B0L001S, U22-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

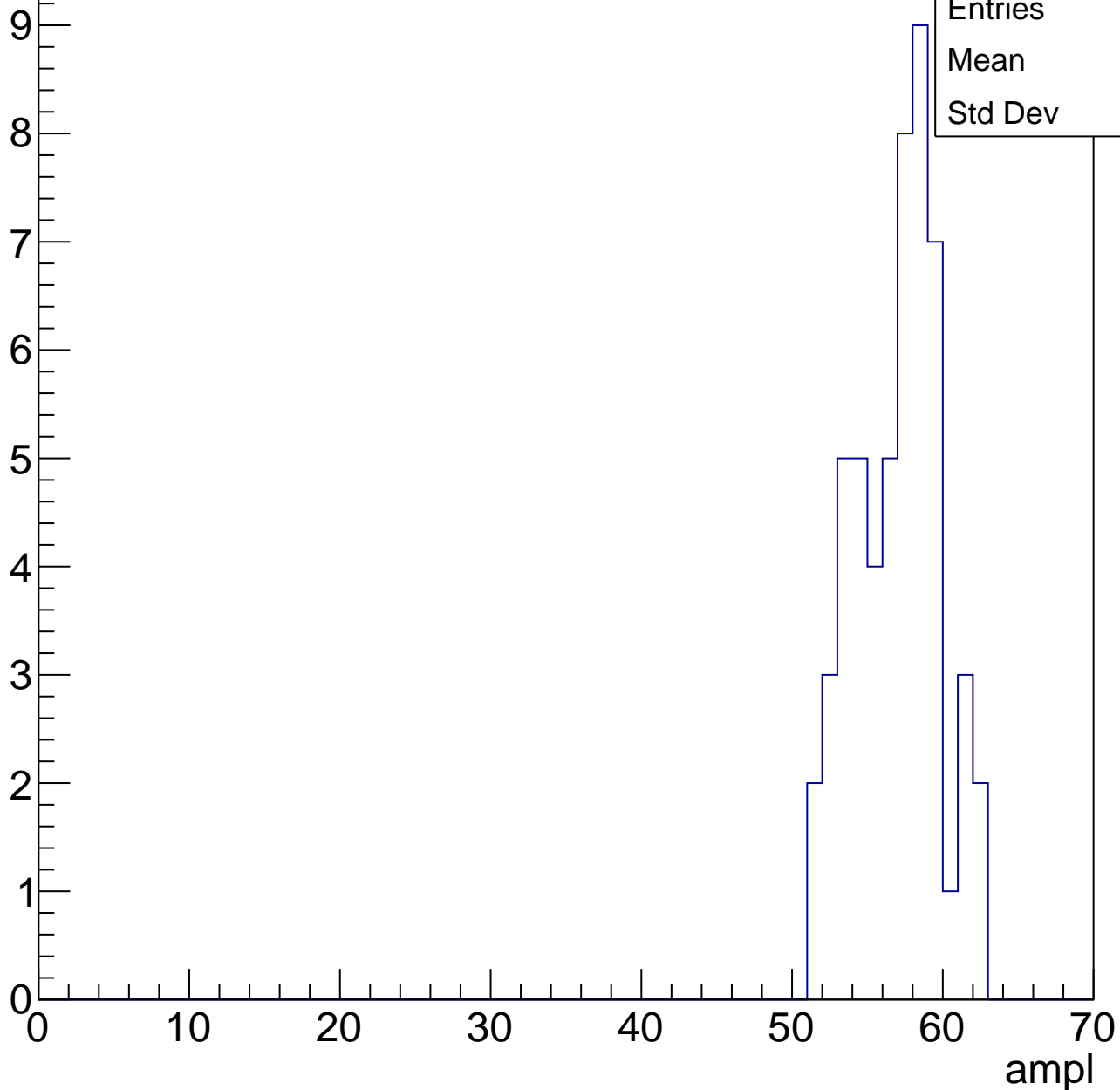
Entries	56
Mean	51.14
Std Dev	3.165



B0L001S, U22-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

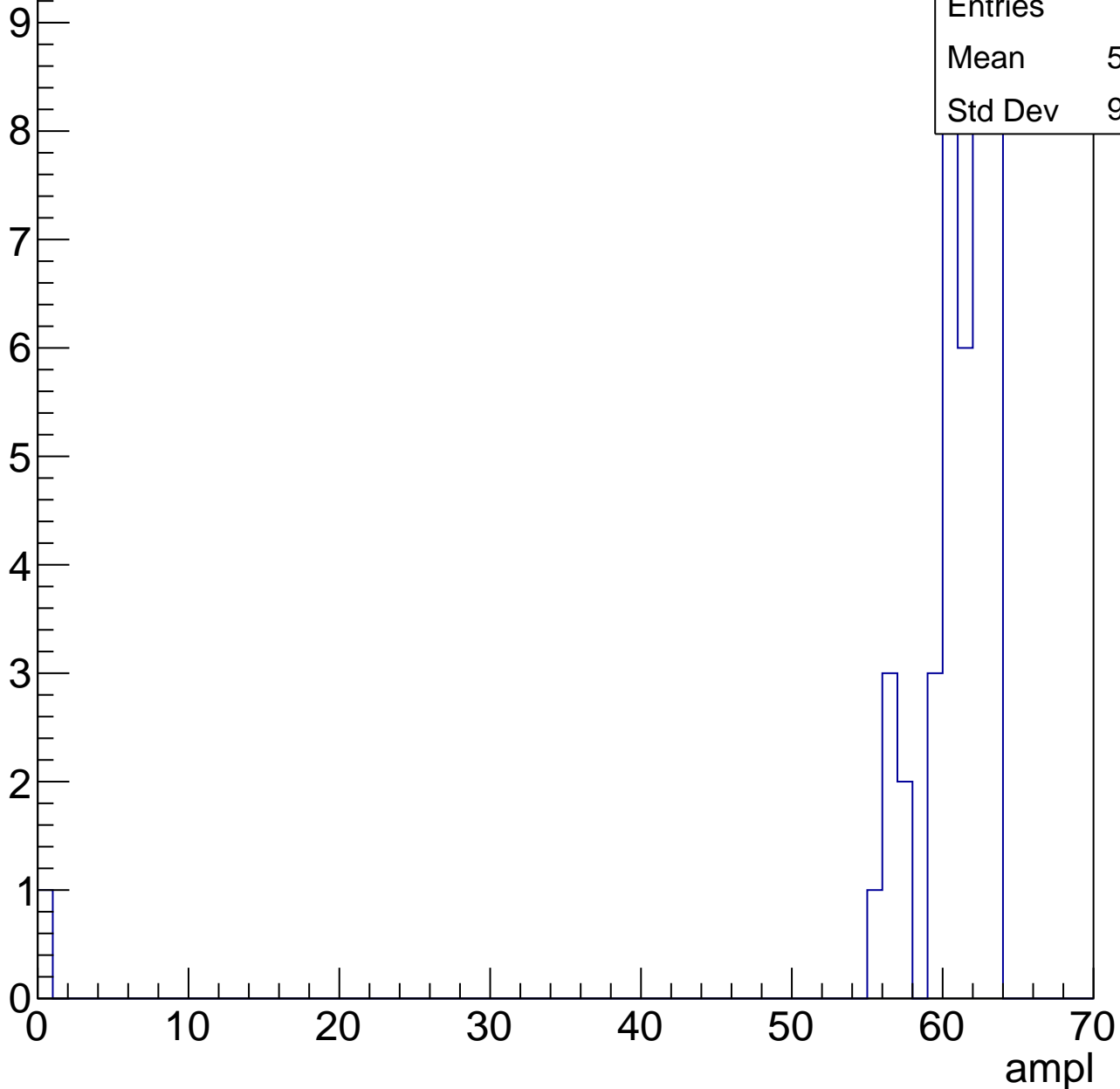


B0L001S, U22-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59.12
Std Dev	9.487



B0L001S, U22-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch86, adc0

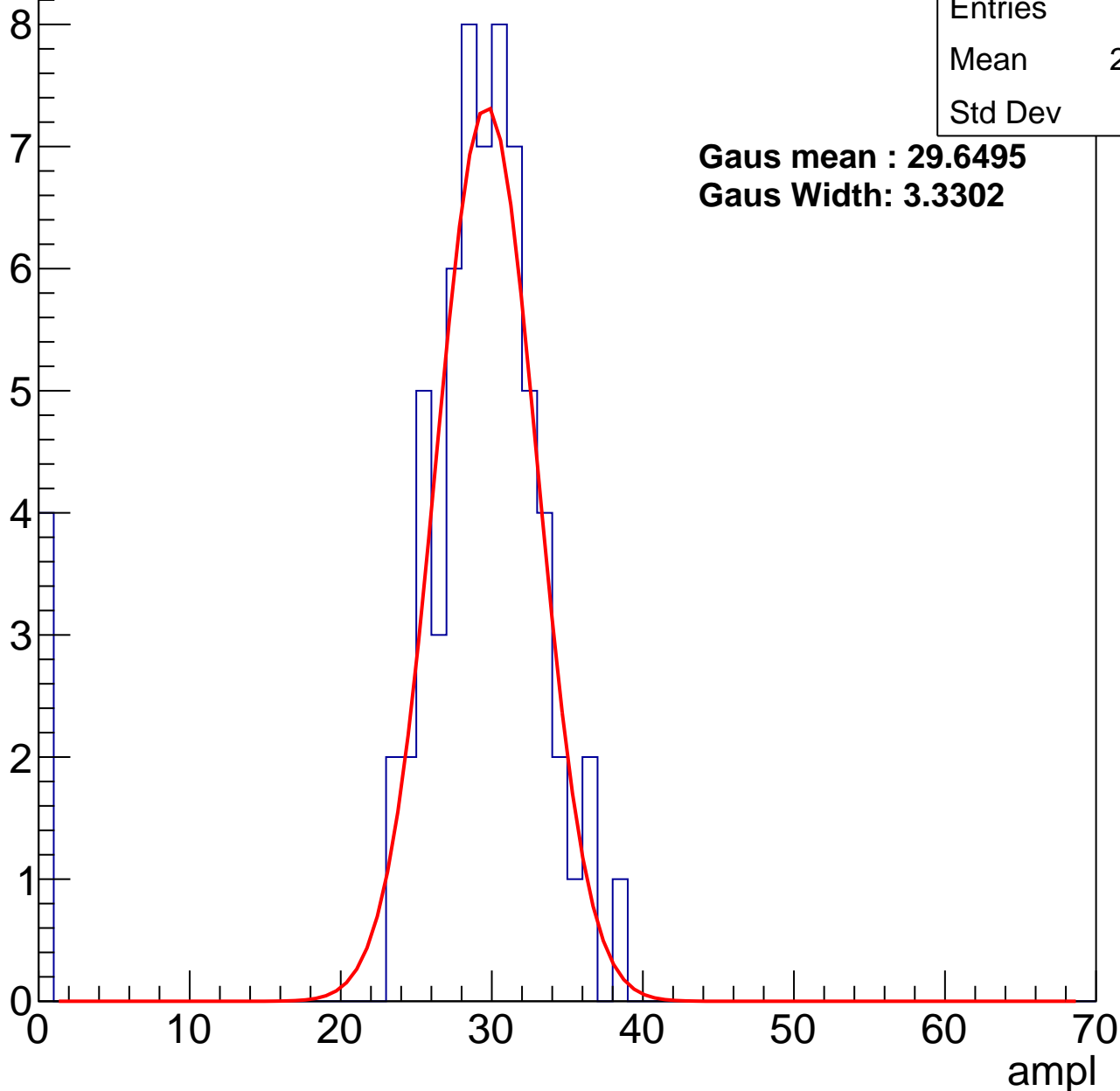
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	27.58
Std Dev	7.63

Gaus mean : 29.6495

Gaus Width: 3.3302



B0L001S, U22-ch86, adc1

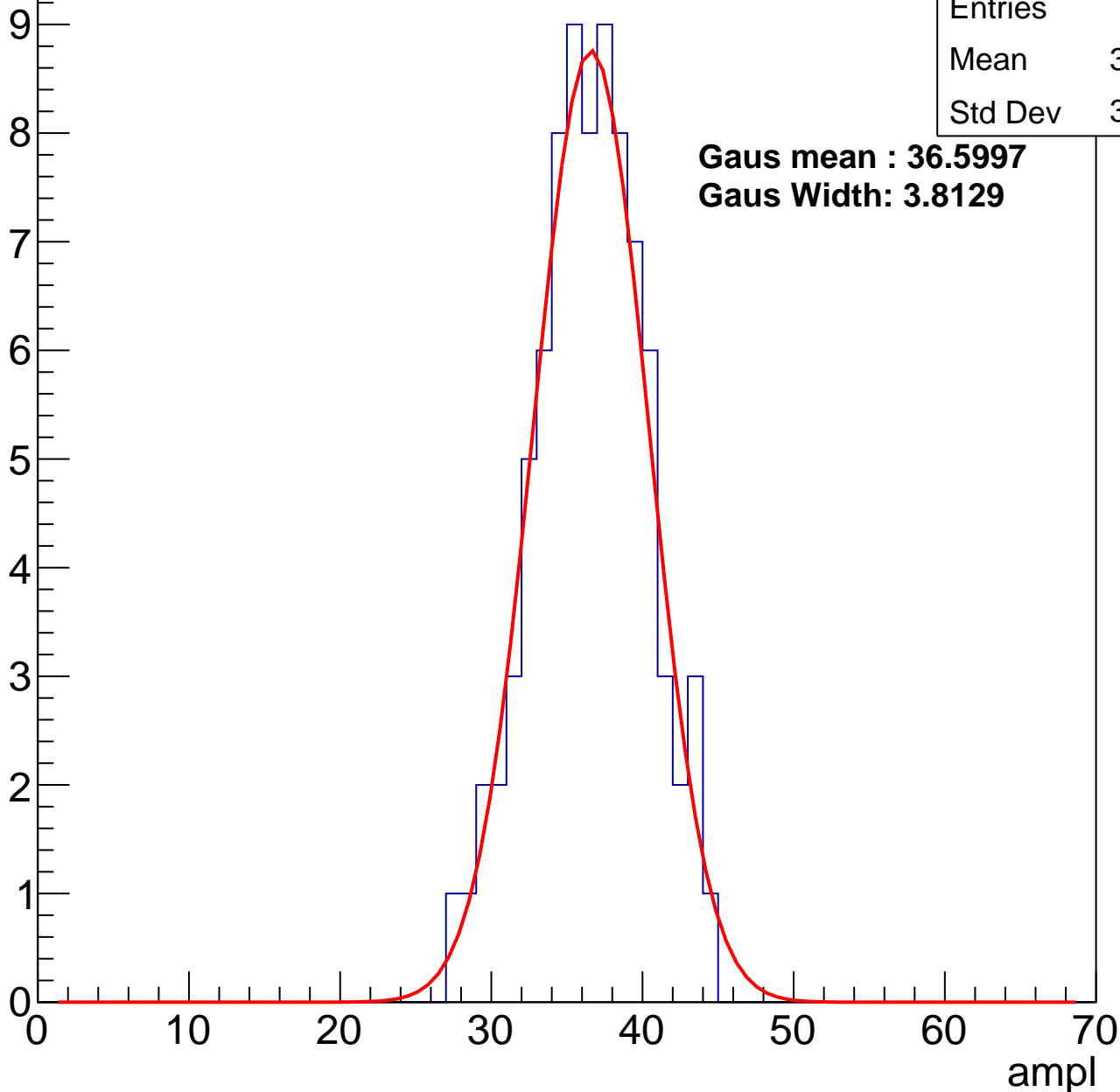
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	84
Mean	36.06
Std Dev	3.656

Gaus mean : 36.5997

Gaus Width: 3.8129



B0L001S, U22-ch86, adc2

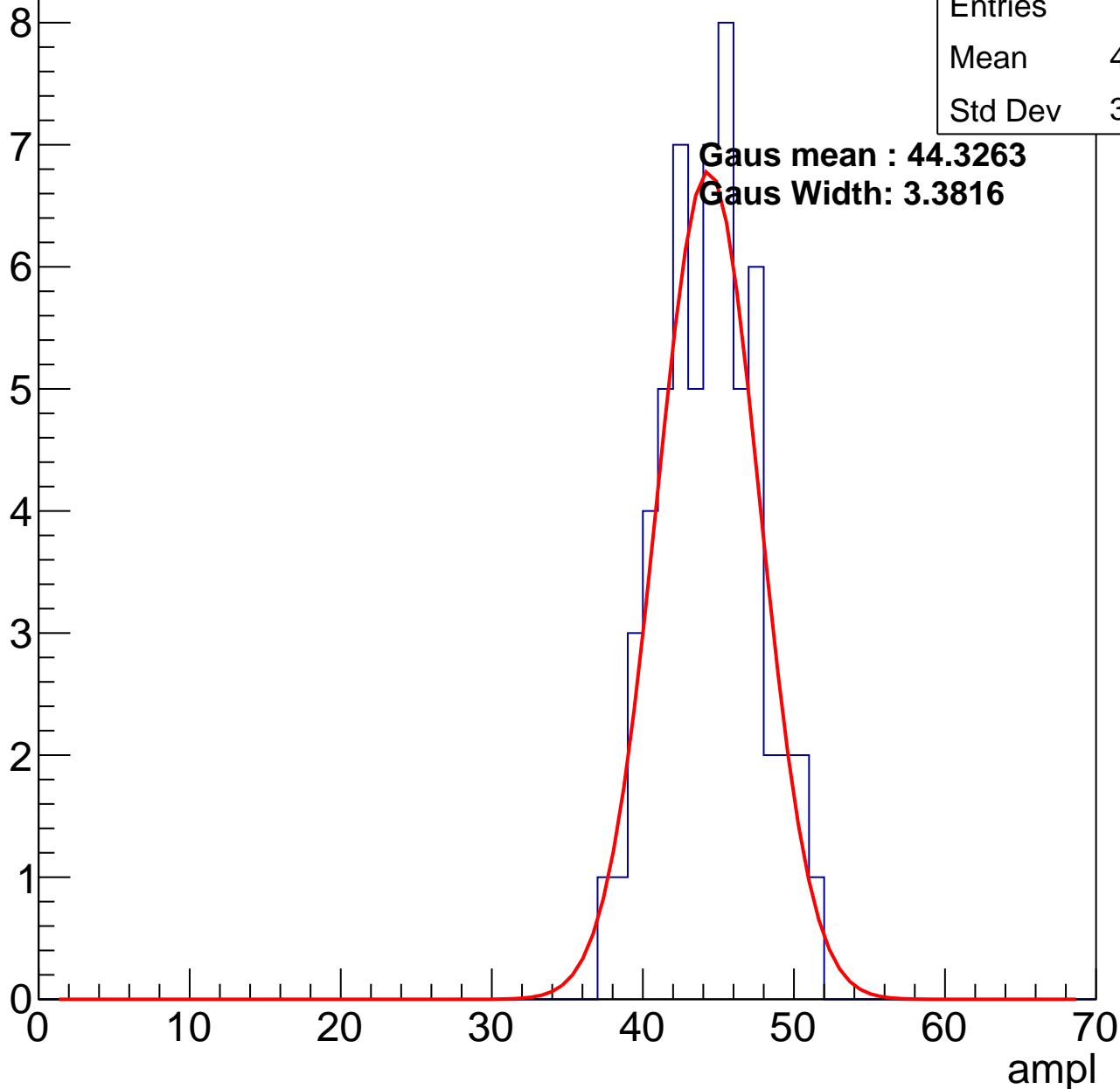
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	43.92
Std Dev	3.153

Gaus mean : 44.3263

Gaus Width: 3.3816

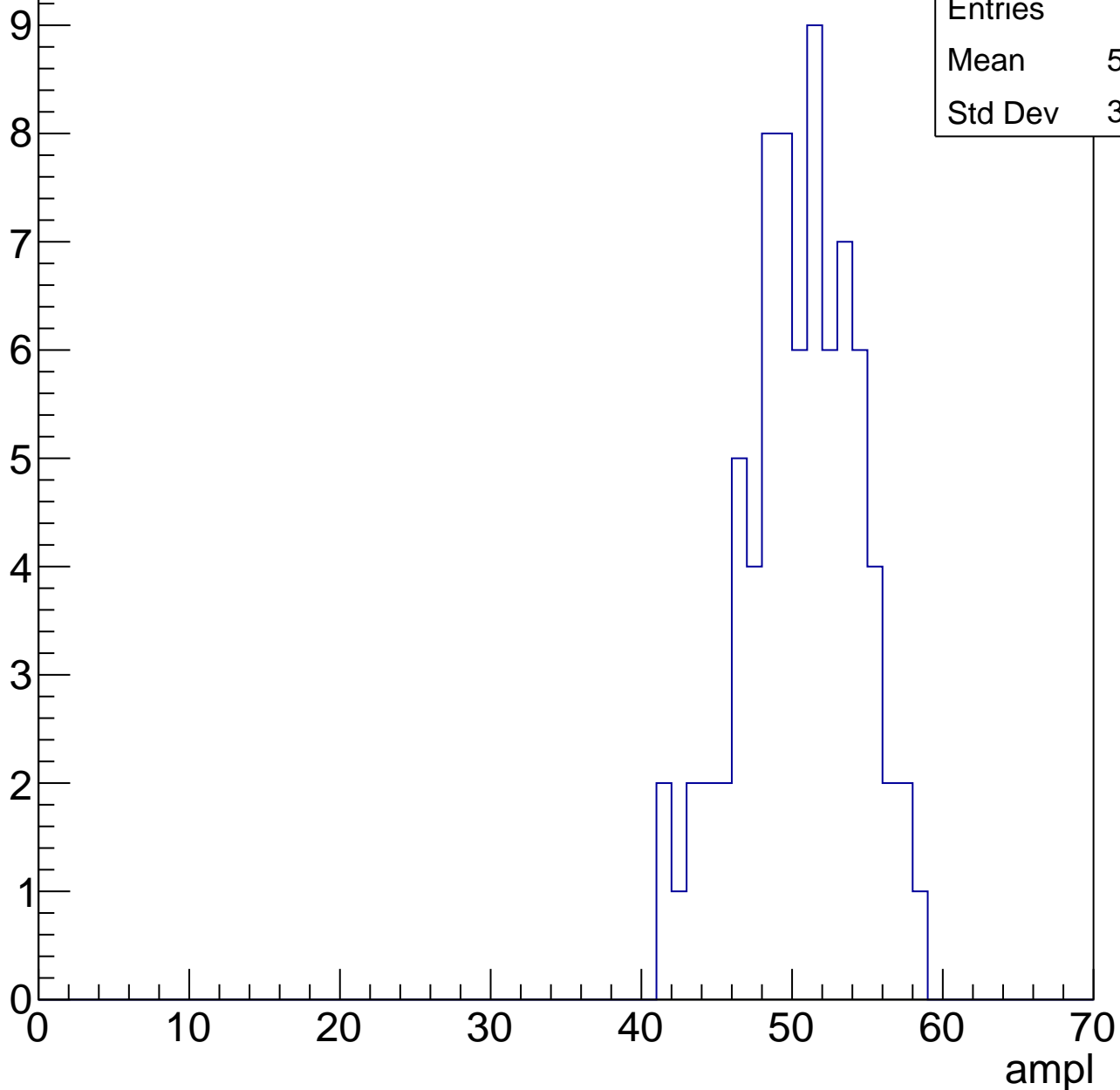


B0L001S, U22-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	50.03
Std Dev	3.827

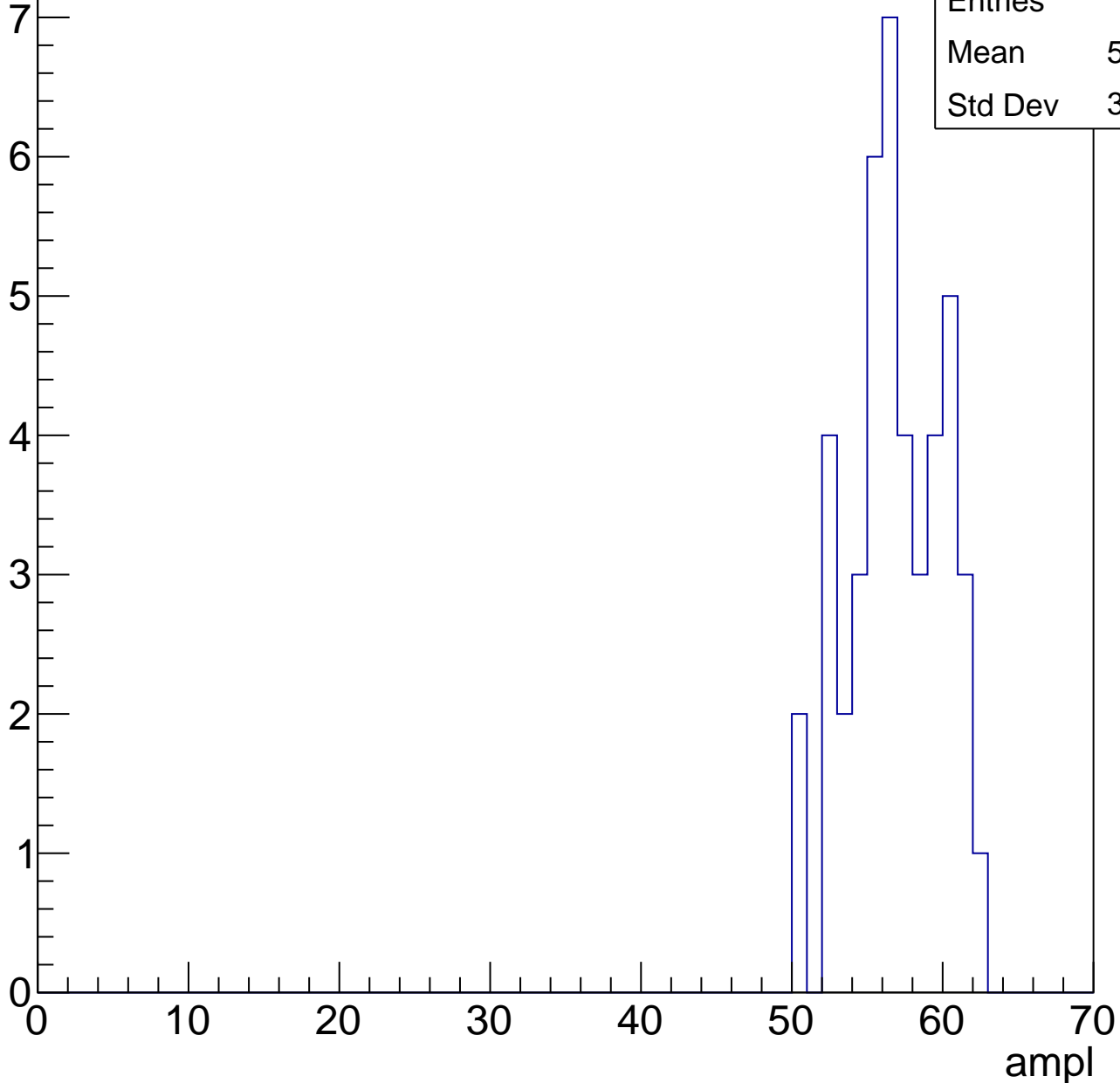


B0L001S, U22-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	56.39
Std Dev	3.047

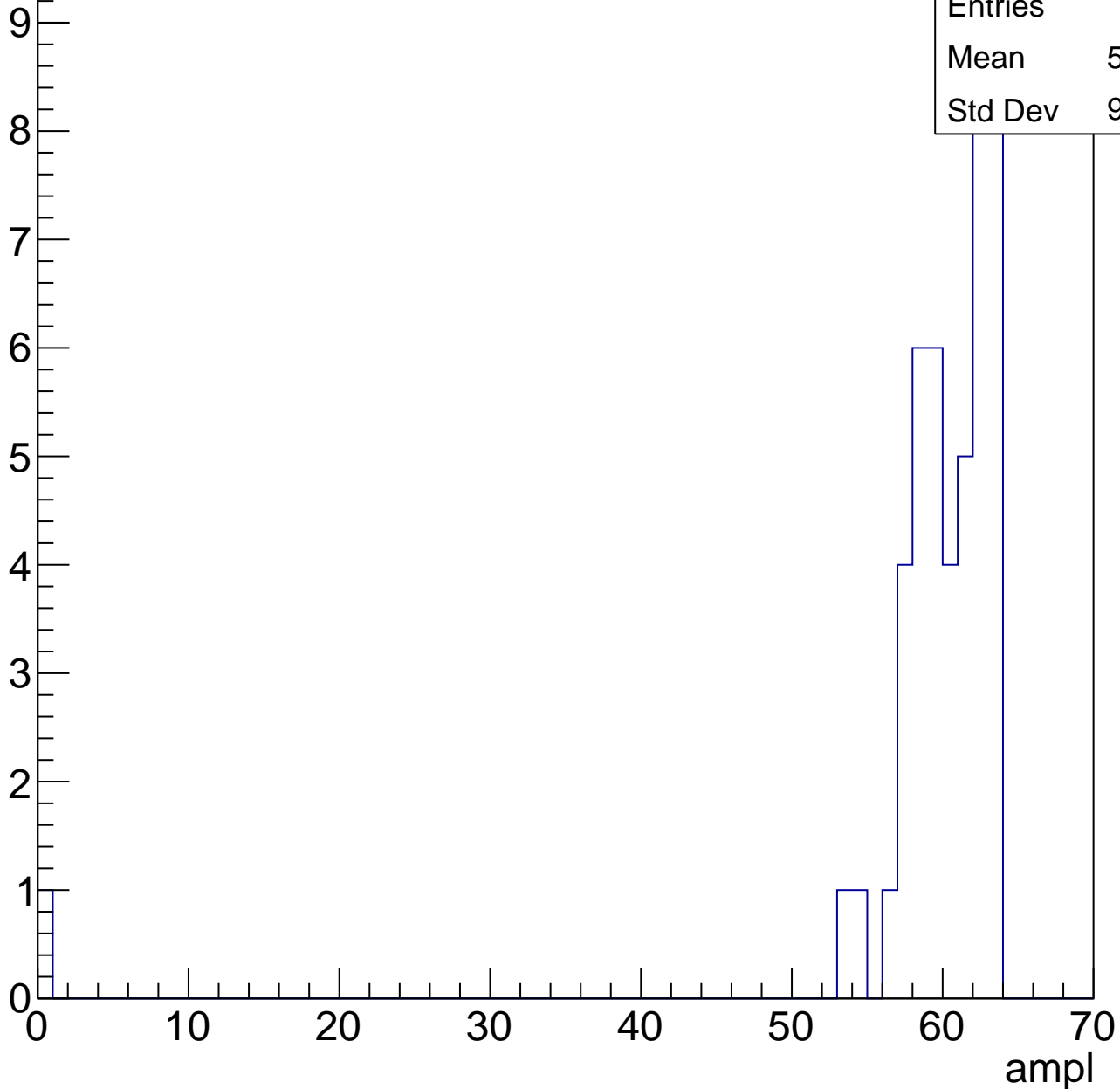


B0L001S, U22-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

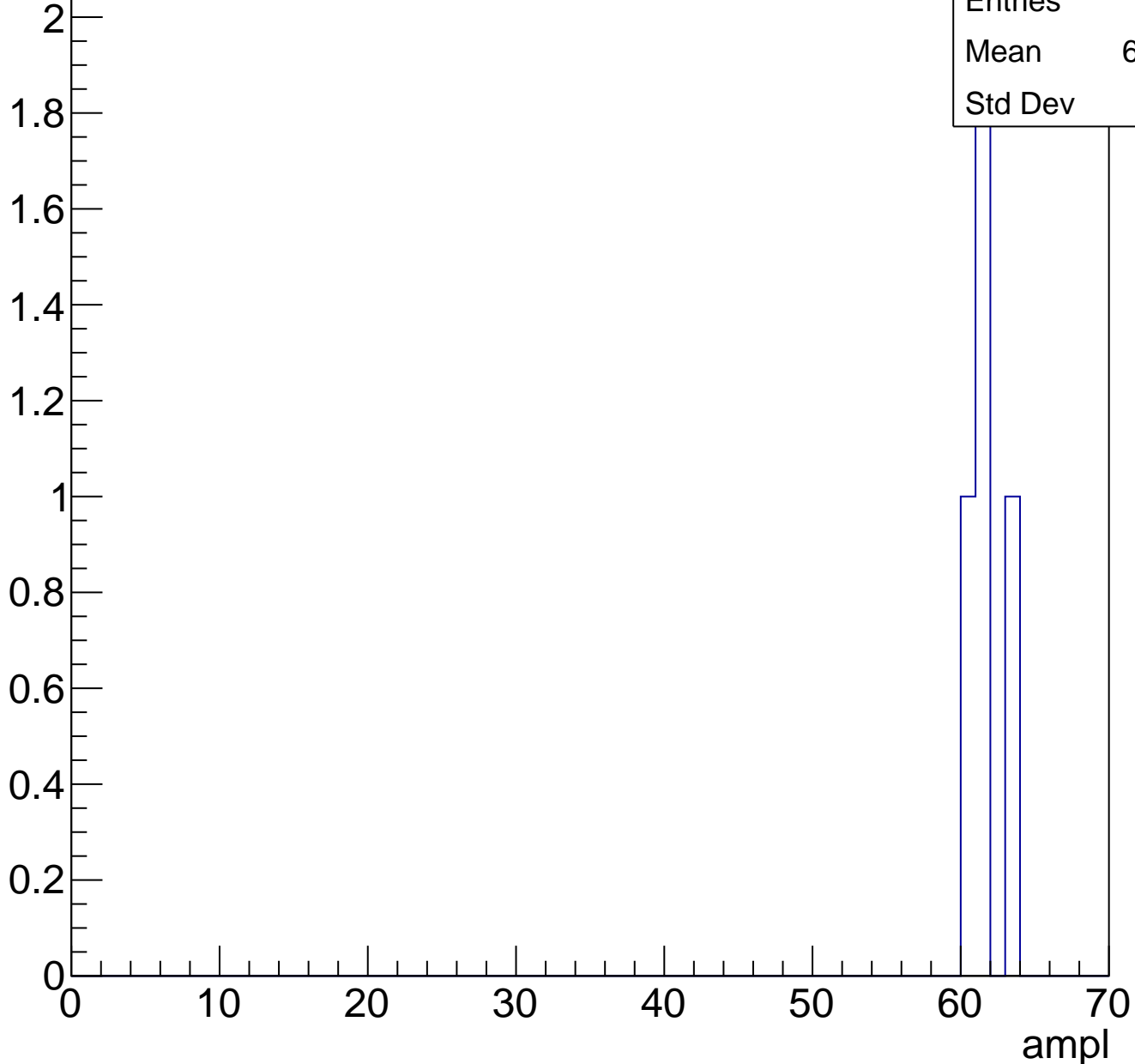
Entries	47
Mean	58.79
Std Dev	9.015



B0L001S, U22-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch87, adc0

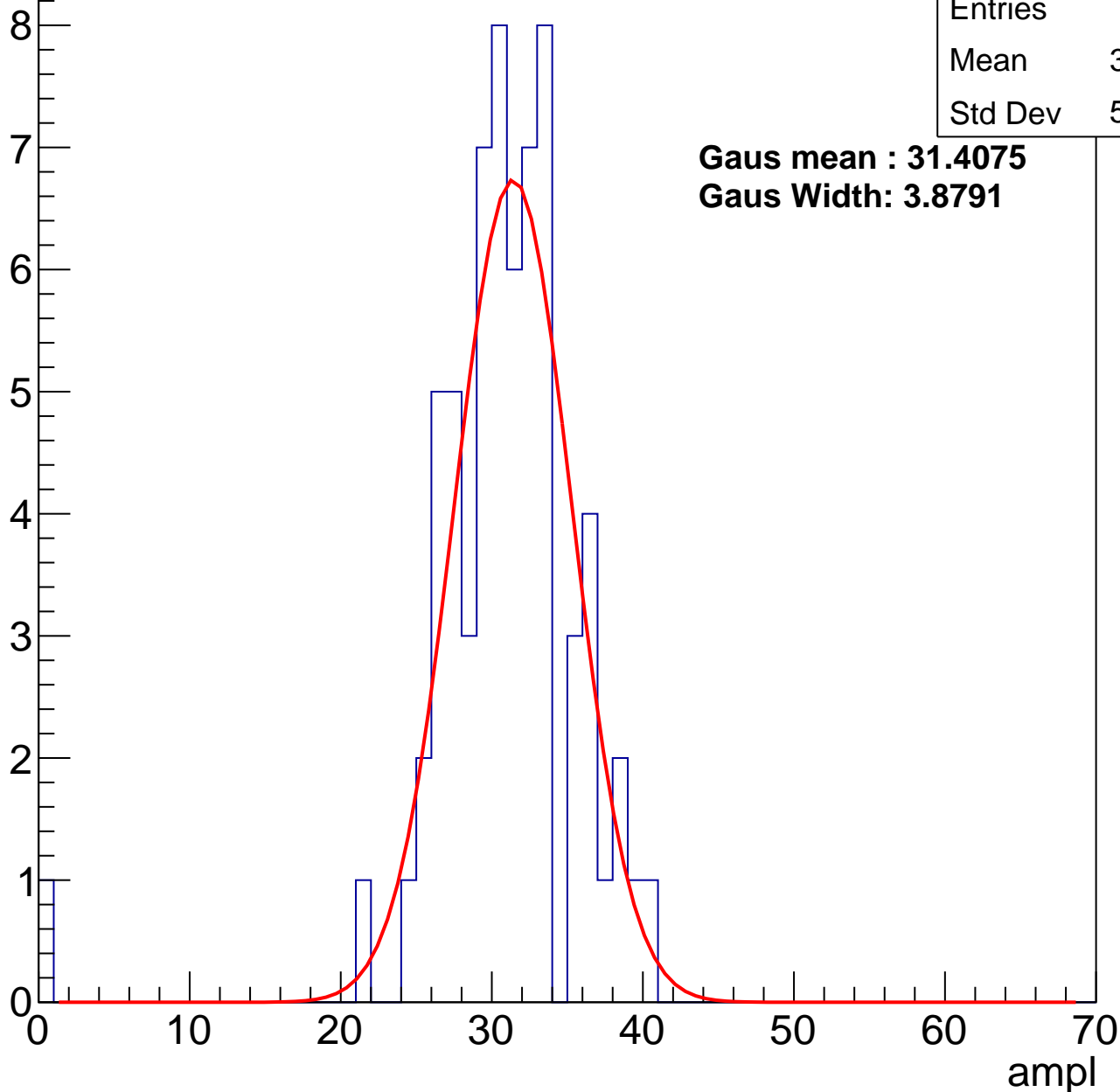
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.33
Std Dev	5.347

Gaus mean : 31.4075

Gaus Width: 3.8791



B0L001S, U22-ch87, adc1

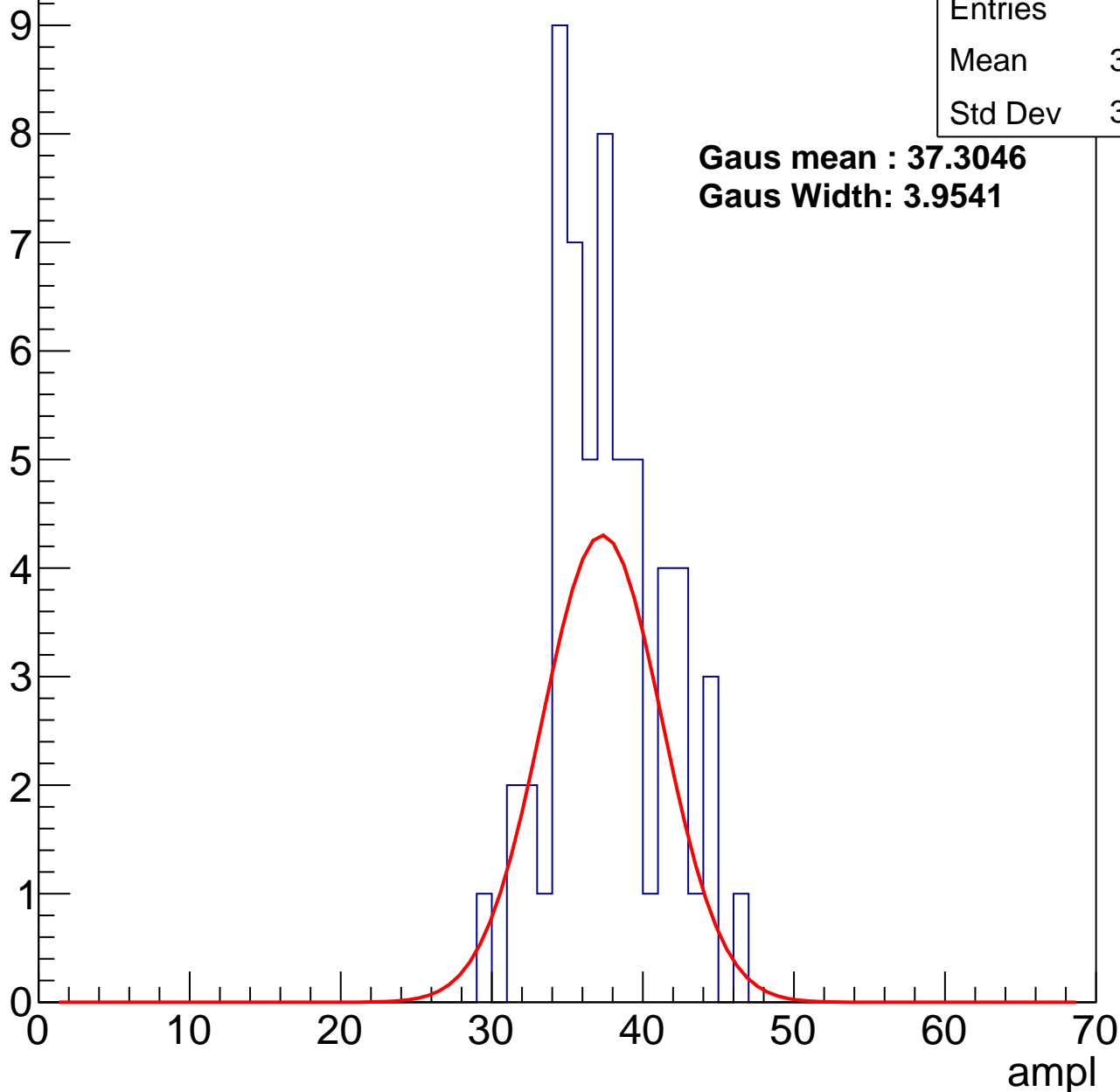
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	37.17
Std Dev	3.627

Gaus mean : 37.3046

Gaus Width: 3.9541



B0L001S, U22-ch87, adc2

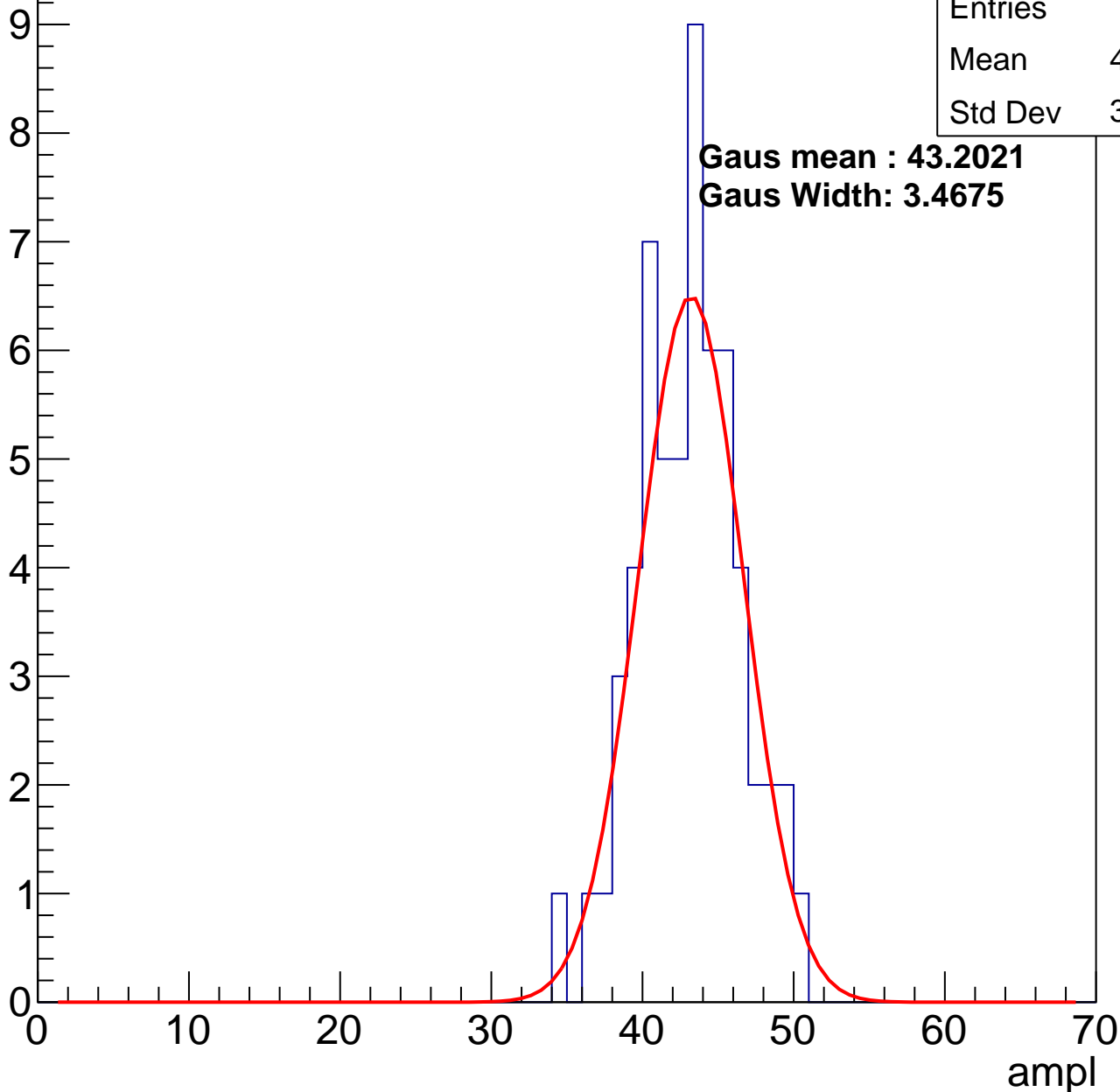
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	42.63
Std Dev	3.329

Gaus mean : 43.2021

Gaus Width: 3.4675



B0L001S, U22-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	85
Mean	50.19
Std Dev	3.839

Entry

10

8

6

4

2

0

0

10

20

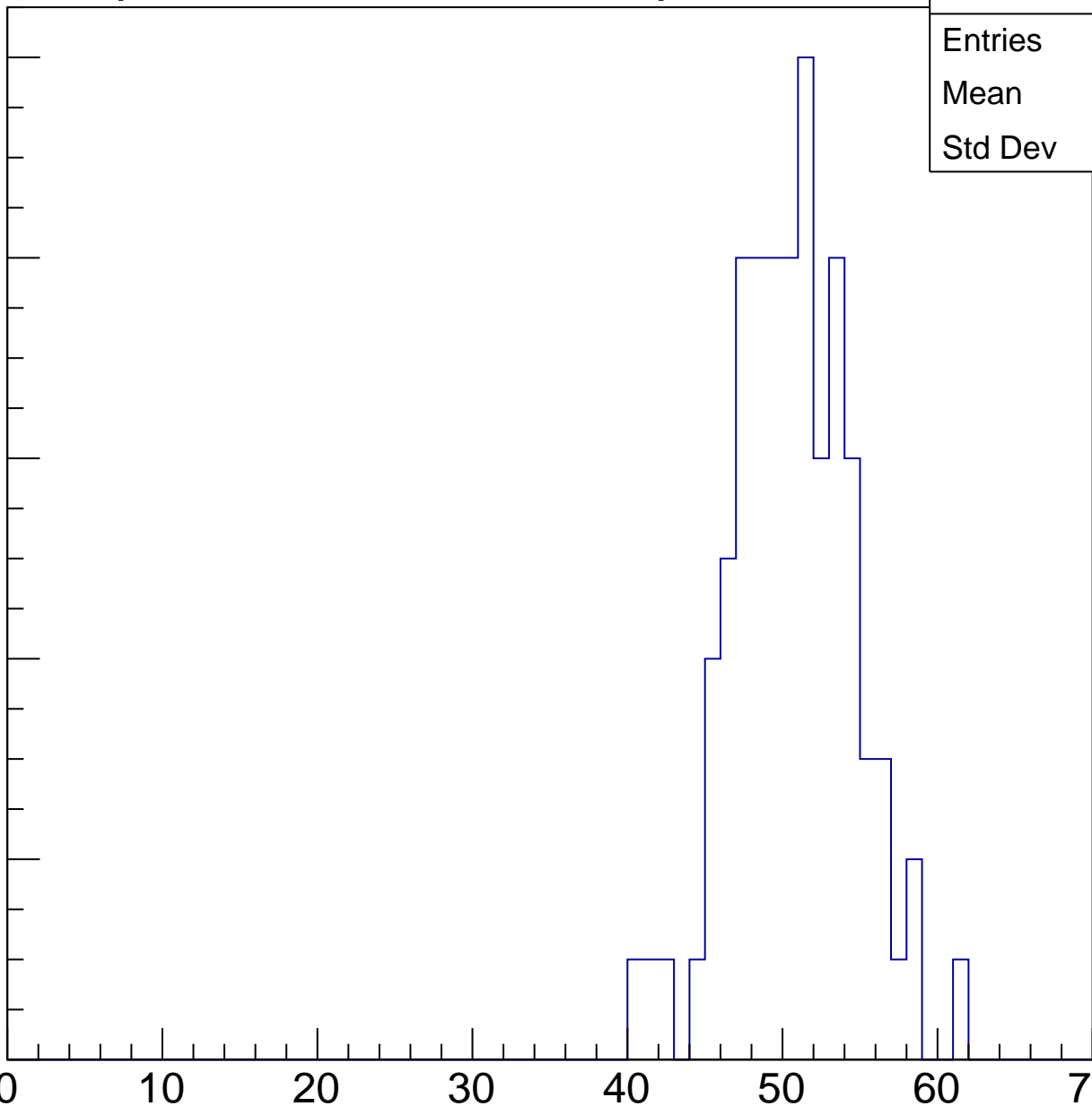
30

40

50

60

ampl

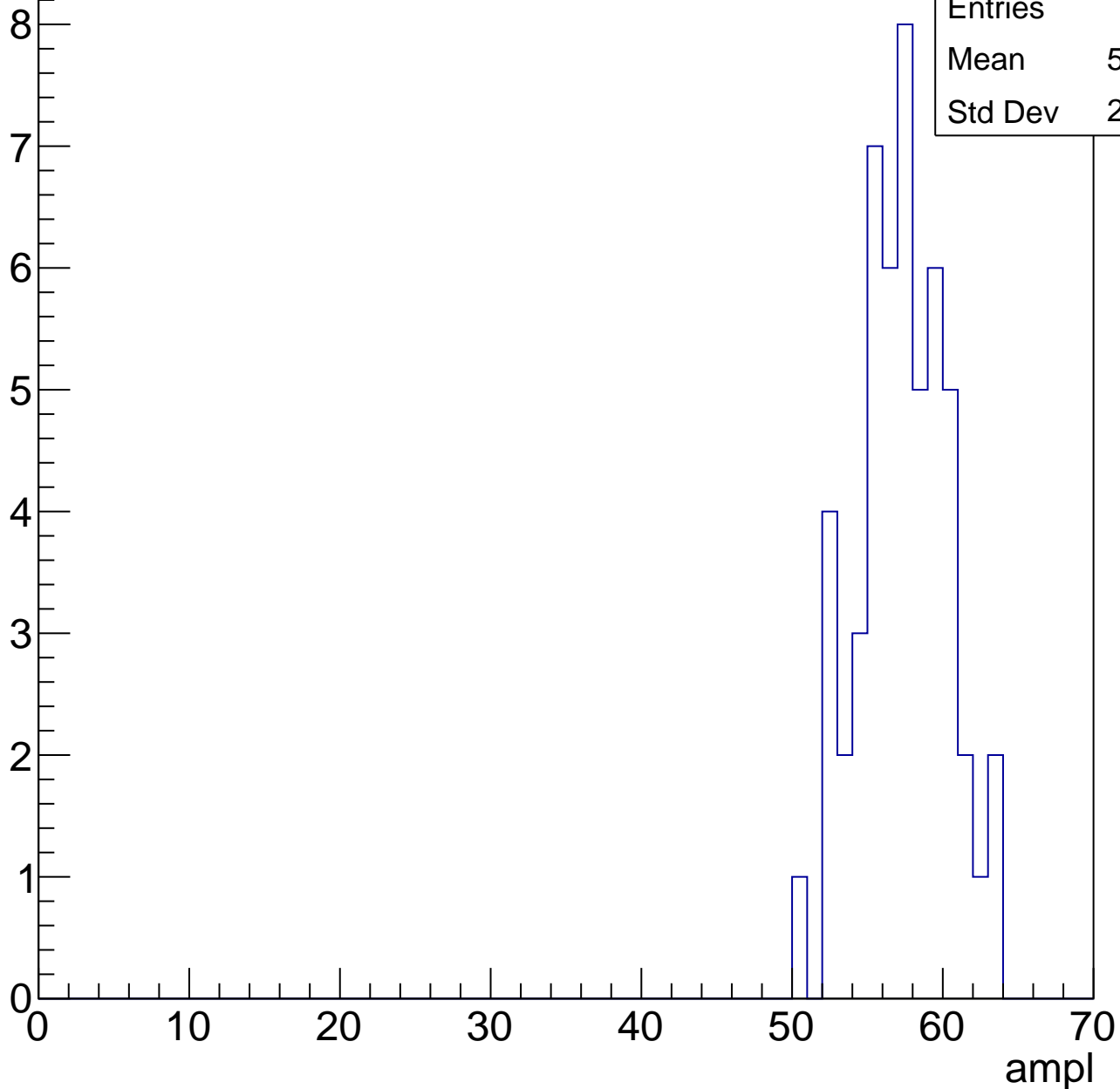


B0L001S, U22-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.87
Std Dev	2.922

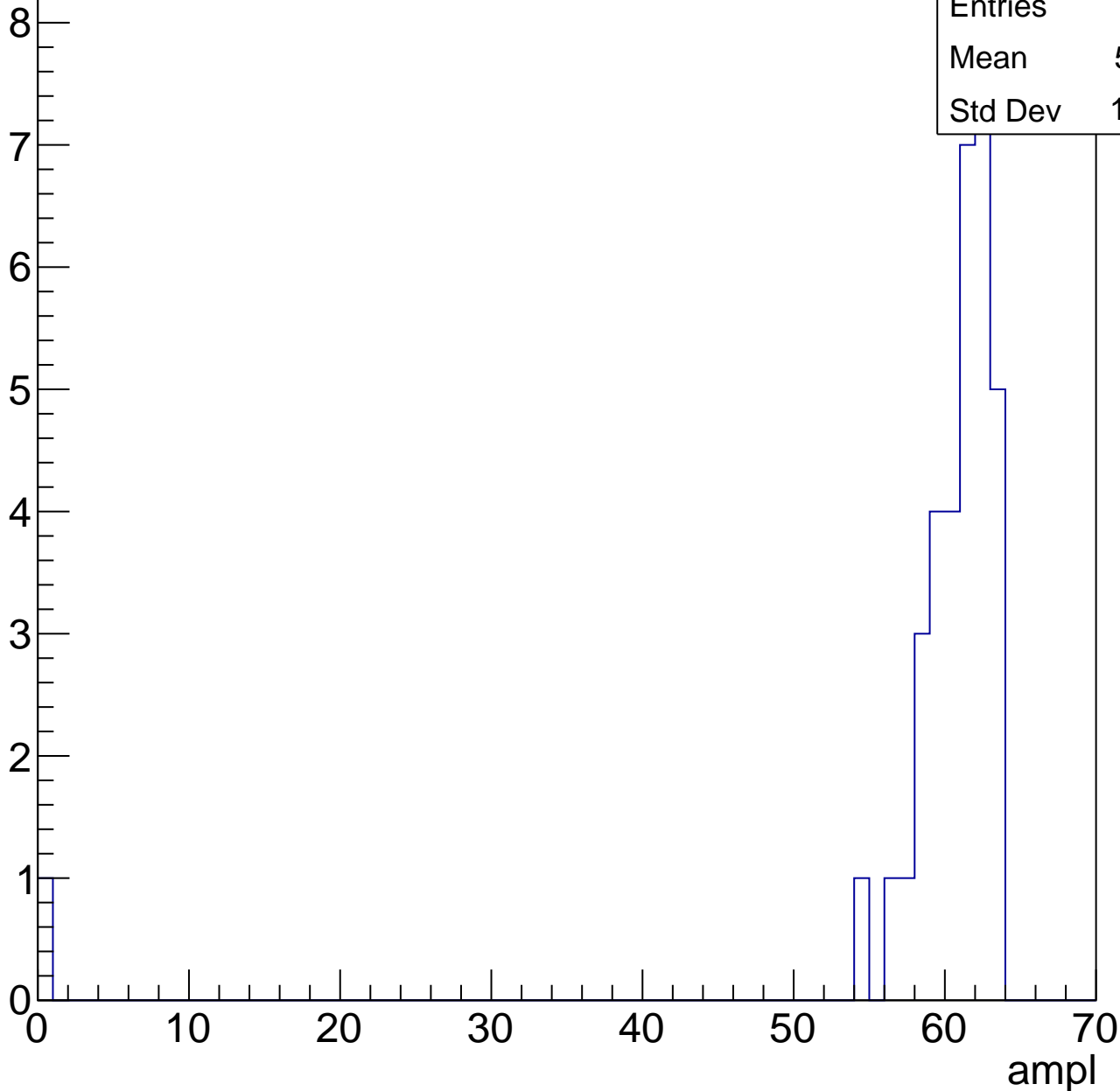


B0L001S, U22-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.71
Std Dev	10.29



B0L001S, U22-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch88, adc0

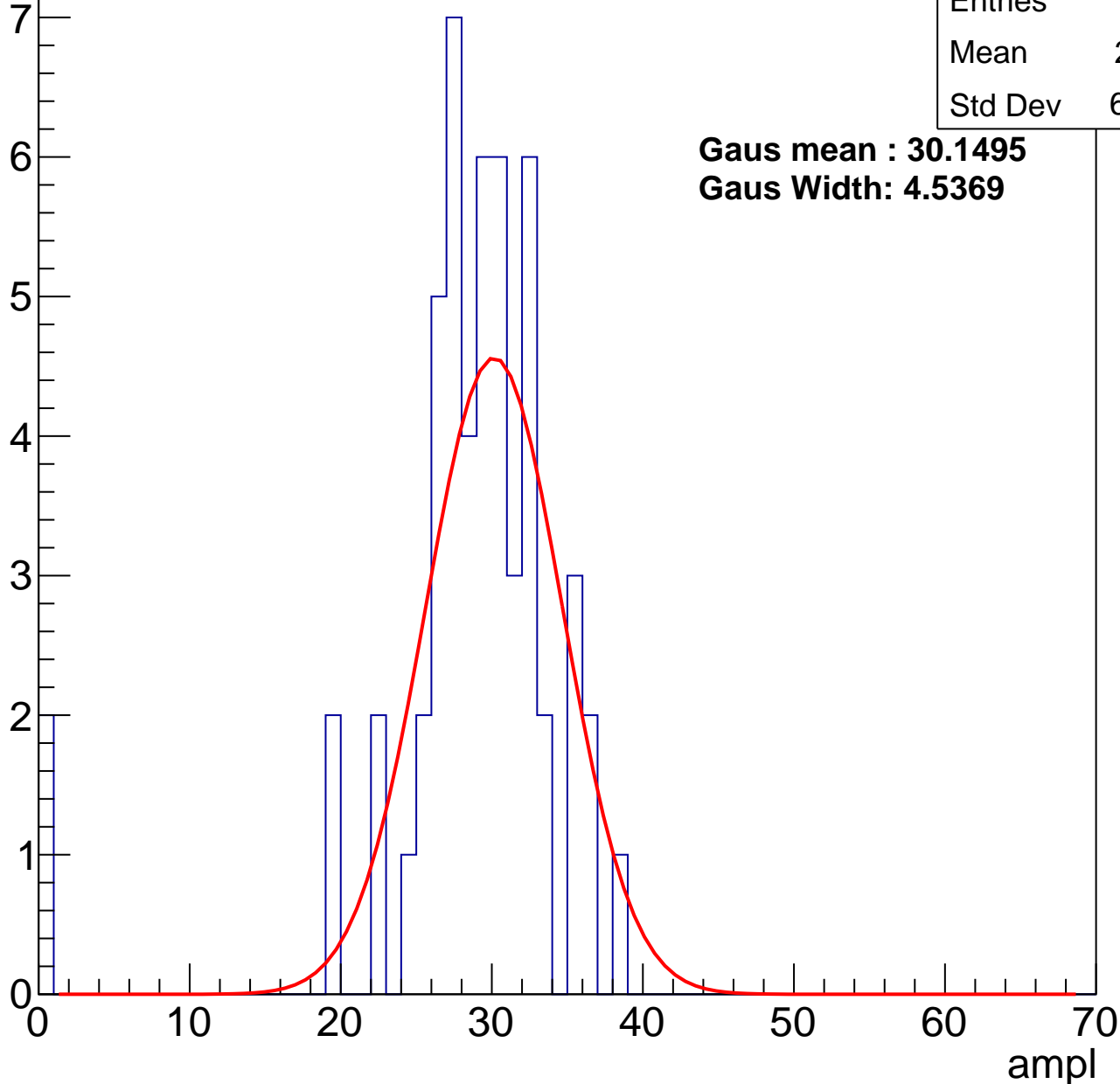
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	27.91
Std Dev	6.723

Gaus mean : 30.1495

Gaus Width: 4.5369



B0L001S, U22-ch88, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	36.02
Std Dev	3.629

Gaus mean : 36.6634

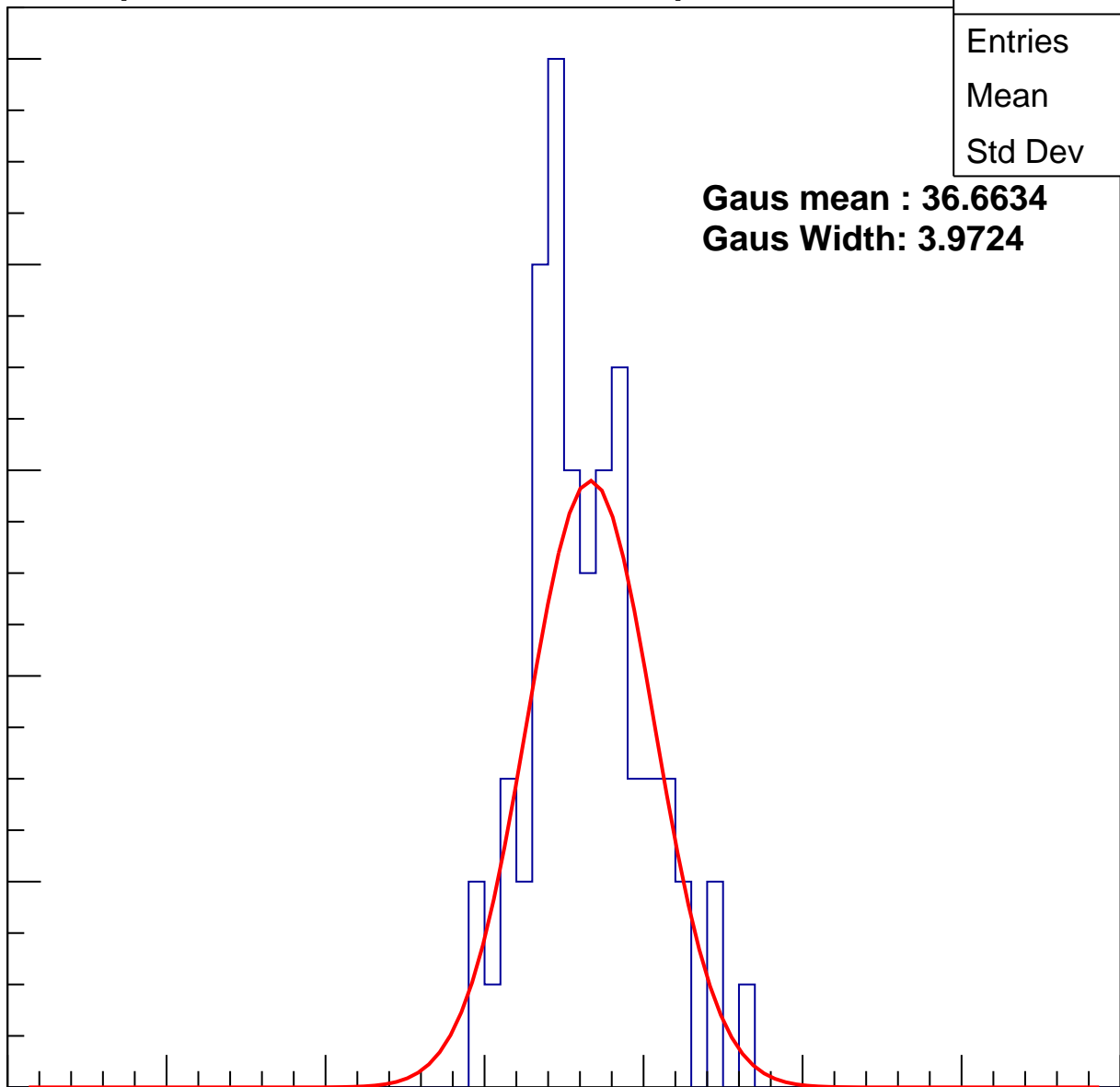
Gaus Width: 3.9724

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch88, adc2

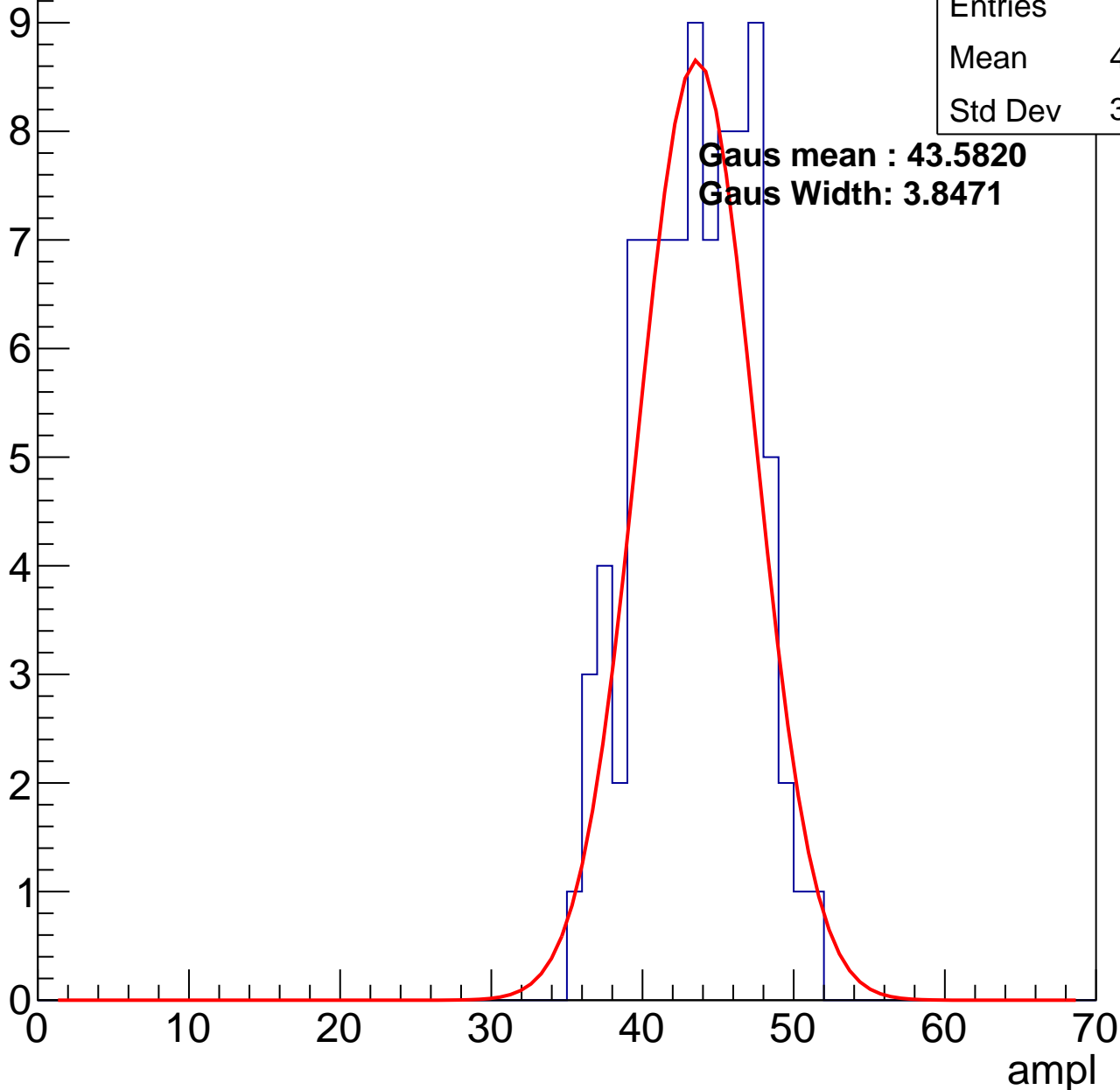
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	43.02
Std Dev	3.662

Gaus mean : 43.5820

Gaus Width: 3.8471

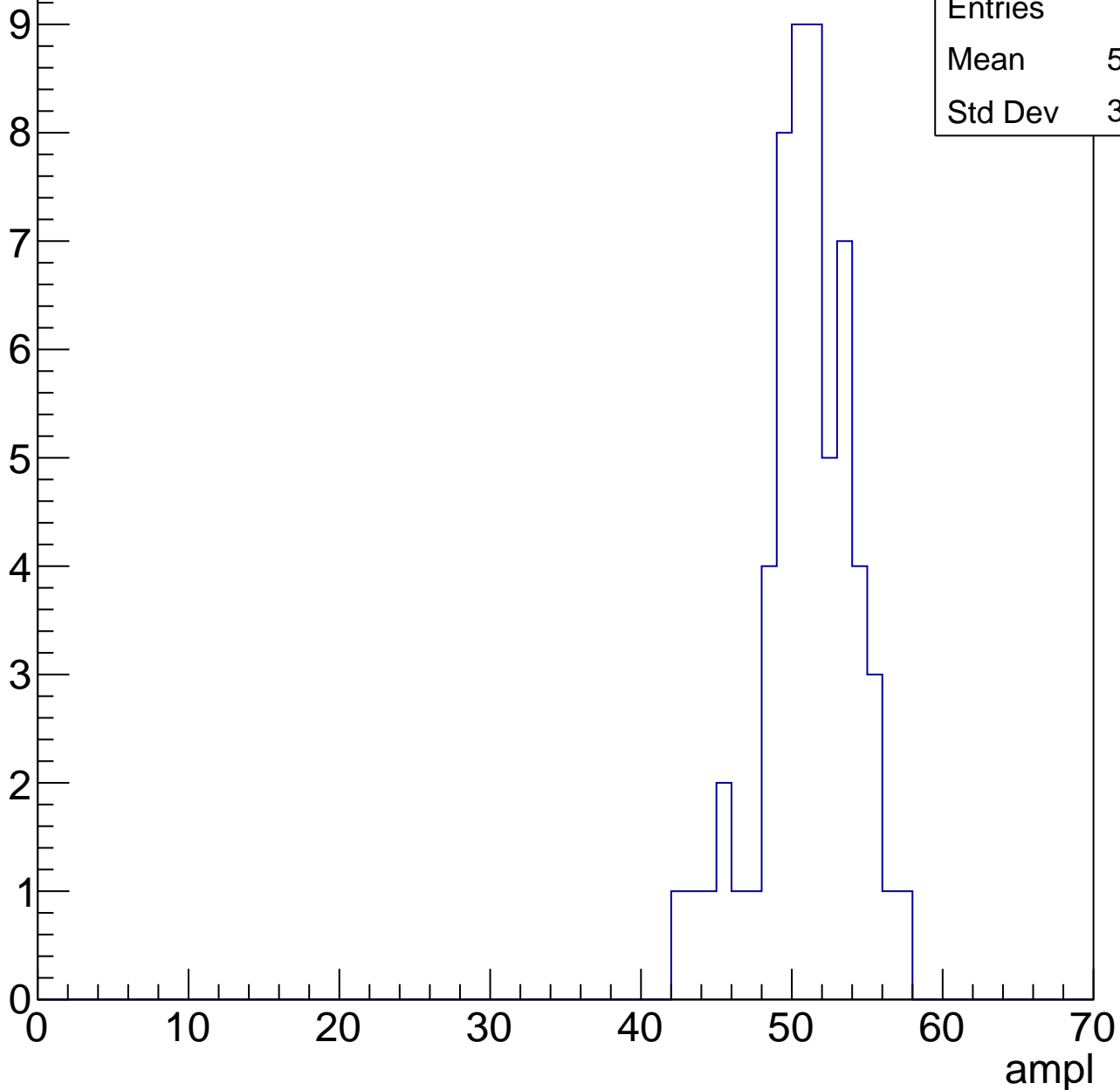


B0L001S, U22-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.52
Std Dev	3.075

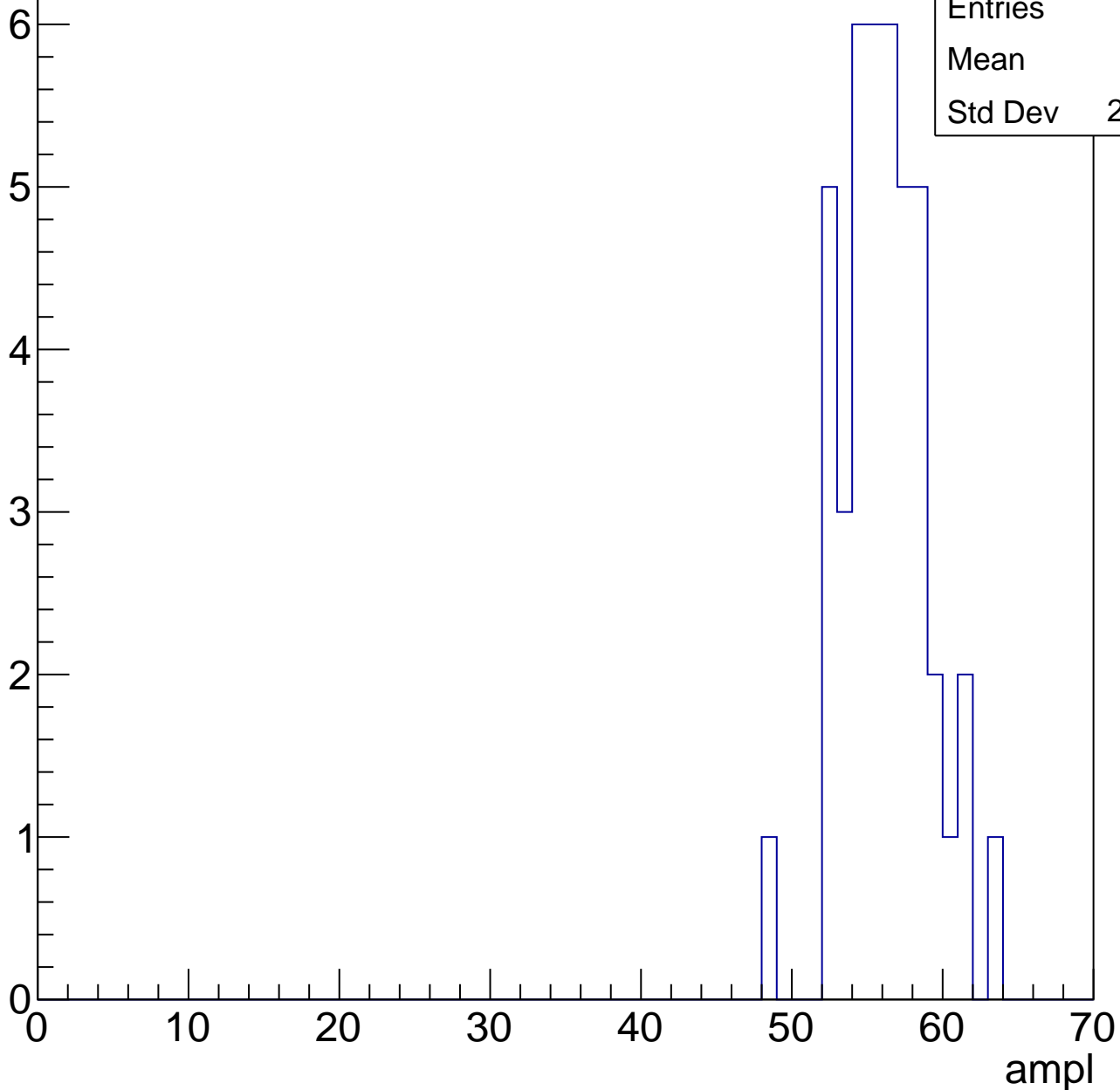


B0L001S, U22-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

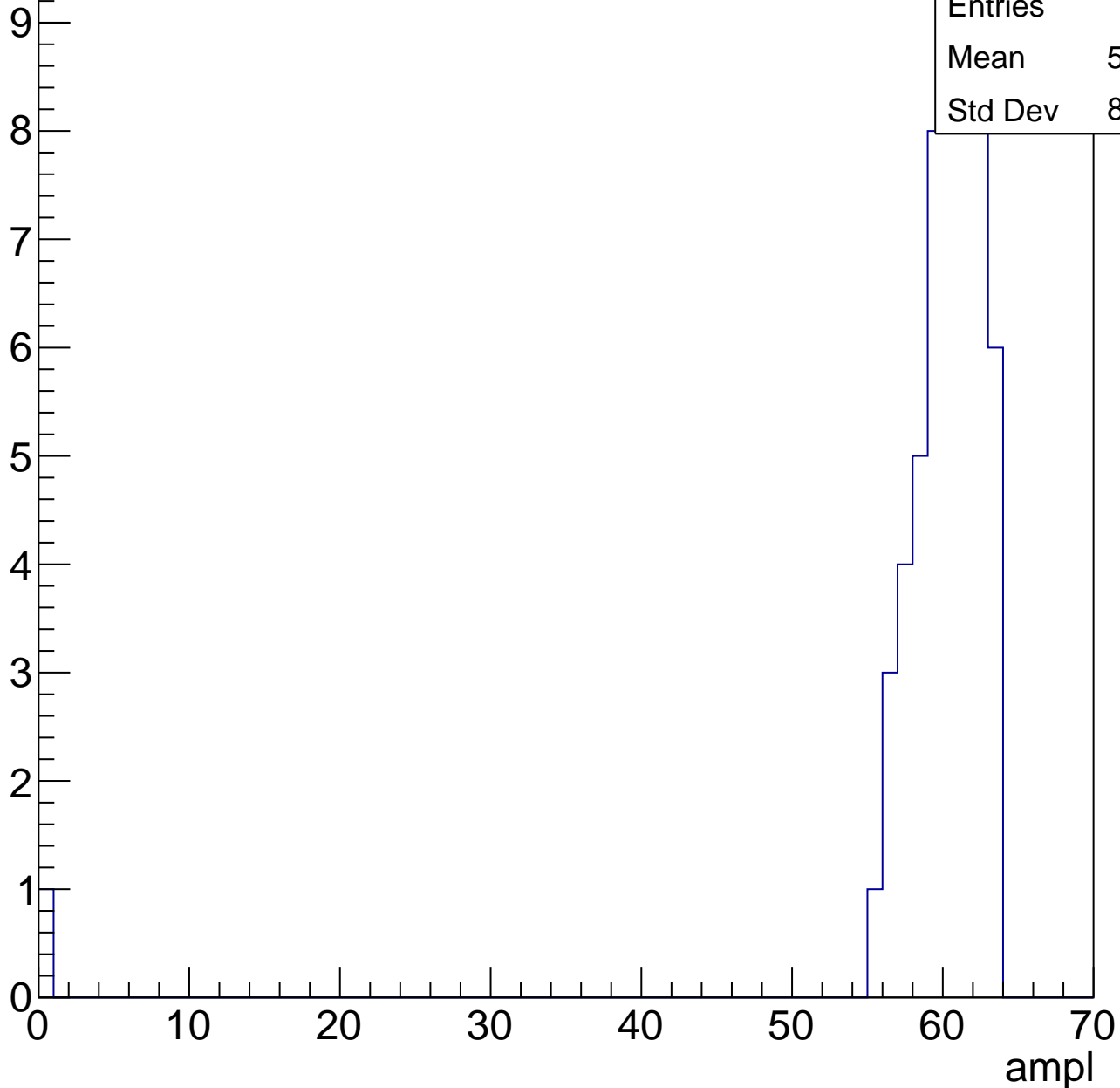
Entries	43
Mean	55.7
Std Dev	2.874



B0L001S, U22-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch89, adc0

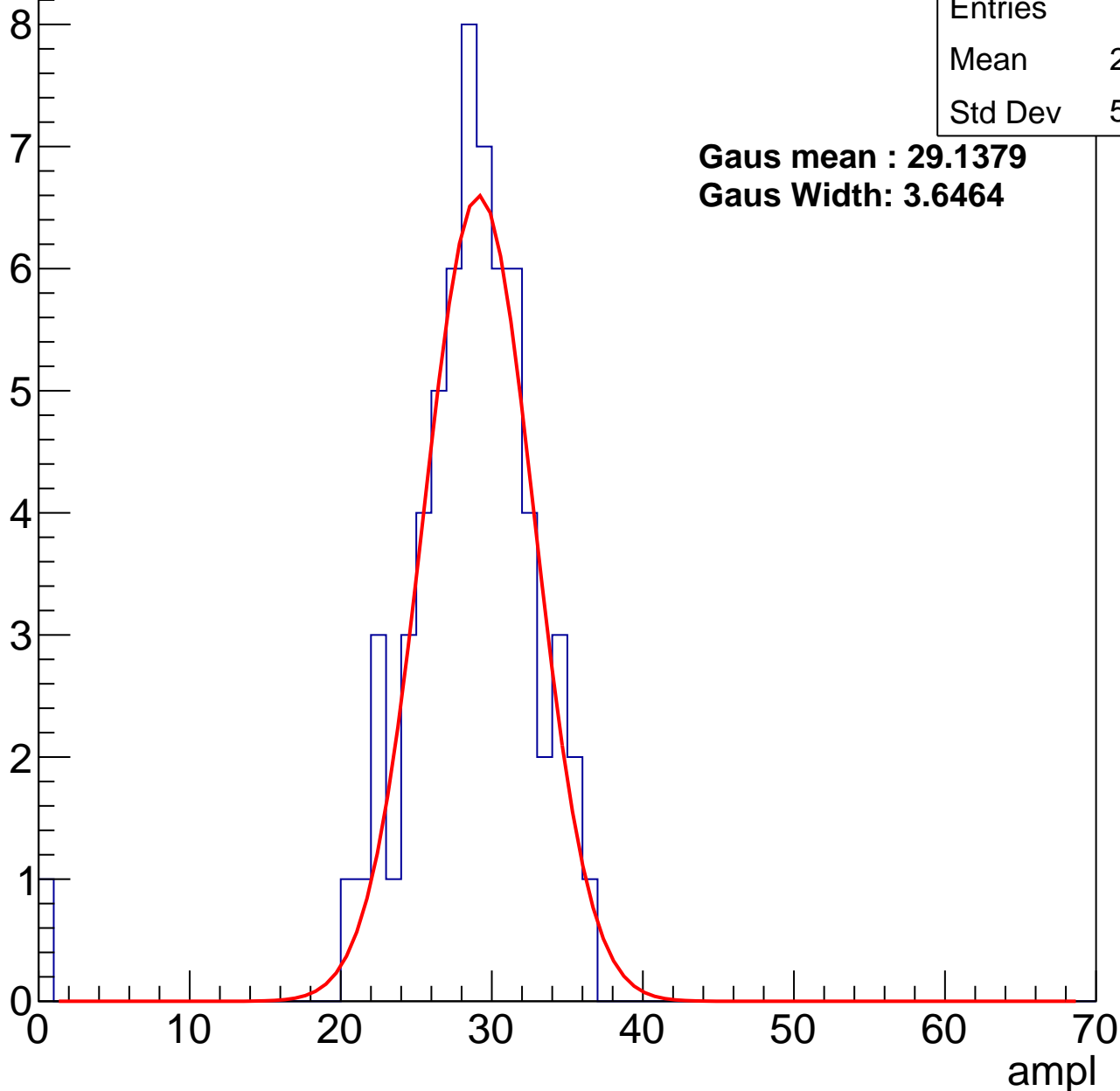
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	27.95
Std Dev	5.014

Gaus mean : 29.1379

Gaus Width: 3.6464



B0L001S, U22-ch89, adc1

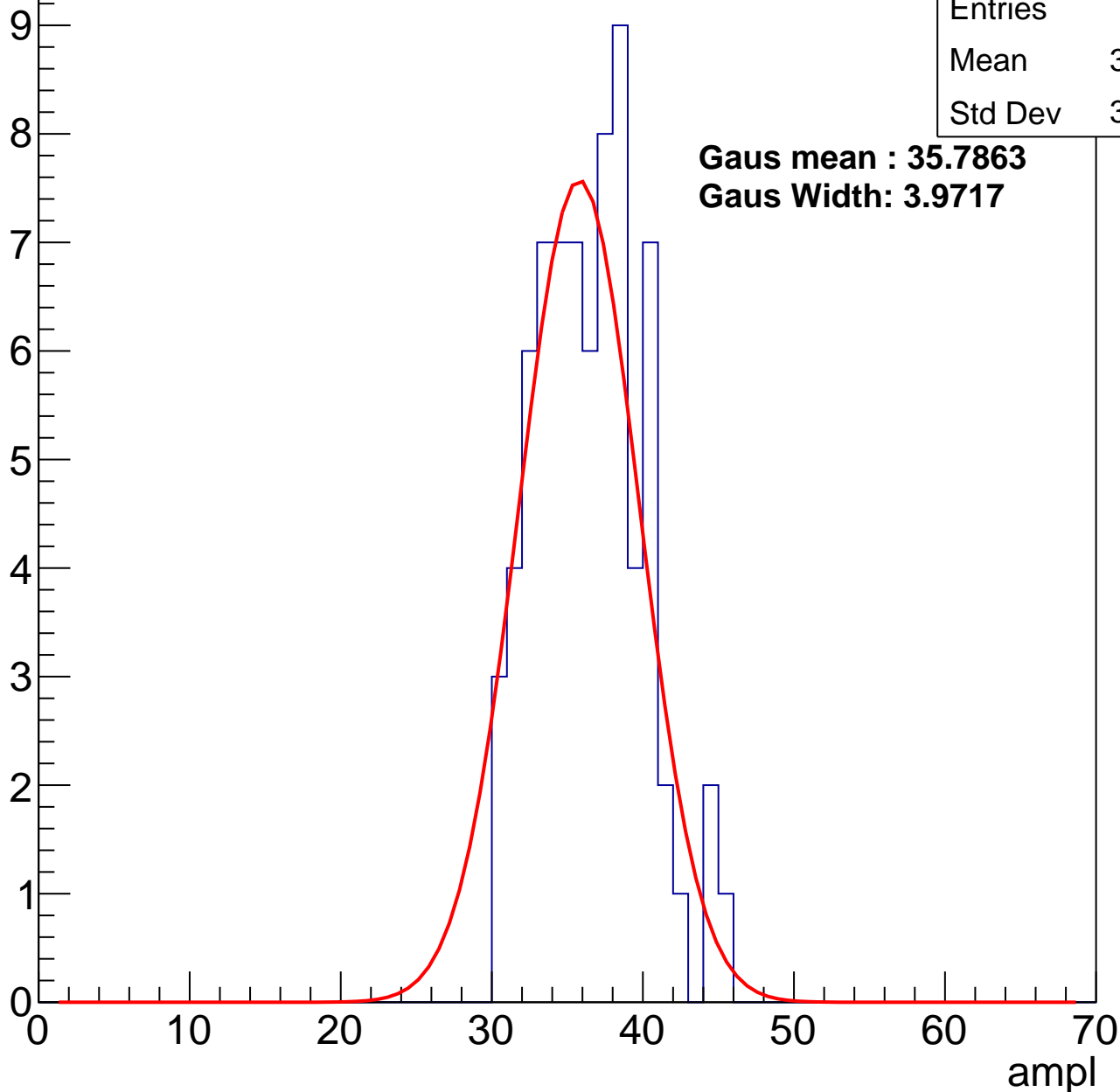
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	36.04
Std Dev	3.466

Gaus mean : 35.7863

Gaus Width: 3.9717



B0L001S, U22-ch89, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	60
Mean	42.32
Std Dev	3.462

Gaus mean : 43.1248

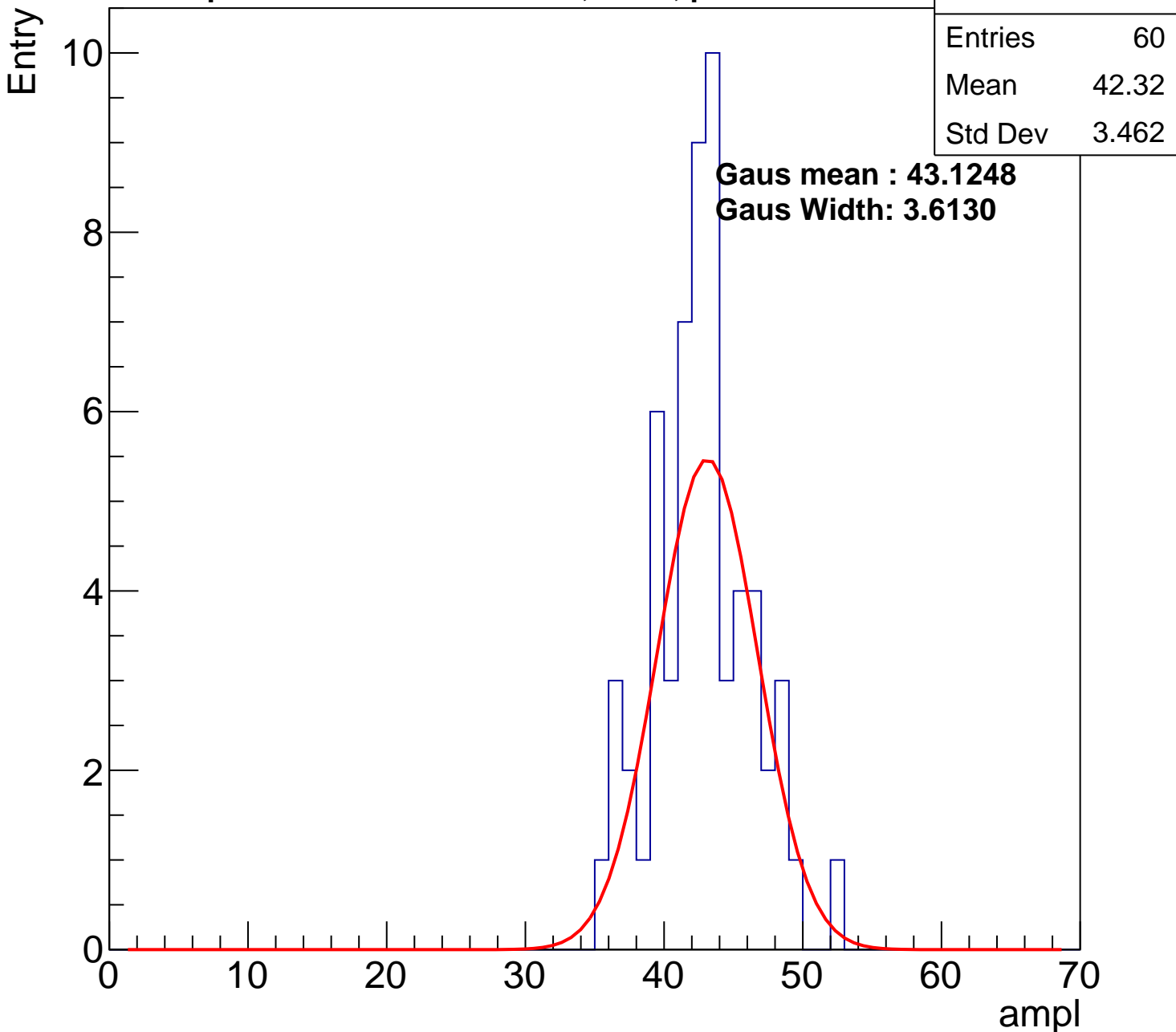
Gaus Width: 3.6130

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

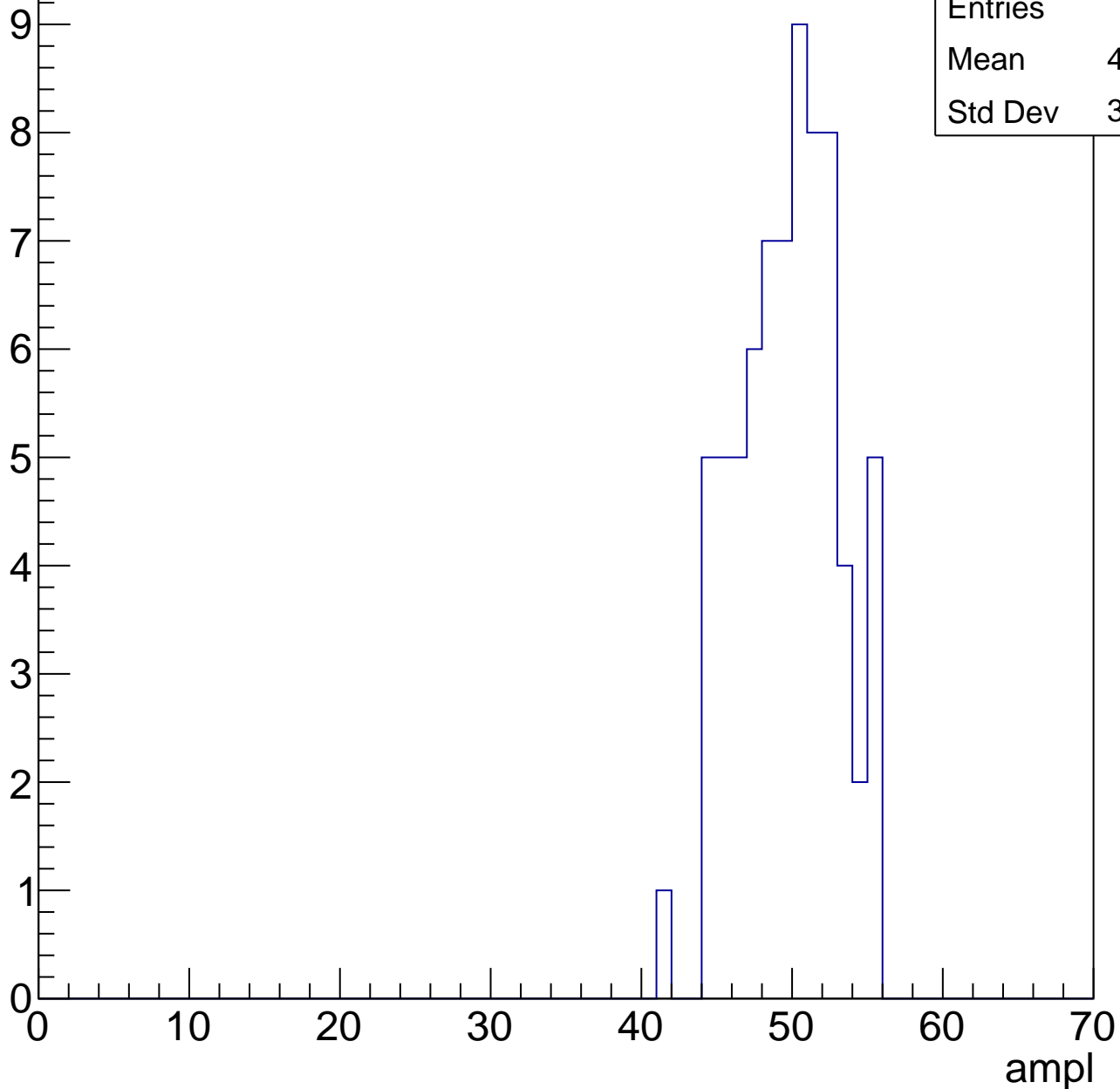


B0L001S, U22-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

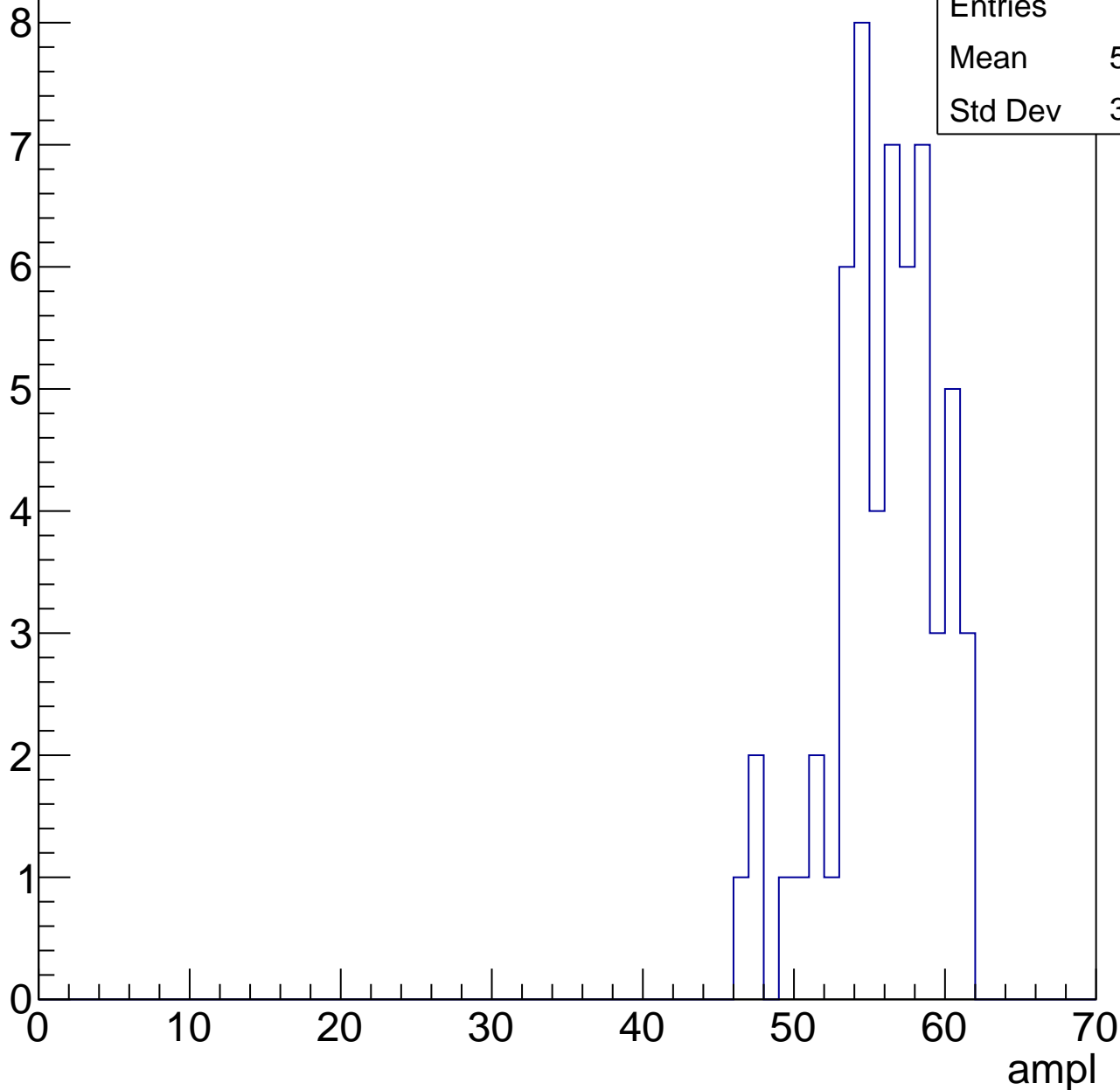
Entries	72
Mean	49.25
Std Dev	3.222



B0L001S, U22-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



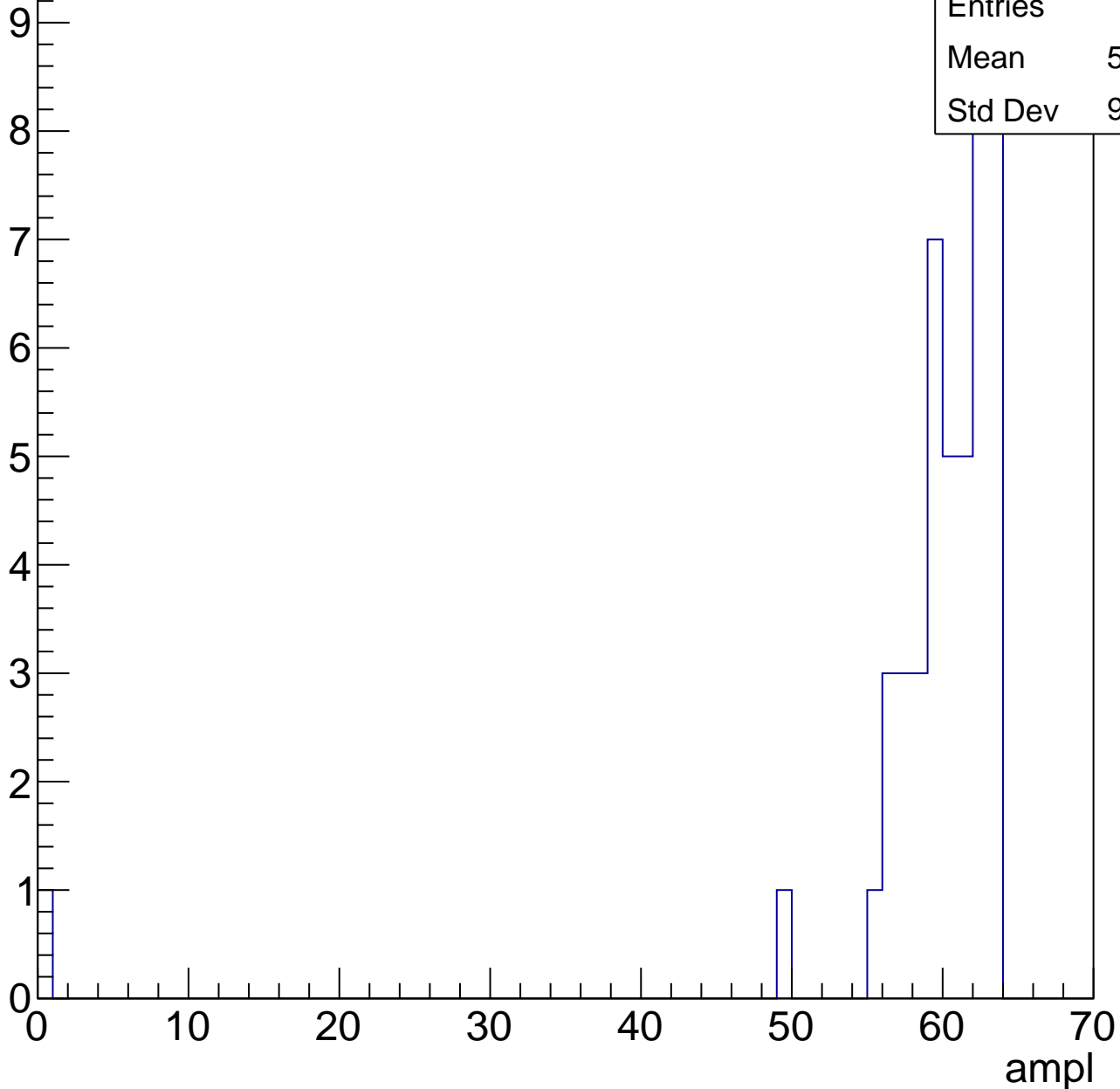
Entries	57
Mean	55.49
Std Dev	3.515

B0L001S, U22-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

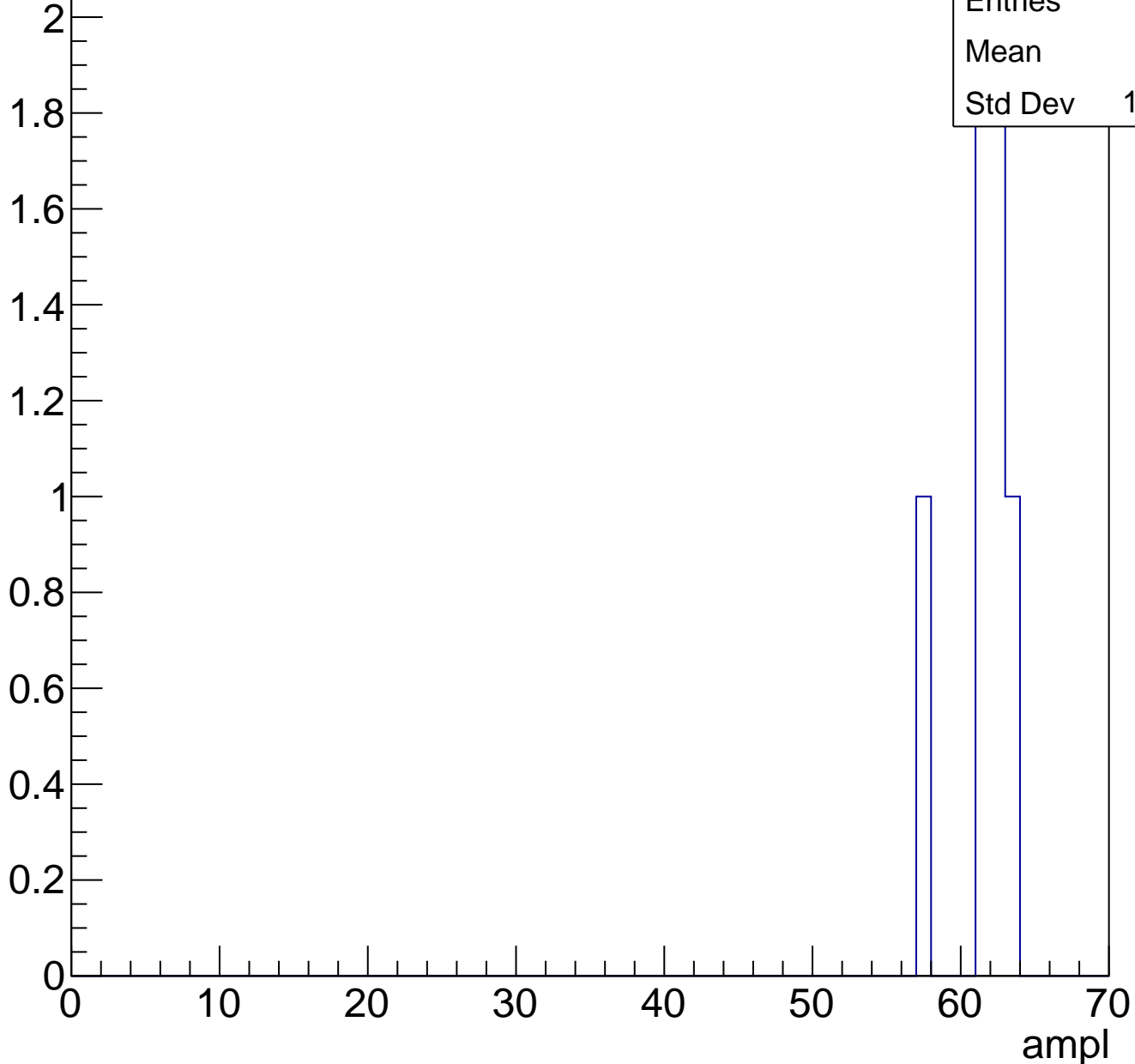
Entries	46
Mean	58.65
Std Dev	9.178



B0L001S, U22-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch90, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	29.85
Std Dev	4.954

Gaus mean : 31.2372

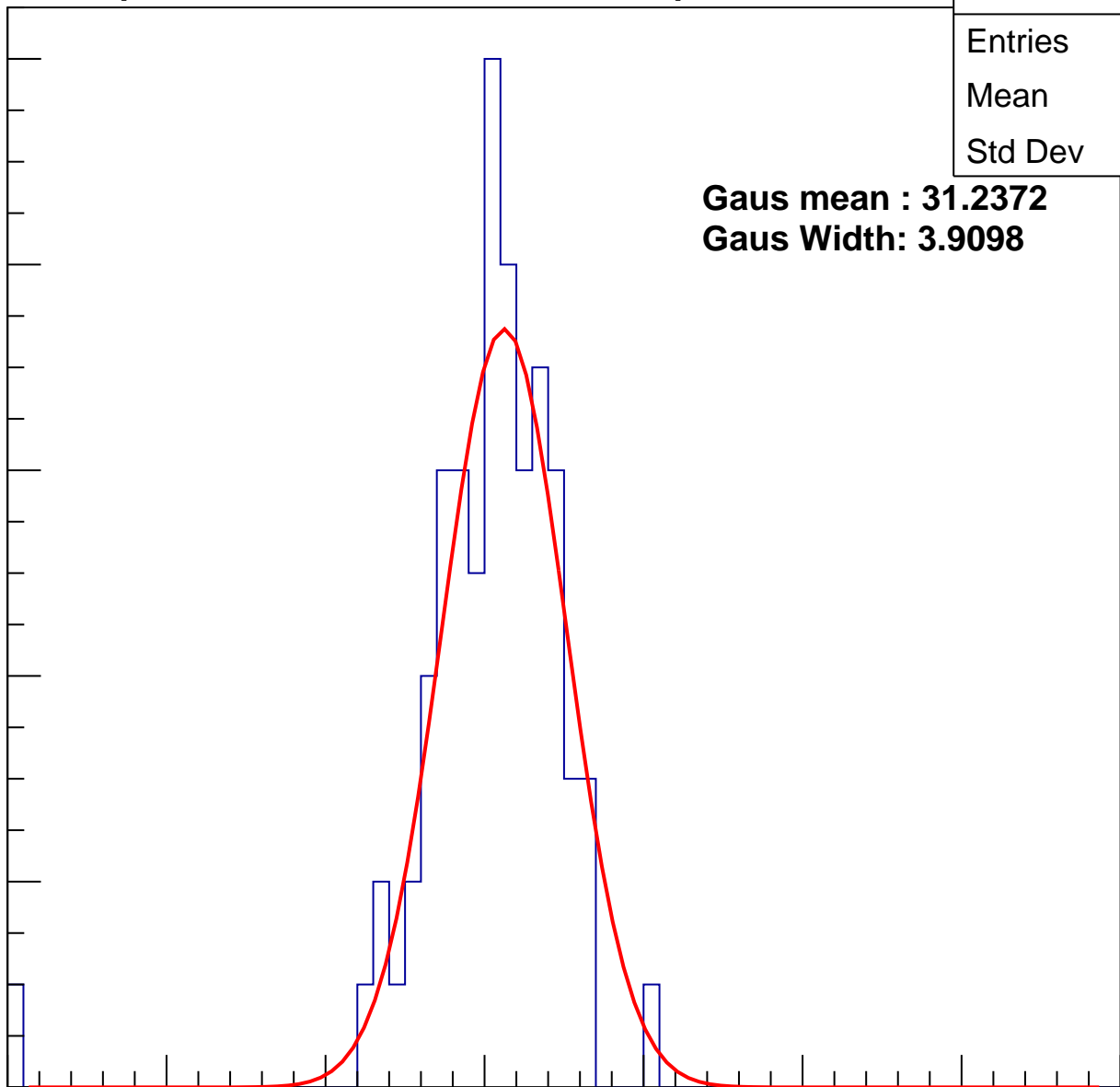
Gaus Width: 3.9098

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



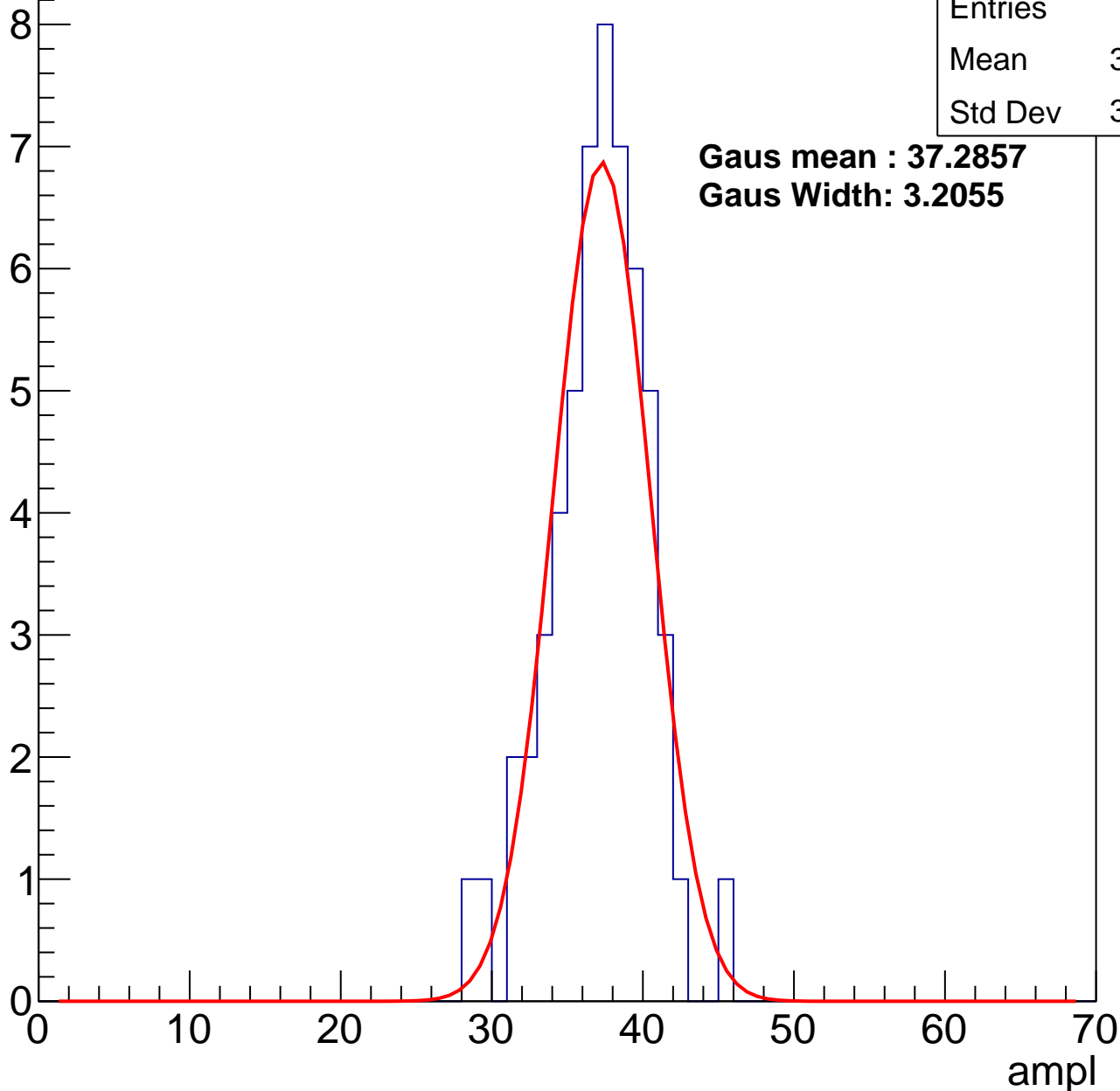
B0L001S, U22-ch90, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	36.62
Std Dev	3.227

Gaus mean : 37.2857
Gaus Width: 3.2055



B0L001S, U22-ch90, adc2

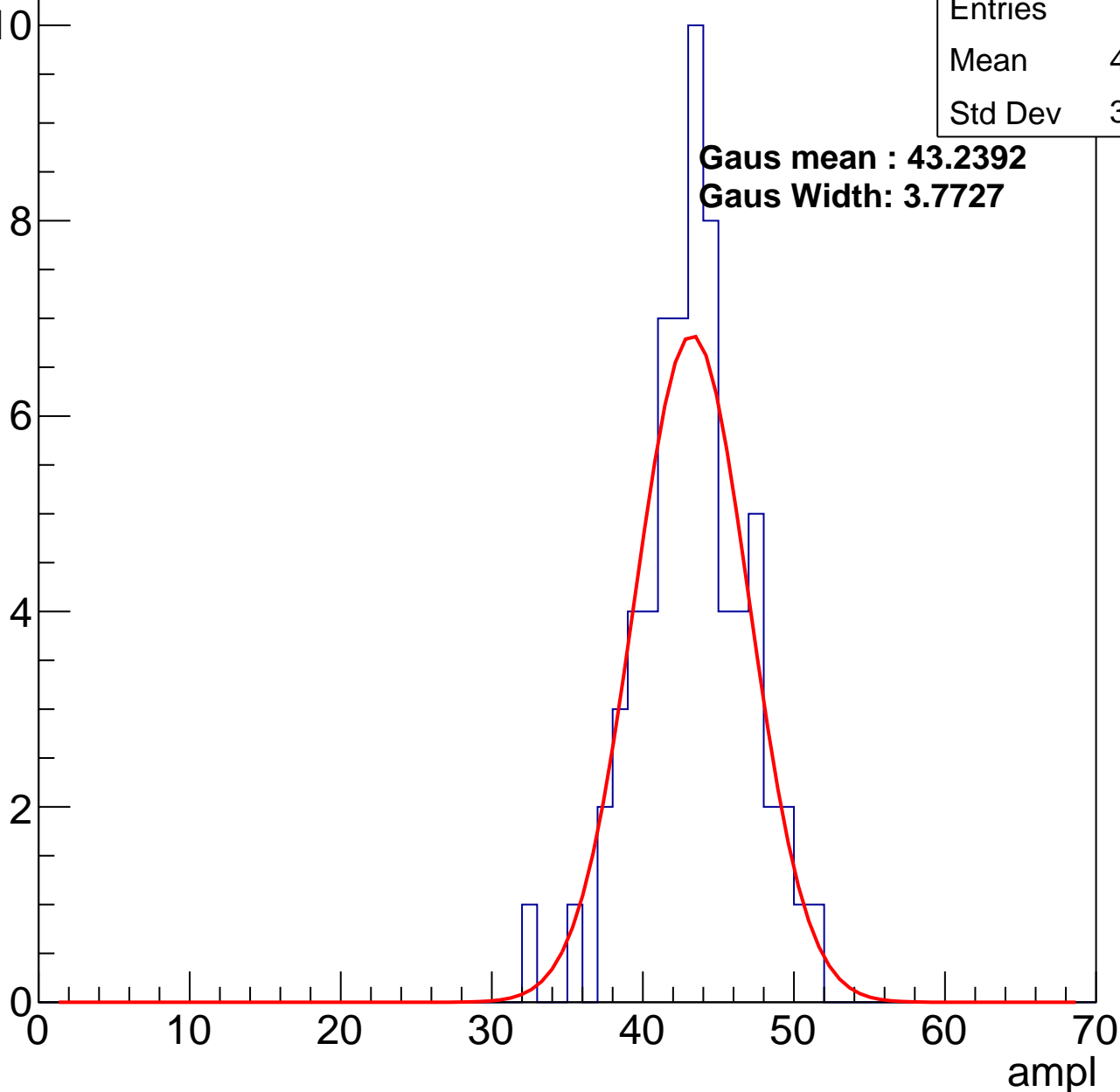
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	42.85
Std Dev	3.569

Gaus mean : 43.2392

Gaus Width: 3.7727

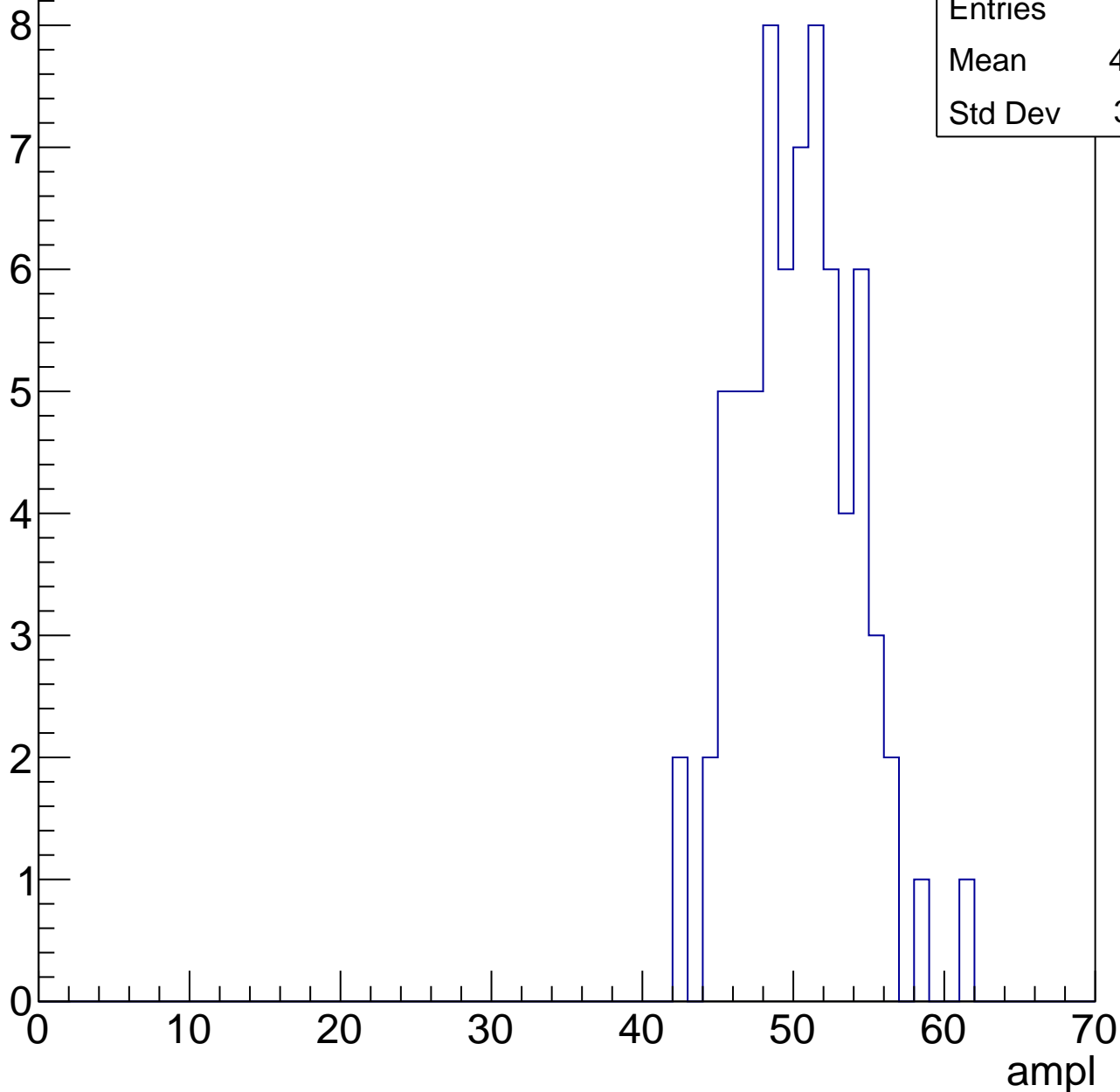


B0L001S, U22-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

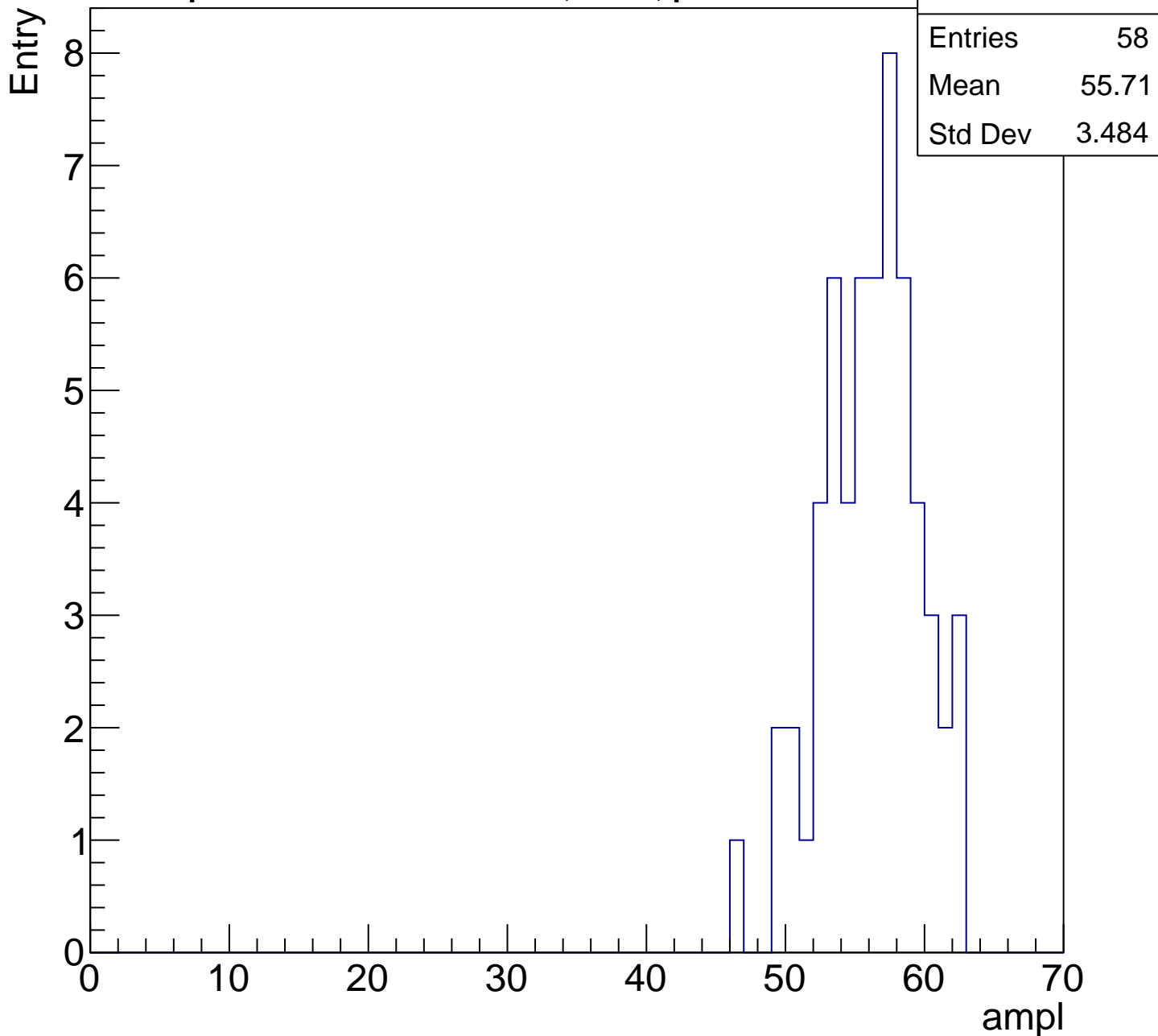
Entry

Entries	71
Mean	49.89
Std Dev	3.721



B0L001S, U22-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

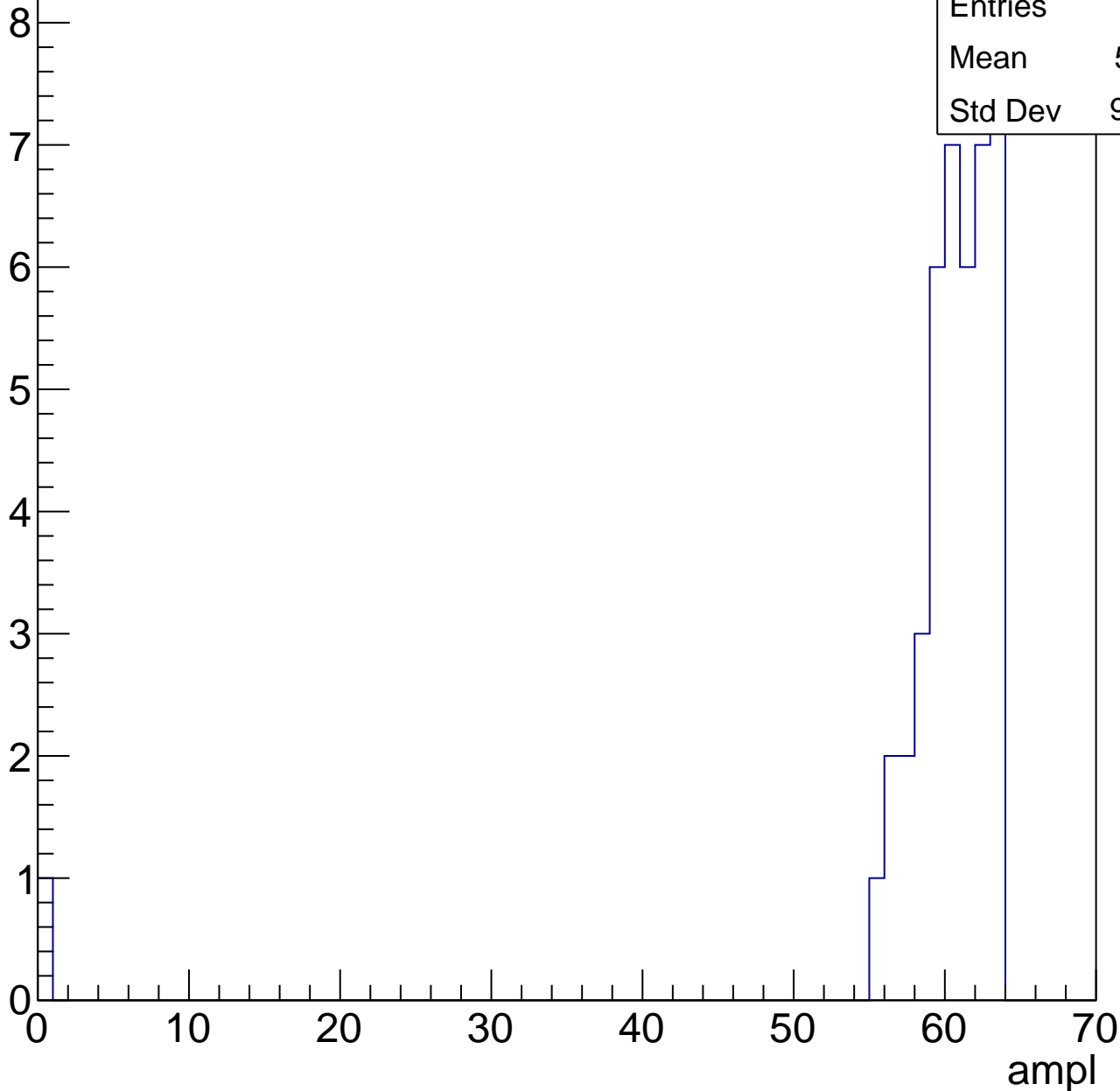


B0L001S, U22-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.91
Std Dev	9.336



B0L001S, U22-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch91, adc0

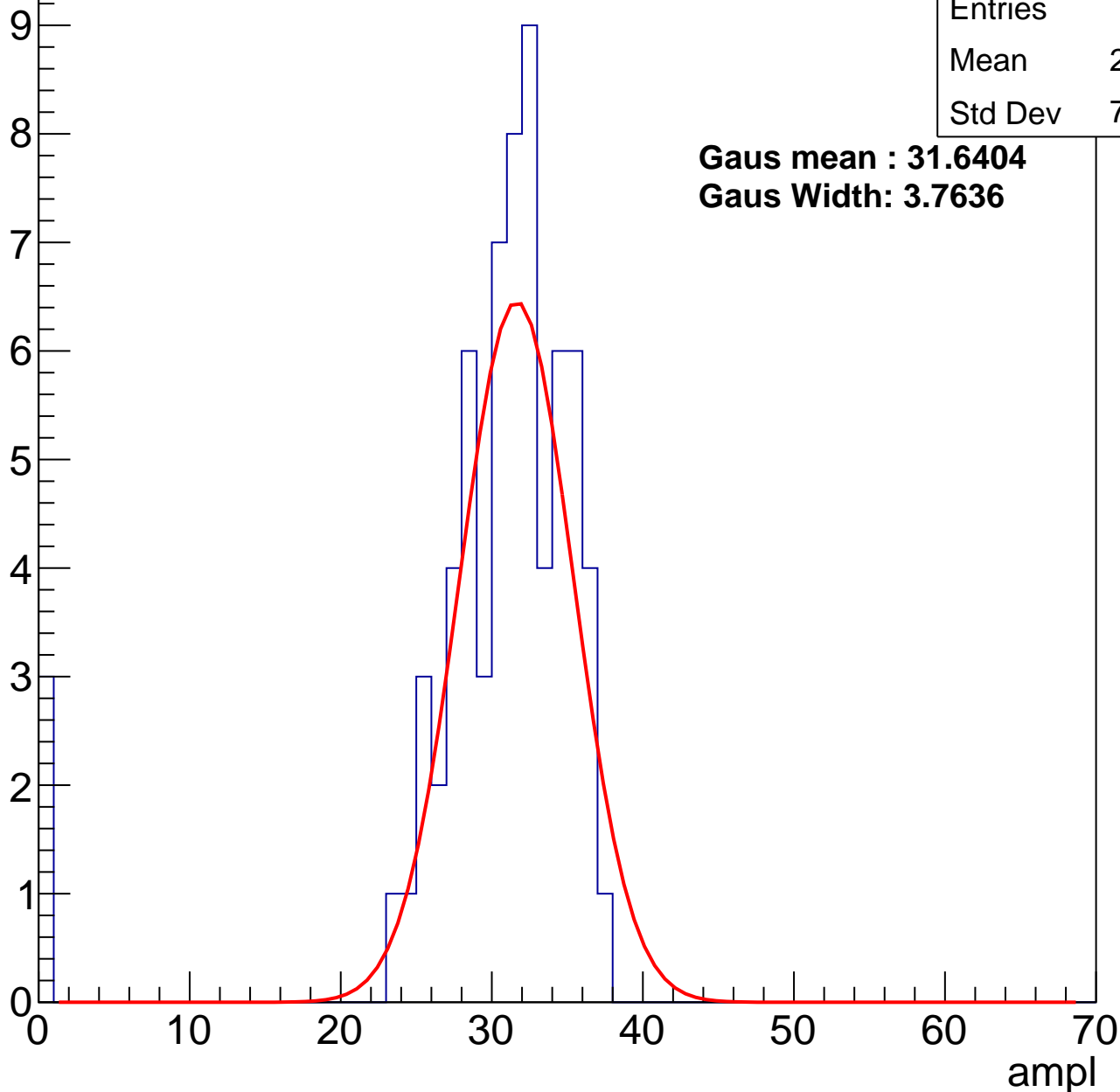
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.56
Std Dev	7.134

Gaus mean : 31.6404

Gaus Width: 3.7636



B0L001S, U22-ch91, adc1

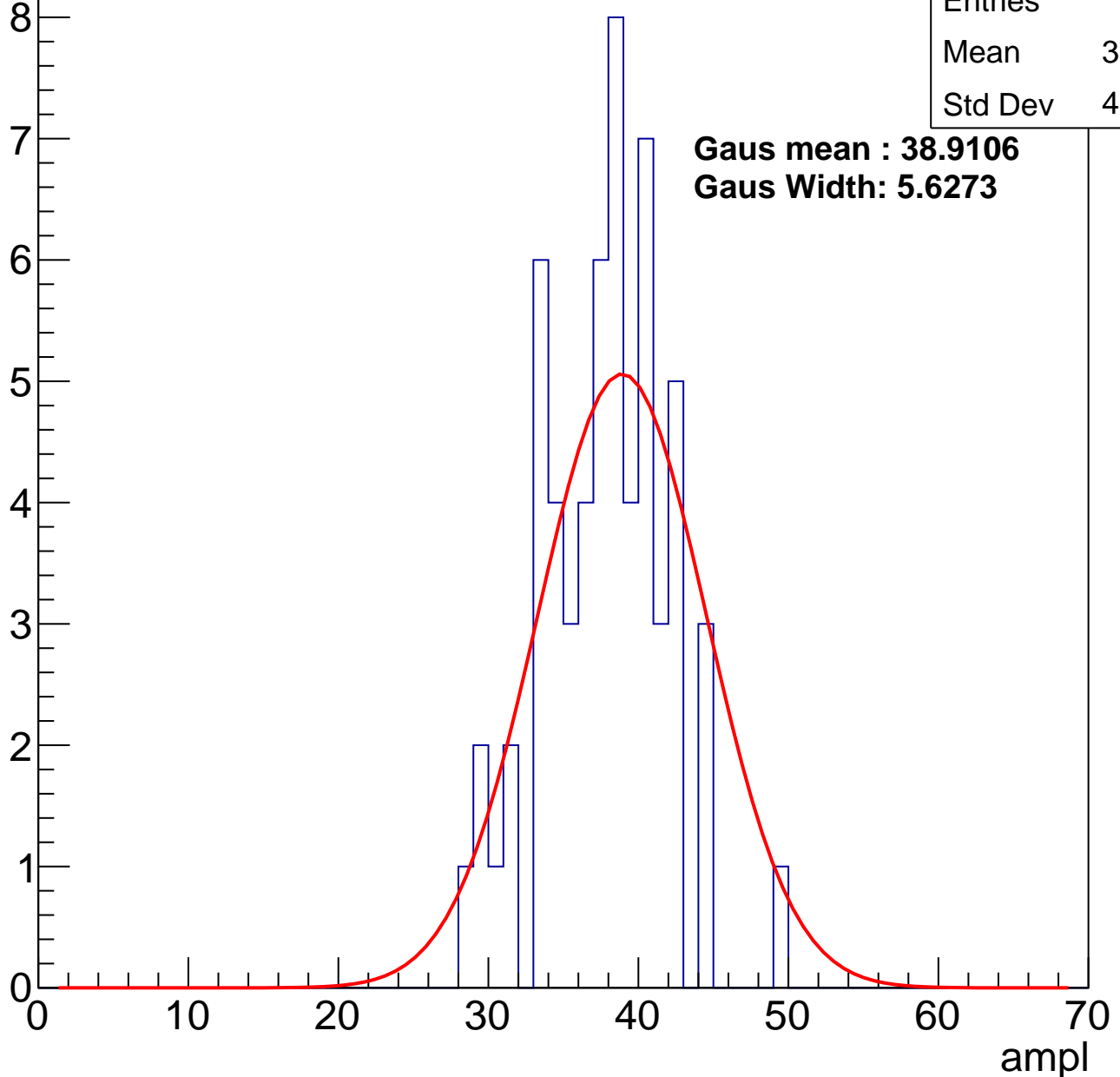
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.28
Std Dev	4.132

Gaus mean : 38.9106

Gaus Width: 5.6273



B0L001S, U22-ch91, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	43.21
Std Dev	3.565

Gaus mean : 43.4352

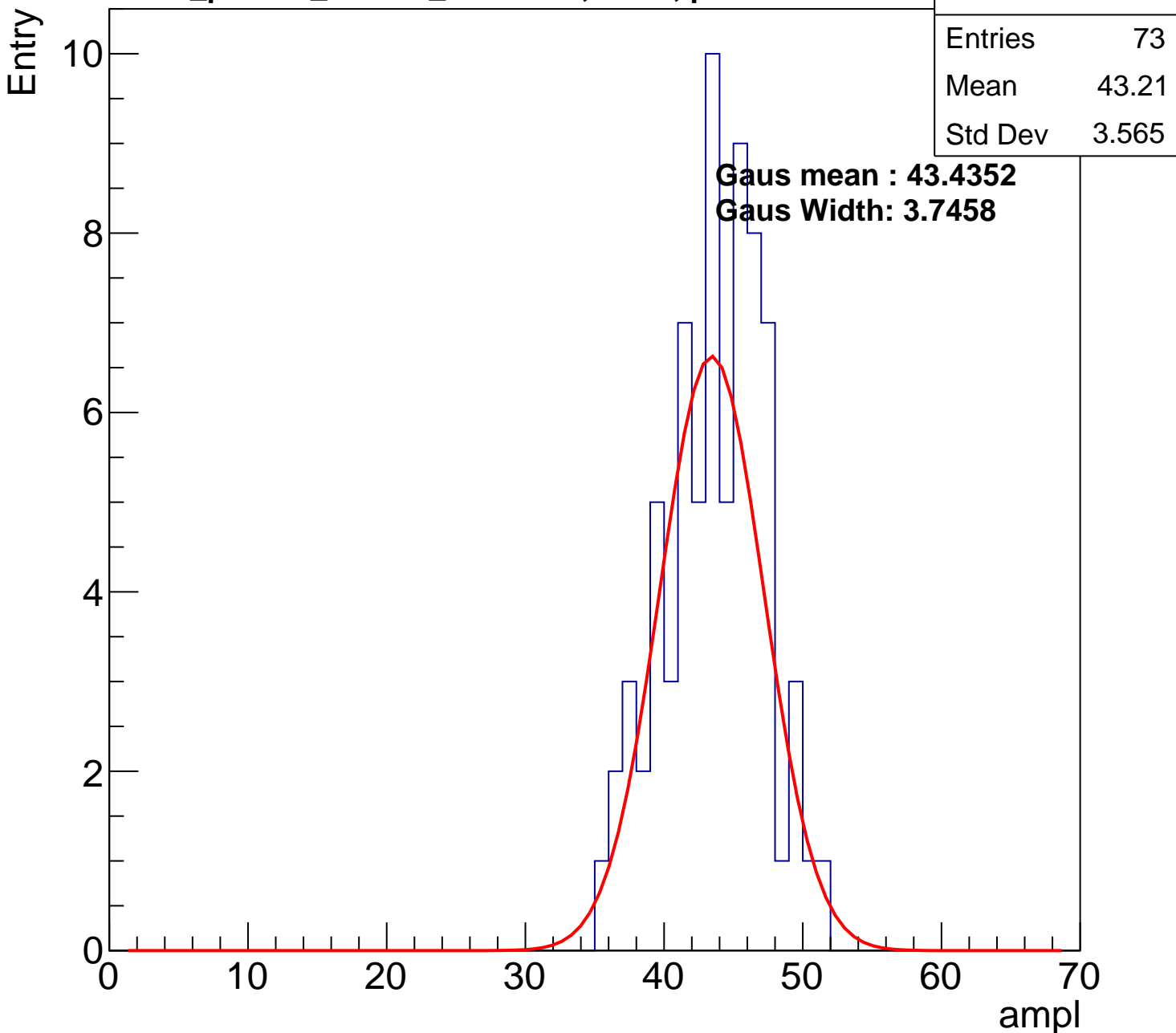
Gaus Width: 3.7458

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

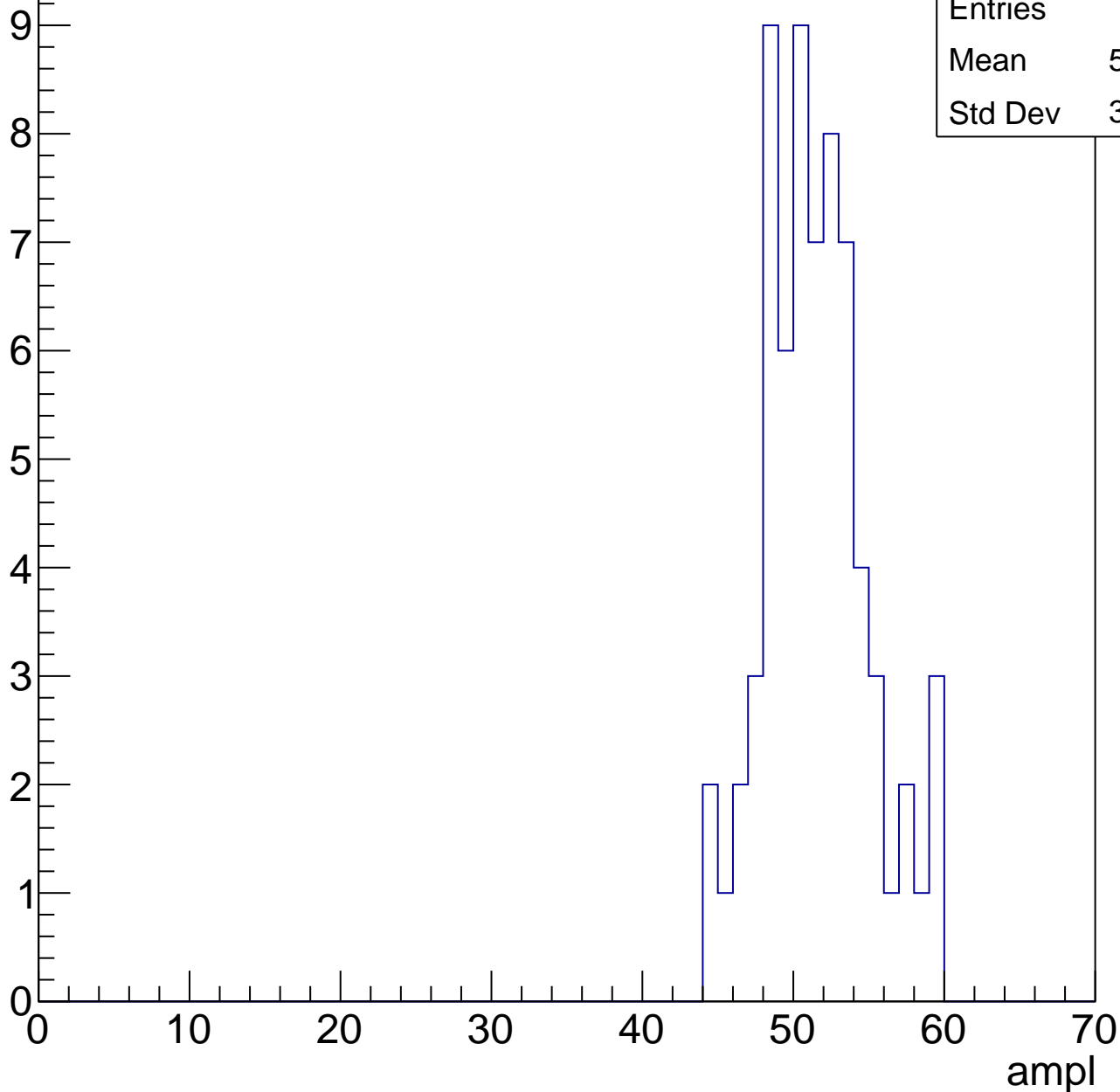


B0L001S, U22-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.06
Std Dev	3.438

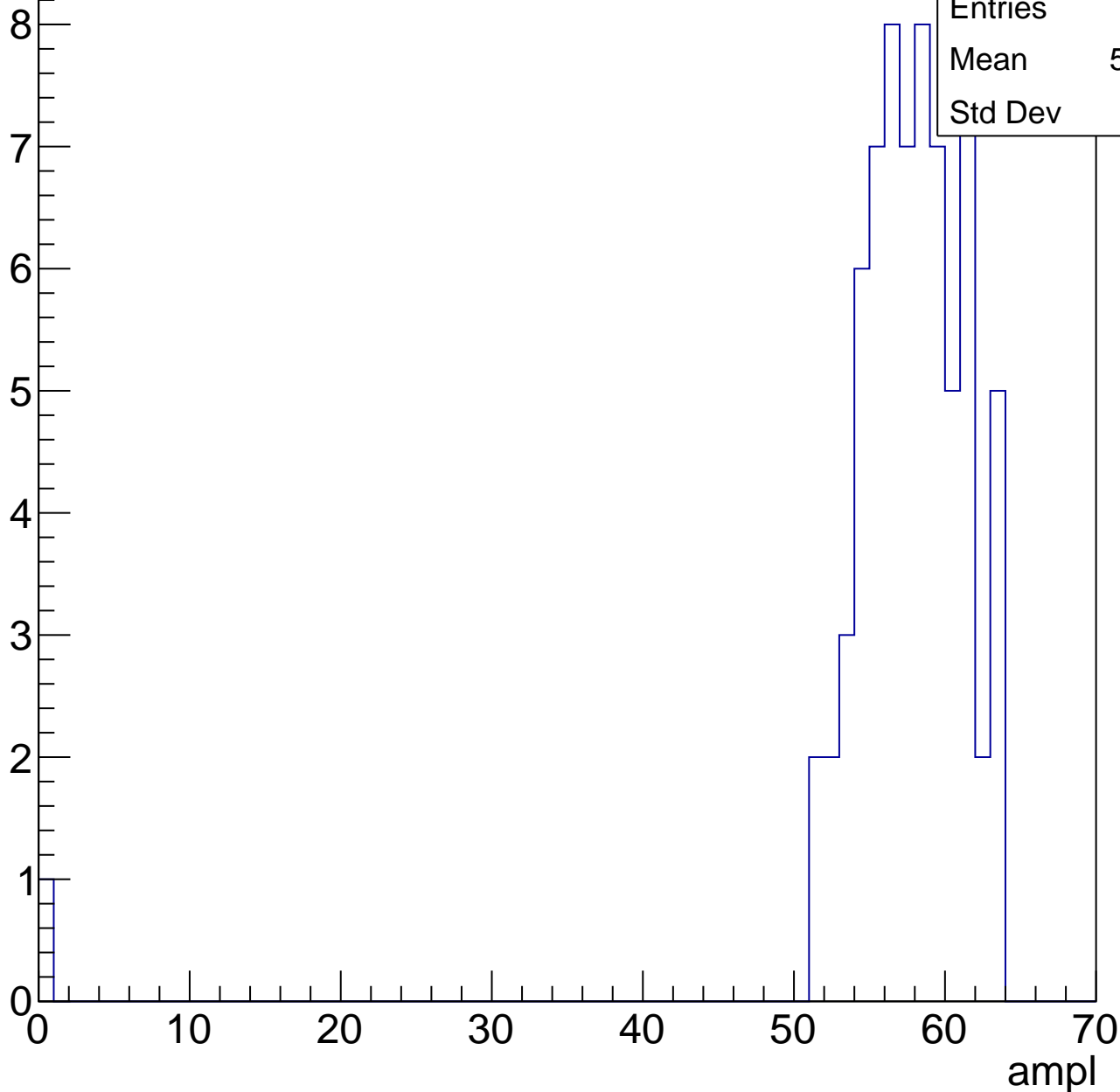


B0L001S, U22-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.69
Std Dev	7.45

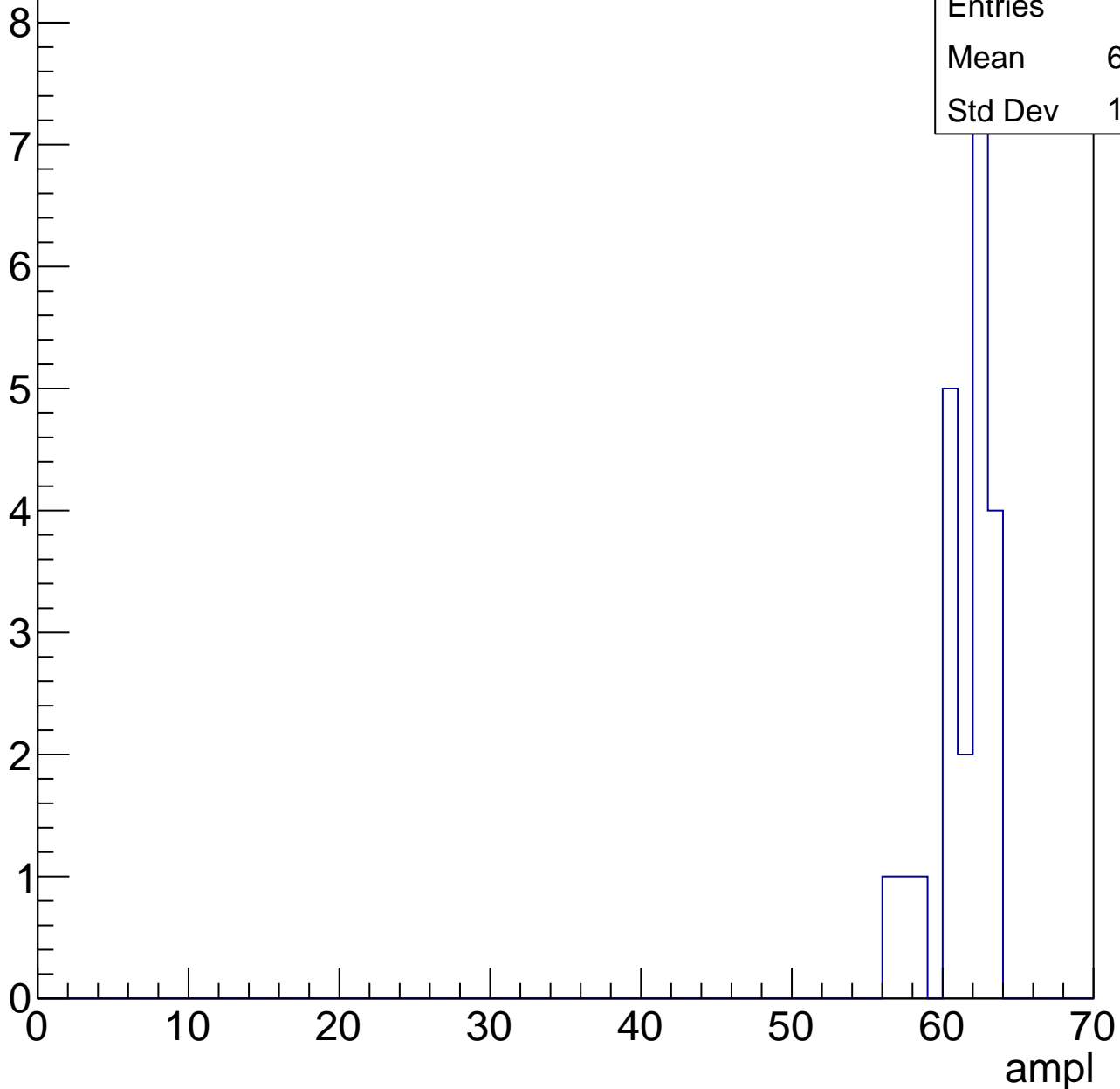


B0L001S, U22-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	60.95
Std Dev	1.894



B0L001S, U22-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch92, adc0

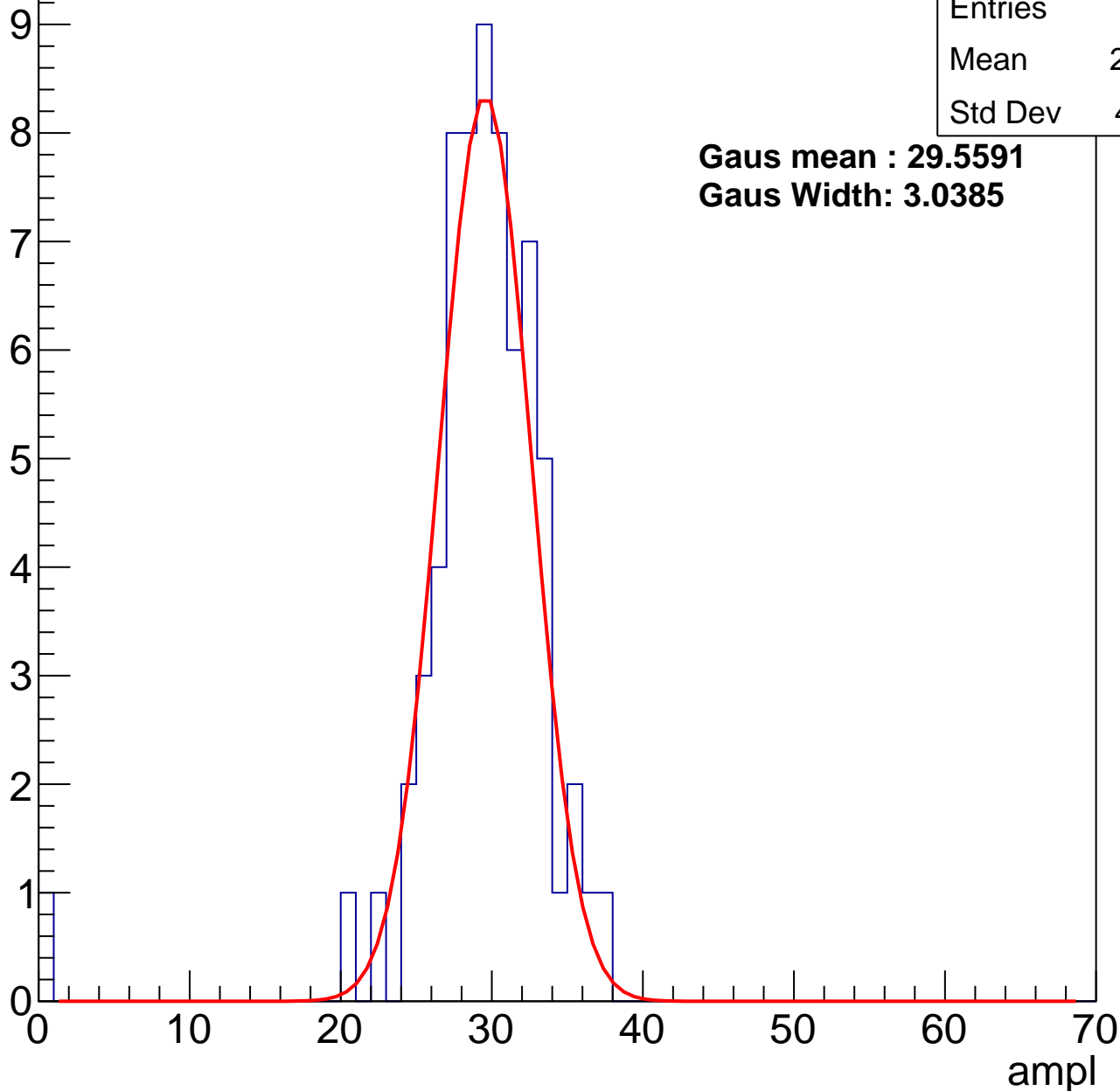
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.85
Std Dev	4.741

Gaus mean : 29.5591

Gaus Width: 3.0385



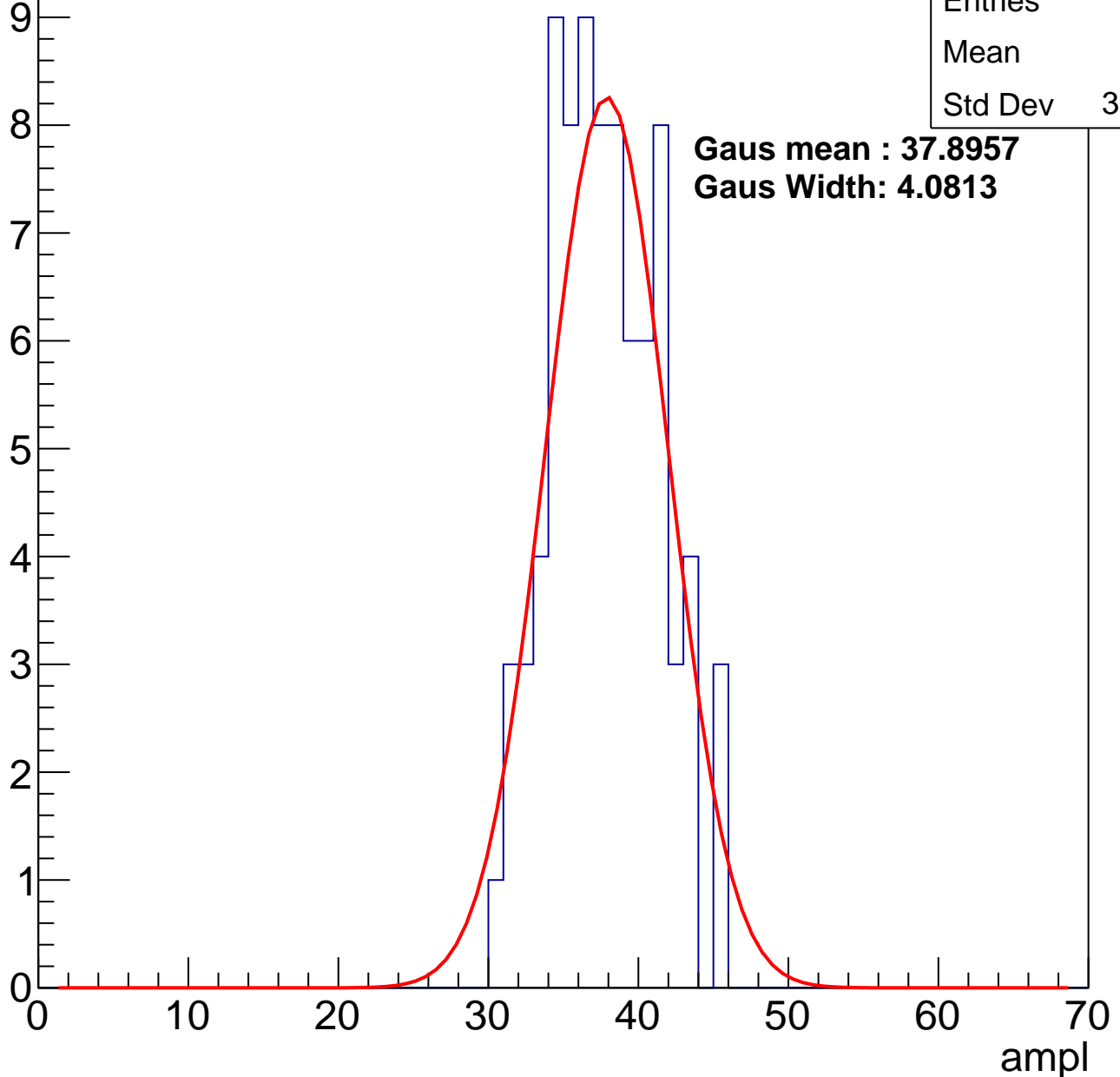
B0L001S, U22-ch92, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	37.3
Std Dev	3.532

Gaus mean : 37.8957
Gaus Width: 4.0813



B0L001S, U22-ch92, adc2

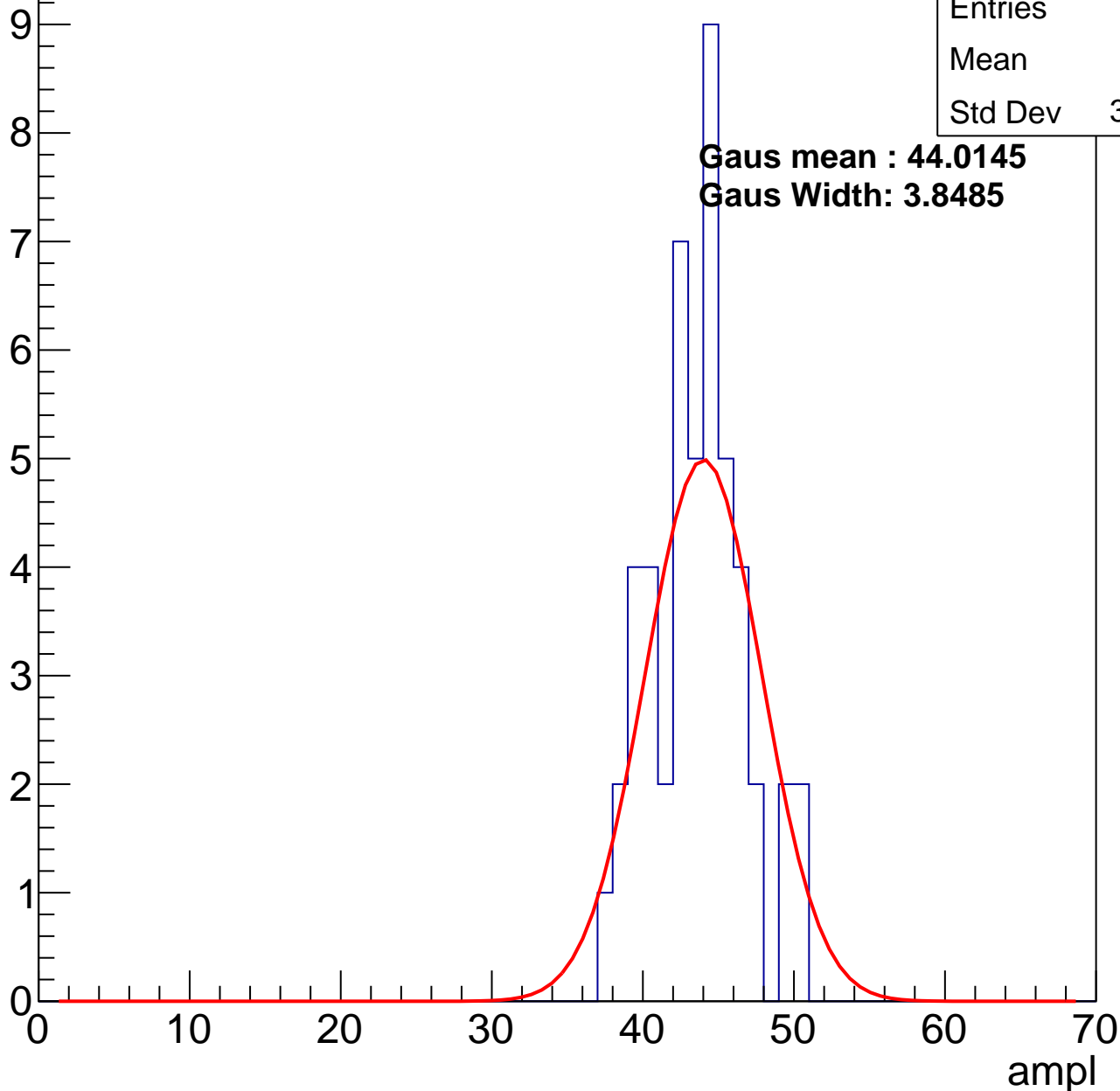
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	43.2
Std Dev	3.084

Gaus mean : 44.0145

Gaus Width: 3.8485

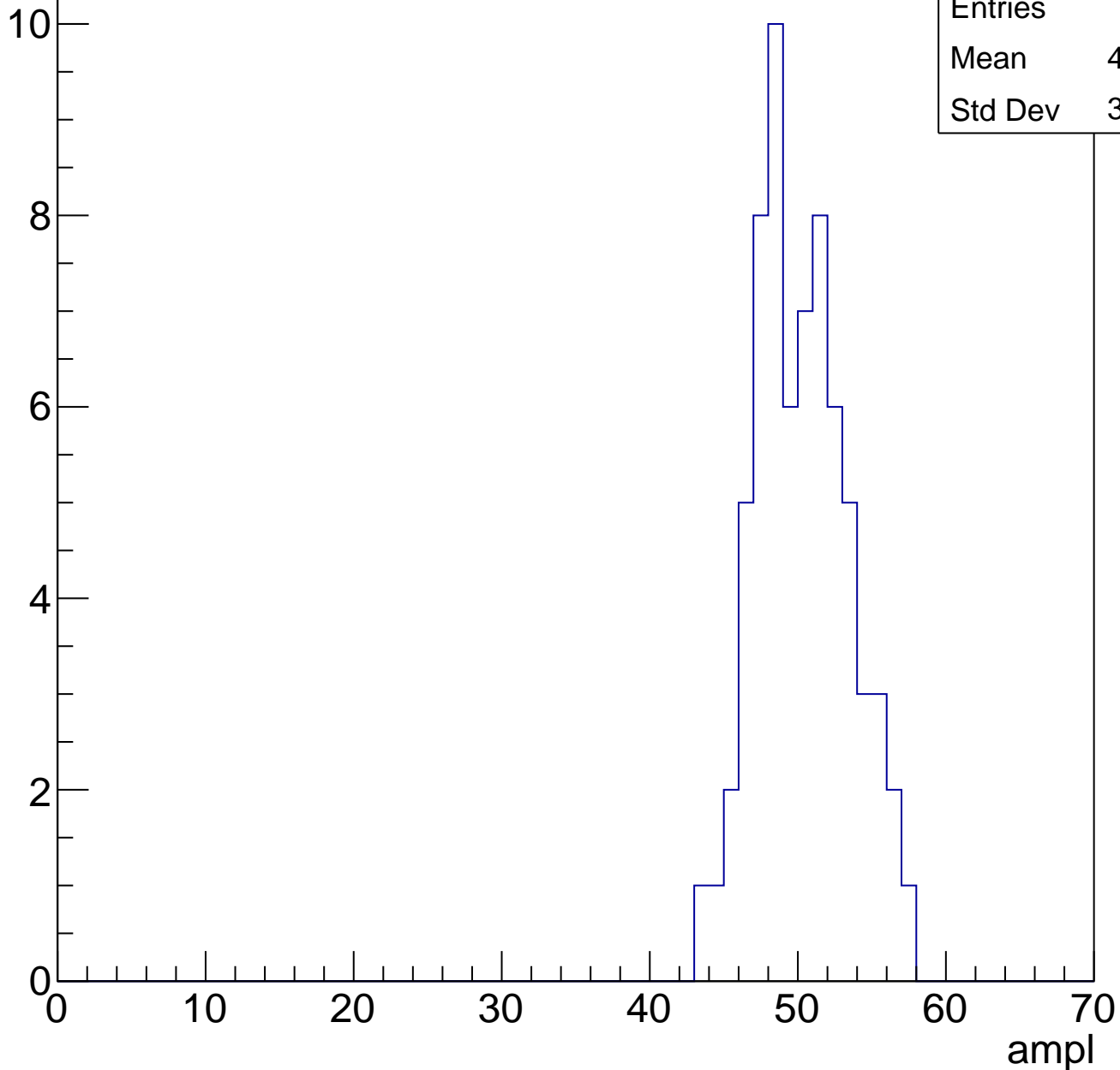


B0L001S, U22-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	68
Mean	49.82
Std Dev	3.096

Entry

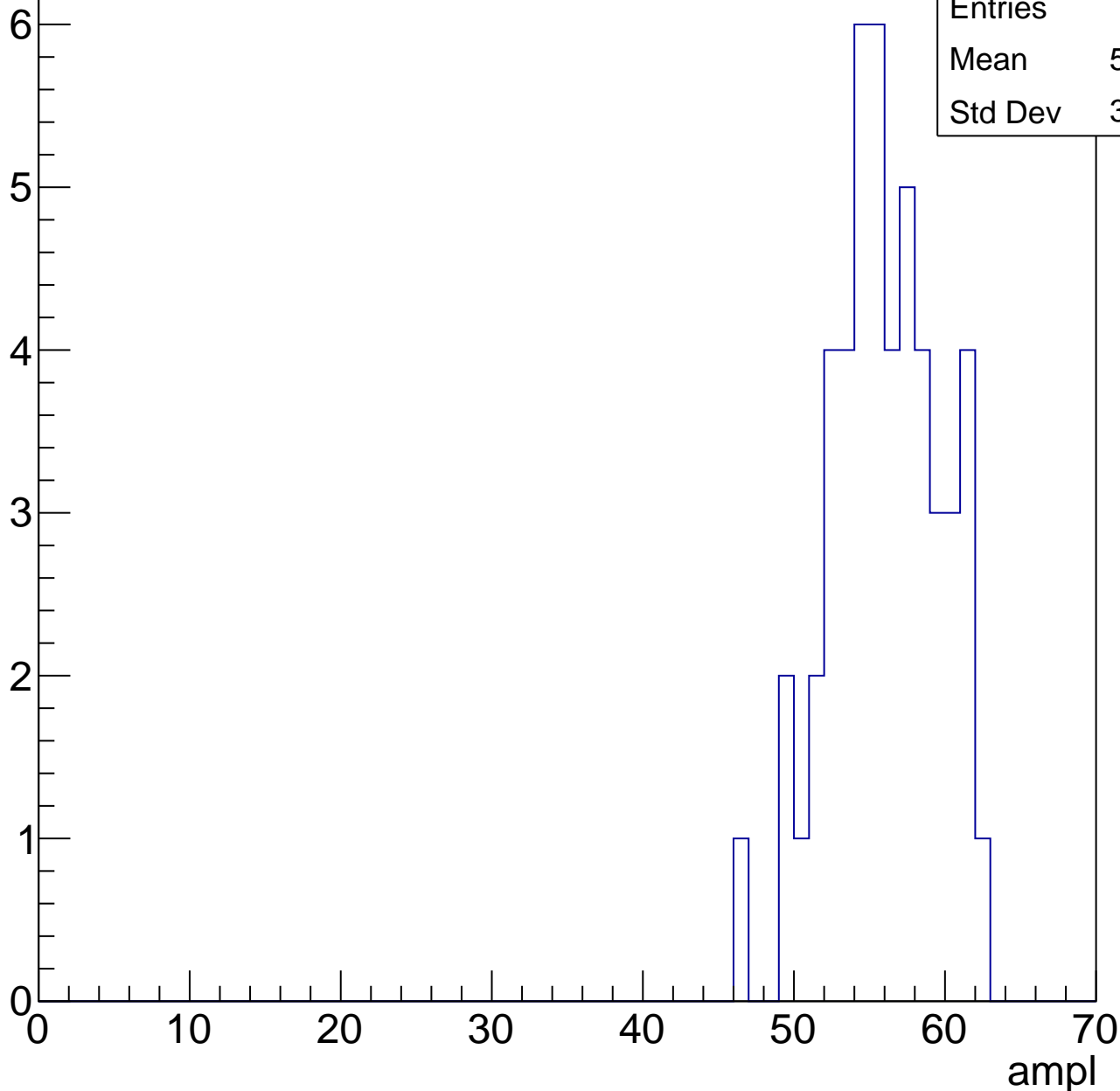


B0L001S, U22-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	55.48
Std Dev	3.557



B0L001S, U22-ch92, adc5

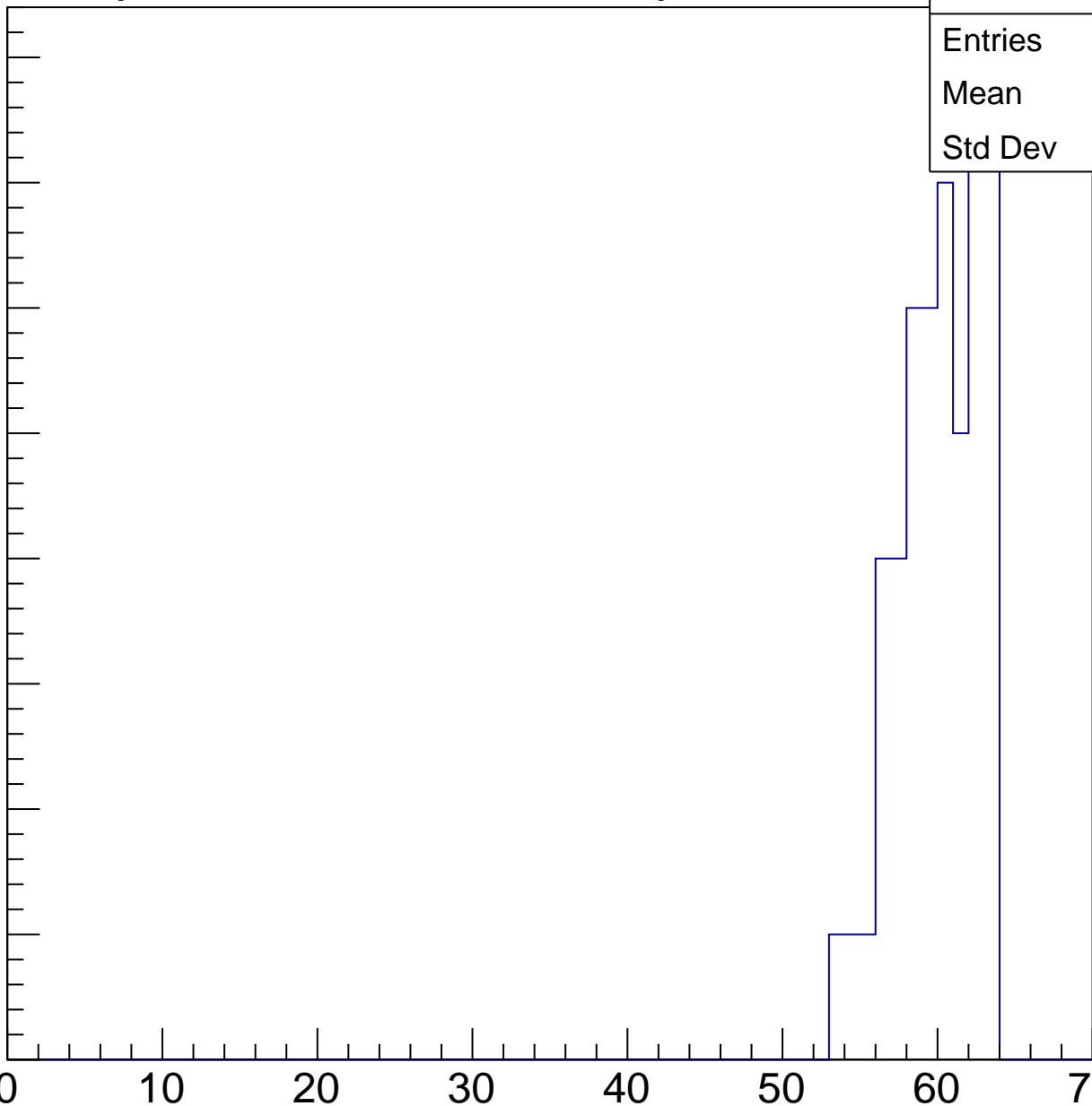
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.63
Std Dev	2.589

ampl



B0L001S, U22-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

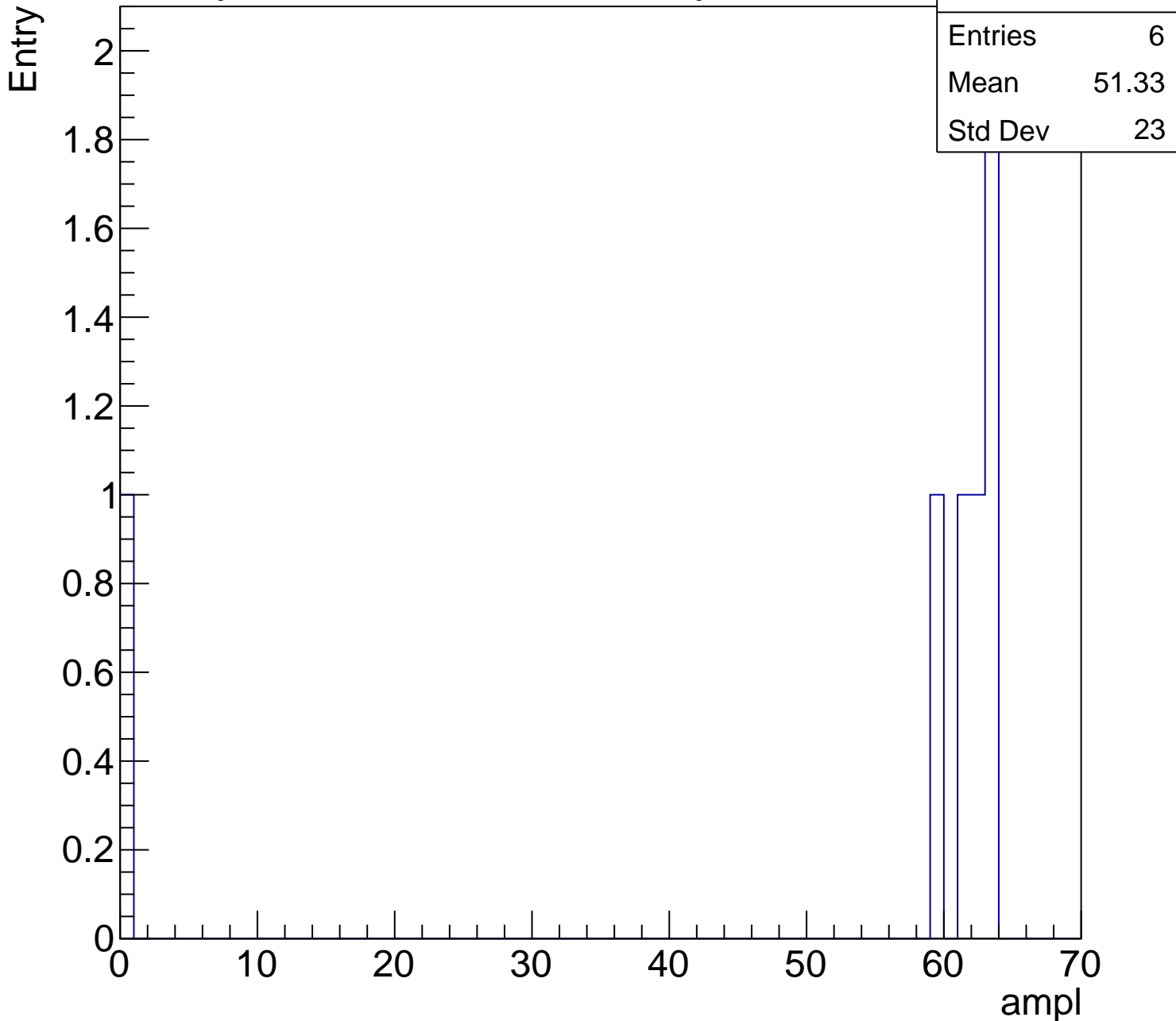
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.33
Std Dev	23

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch93, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	27.39
Std Dev	7.857

Gaus mean : 29.3327

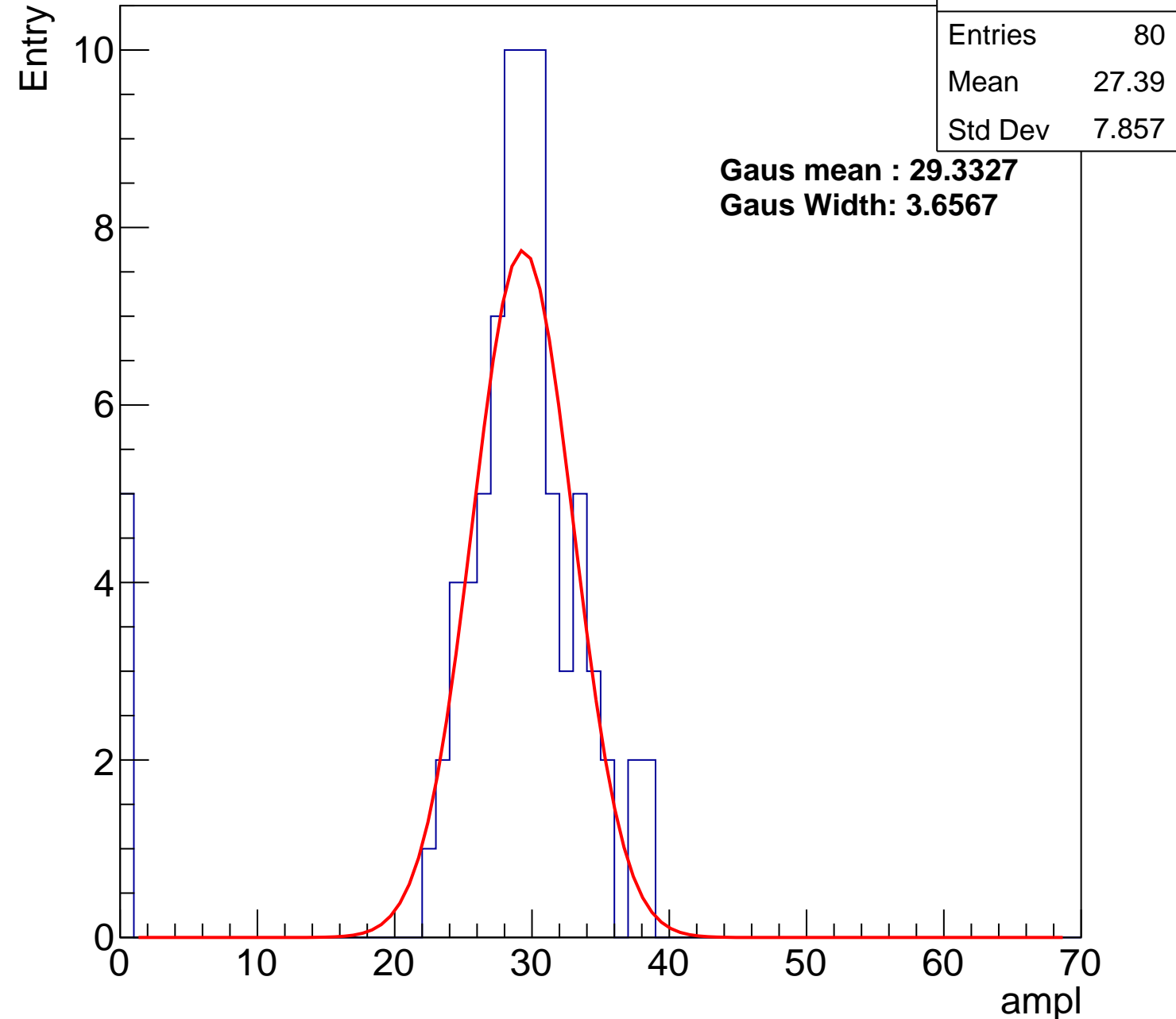
Gaus Width: 3.6567

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

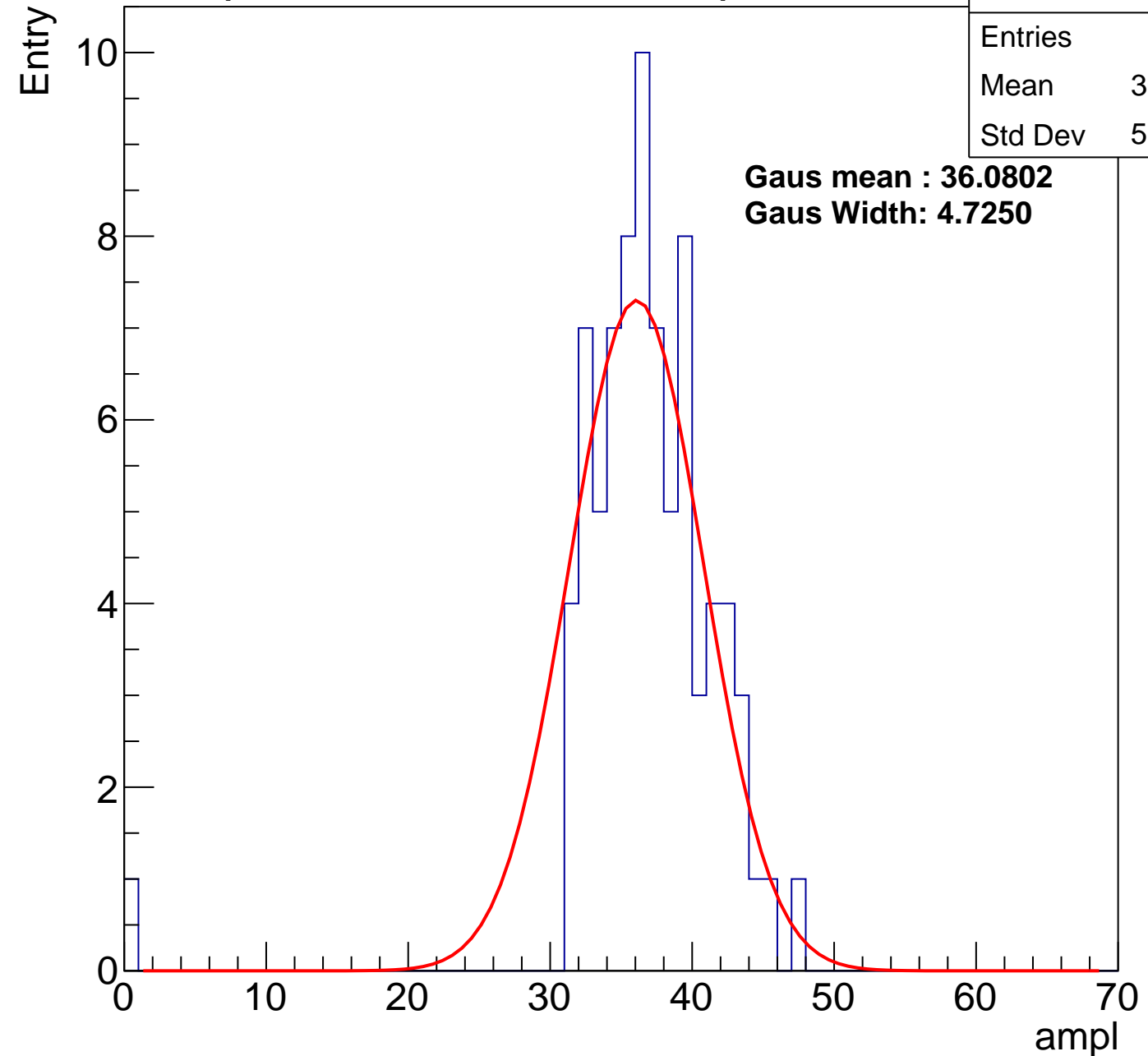


B0L001S, U22-ch93, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	36.32
Std Dev	5.495

Gaus mean : 36.0802
Gaus Width: 4.7250



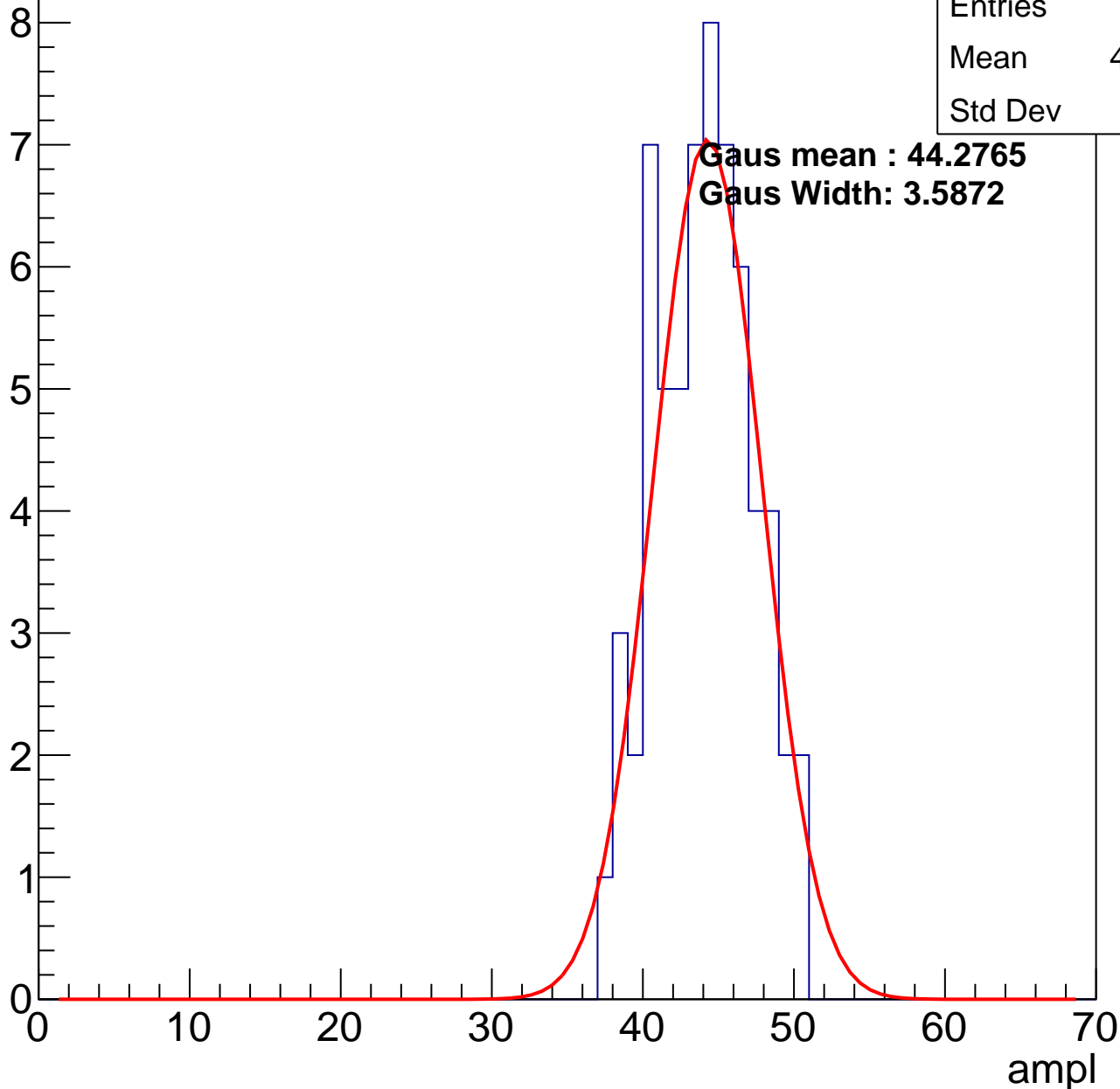
B0L001S, U22-ch93, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.59
Std Dev	3.17

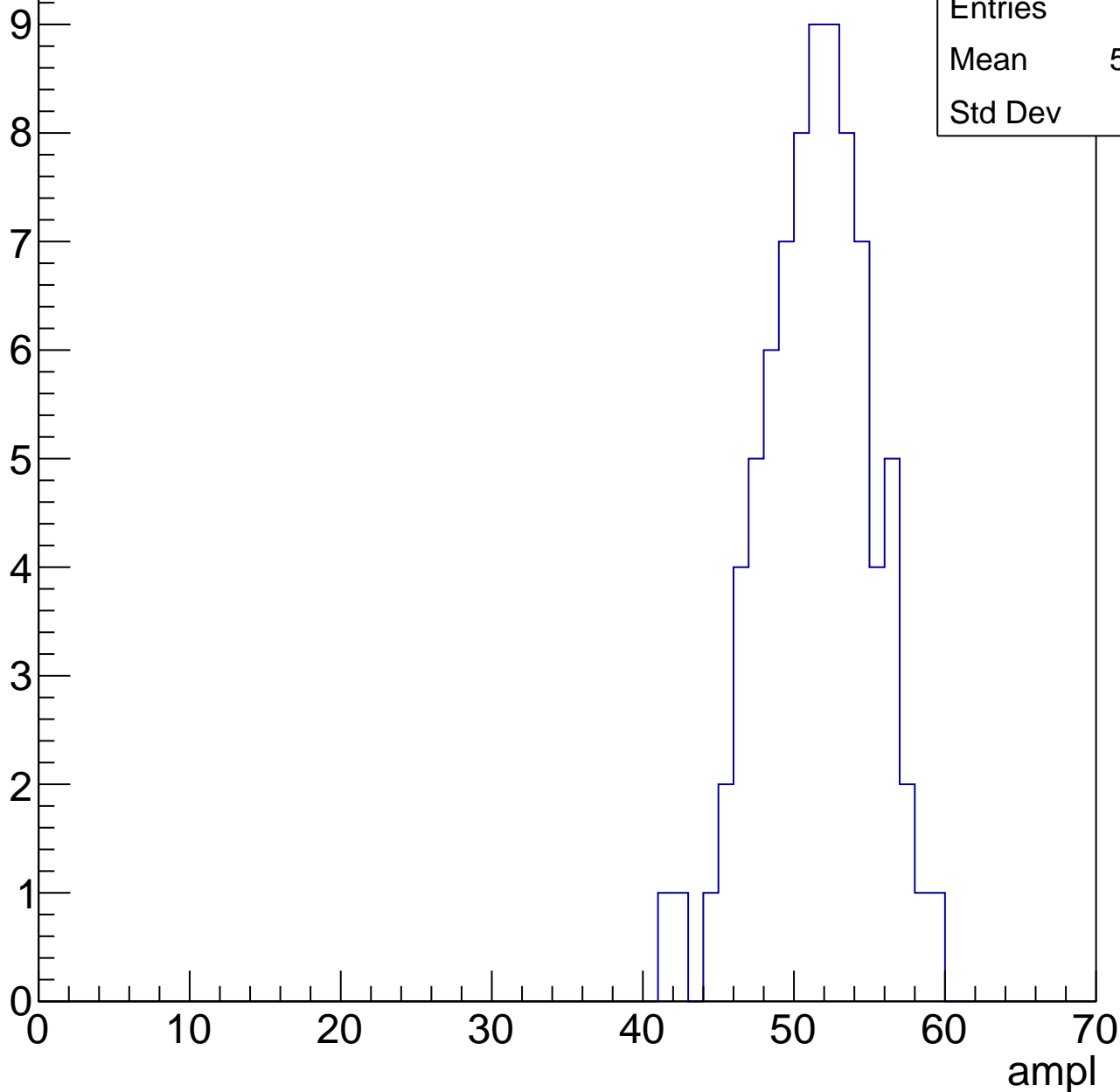
Gaus mean : 44.2765
Gaus Width: 3.5872



B0L001S, U22-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

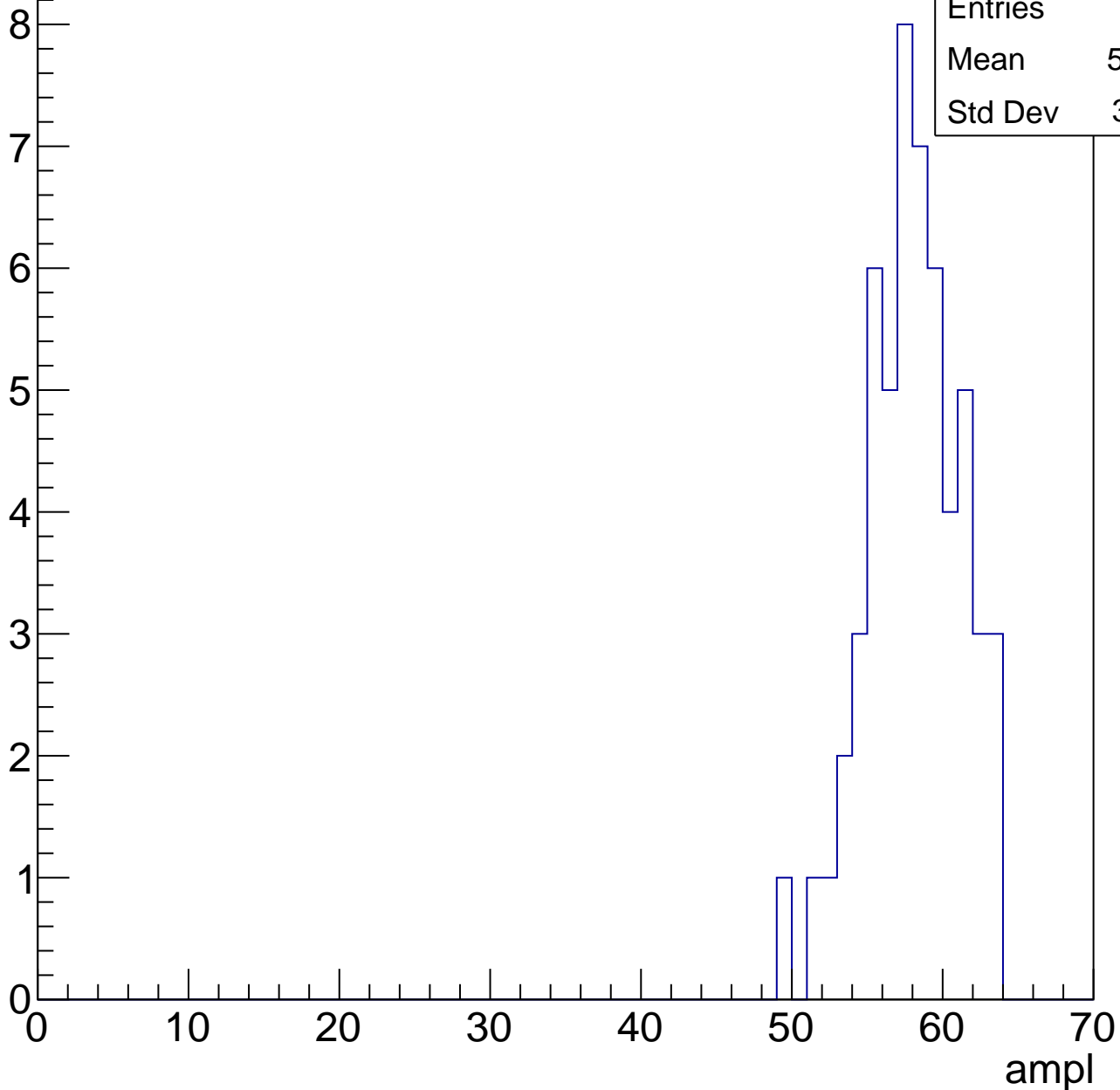


B0L001S, U22-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	57.56
Std Dev	3.091

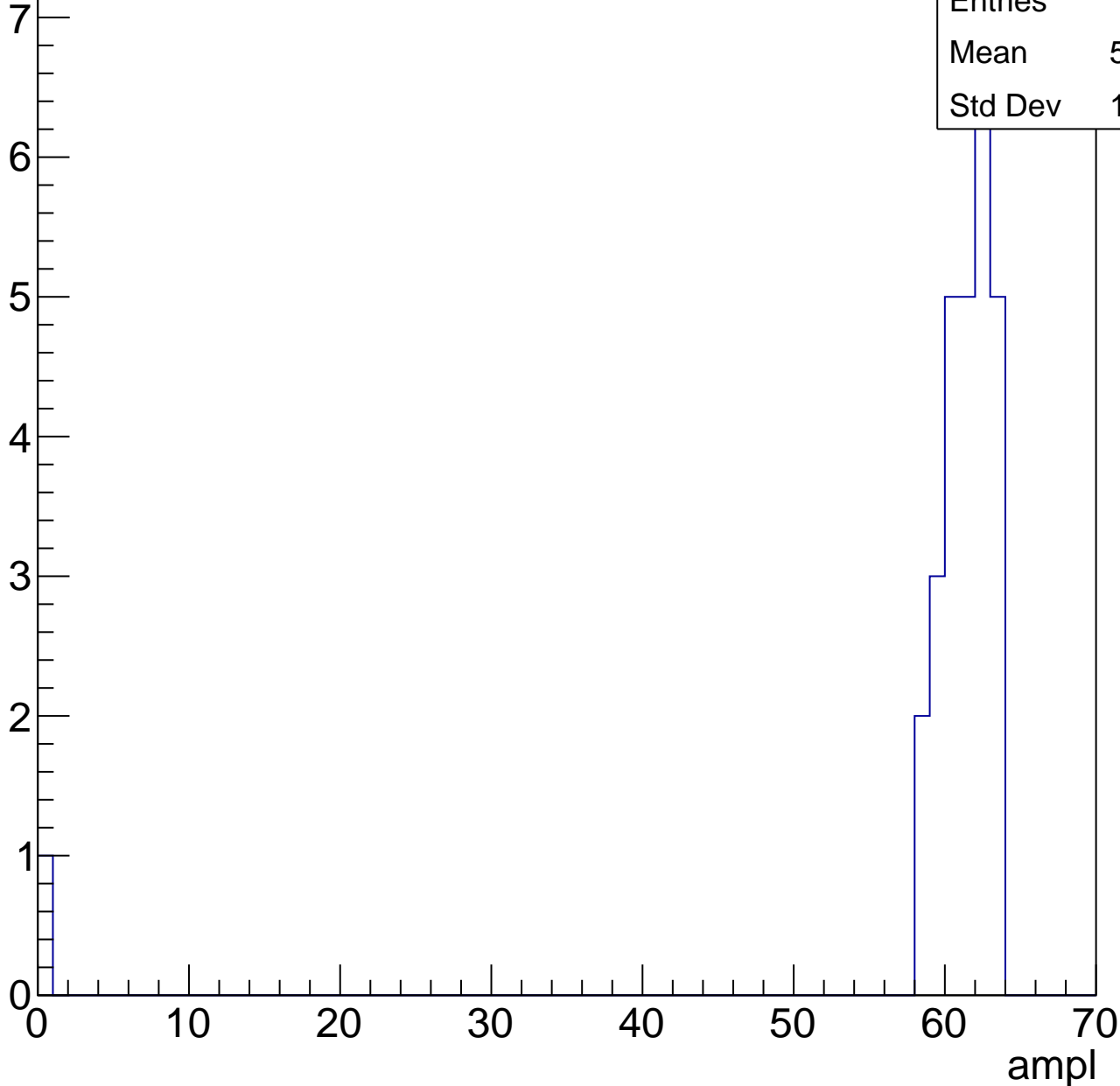


B0L001S, U22-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.82
Std Dev	11.42



B0L001S, U22-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch94, adc0

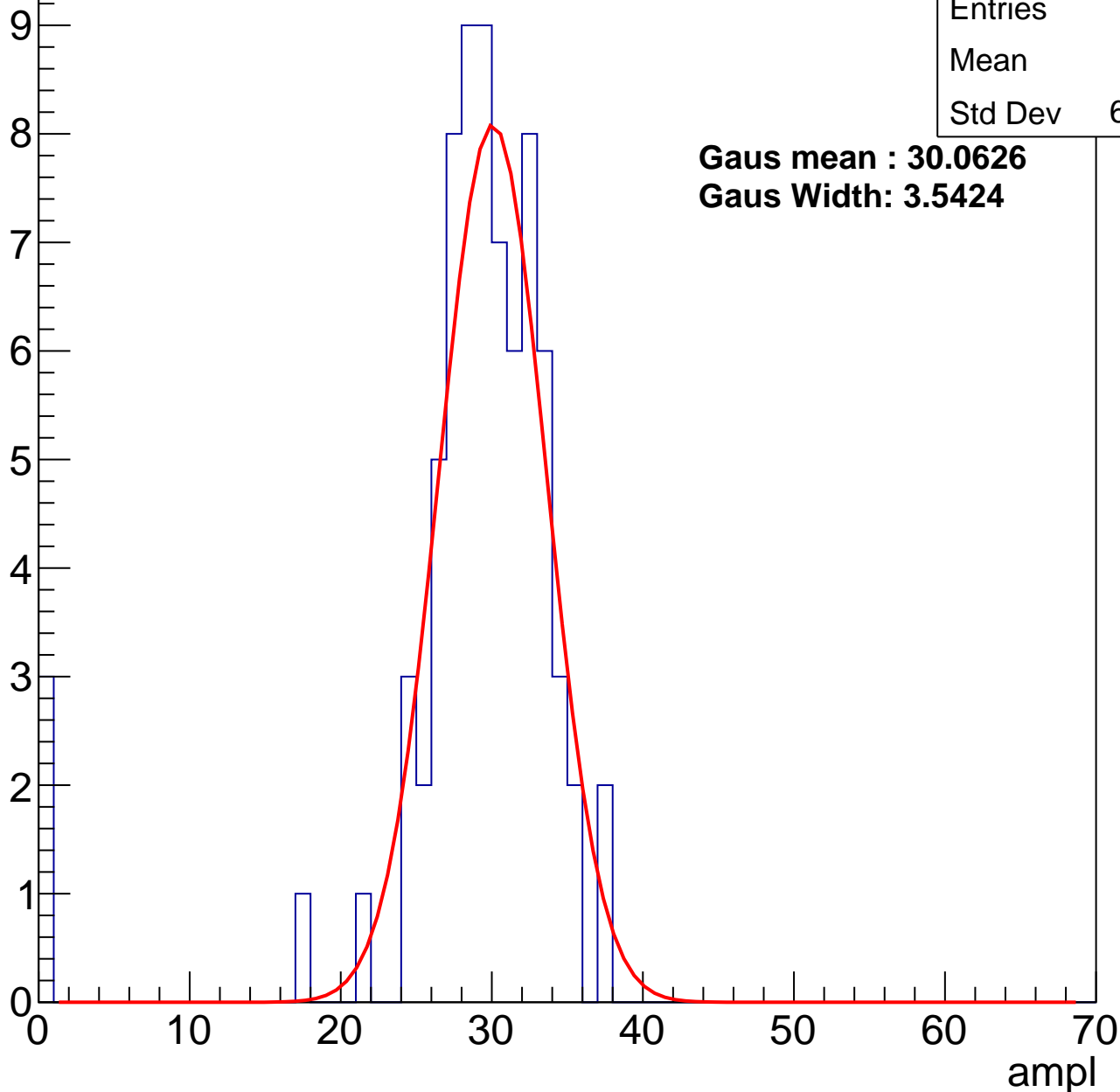
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	28.2
Std Dev	6.683

Gaus mean : 30.0626

Gaus Width: 3.5424



B0L001S, U22-ch94, adc1

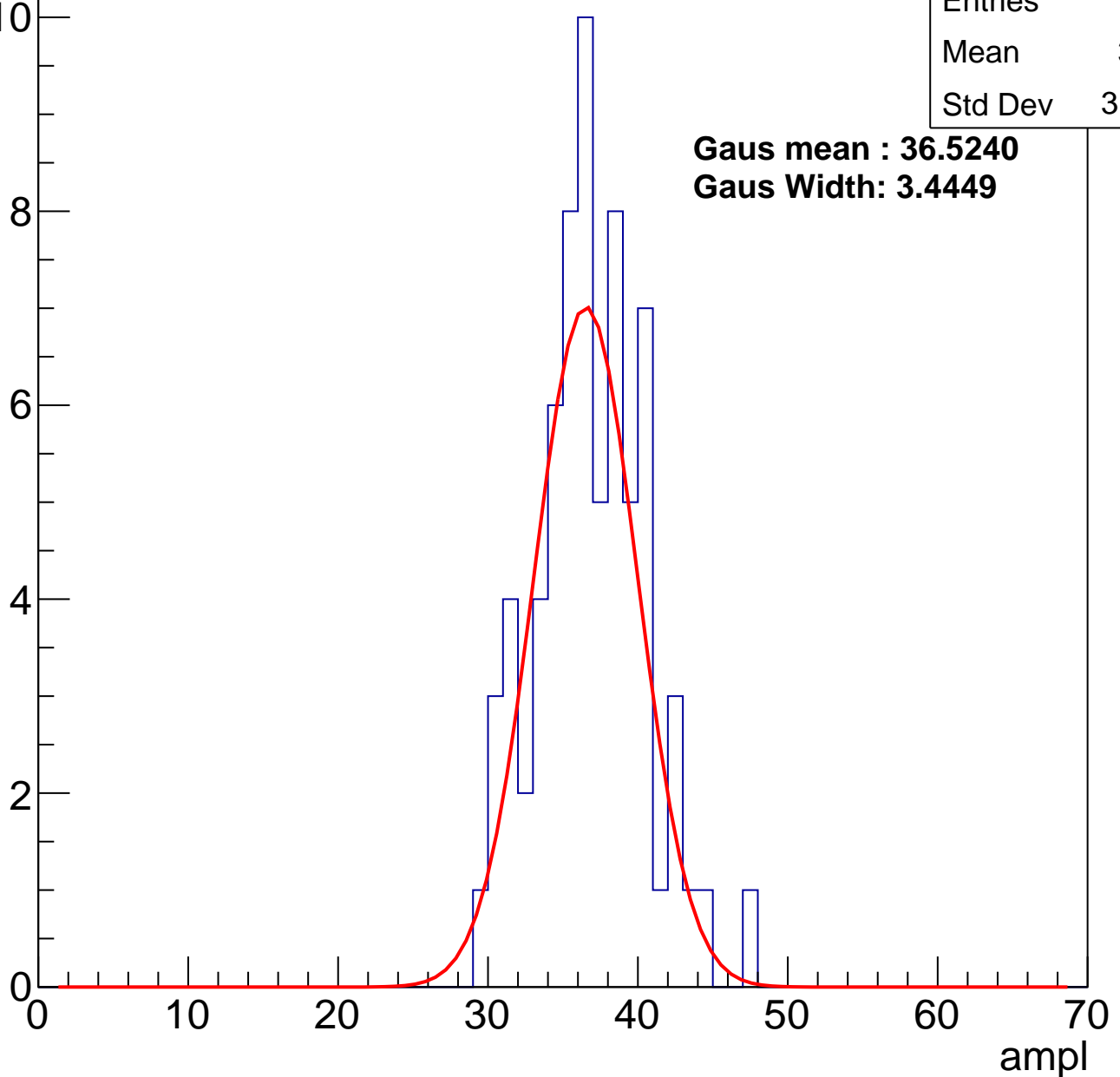
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.4
Std Dev	3.599

Gaus mean : 36.5240

Gaus Width: 3.4449



B0L001S, U22-ch94, adc2

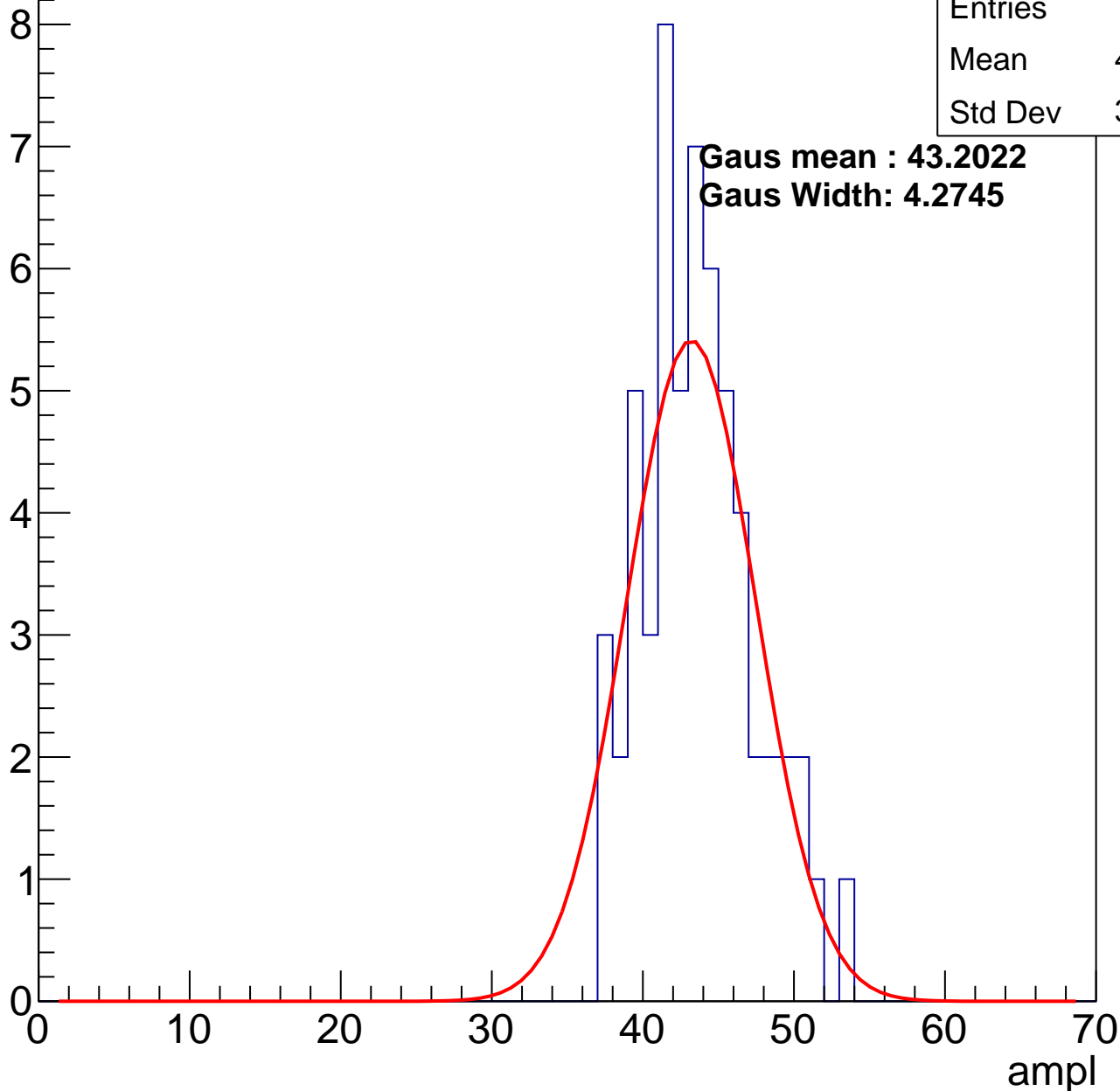
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.21
Std Dev	3.661

Gaus mean : 43.2022

Gaus Width: 4.2745

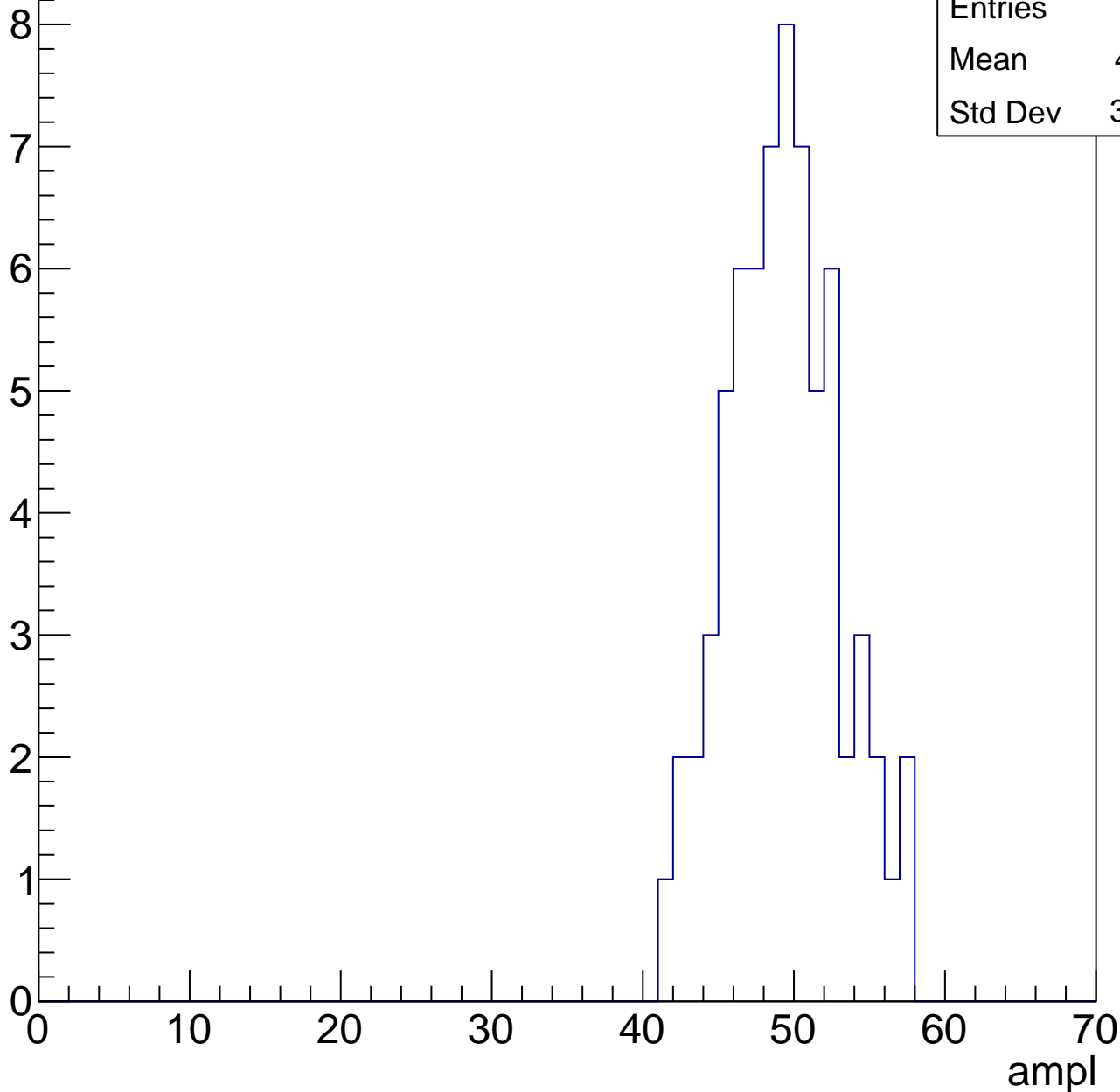


B0L001S, U22-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

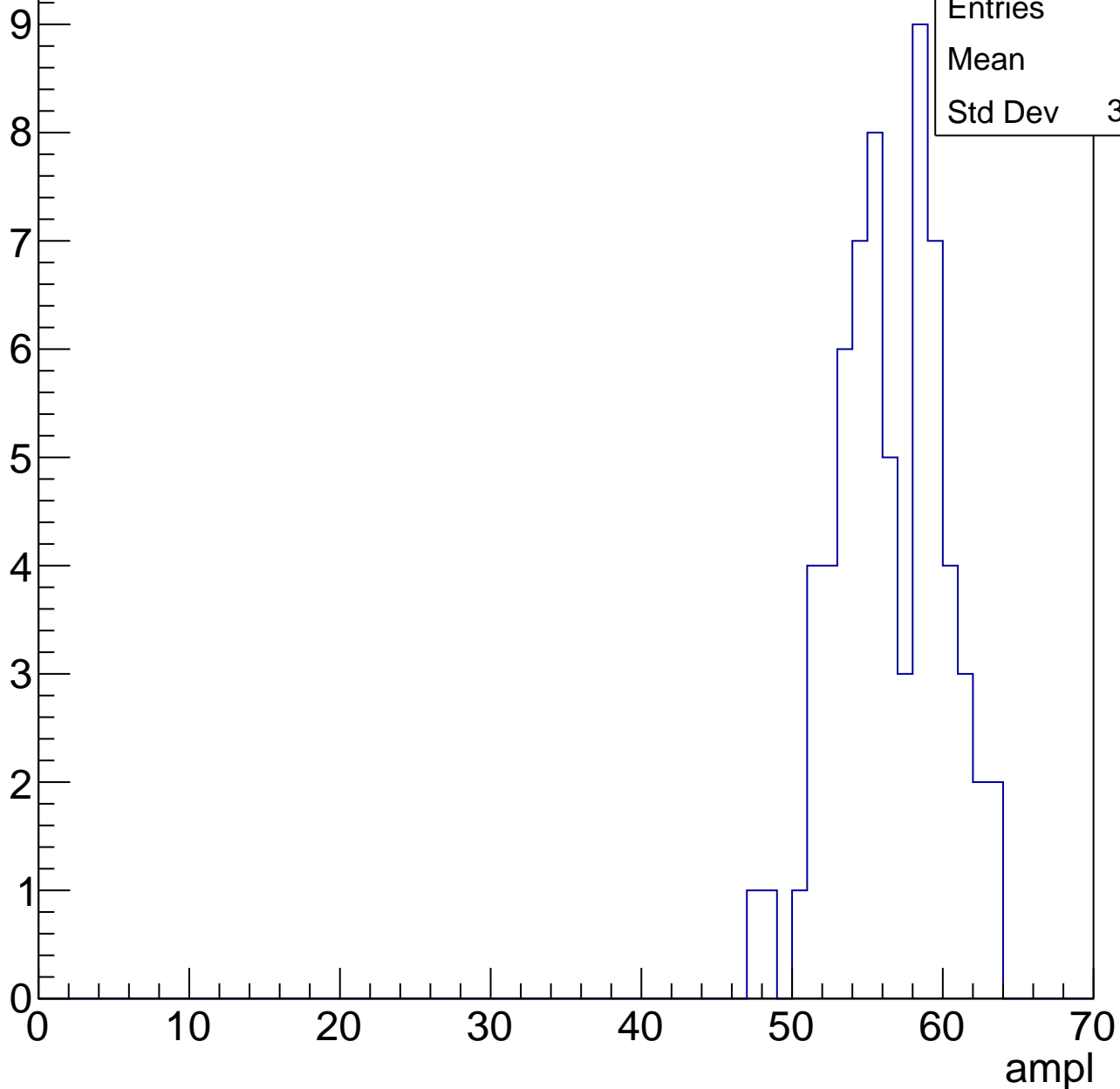
Entries	68
Mean	48.81
Std Dev	3.655



B0L001S, U22-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

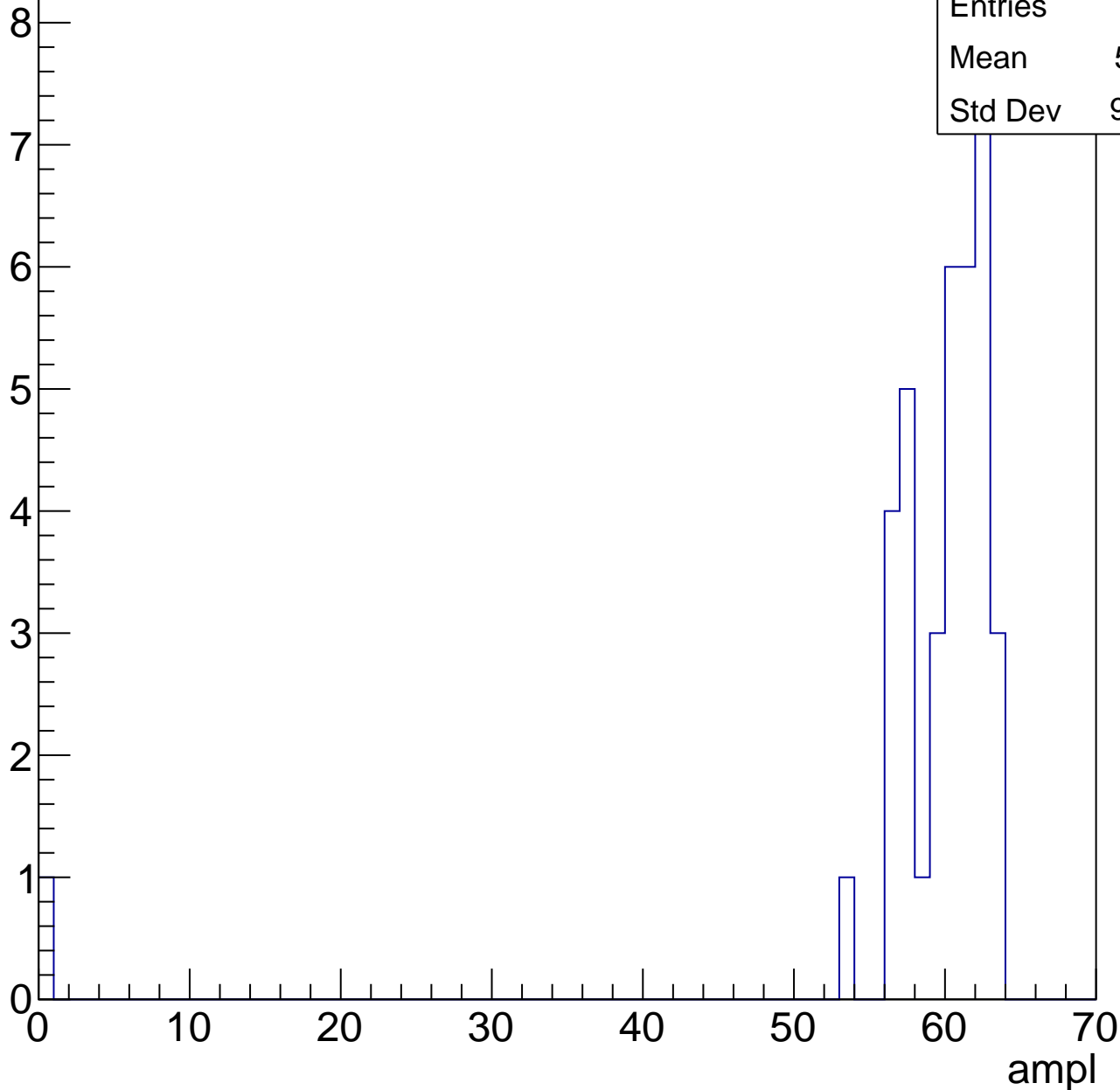


B0L001S, U22-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

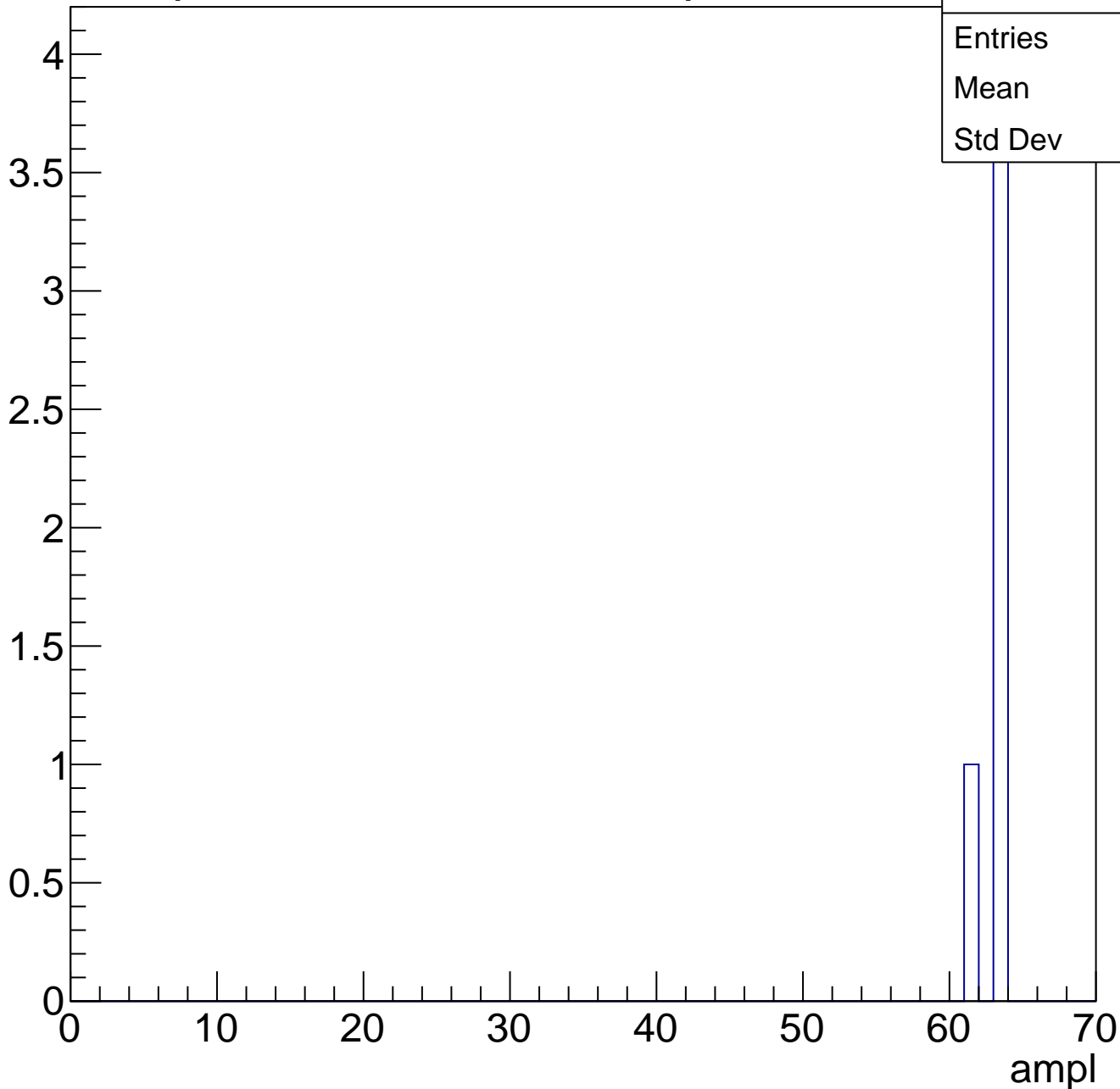
Entries	38
Mean	58.11
Std Dev	9.859



B0L001S, U22-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch95, adc0

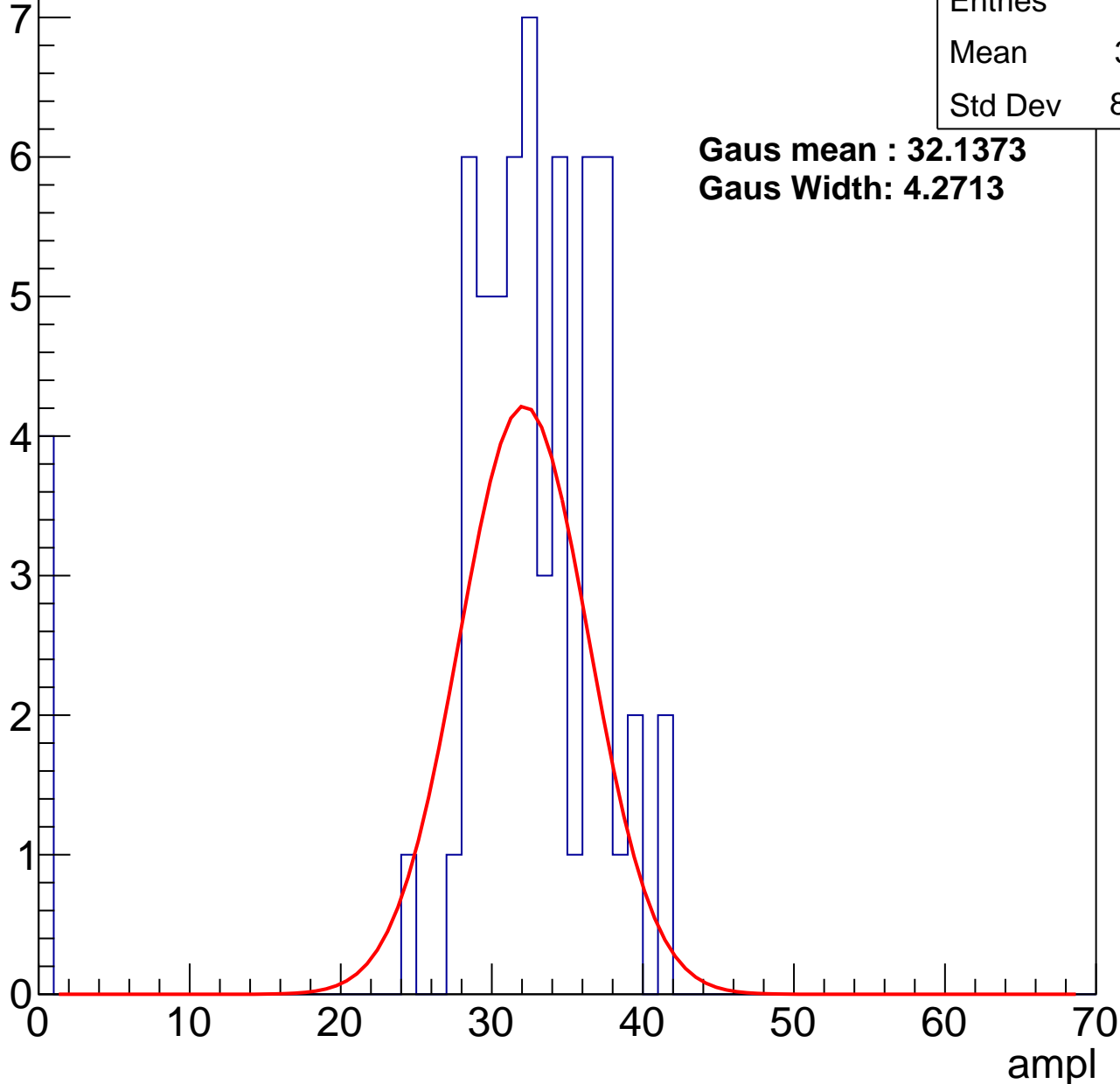
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.61
Std Dev	8.809

Gaus mean : 32.1373

Gaus Width: 4.2713



B0L001S, U22-ch95, adc1

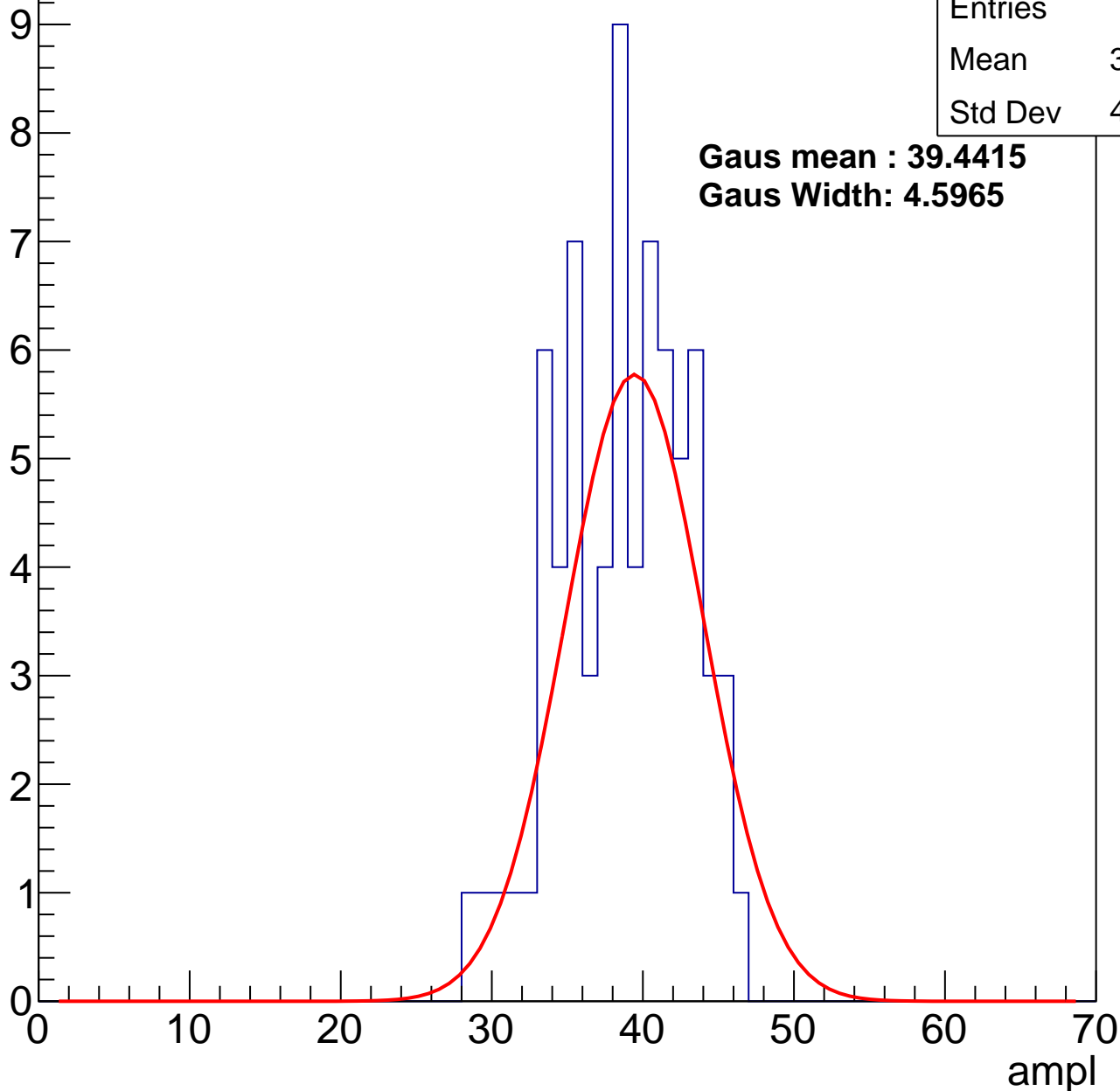
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	38.22
Std Dev	4.145

Gaus mean : 39.4415

Gaus Width: 4.5965



B0L001S, U22-ch95, adc2

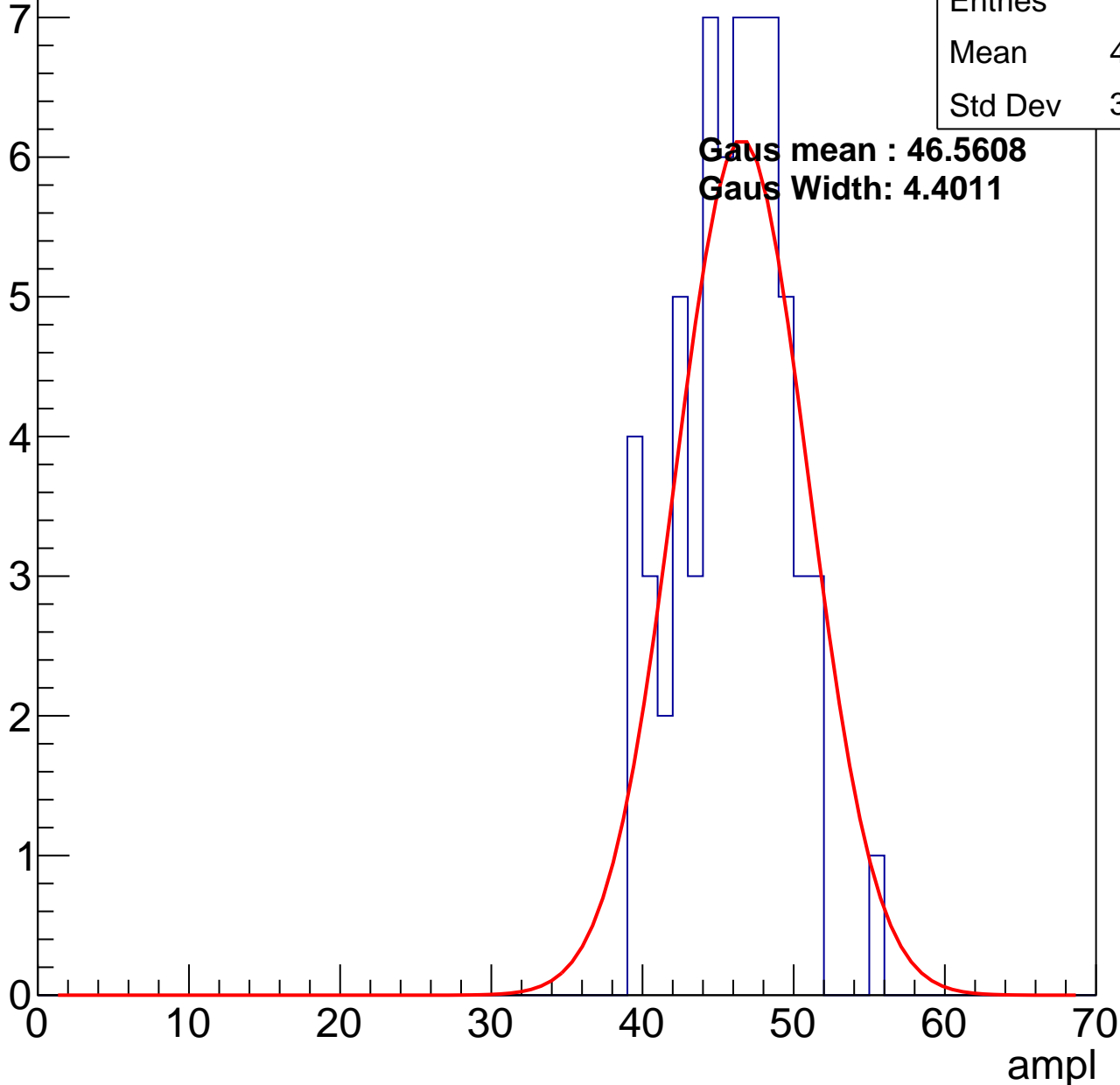
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	45.48
Std Dev	3.477

Gaus mean : 46.5608

Gaus Width: 4.4011

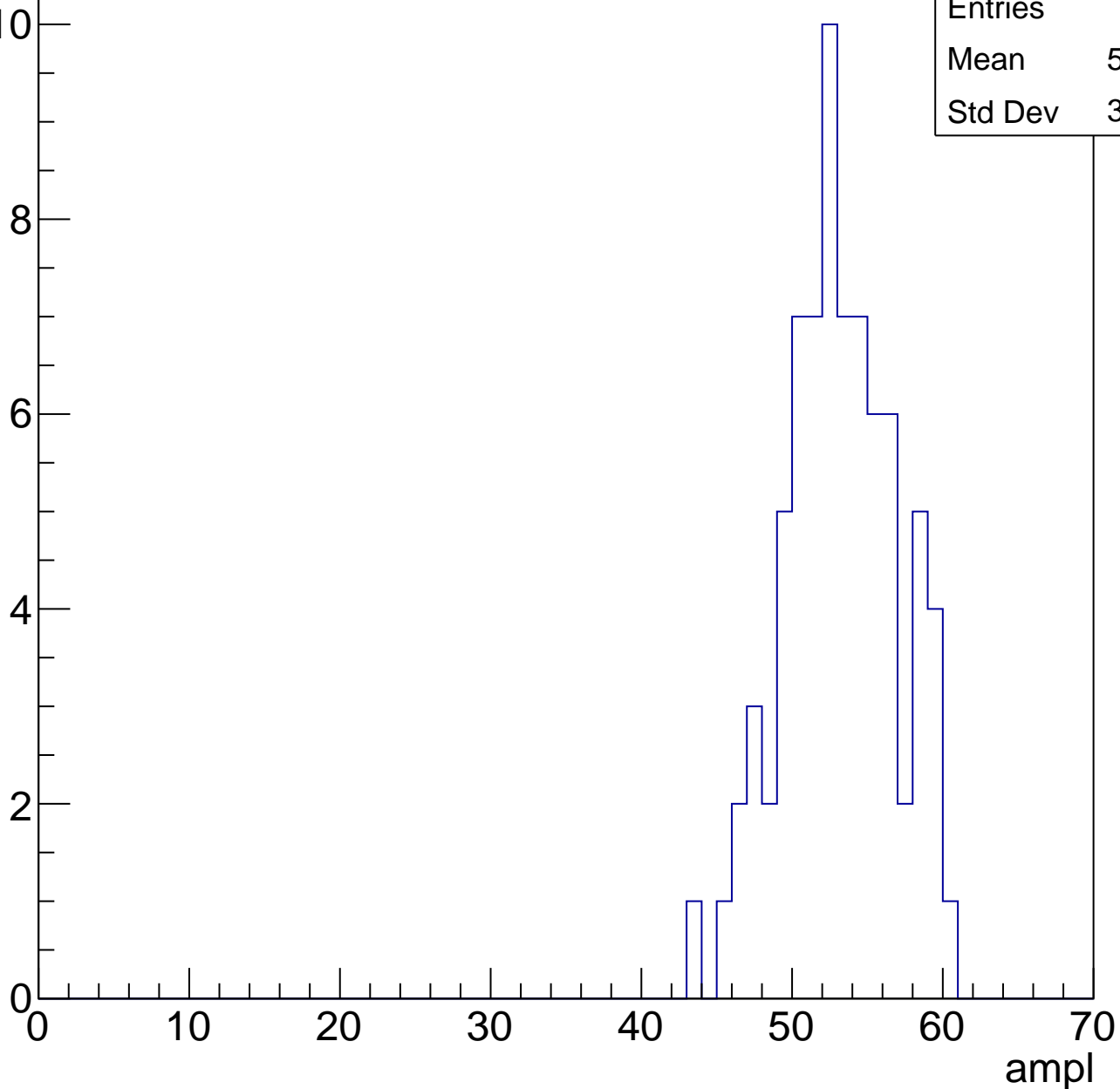


B0L001S, U22-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	52.68
Std Dev	3.679

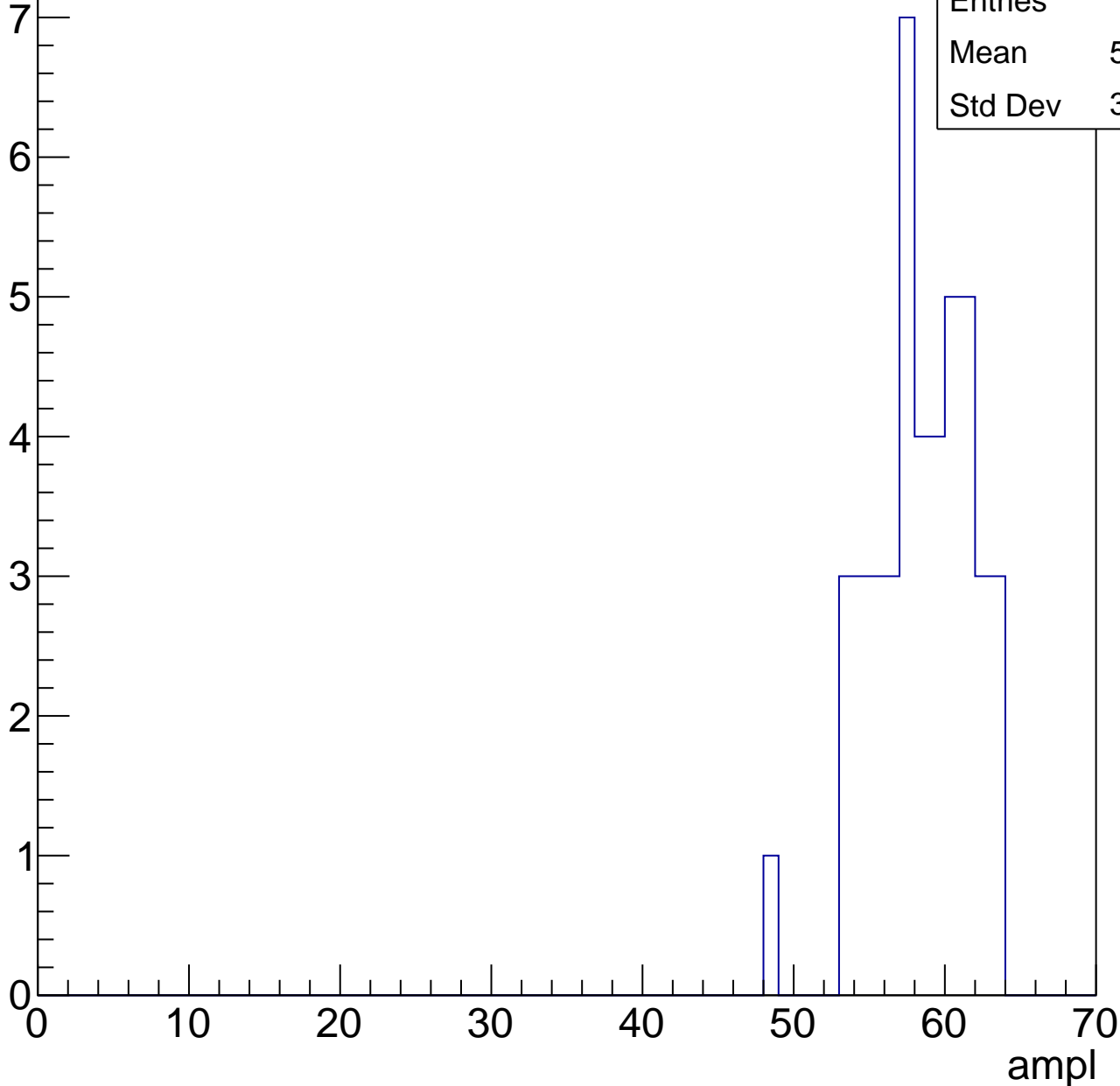


B0L001S, U22-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	57.93
Std Dev	3.236

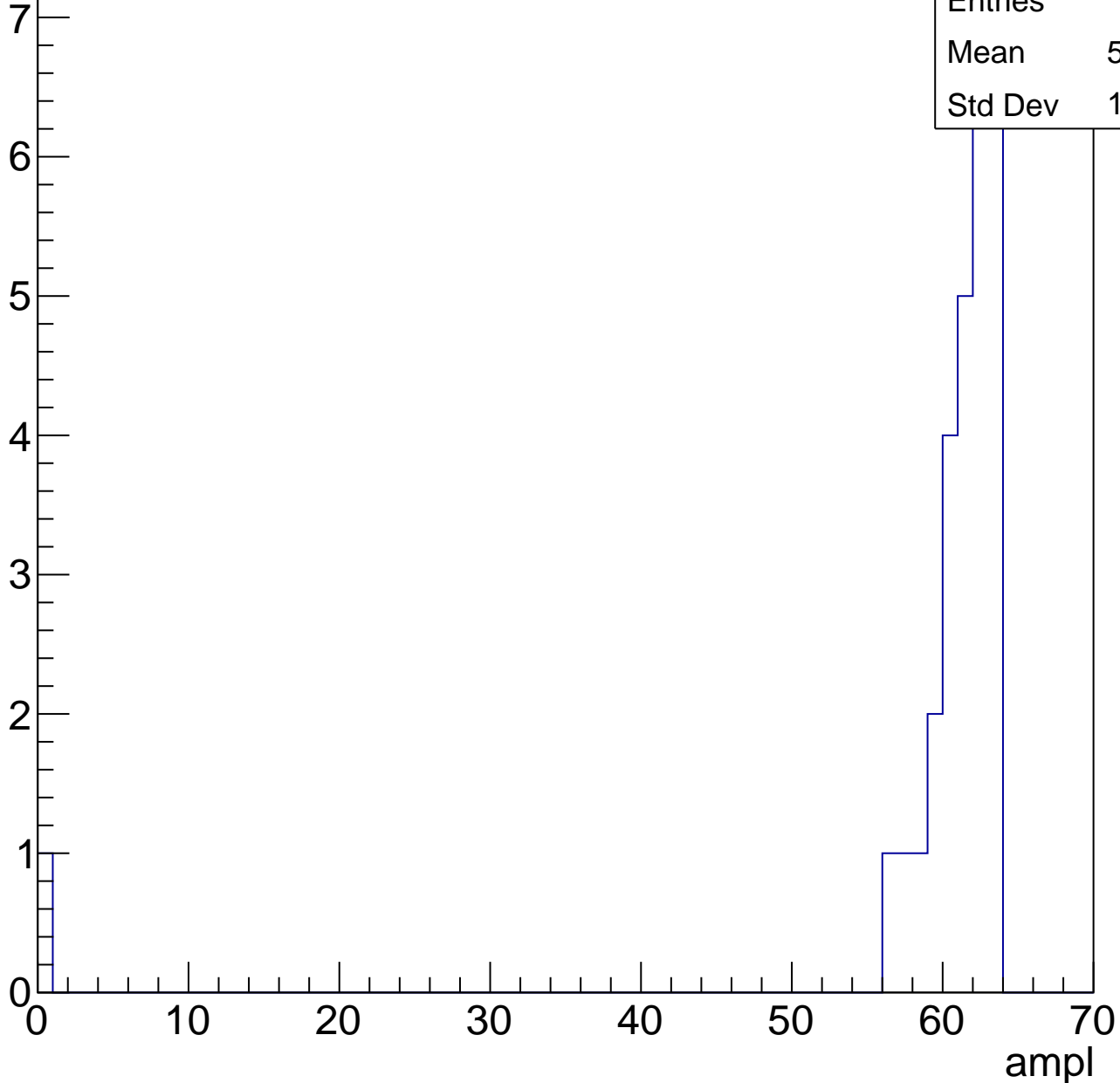


B0L001S, U22-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	58.93
Std Dev	11.29



B0L001S, U22-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch96, adc0

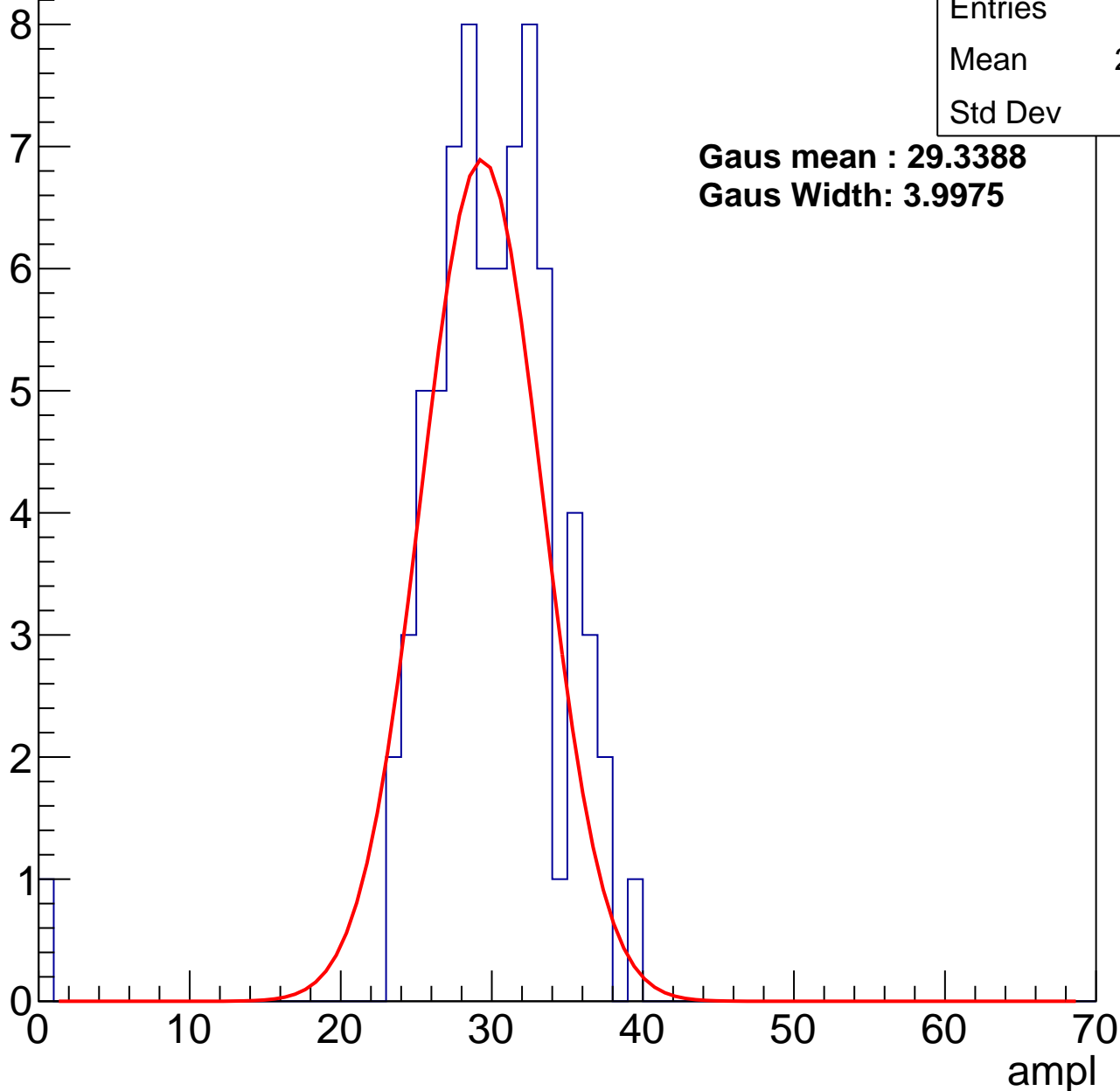
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.41
Std Dev	5.01

Gaus mean : 29.3388

Gaus Width: 3.9975



B0L001S, U22-ch96, adc1

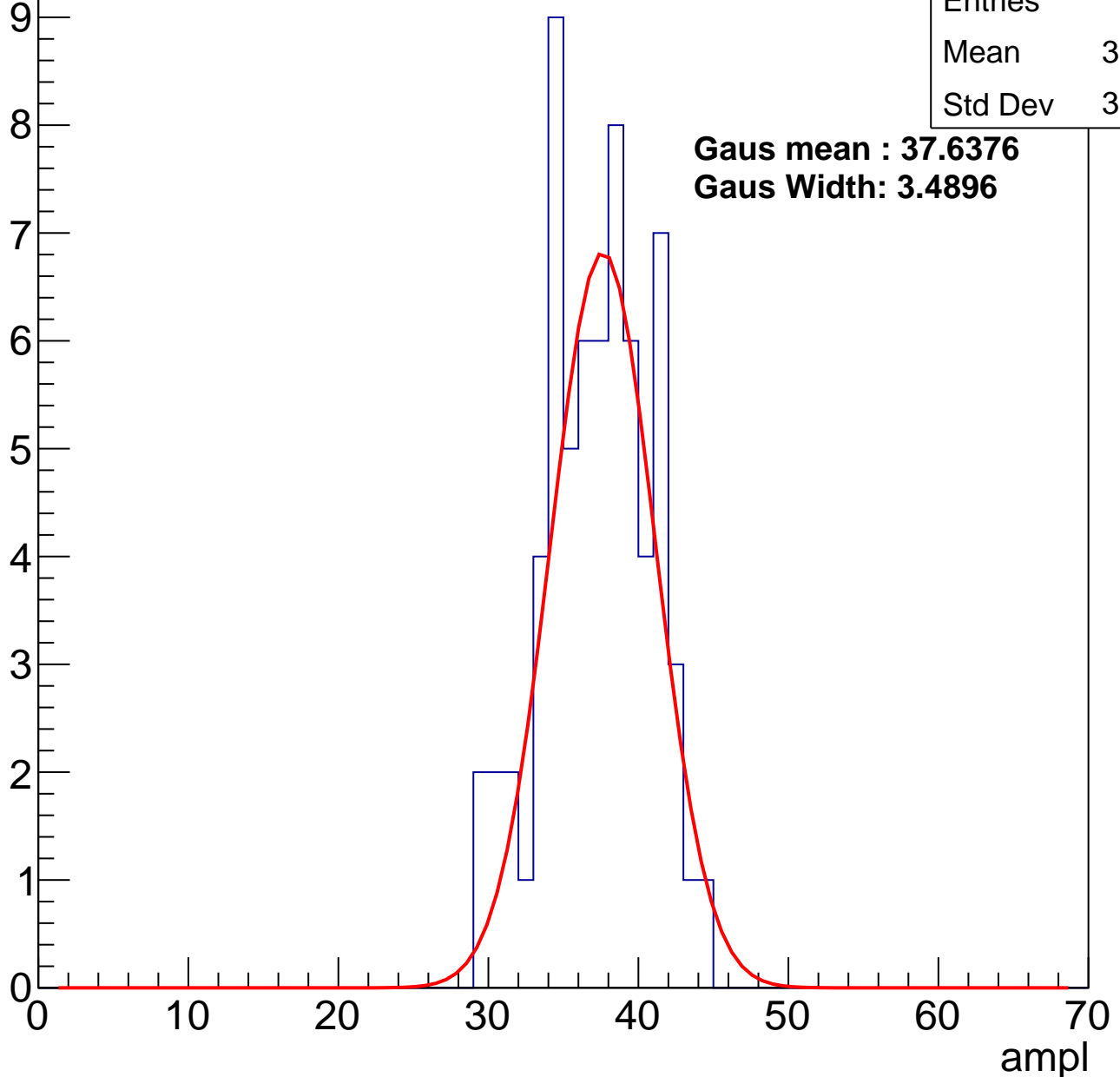
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.73
Std Dev	3.518

Gaus mean : 37.6376

Gaus Width: 3.4896



B0L001S, U22-ch96, adc2

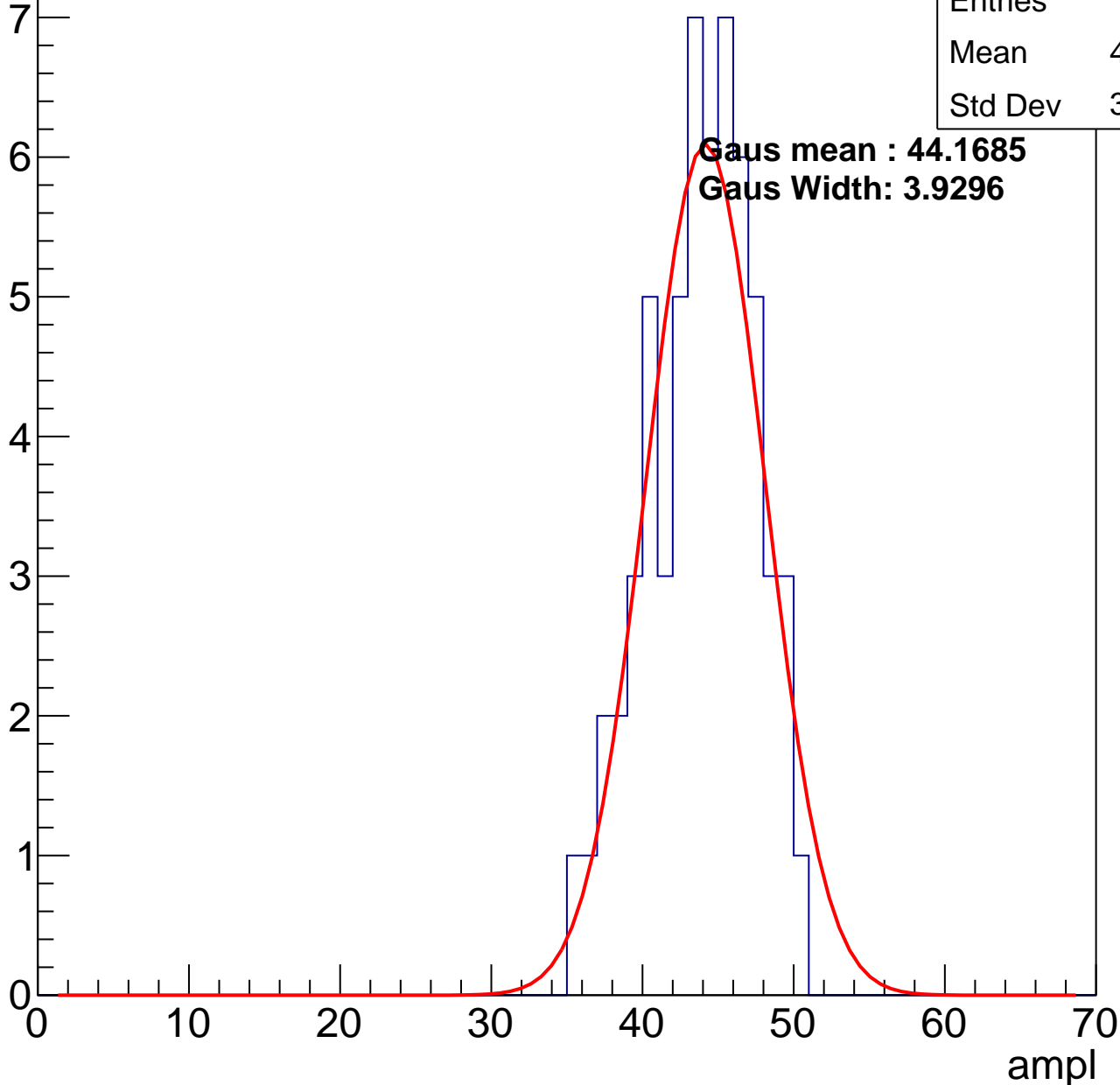
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.38
Std Dev	3.503

Gaus mean : 44.1685

Gaus Width: 3.9296

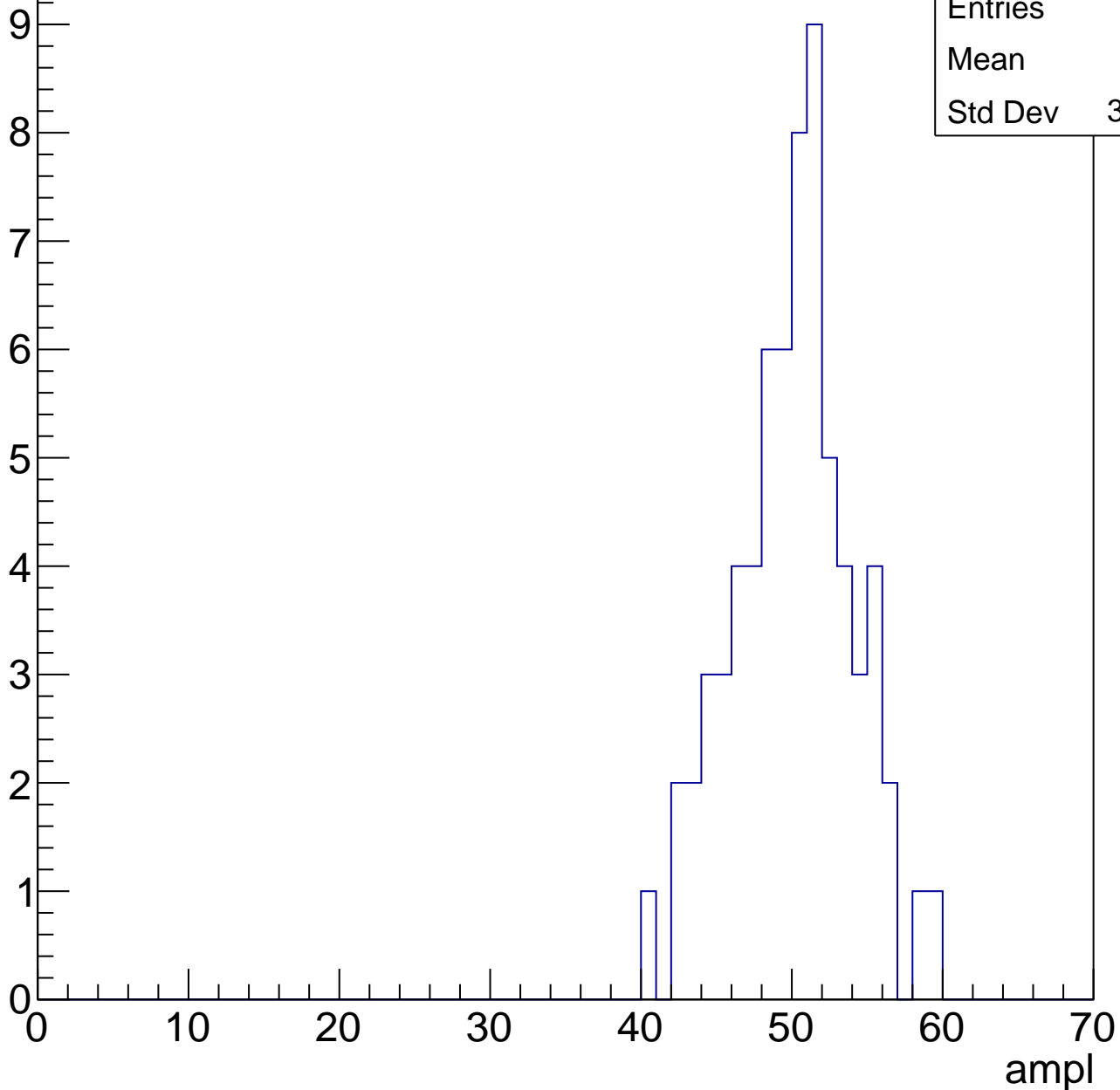


B0L001S, U22-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	49.6
Std Dev	3.945

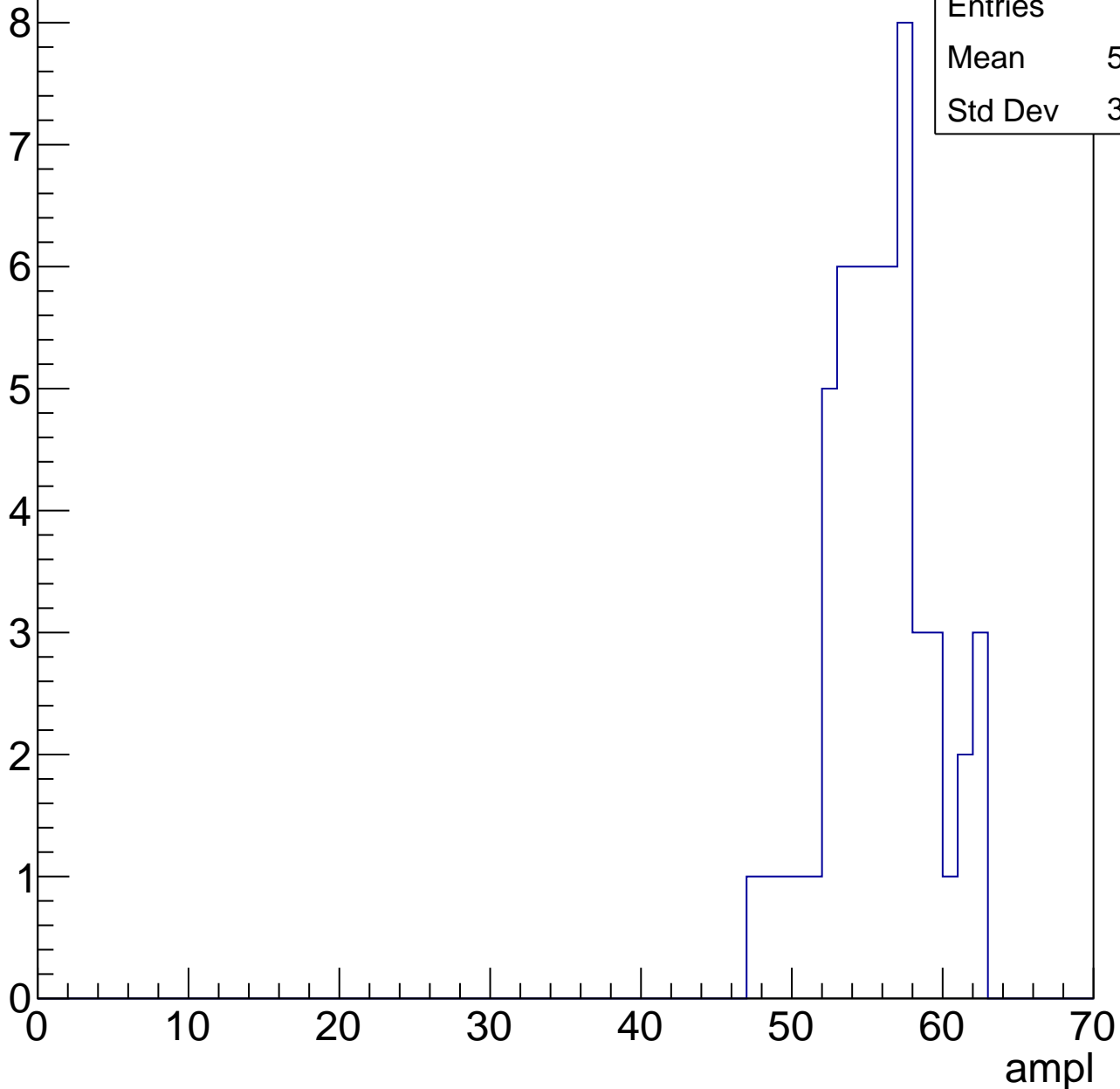


B0L001S, U22-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

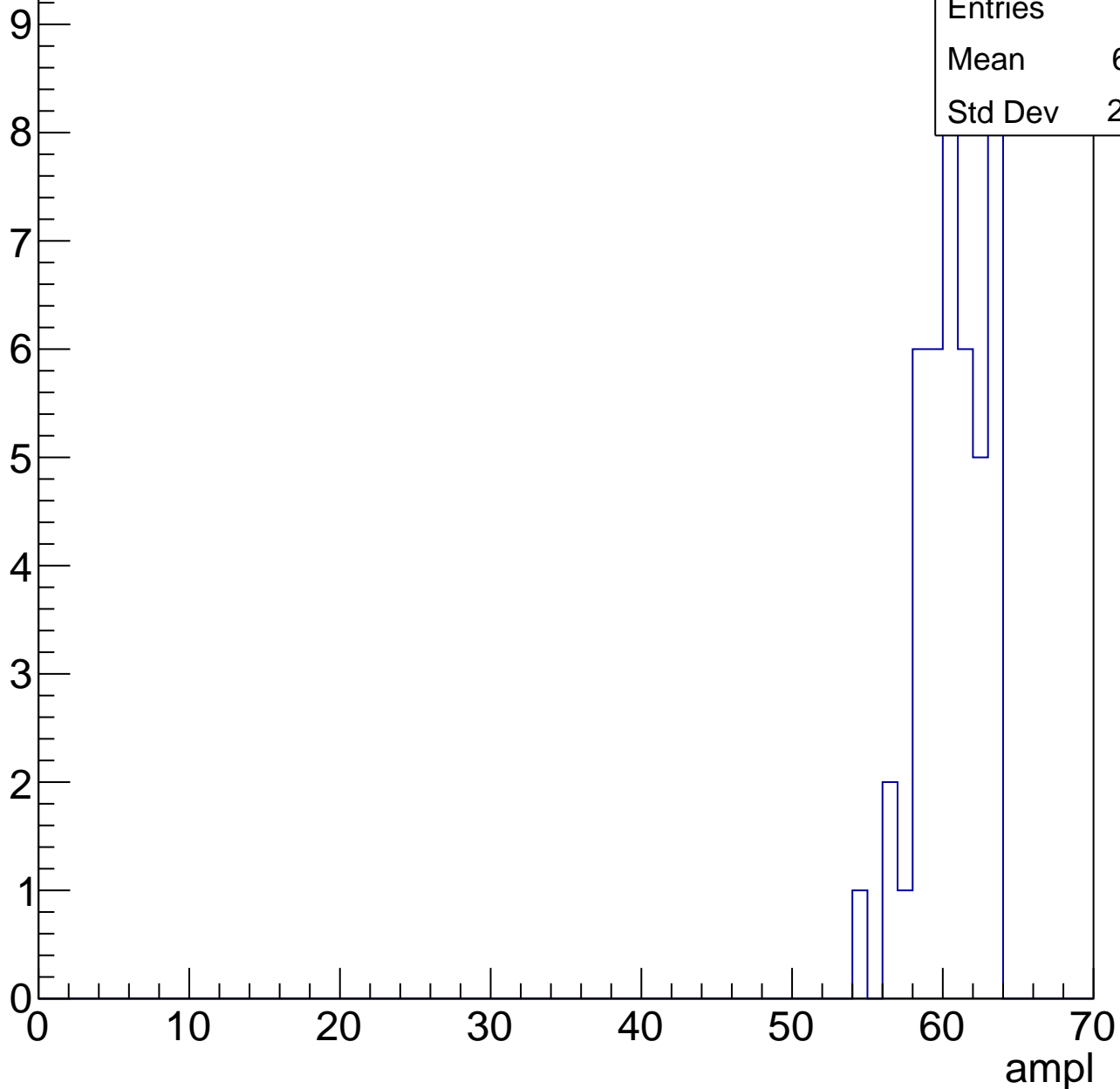
Entries	54
Mean	55.33
Std Dev	3.377



B0L001S, U22-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

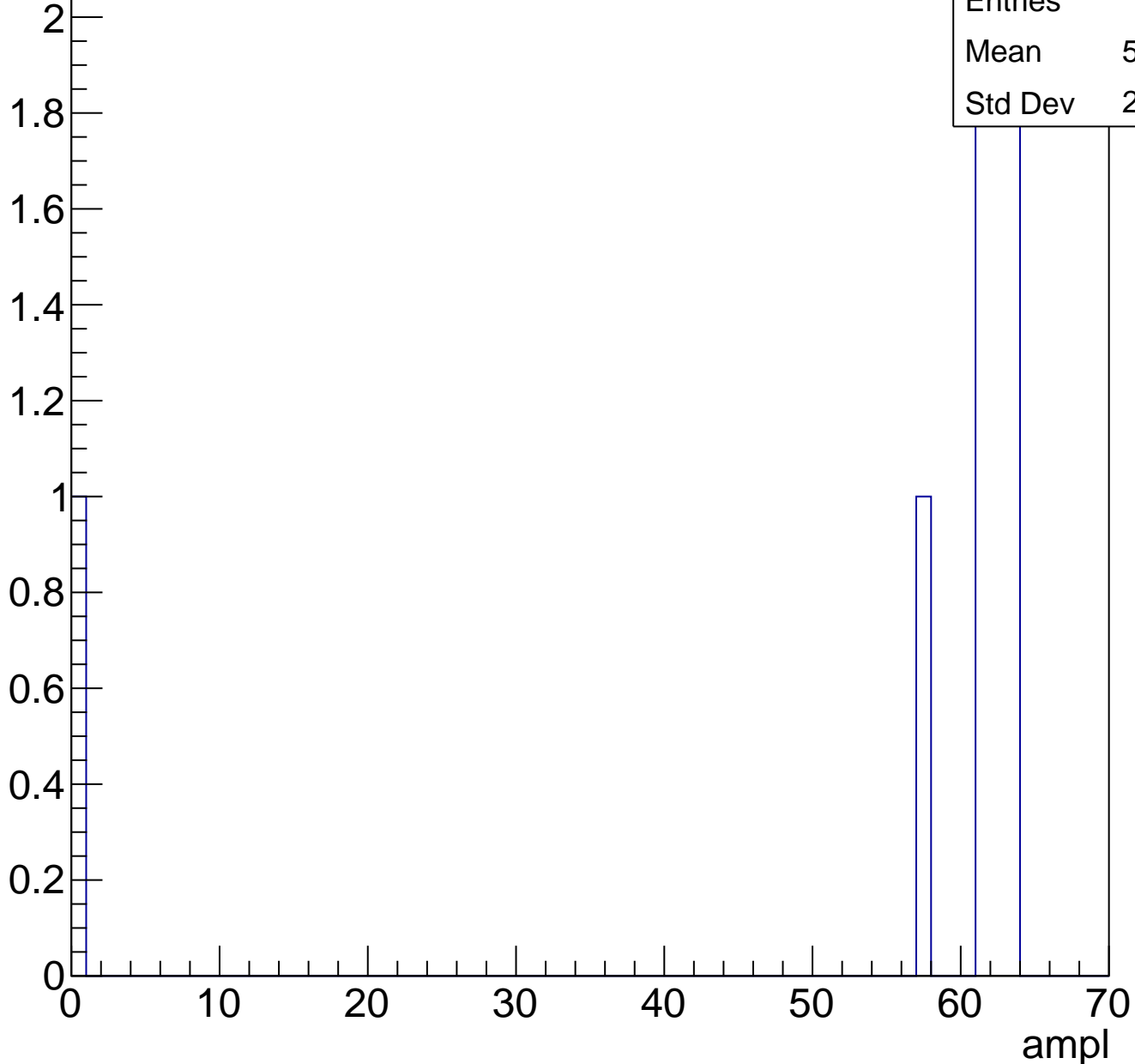


Entries	44
Mean	60.11
Std Dev	2.155

B0L001S, U22-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch97, adc0

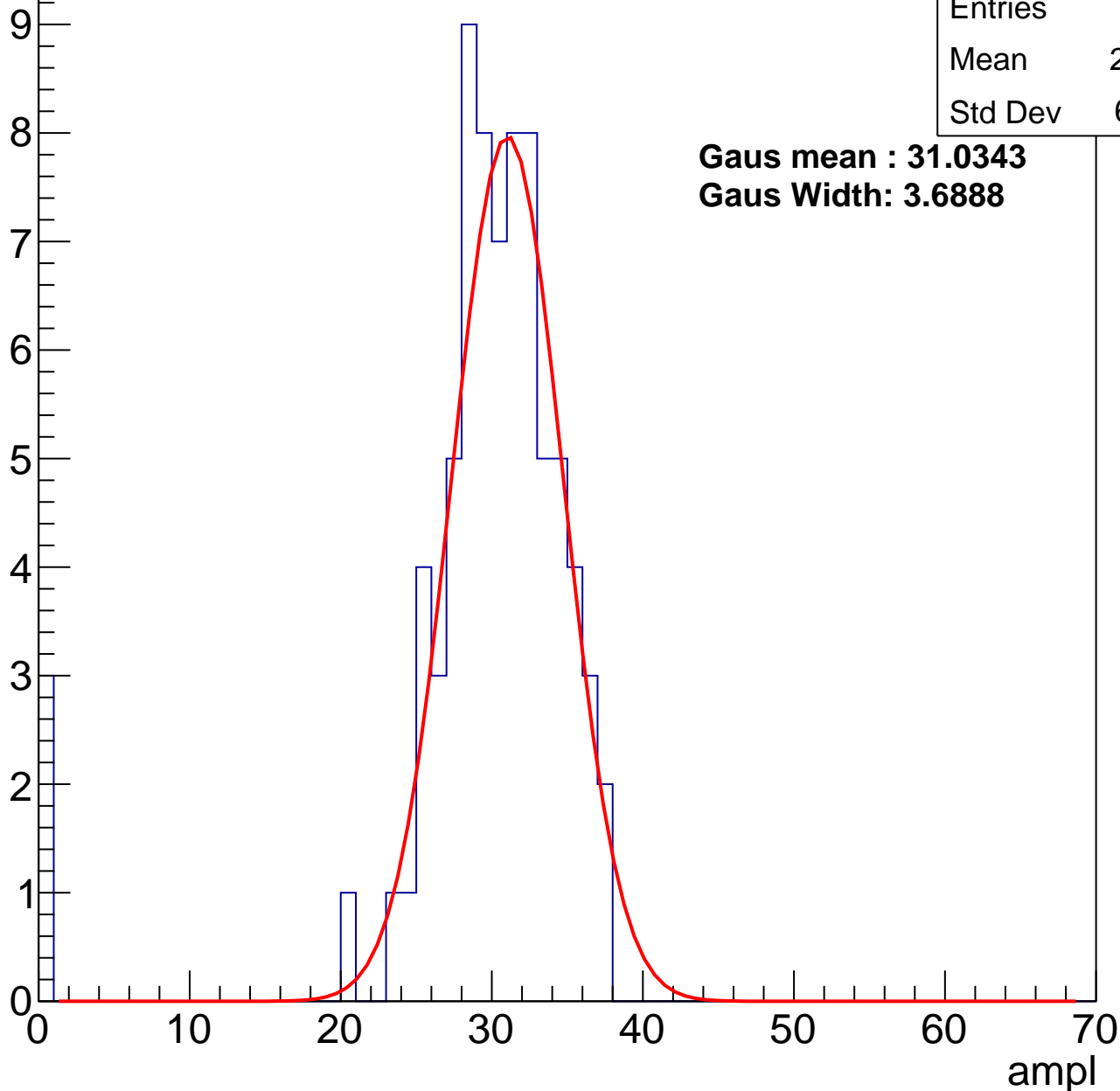
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	29.03
Std Dev	6.761

Gaus mean : 31.0343

Gaus Width: 3.6888



B0L001S, U22-ch97, adc1

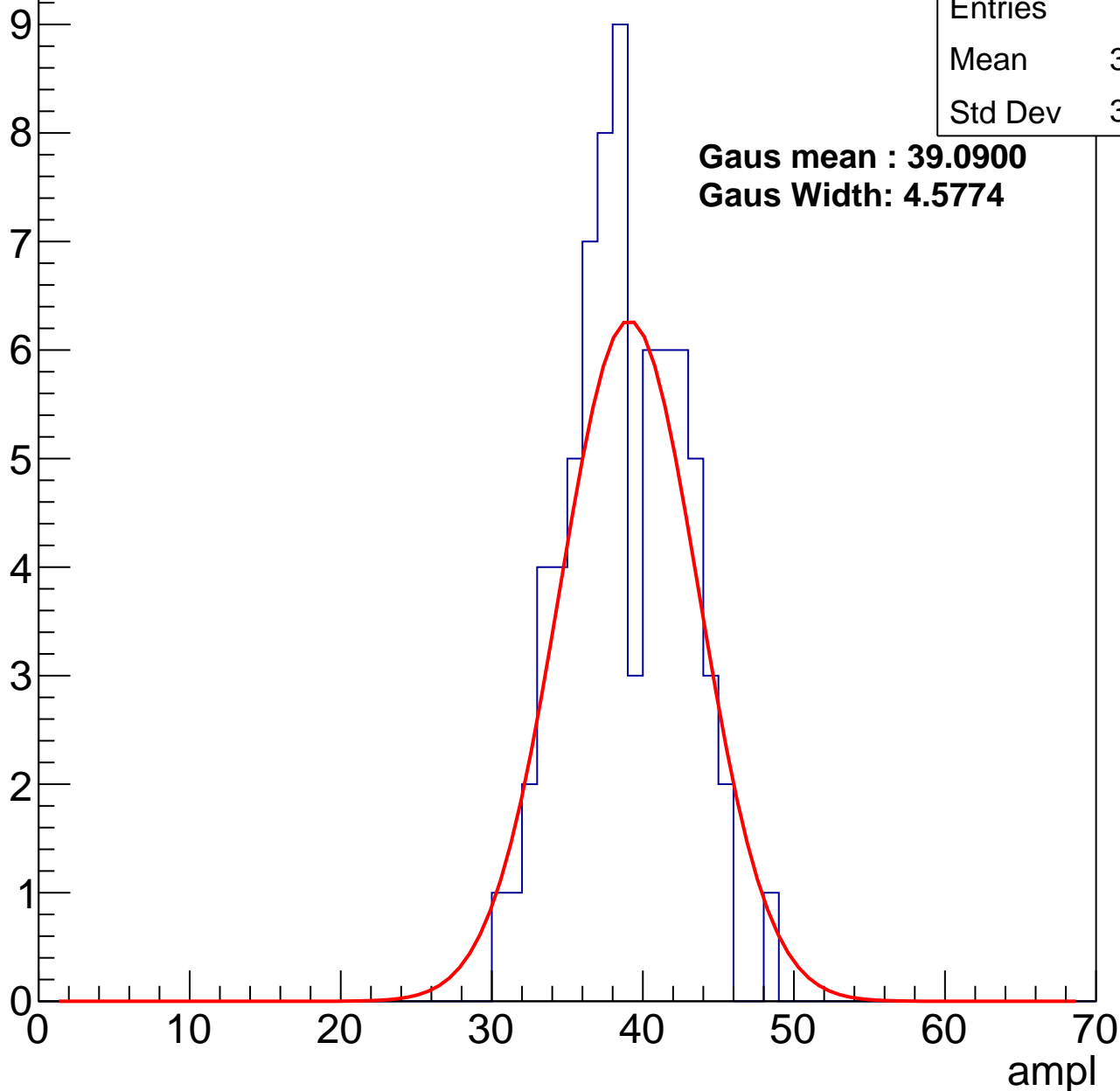
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	38.33
Std Dev	3.756

Gaus mean : 39.0900

Gaus Width: 4.5774



B0L001S, U22-ch97, adc2

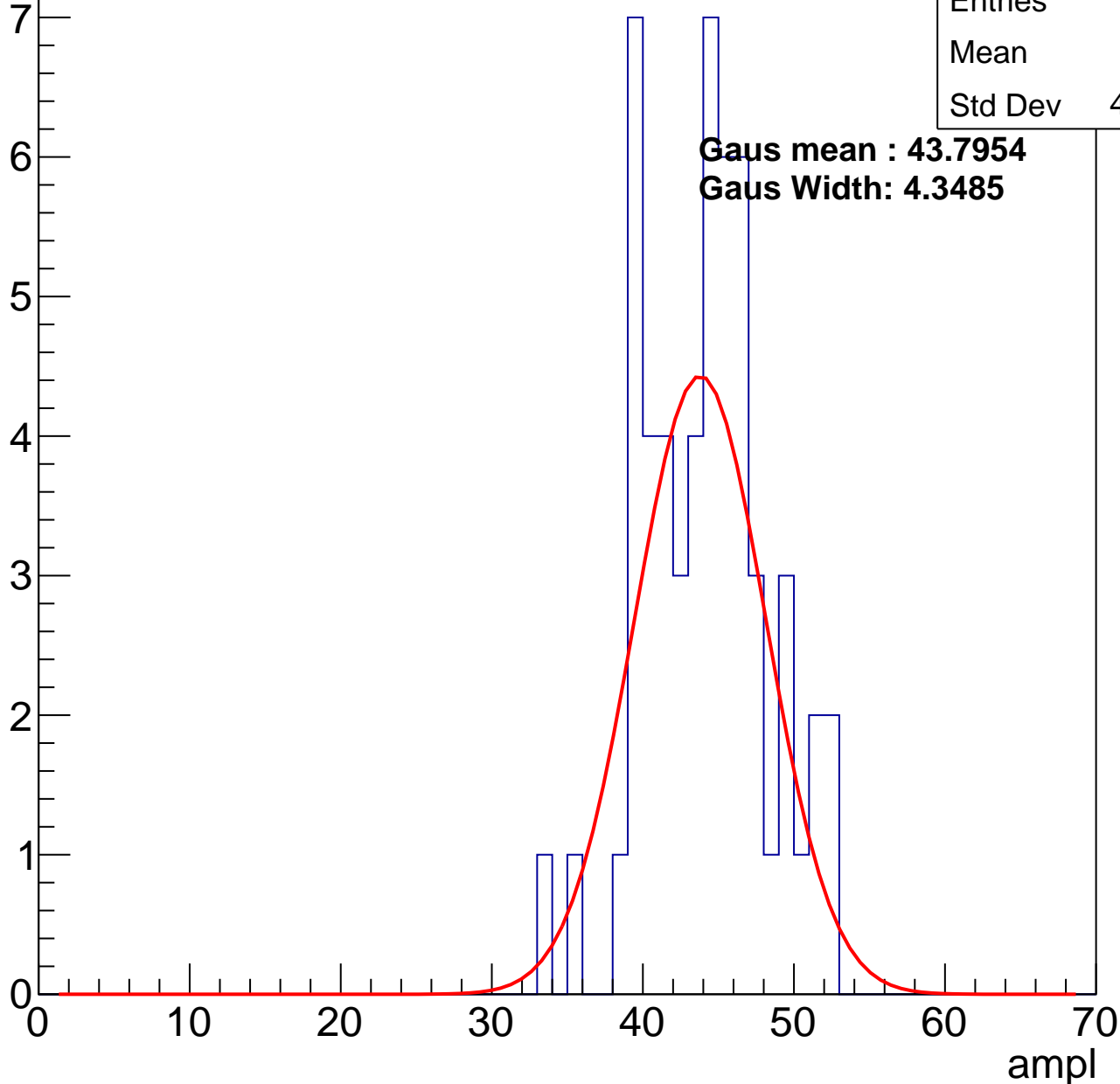
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.7
Std Dev	4.084

Gaus mean : 43.7954

Gaus Width: 4.3485

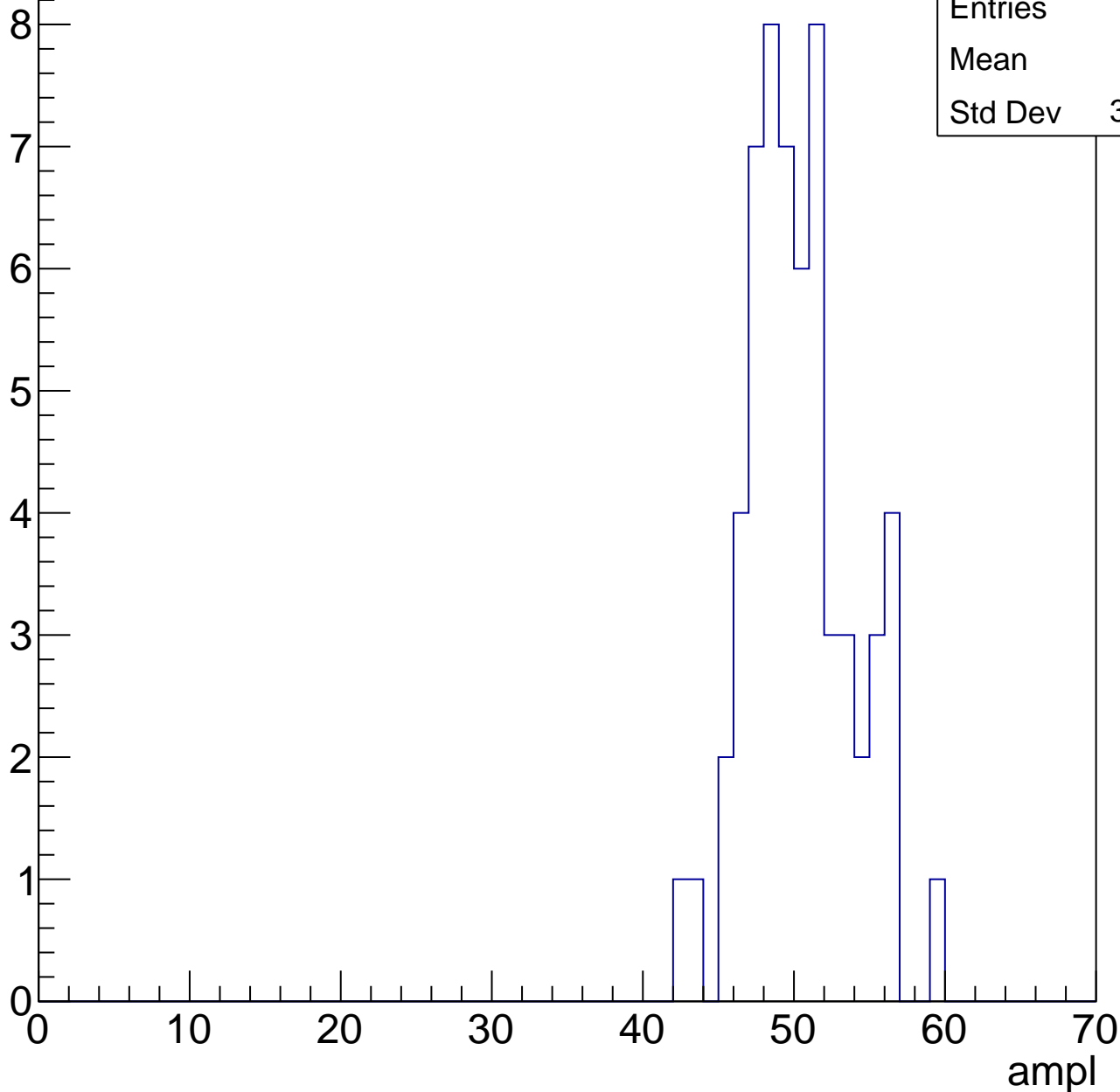


B0L001S, U22-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

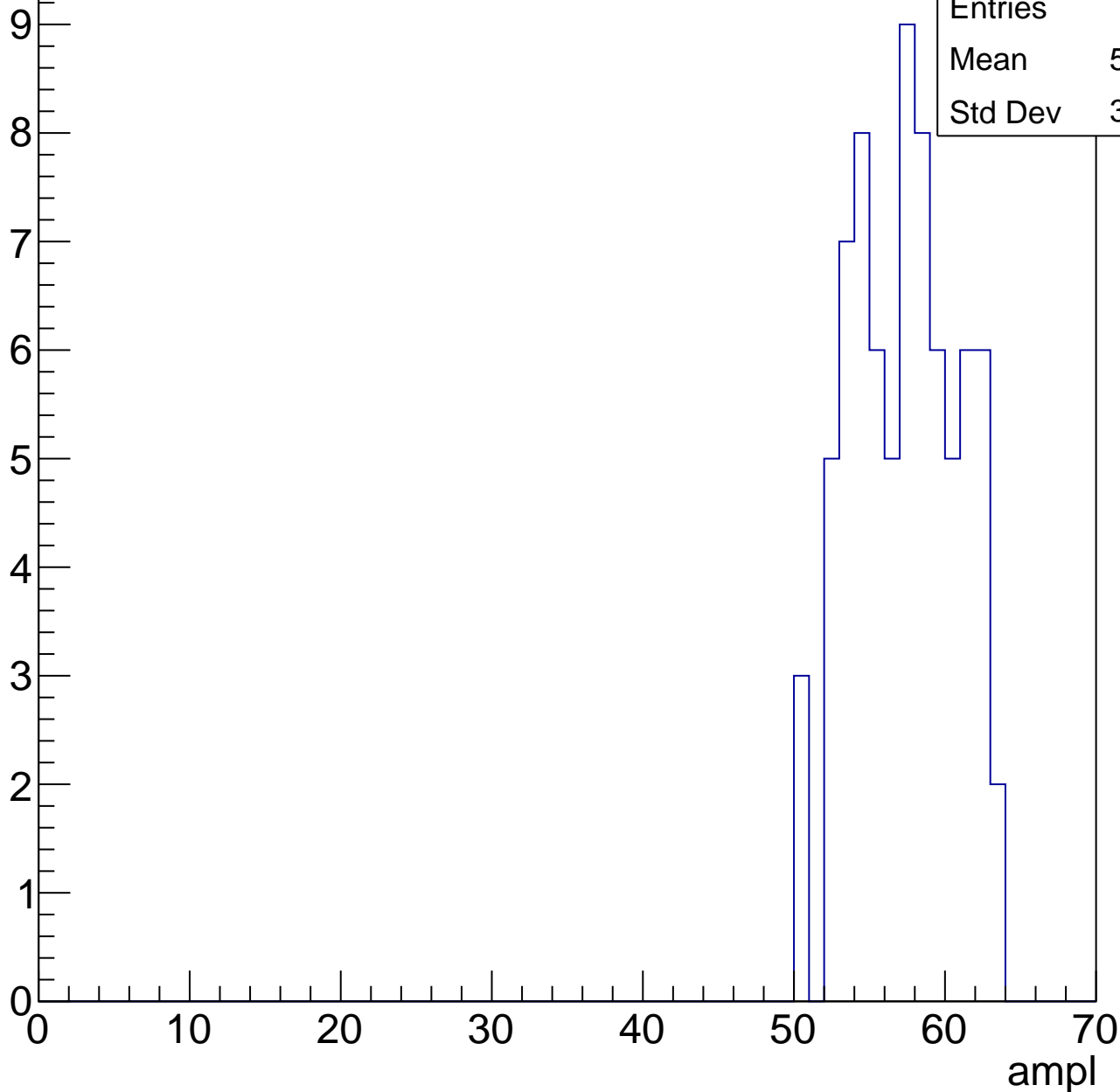
Entries	60
Mean	49.9
Std Dev	3.434



B0L001S, U22-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

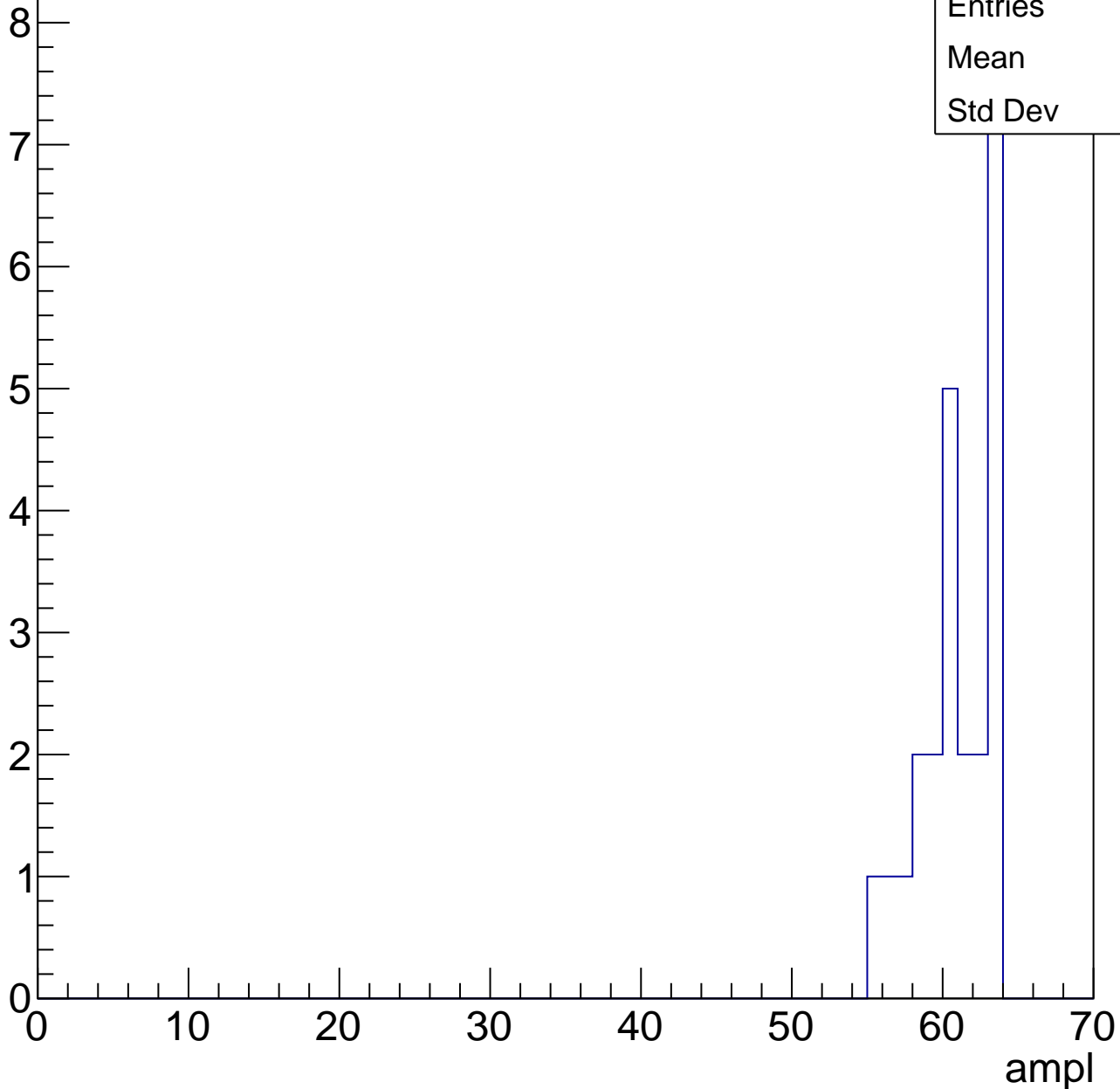


B0L001S, U22-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	24
Mean	60.5
Std Dev	2.38



B0L001S, U22-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

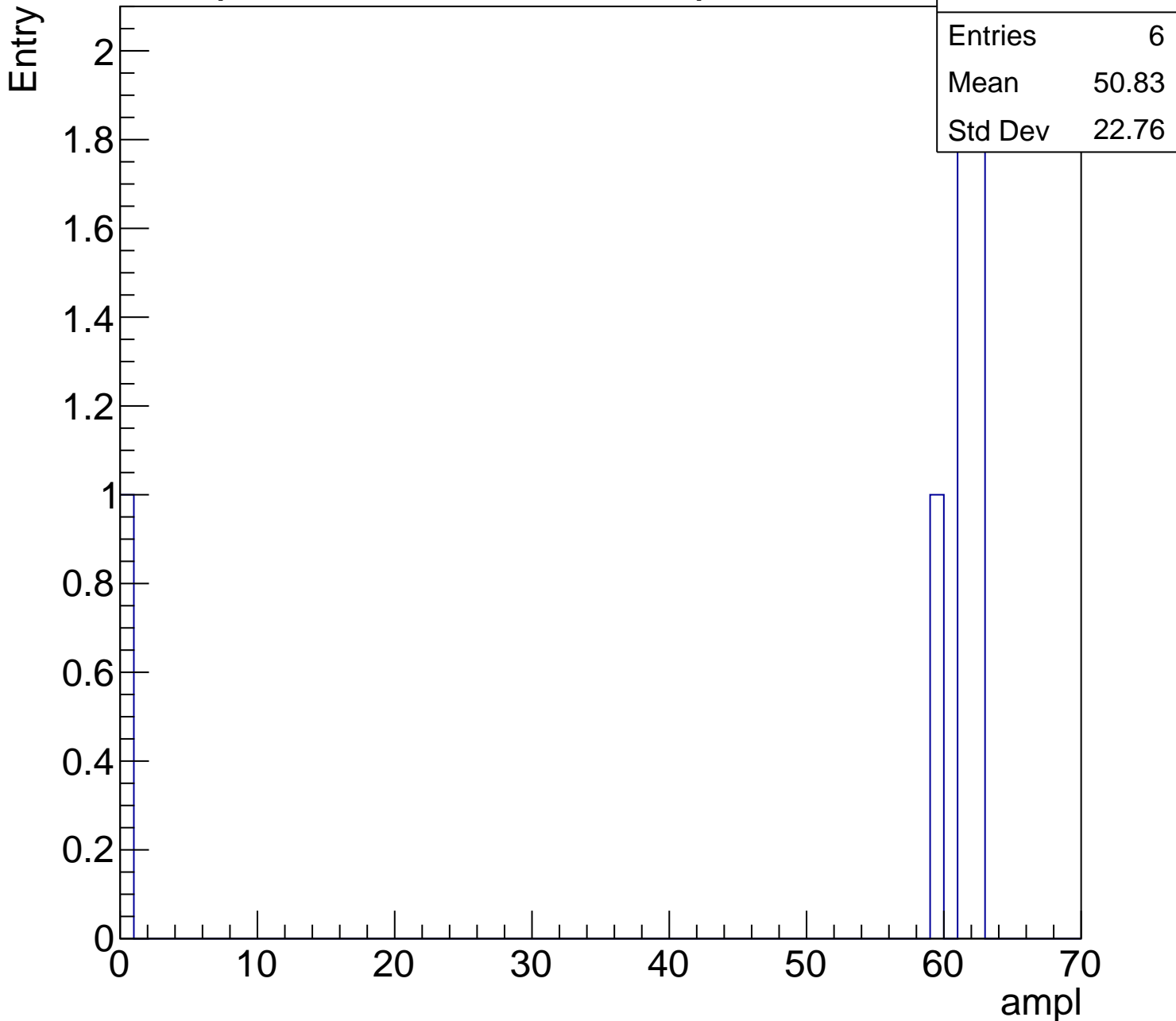
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.83
Std Dev	22.76

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch98, adc0

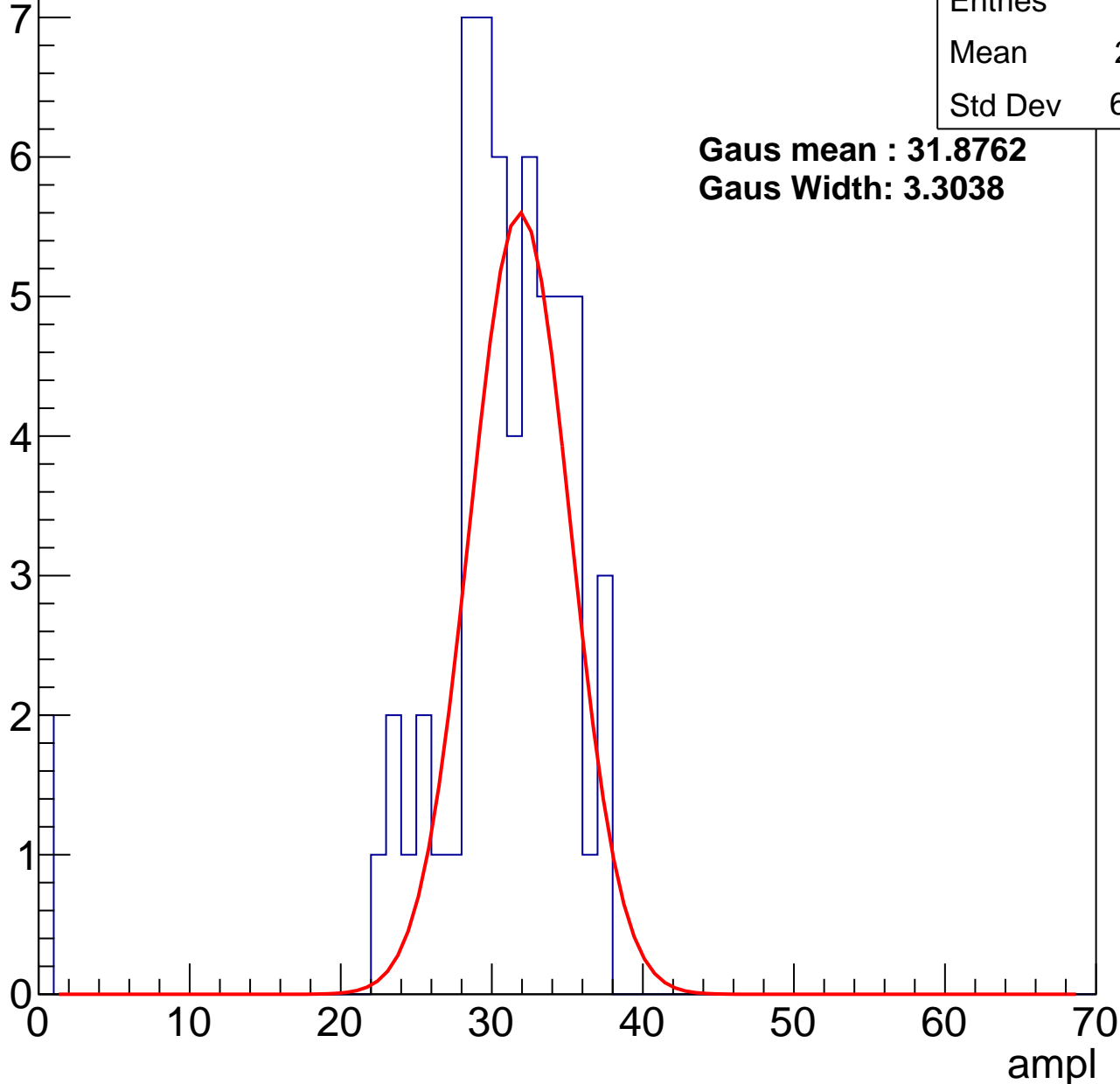
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	29.61
Std Dev	6.587

Gaus mean : 31.8762

Gaus Width: 3.3038



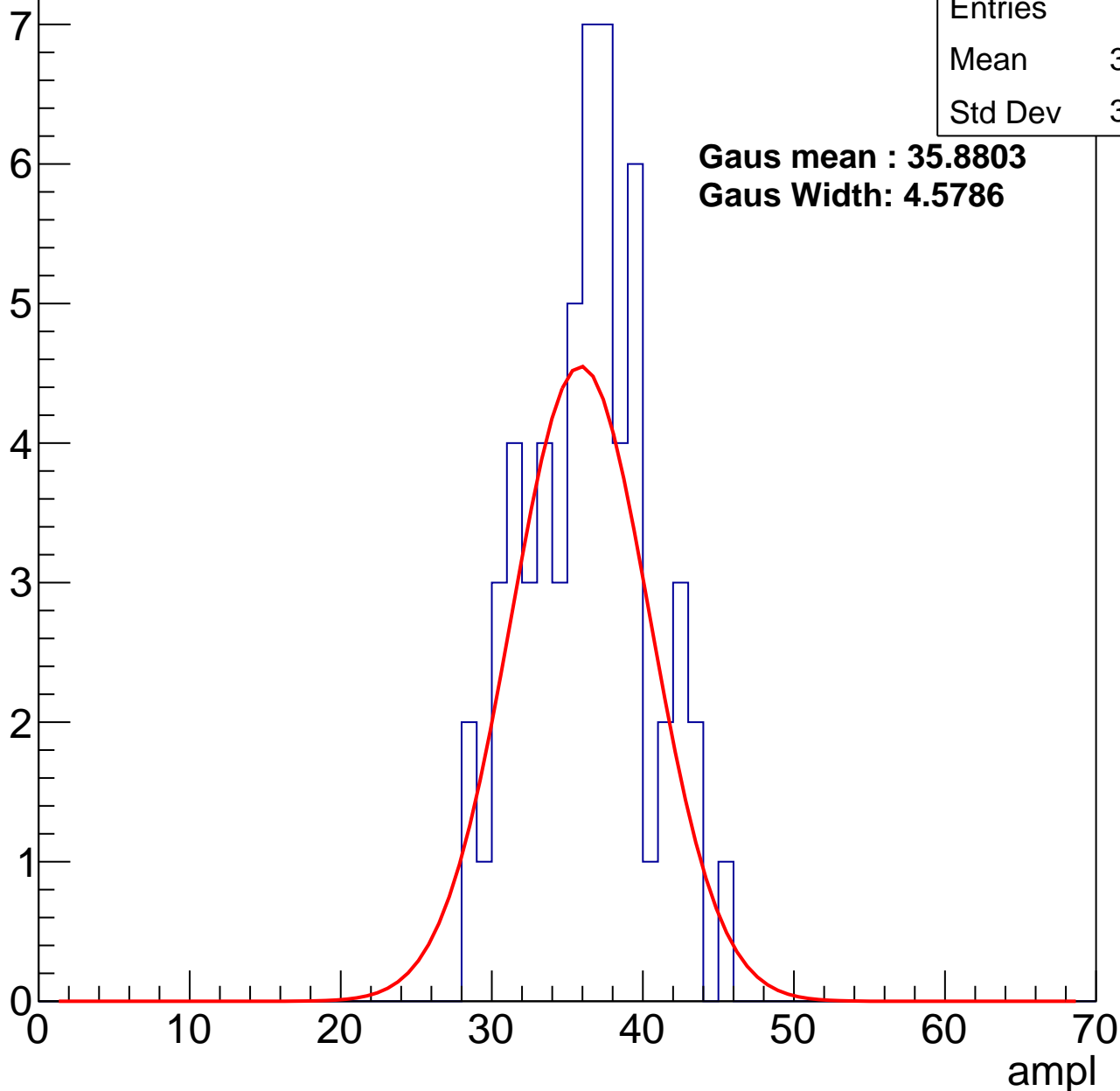
B0L001S, U22-ch98, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	35.86
Std Dev	3.972

Gaus mean : 35.8803
Gaus Width: 4.5786



B0L001S, U22-ch98, adc2

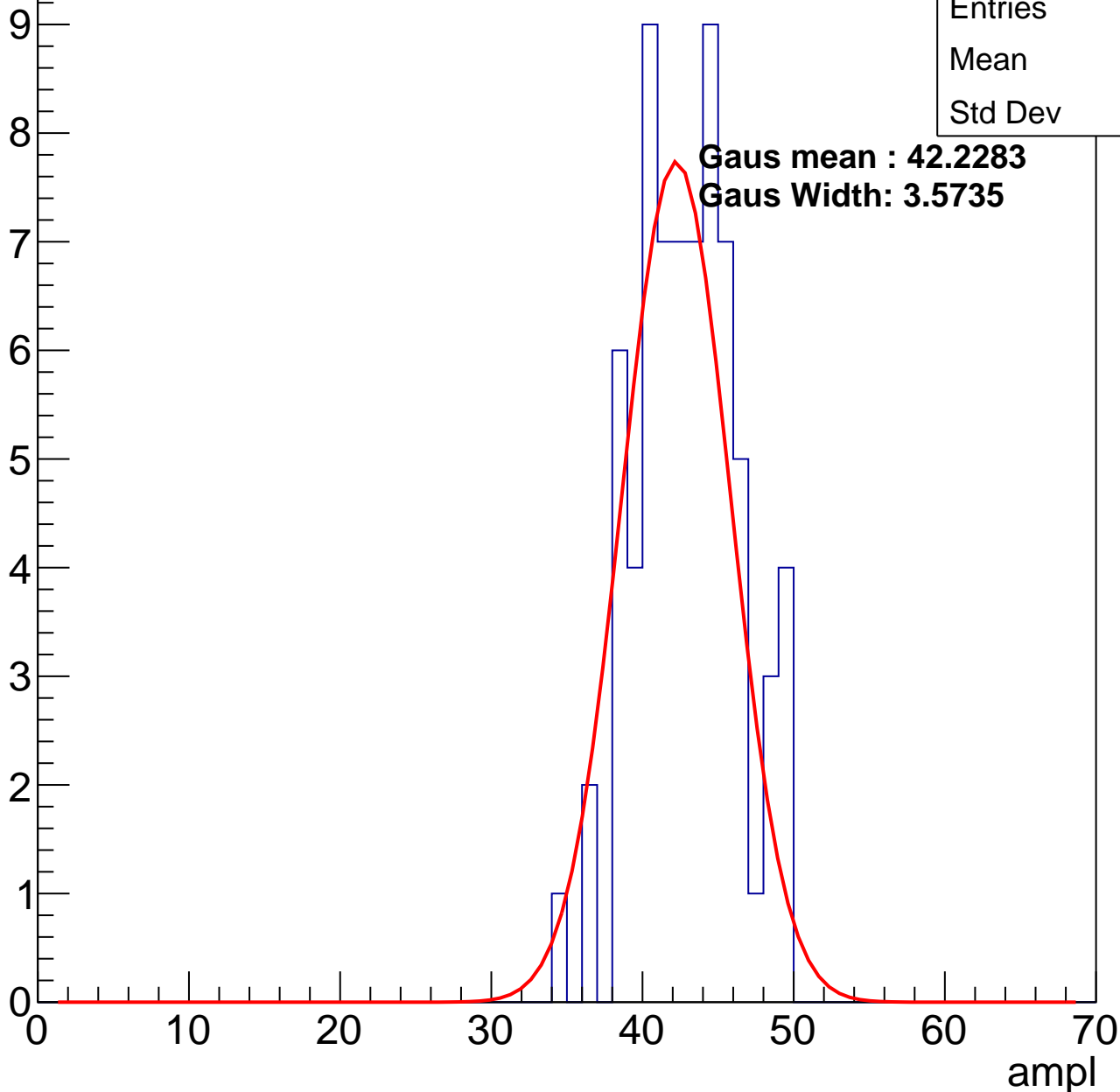
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	42.5
Std Dev	3.35

Gaus mean : 42.2283

Gaus Width: 3.5735

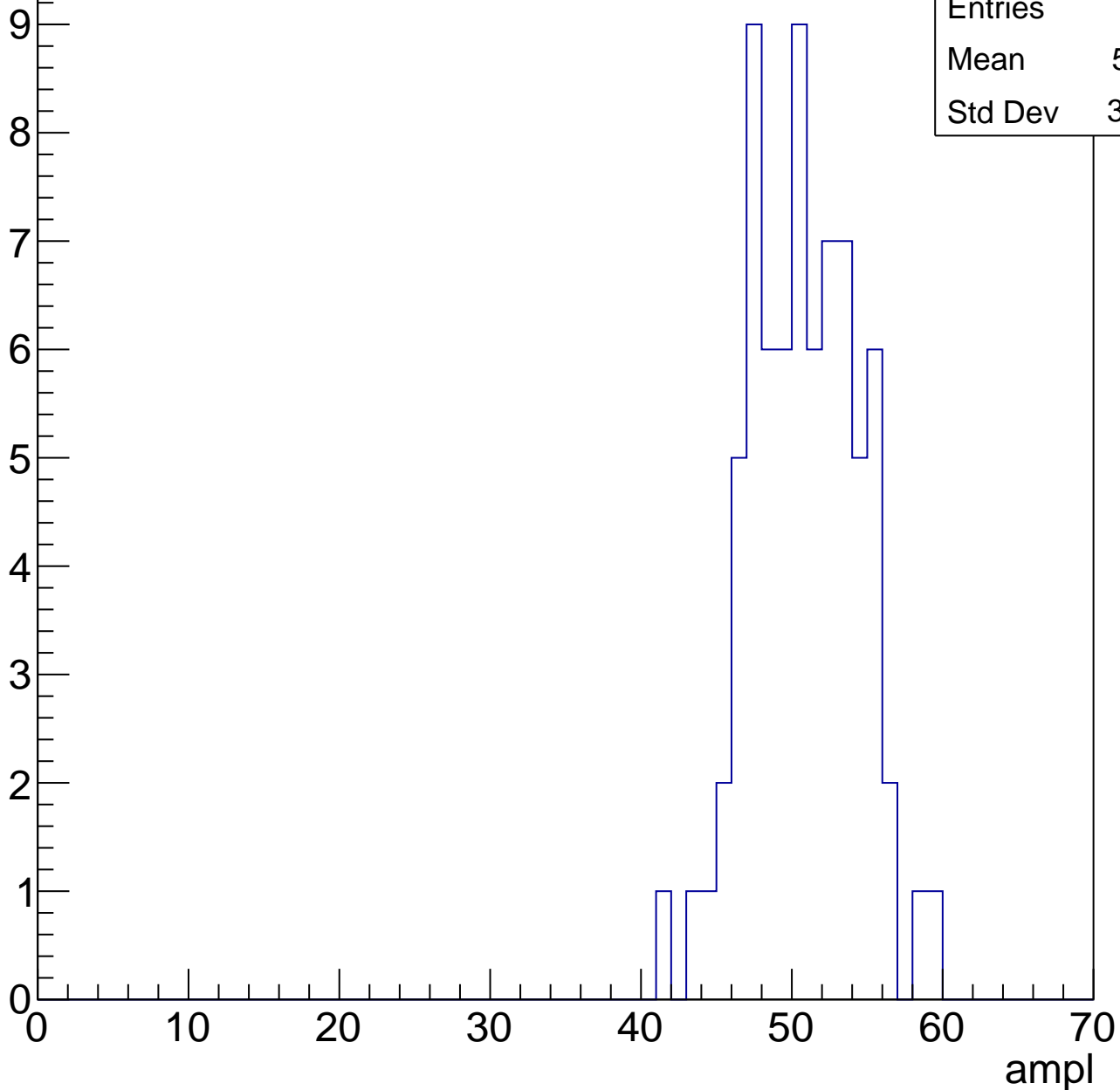


B0L001S, U22-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.31
Std Dev	3.548

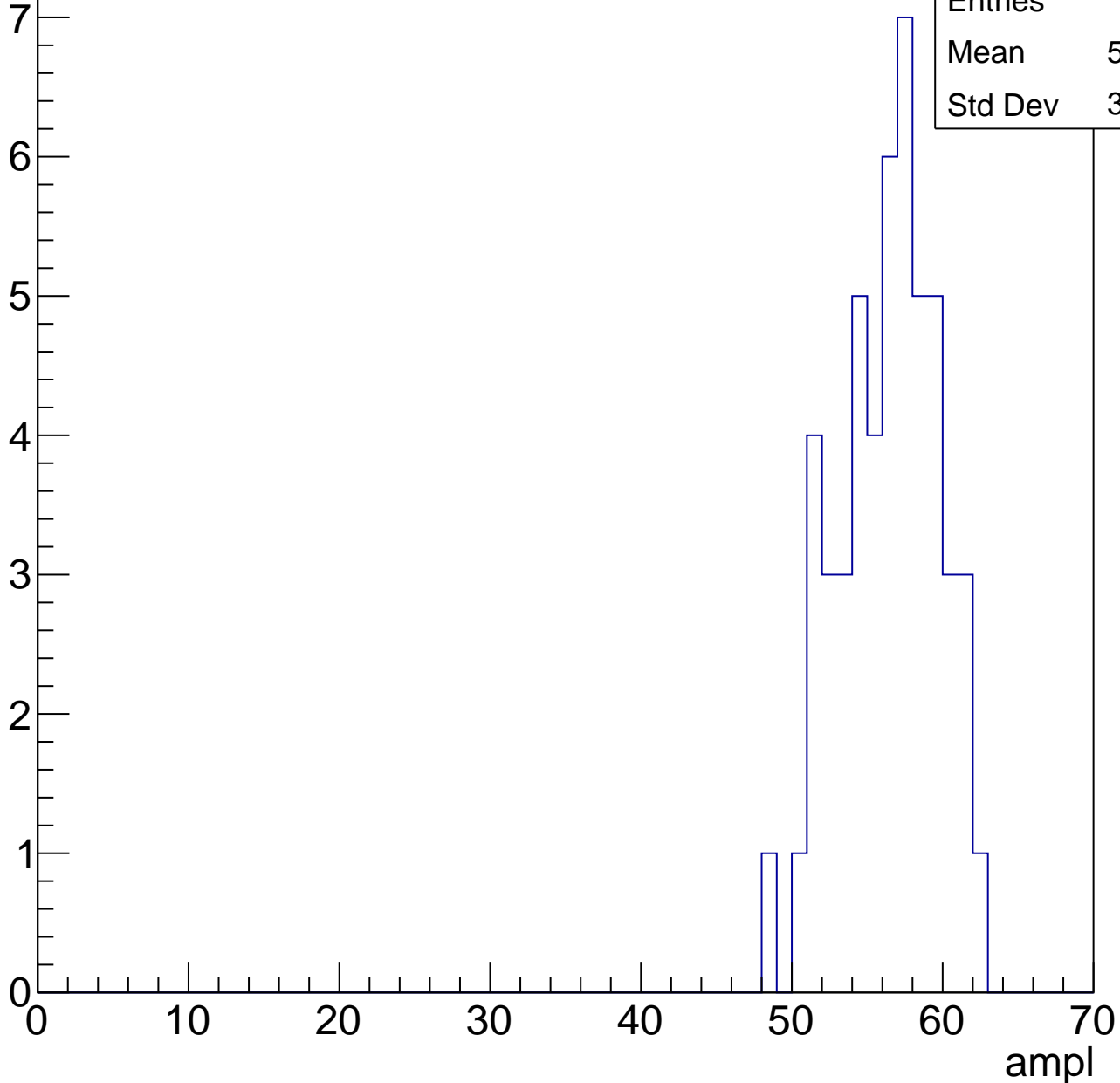


B0L001S, U22-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	55.92
Std Dev	3.223



B0L001S, U22-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries	39
Mean	60.28
Std Dev	2.087

ampl

0

10

20

30

40

50

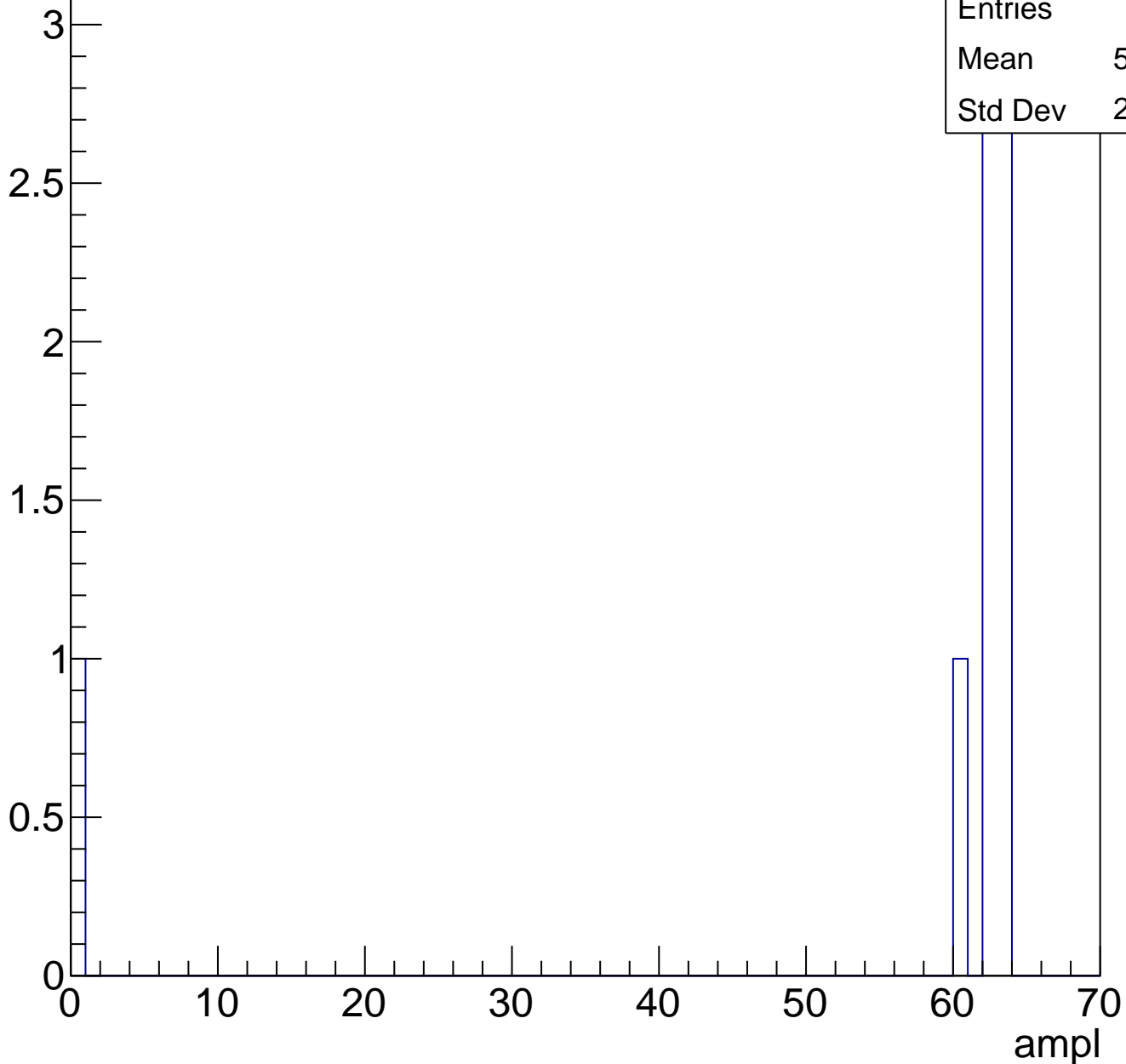
60

70

B0L001S, U22-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch99, adc0

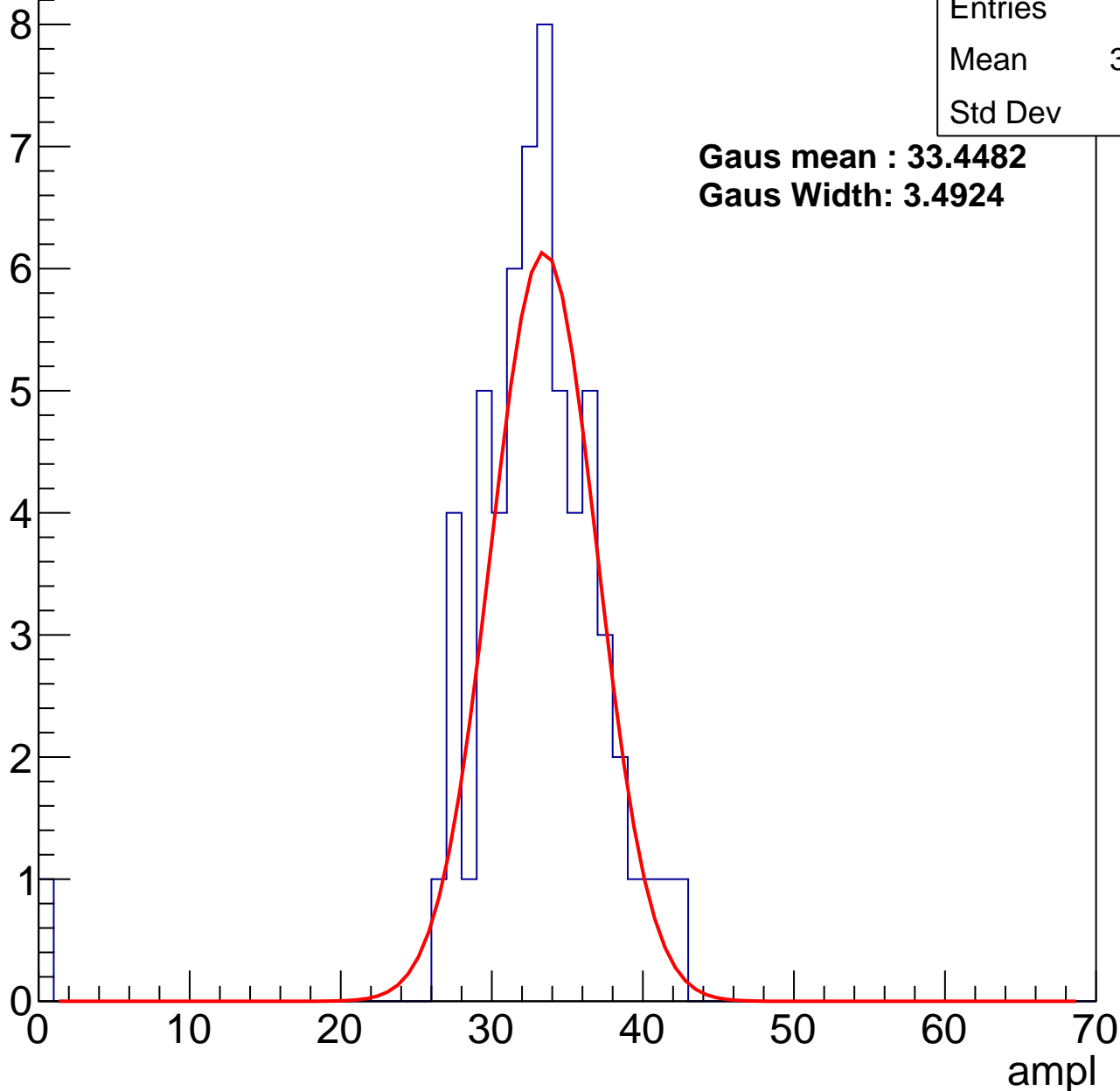
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	32.33
Std Dev	5.51

Gaus mean : 33.4482

Gaus Width: 3.4924



B0L001S, U22-ch99, adc1

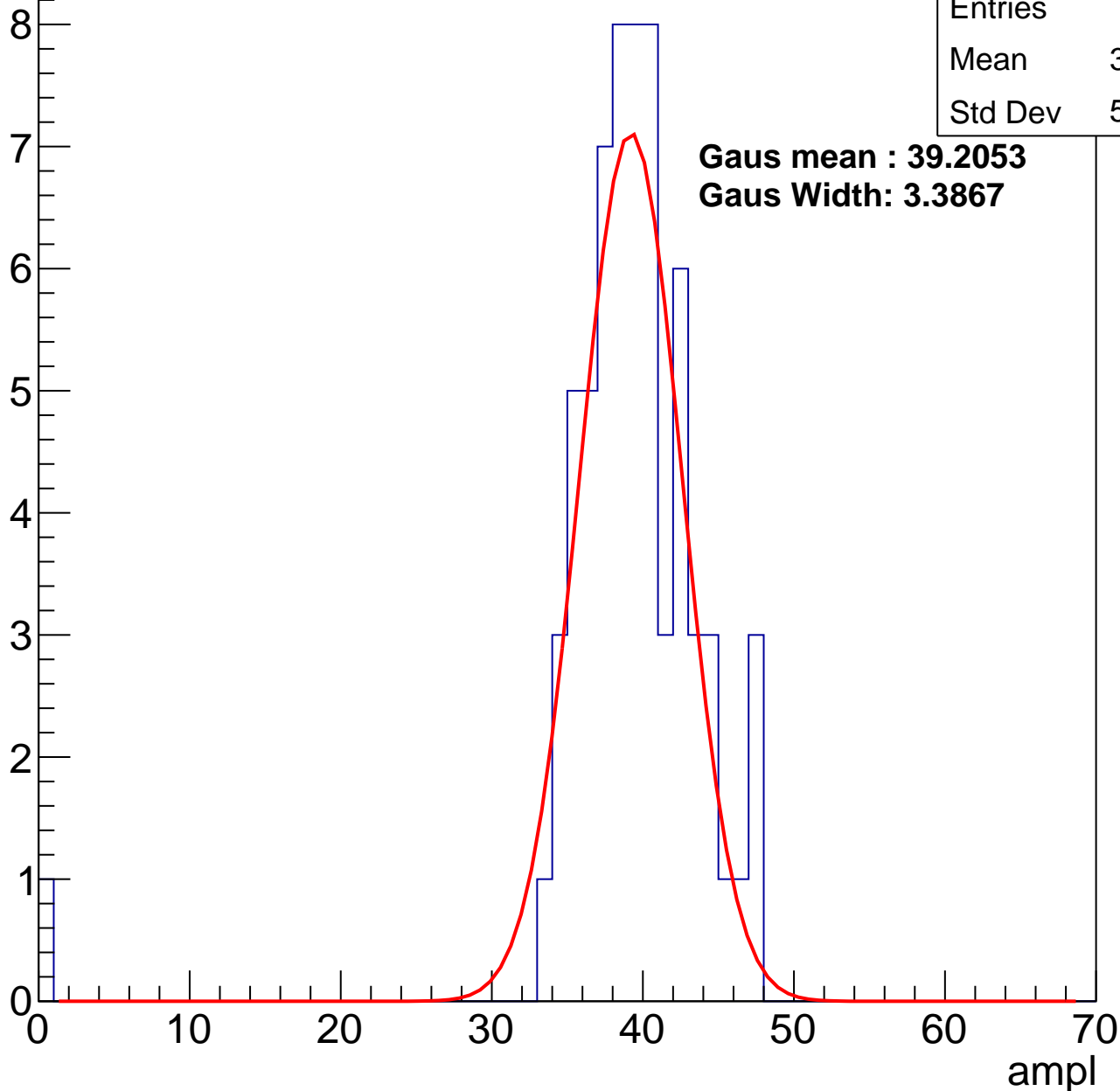
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.68
Std Dev	5.852

Gaus mean : 39.2053

Gaus Width: 3.3867



B0L001S, U22-ch99, adc2

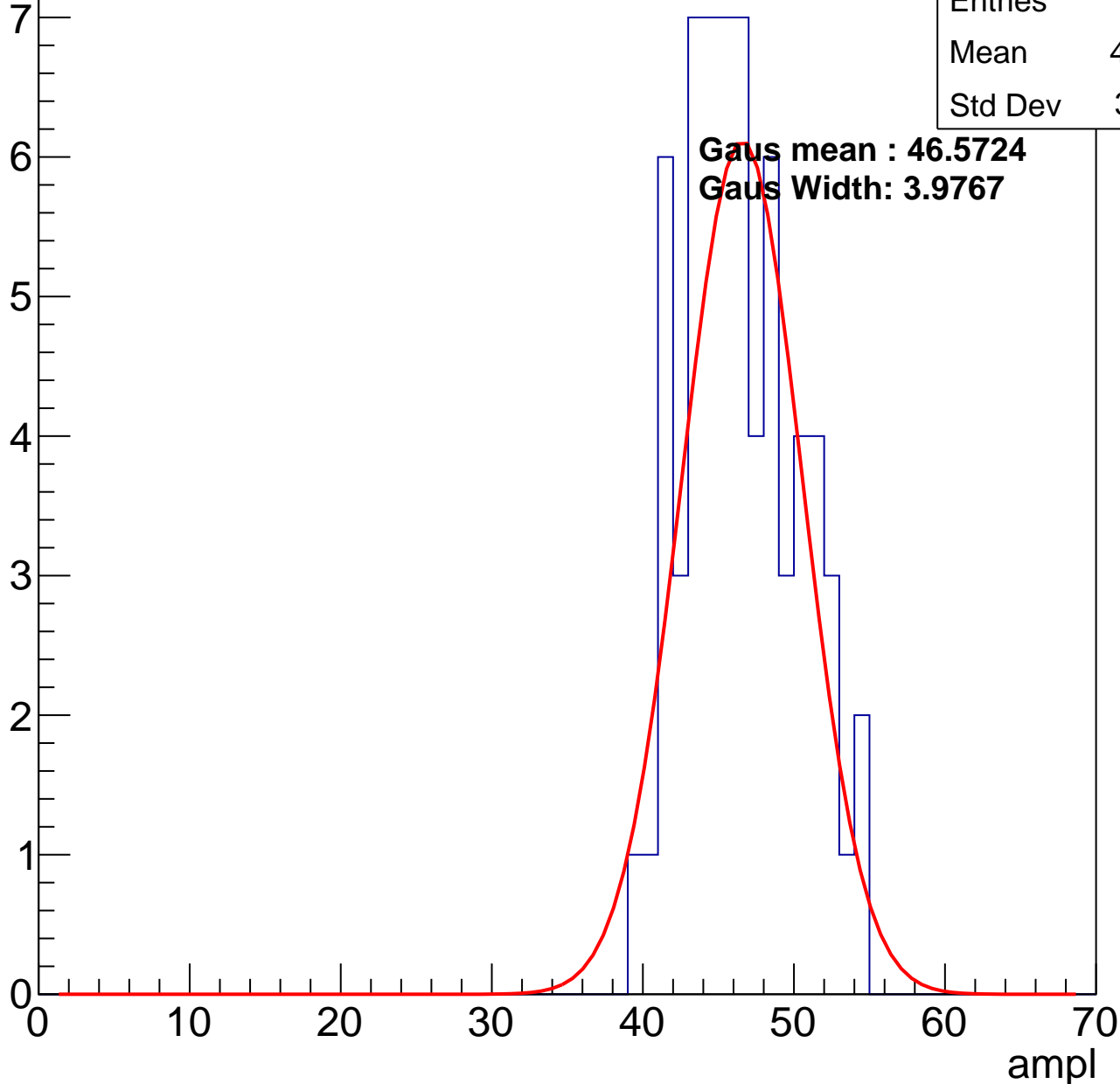
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	46.08
Std Dev	3.661

Gaus mean : 46.5724

Gaus Width: 3.9767

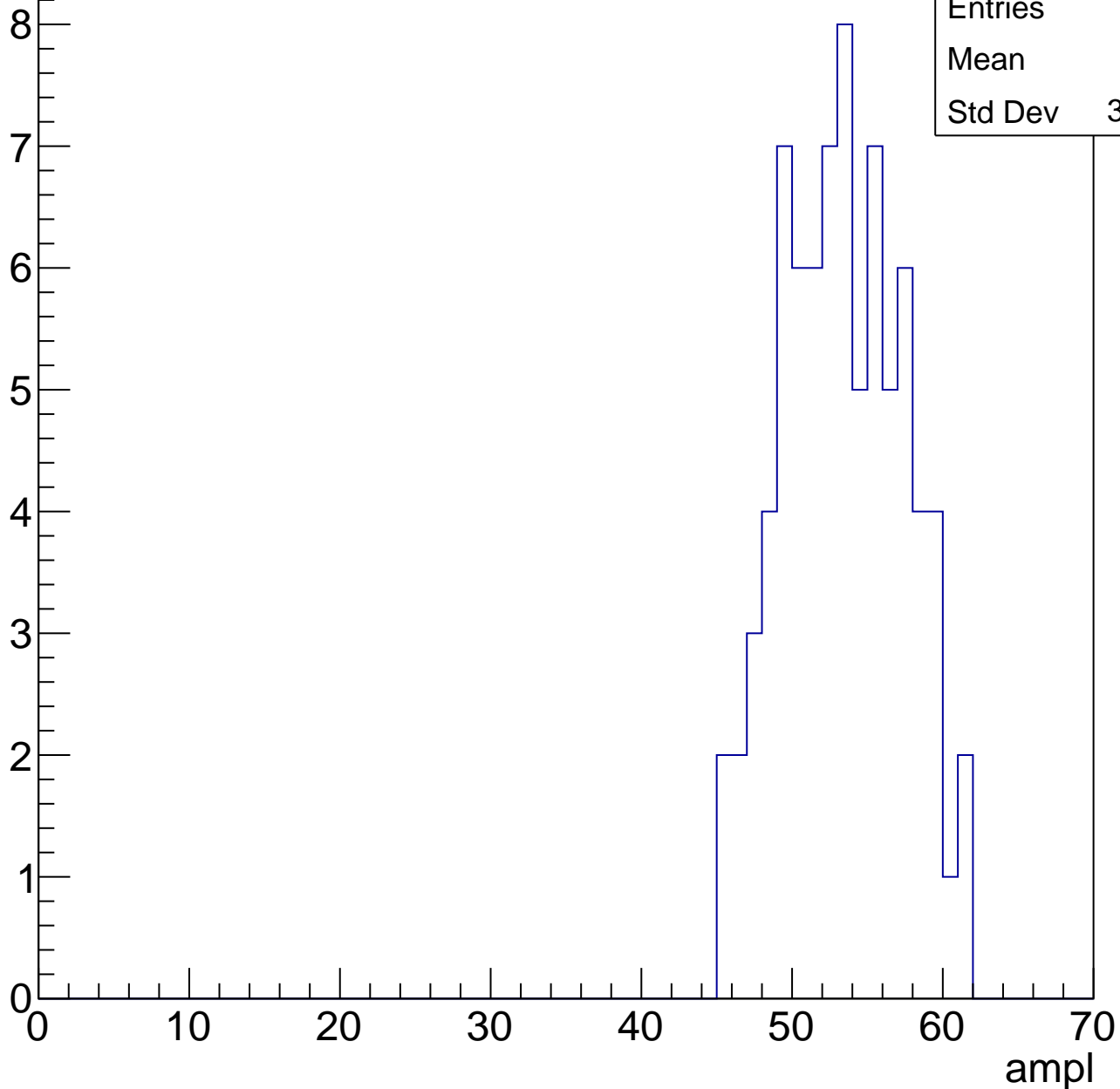


B0L001S, U22-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

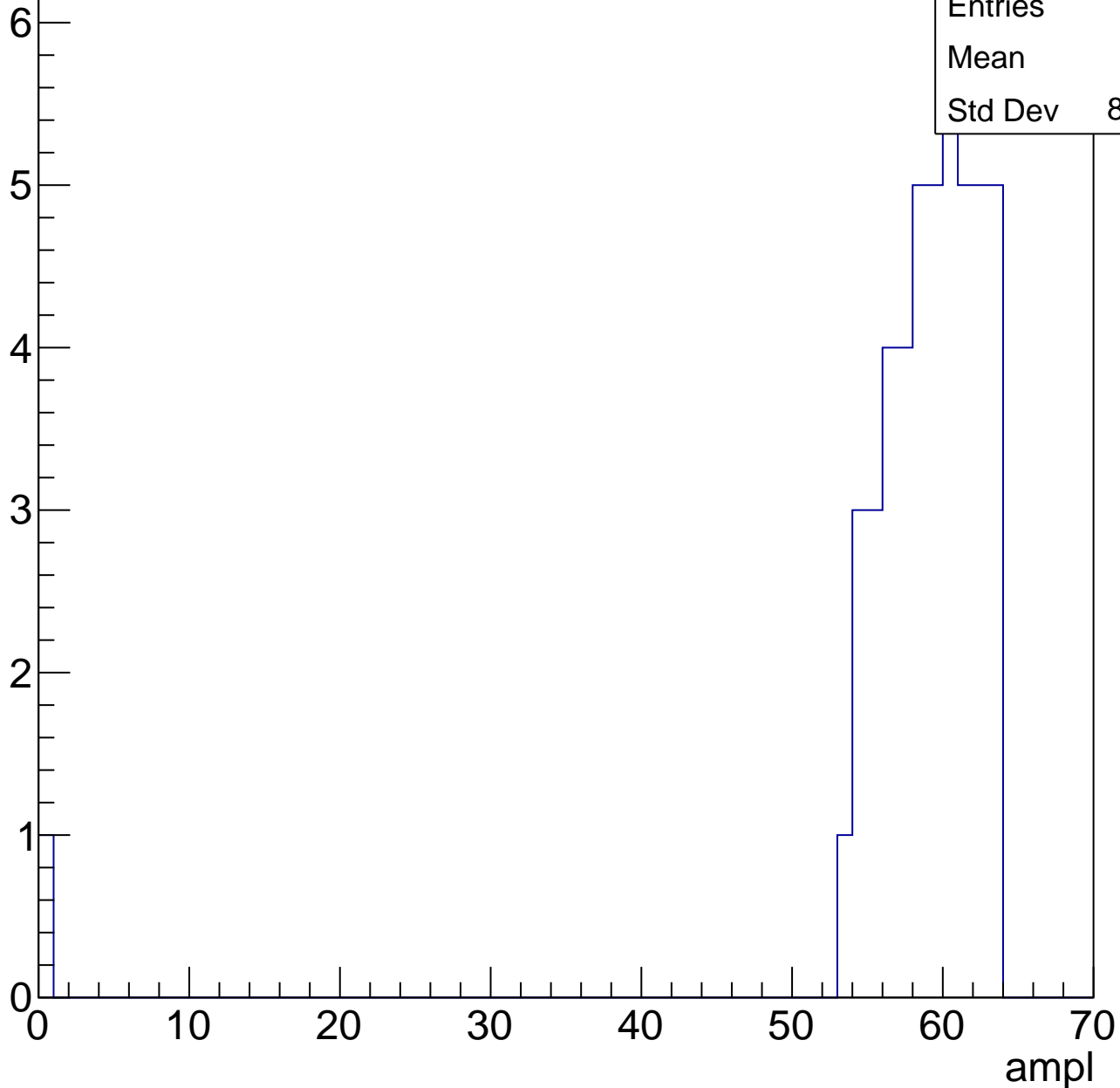
Entries	79
Mean	52.9
Std Dev	3.938



B0L001S, U22-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

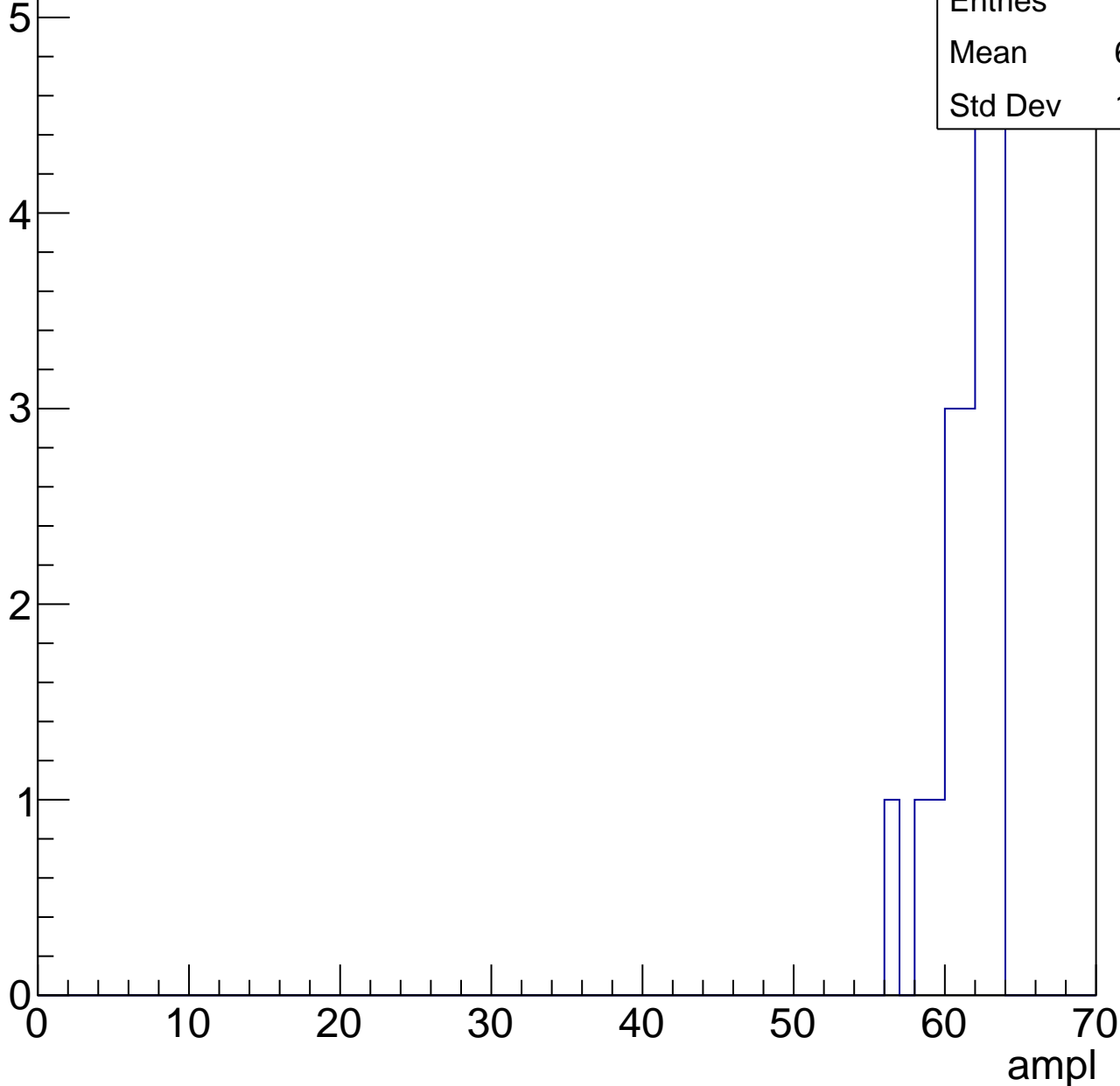


B0L001S, U22-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	61.11
Std Dev	1.861



B0L001S, U22-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch100, adc0

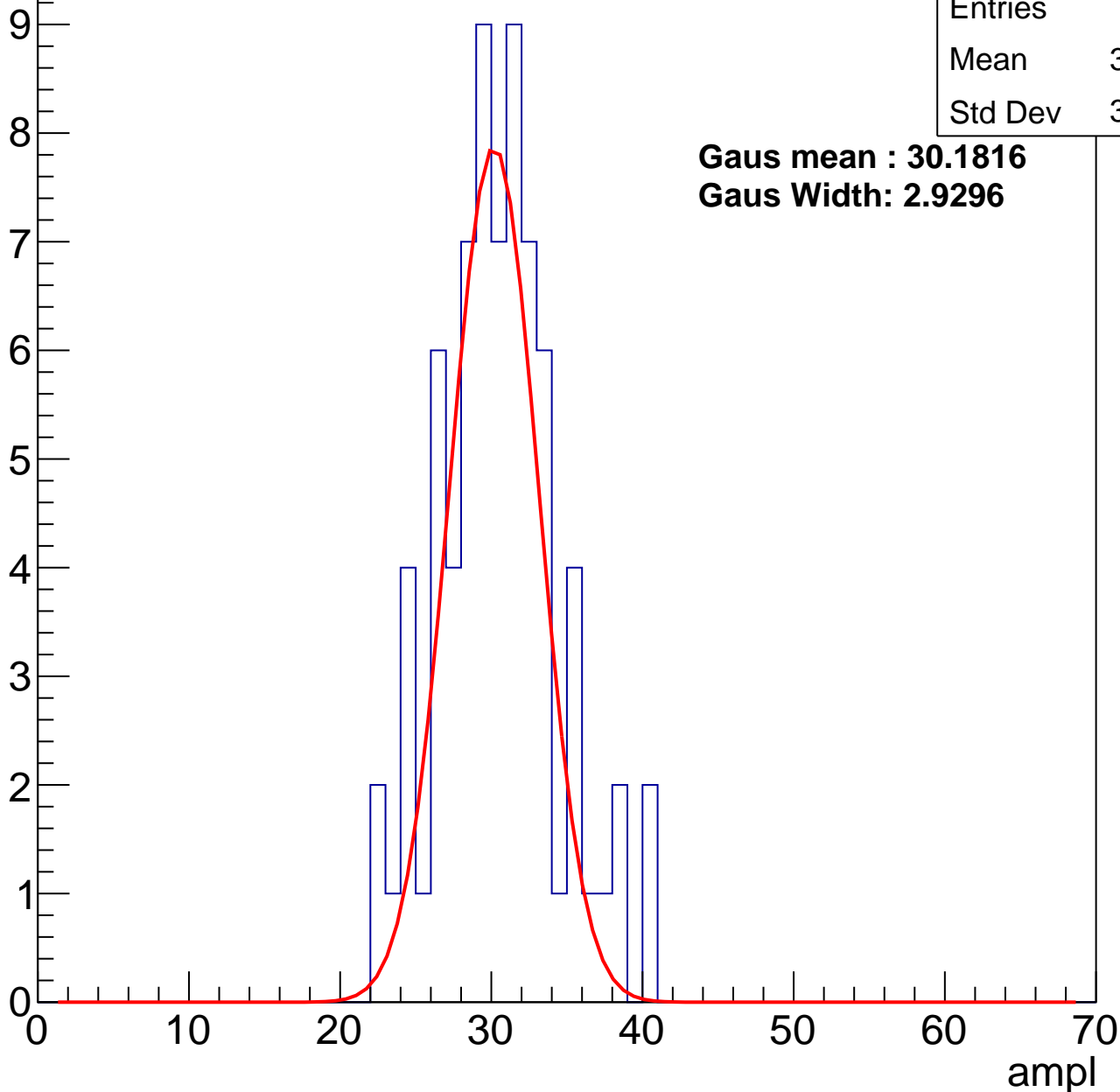
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.04
Std Dev	3.916

Gaus mean : 30.1816

Gaus Width: 2.9296



B0L001S, U22-ch100, adc1

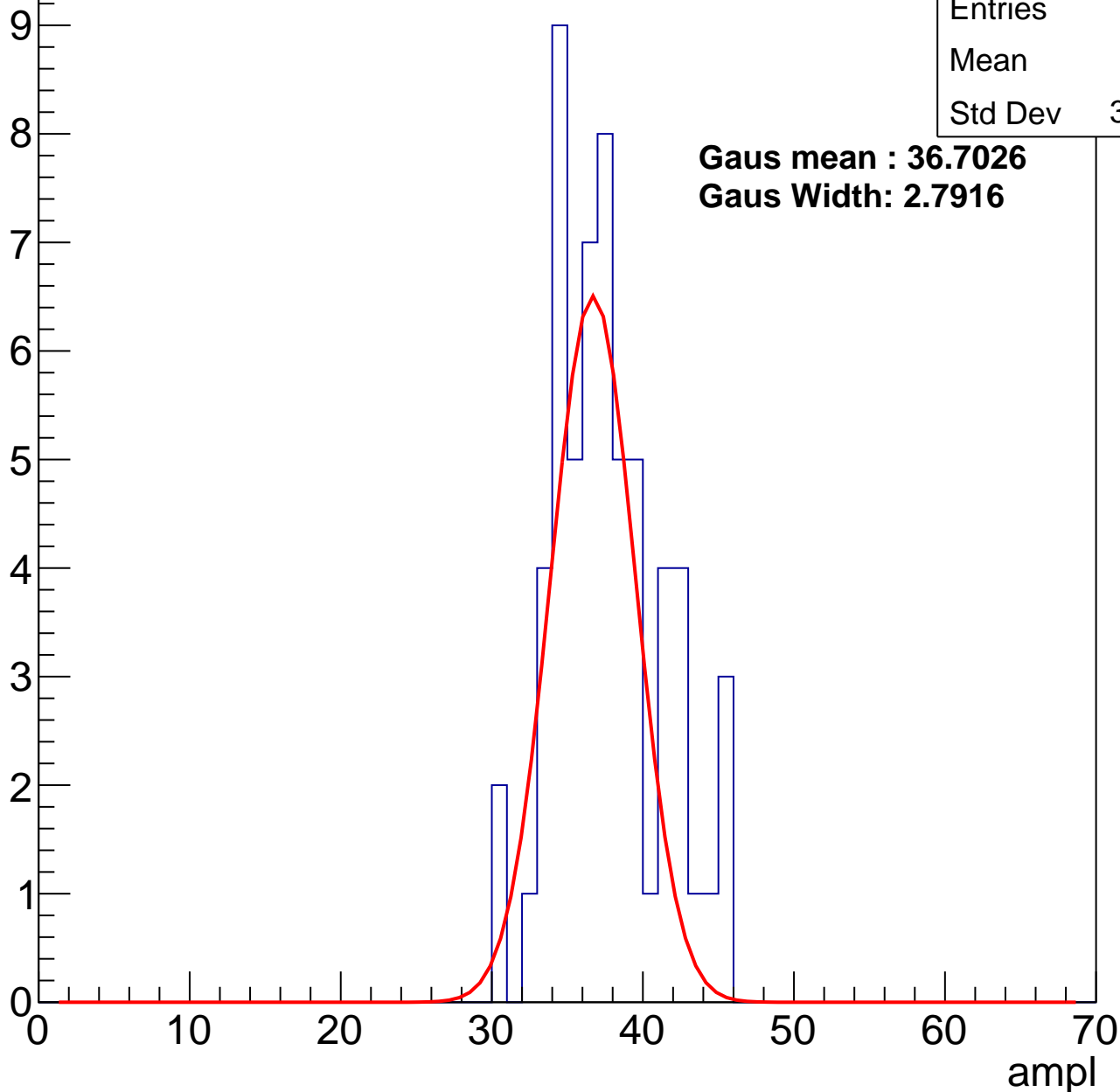
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.2
Std Dev	3.577

Gaus mean : 36.7026

Gaus Width: 2.7916



B0L001S, U22-ch100, adc2

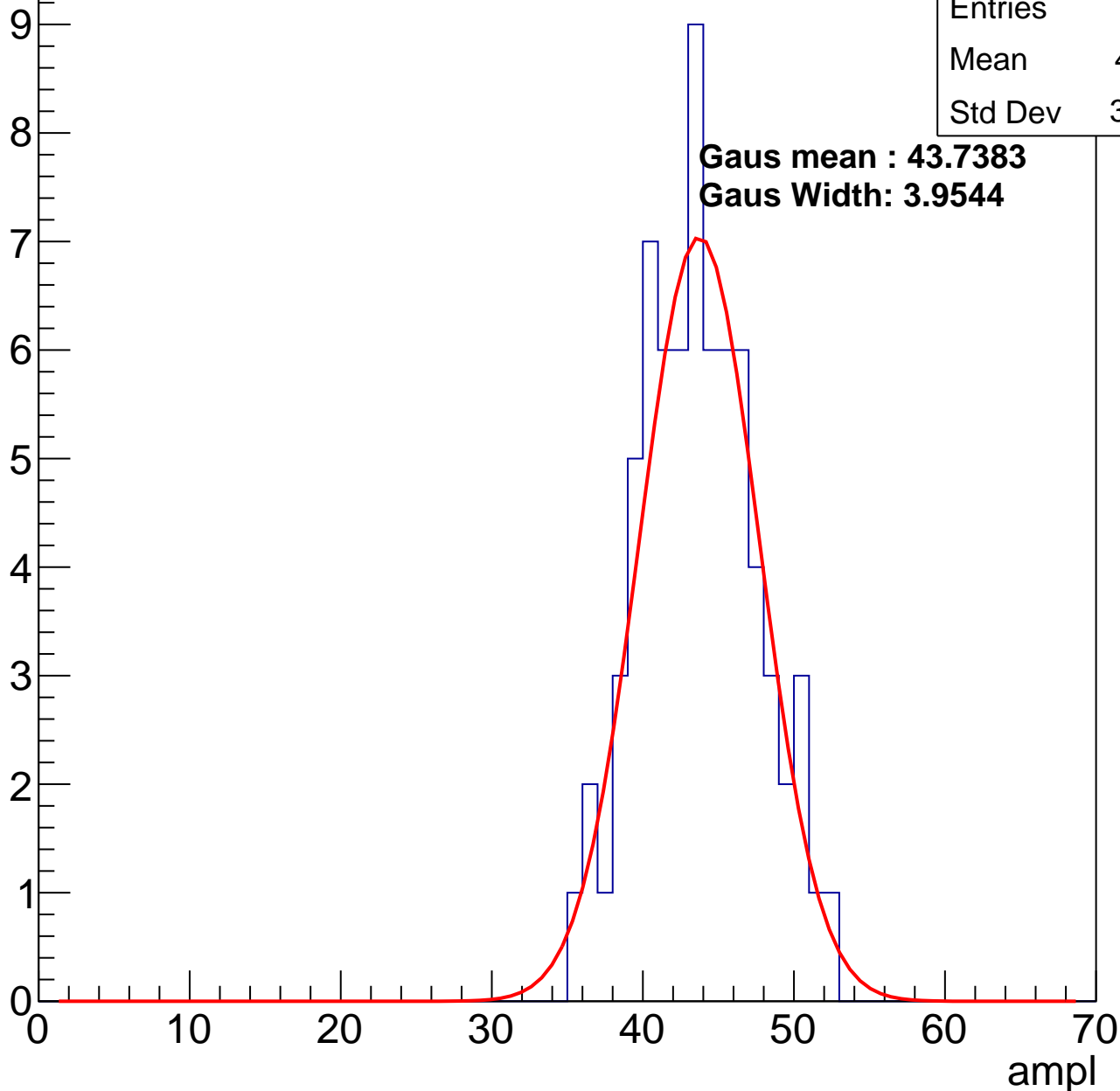
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	43.21
Std Dev	3.782

Gaus mean : 43.7383

Gaus Width: 3.9544

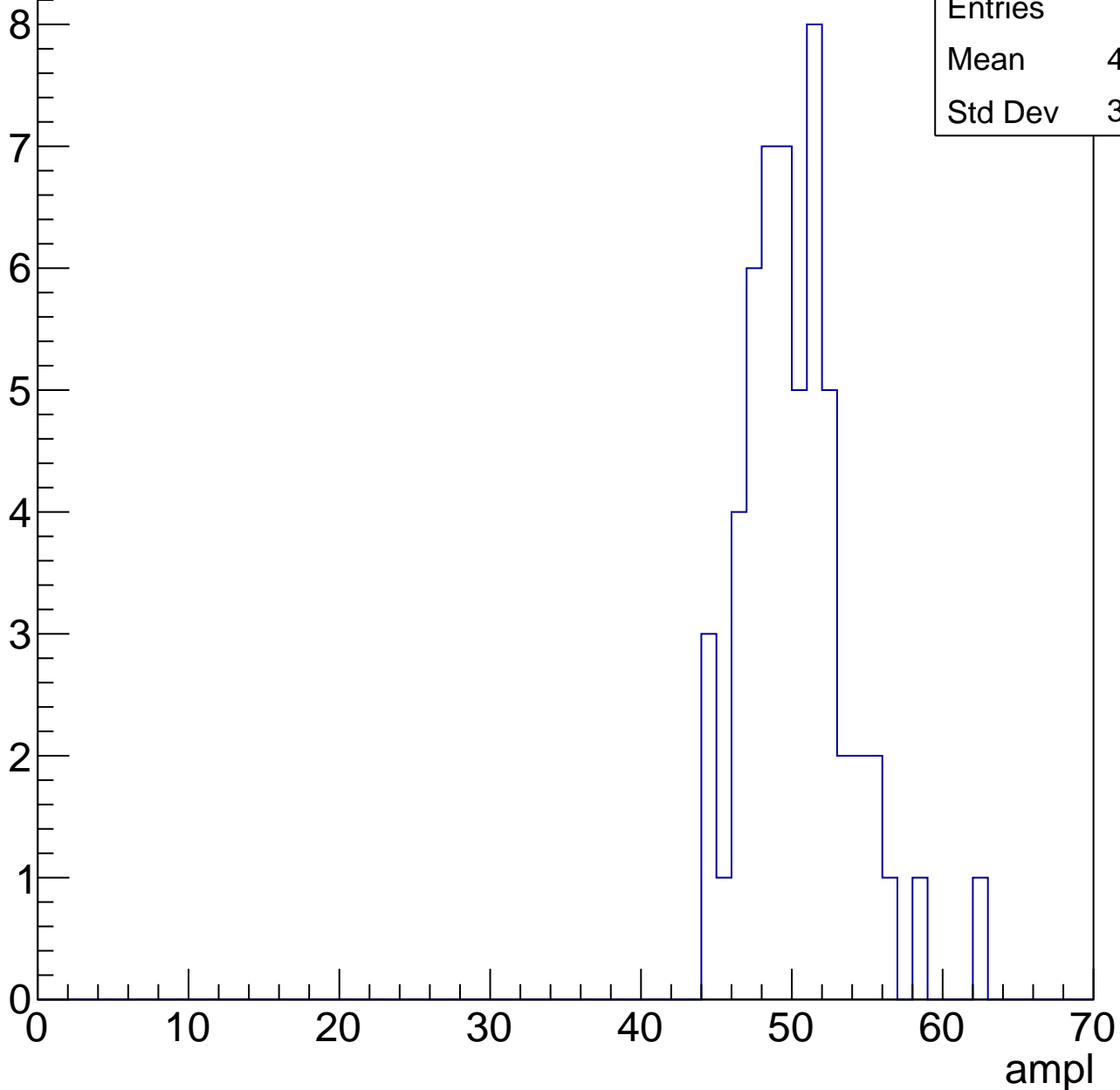


B0L001S, U22-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	49.82
Std Dev	3.454

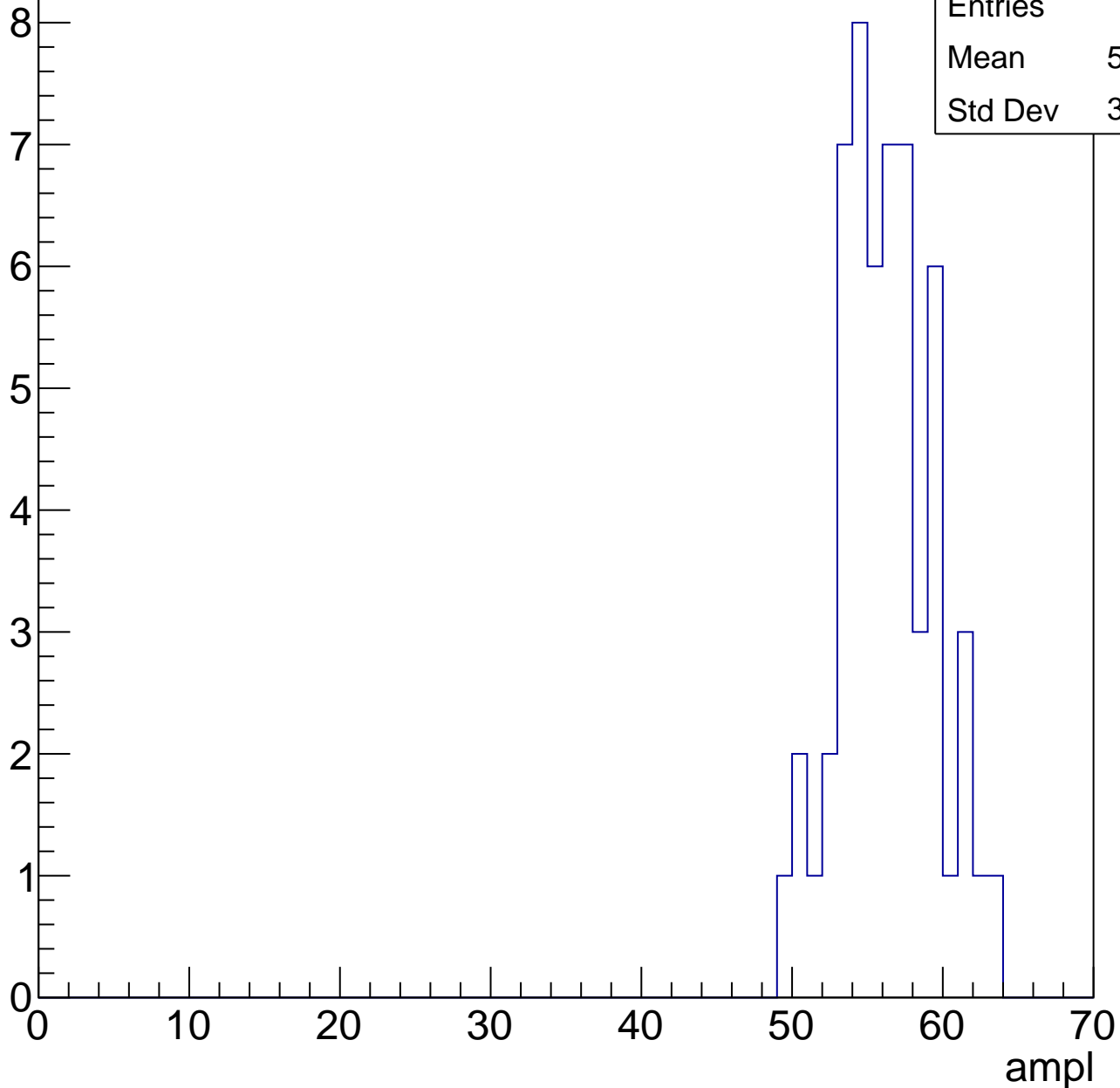


B0L001S, U22-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	55.79
Std Dev	3.063

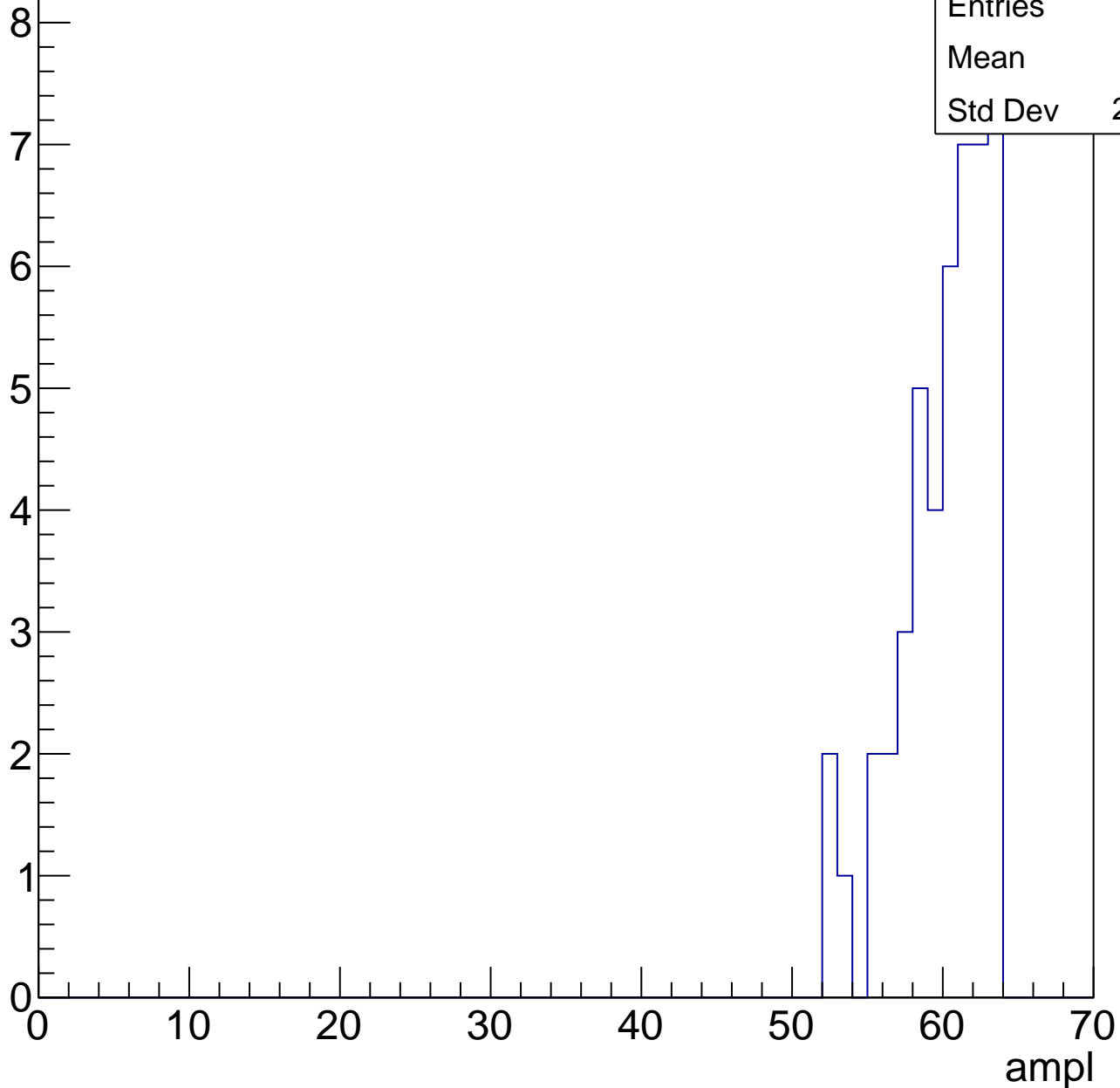


B0L001S, U22-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

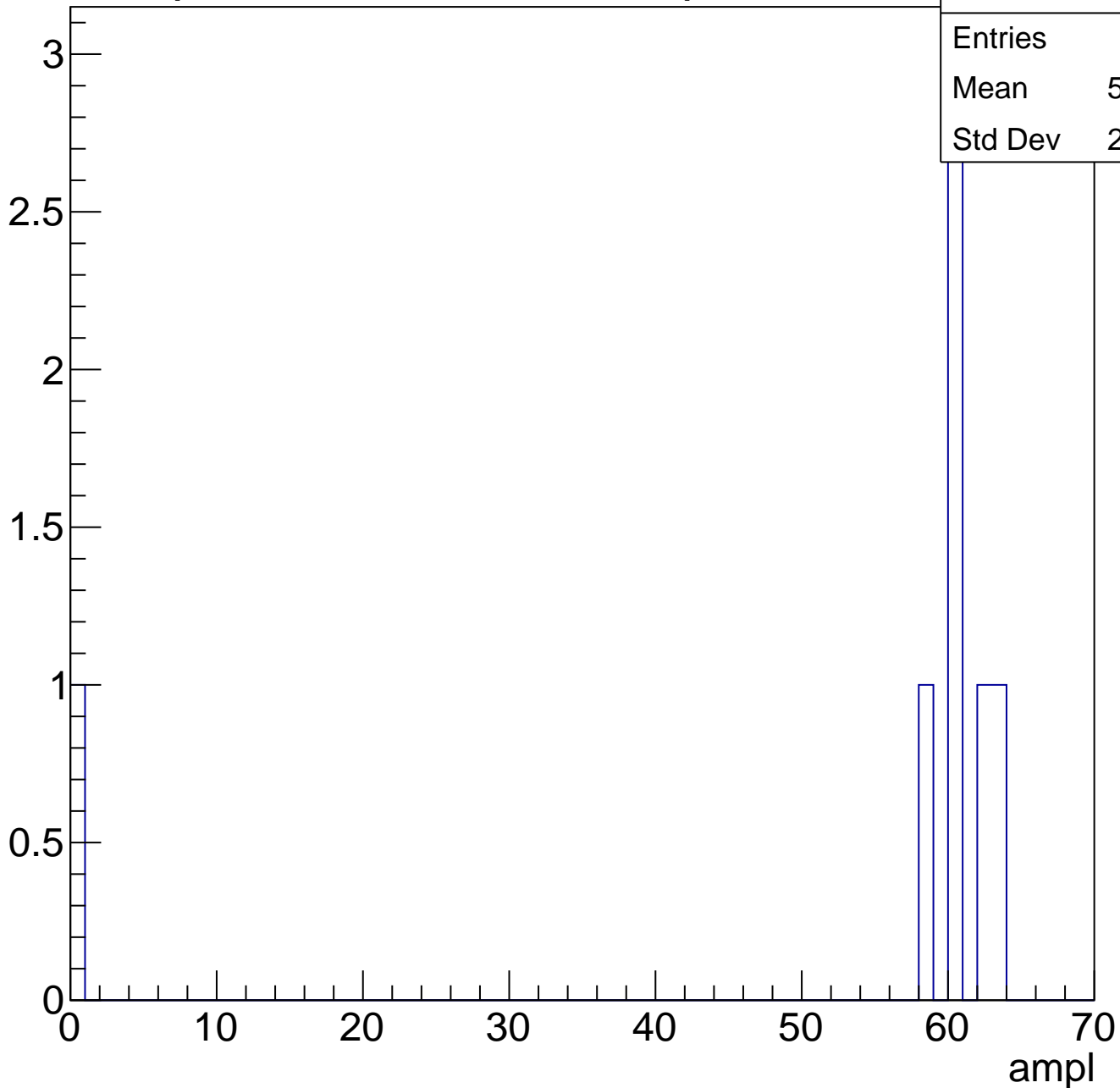
Entries	47
Mean	59.6
Std Dev	2.951



B0L001S, U22-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch101, adc0

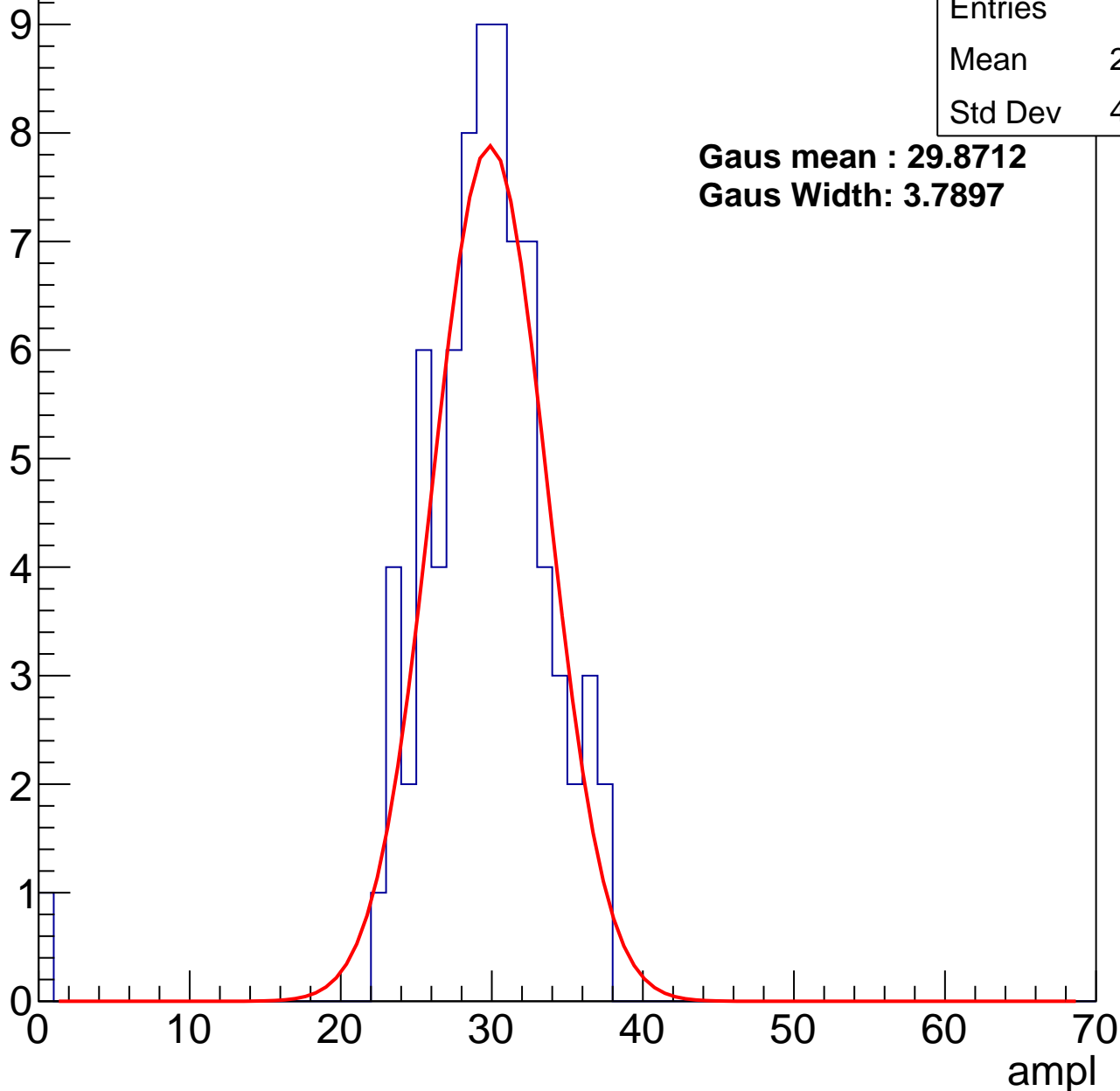
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	28.97
Std Dev	4.849

Gaus mean : 29.8712

Gaus Width: 3.7897



B0L001S, U22-ch101, adc1

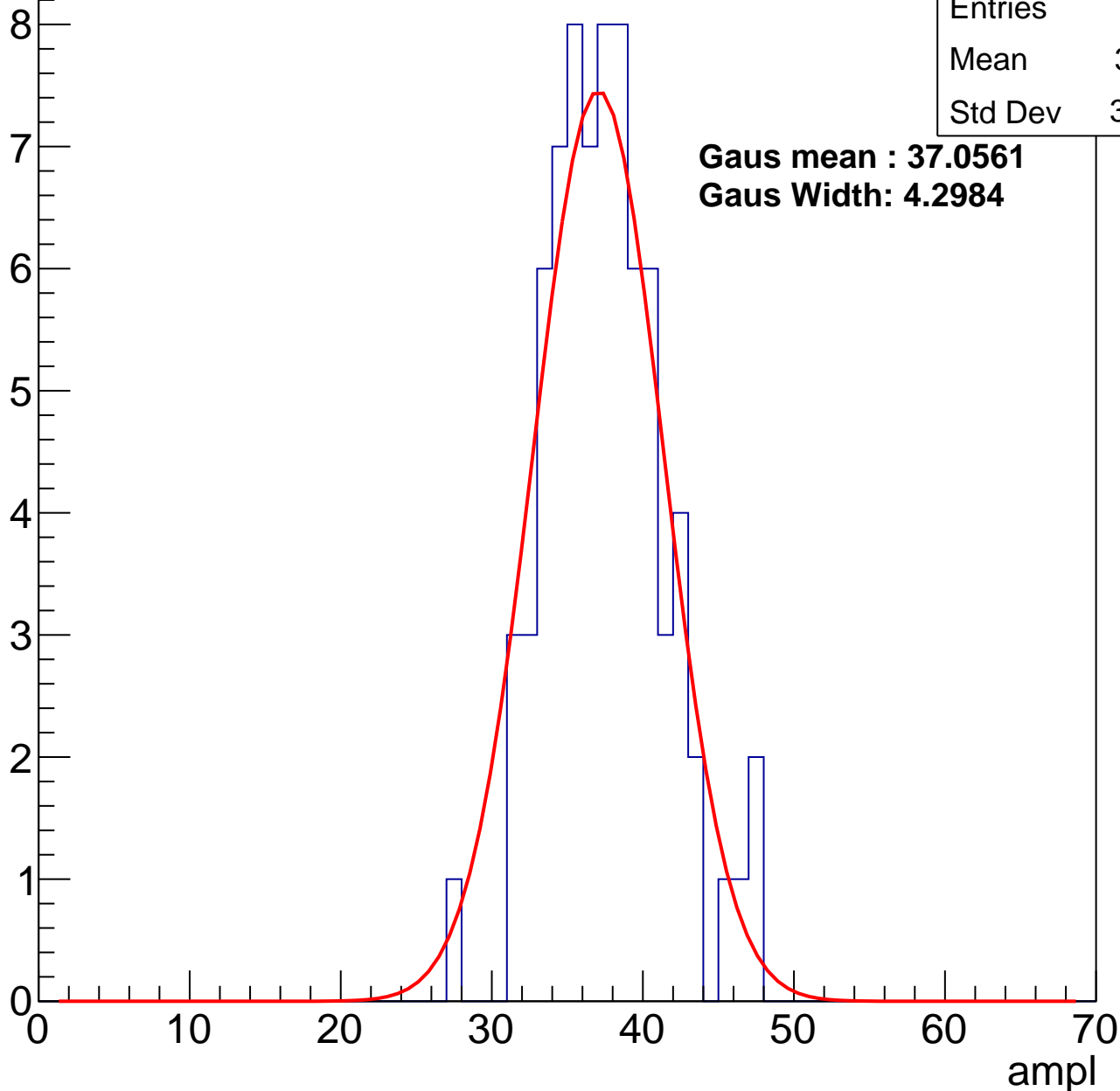
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	37.11
Std Dev	3.858

Gaus mean : 37.0561

Gaus Width: 4.2984



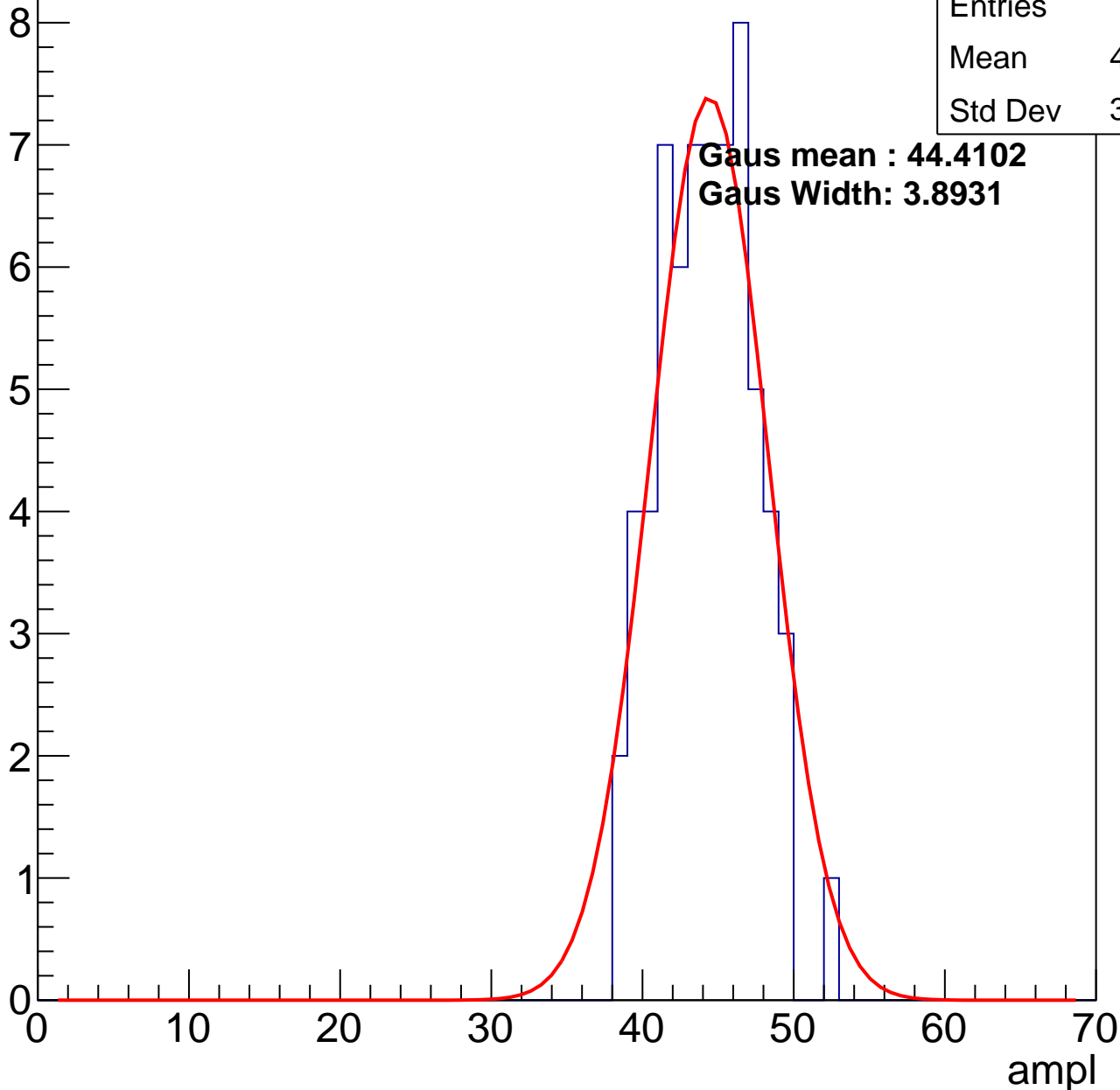
B0L001S, U22-ch101, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.83
Std Dev	3.076

Gaus mean : 44.4102
Gaus Width: 3.8931

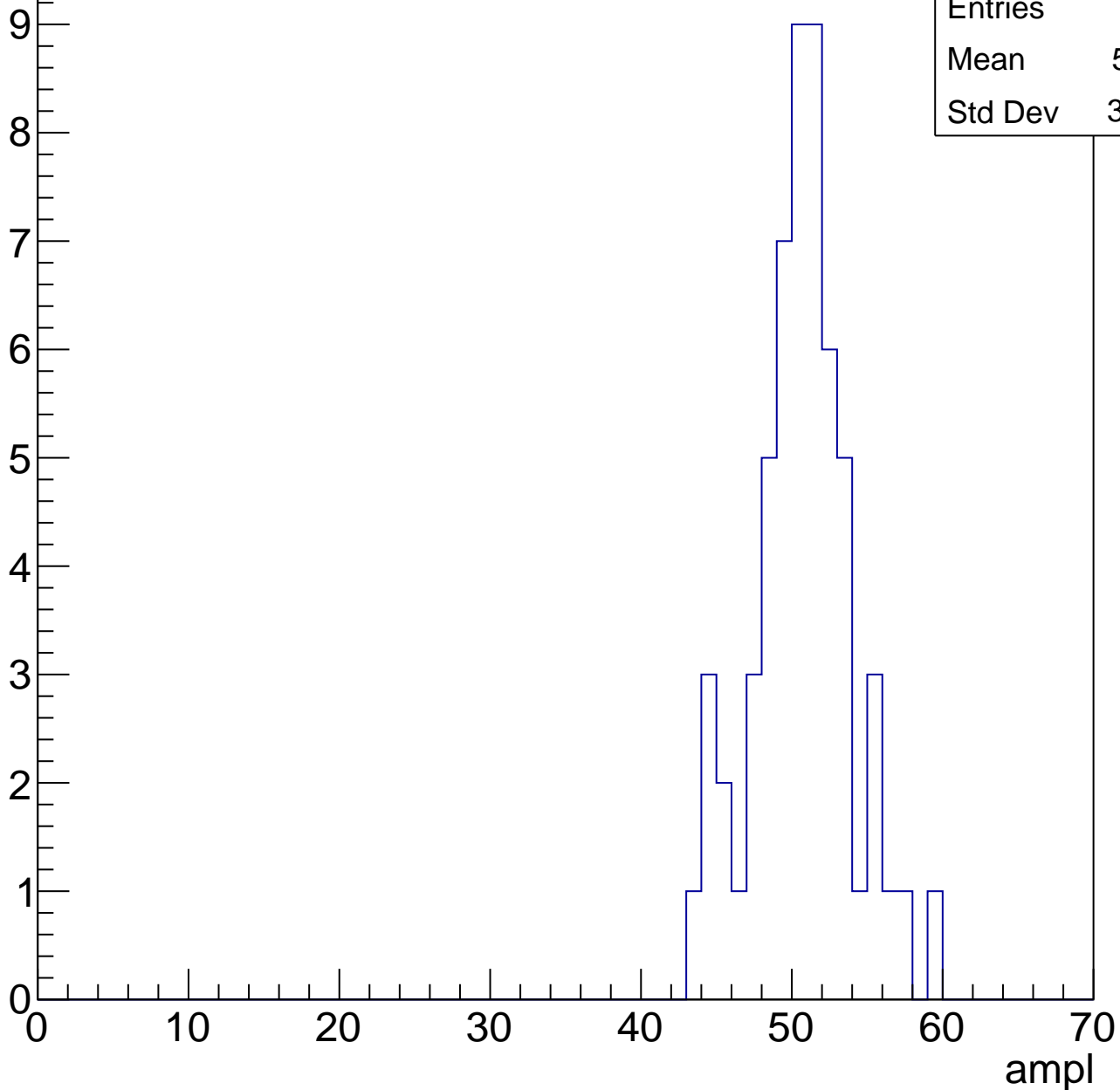


B0L001S, U22-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	50.21
Std Dev	3.242

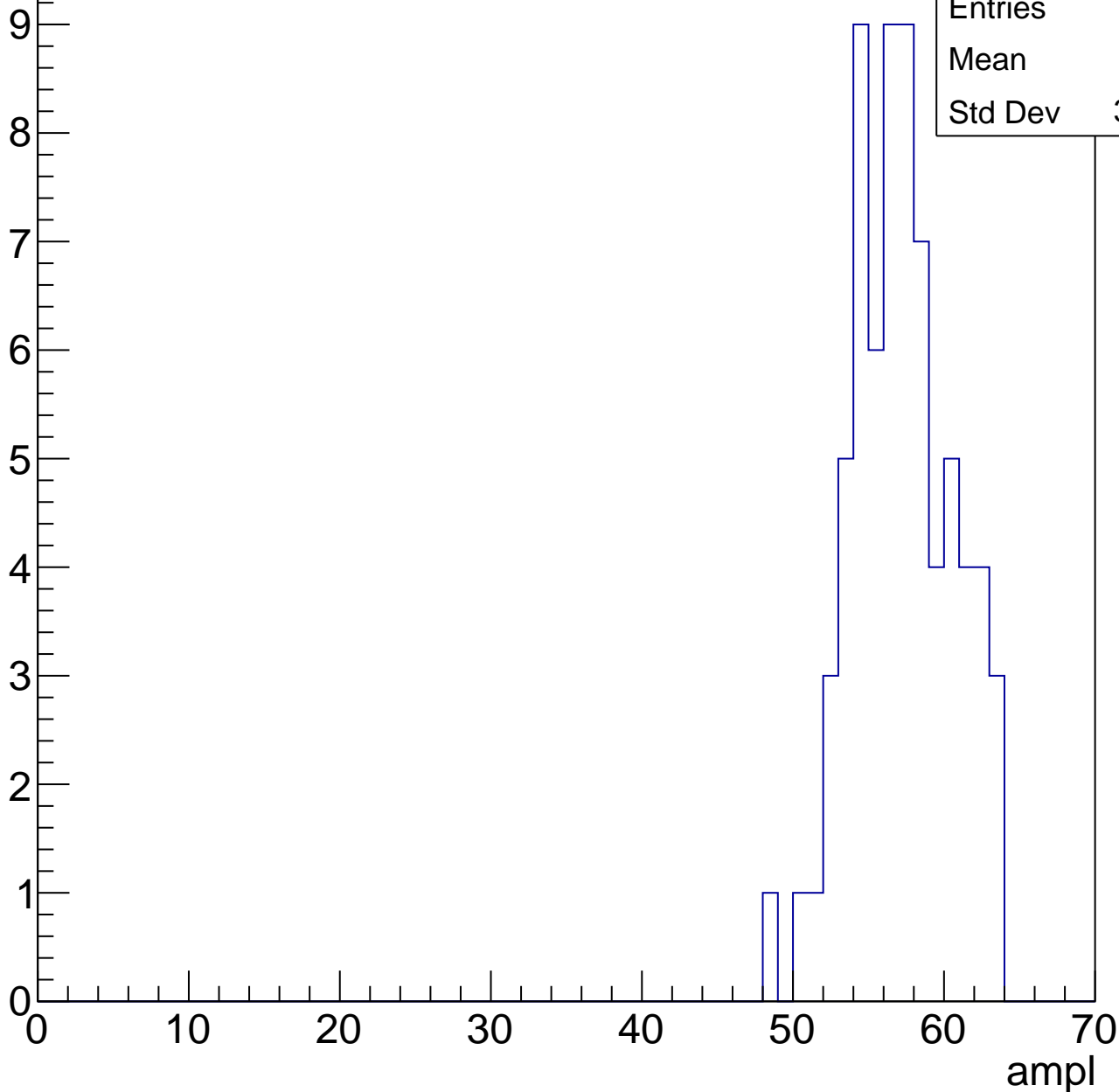


B0L001S, U22-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	56.7
Std Dev	3.291

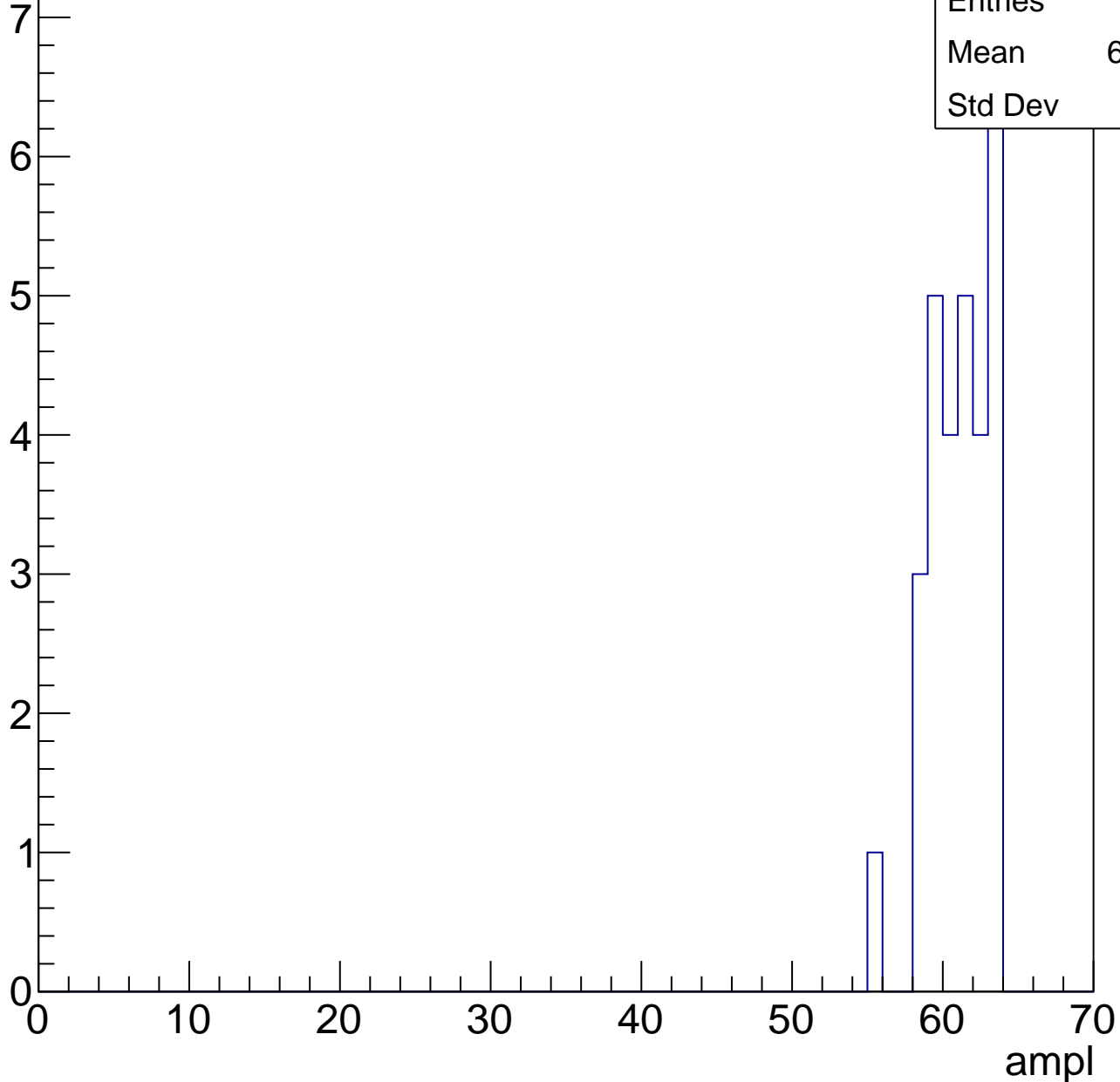


B0L001S, U22-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	29
Mean	60.62
Std Dev	1.99



B0L001S, U22-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

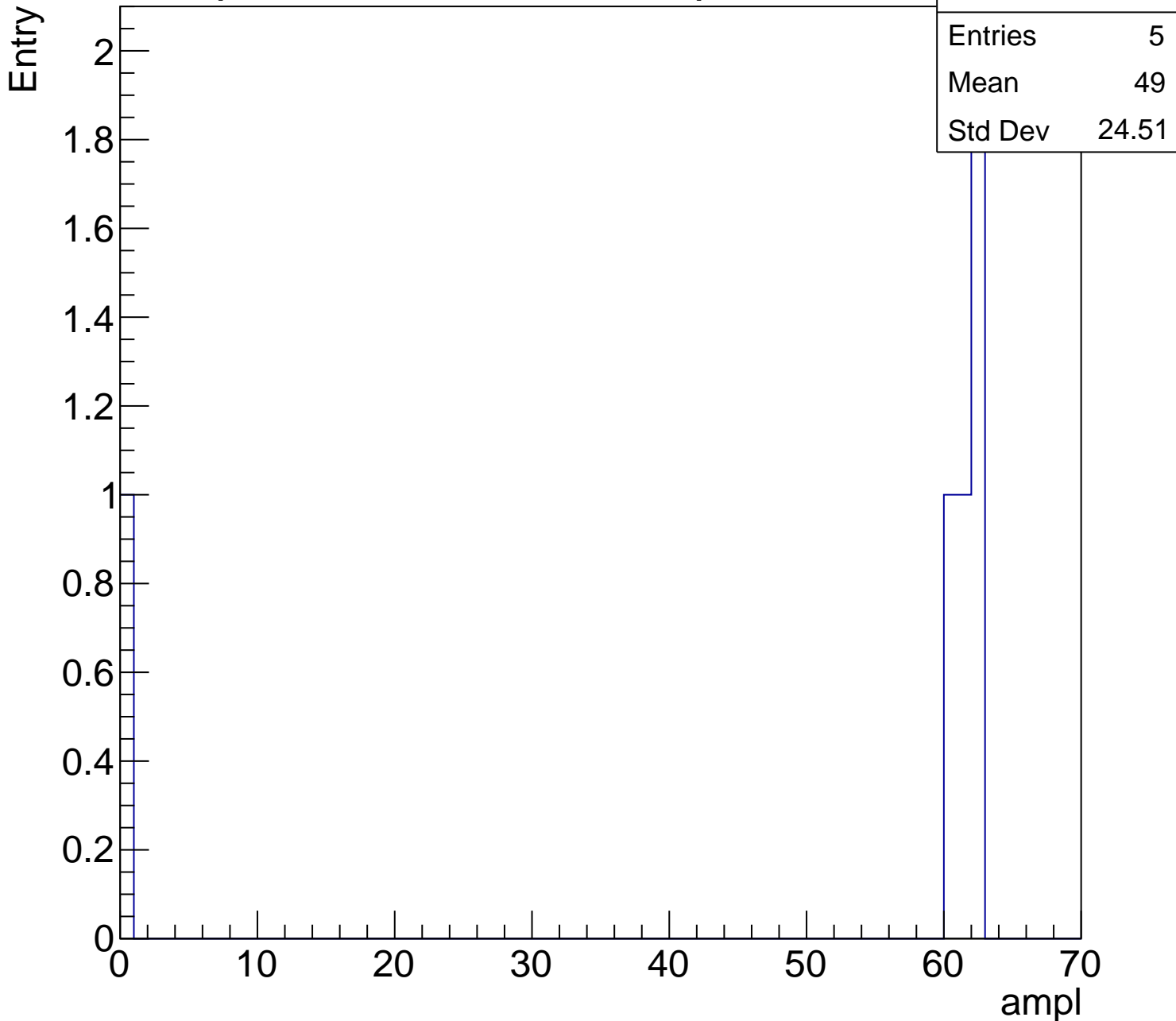
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	49
Std Dev	24.51

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch102, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	29.35
Std Dev	6.129

Gaus mean : 31.1523

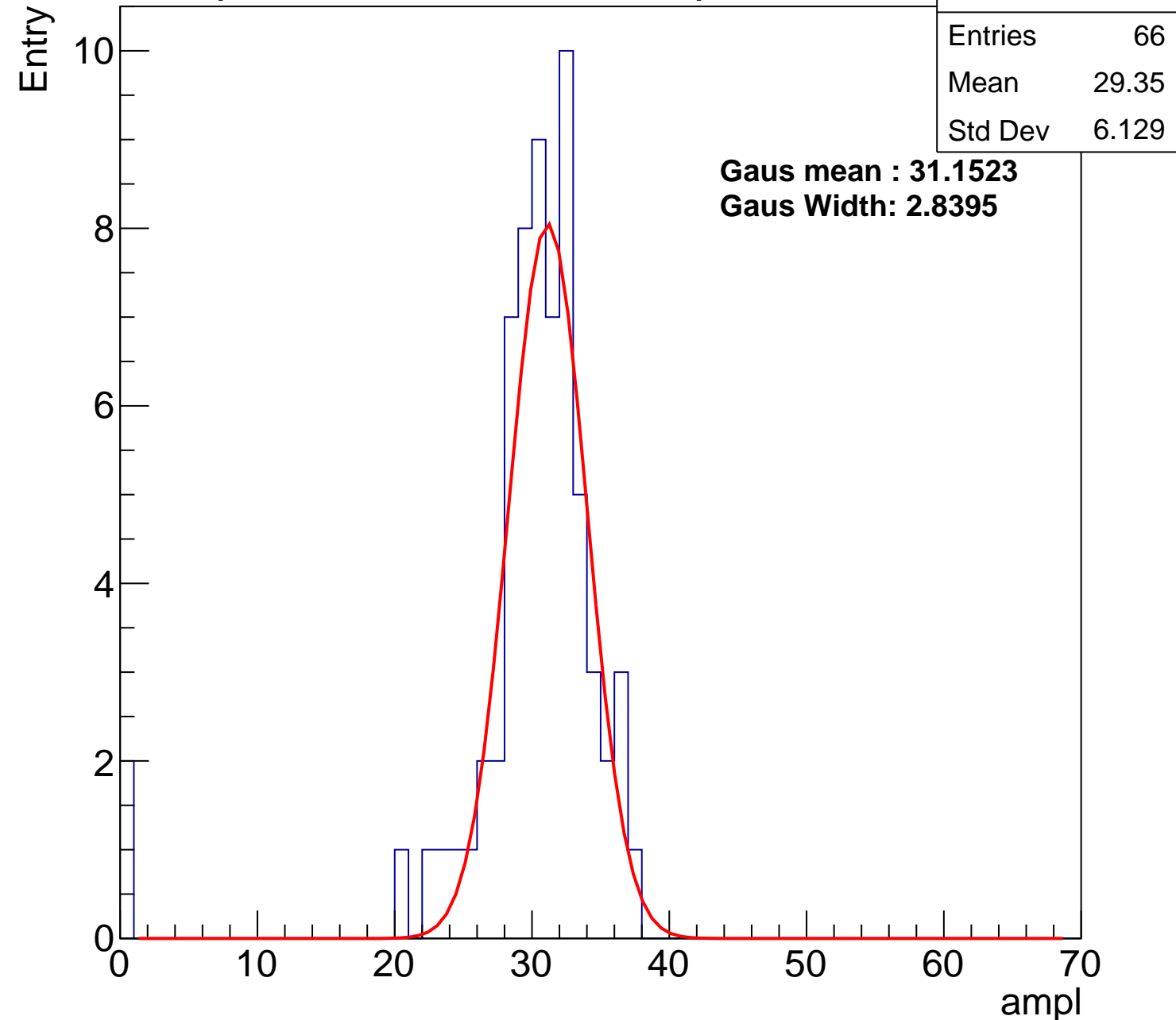
Gaus Width: 2.8395

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch102, adc1

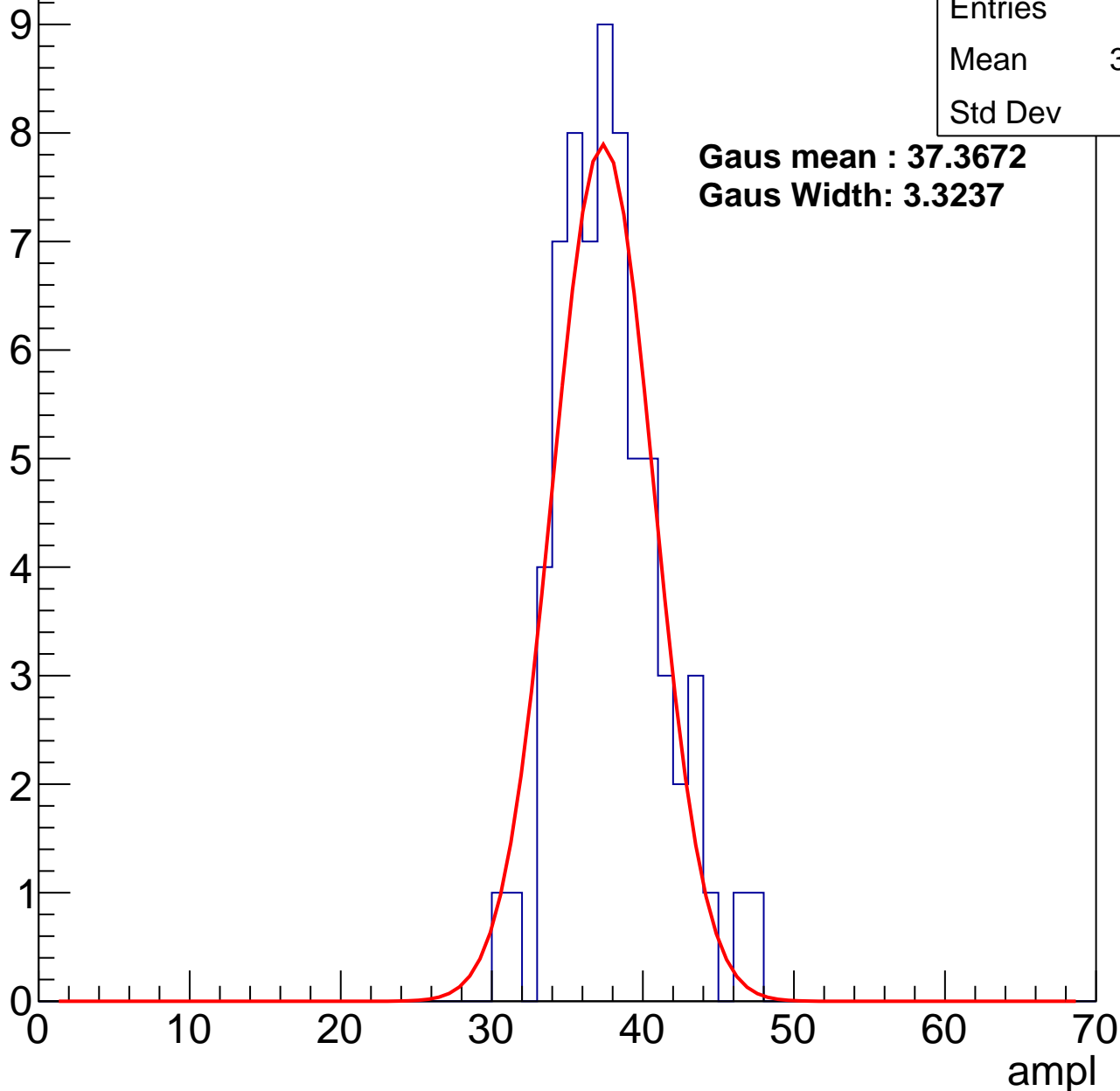
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.39
Std Dev	3.37

Gaus mean : 37.3672

Gaus Width: 3.3237



B0L001S, U22-ch102, adc2

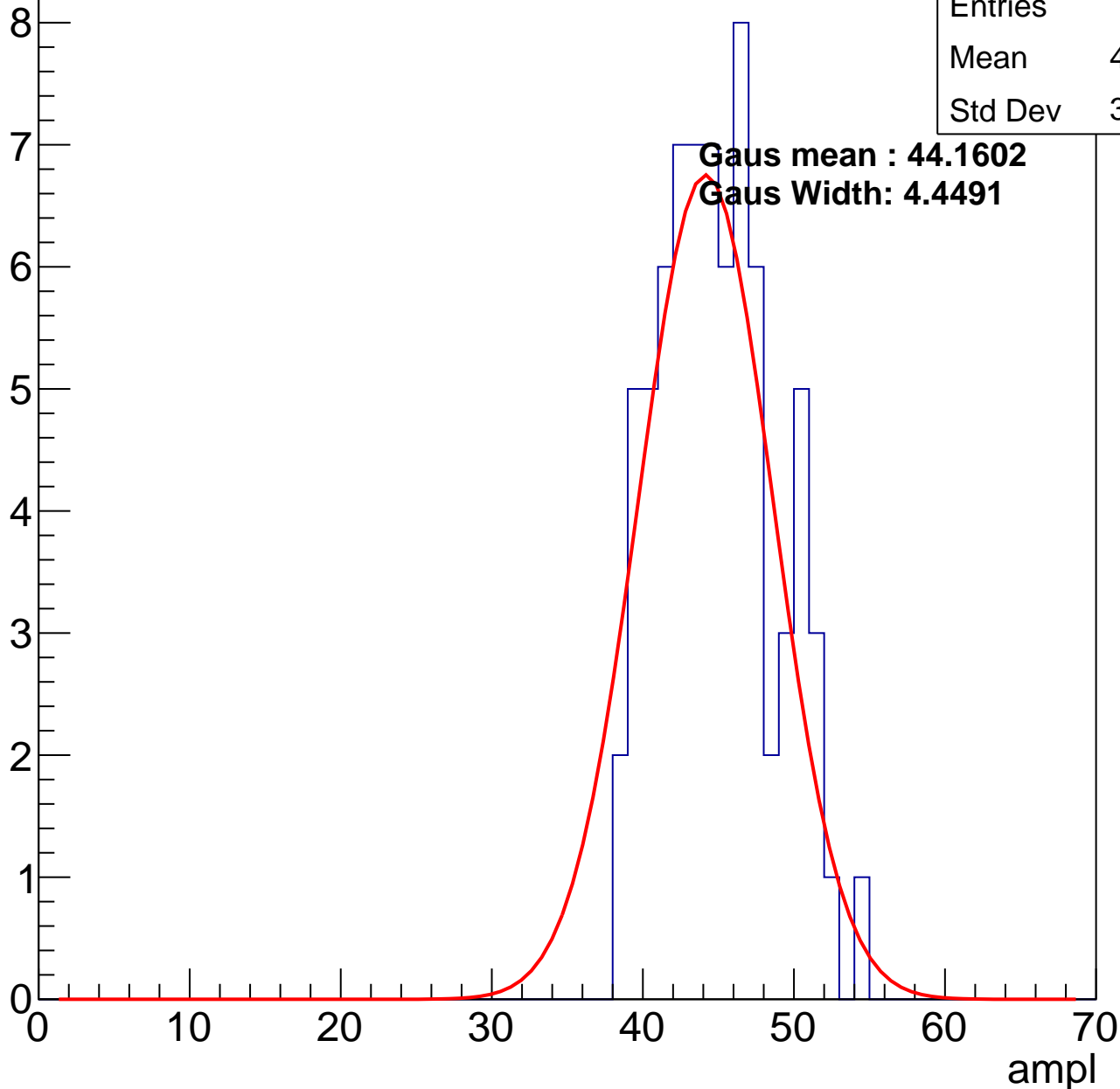
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	44.49
Std Dev	3.746

Gaus mean : 44.1602

Gaus Width: 4.4491

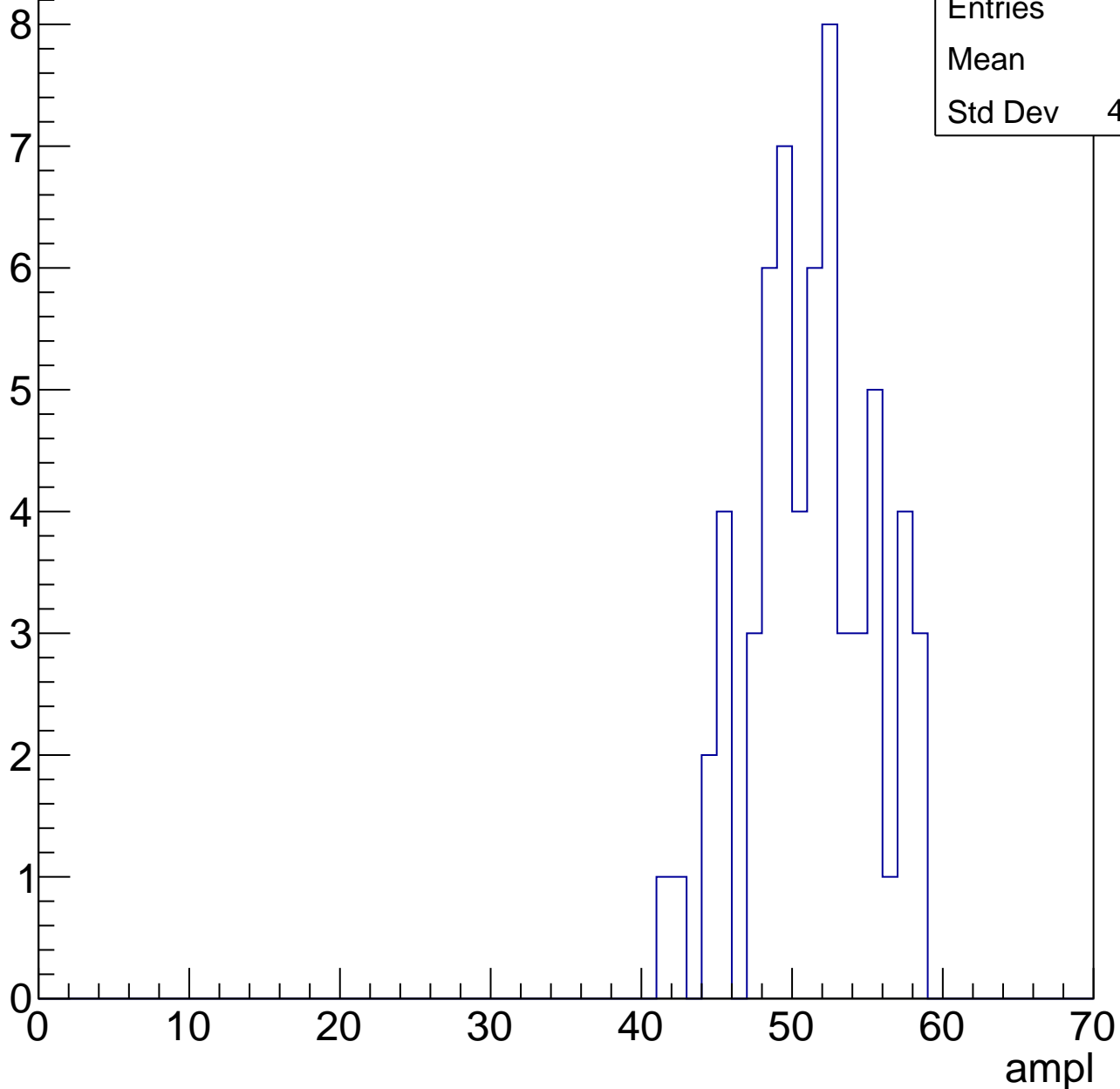


B0L001S, U22-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	50.8
Std Dev	4.044

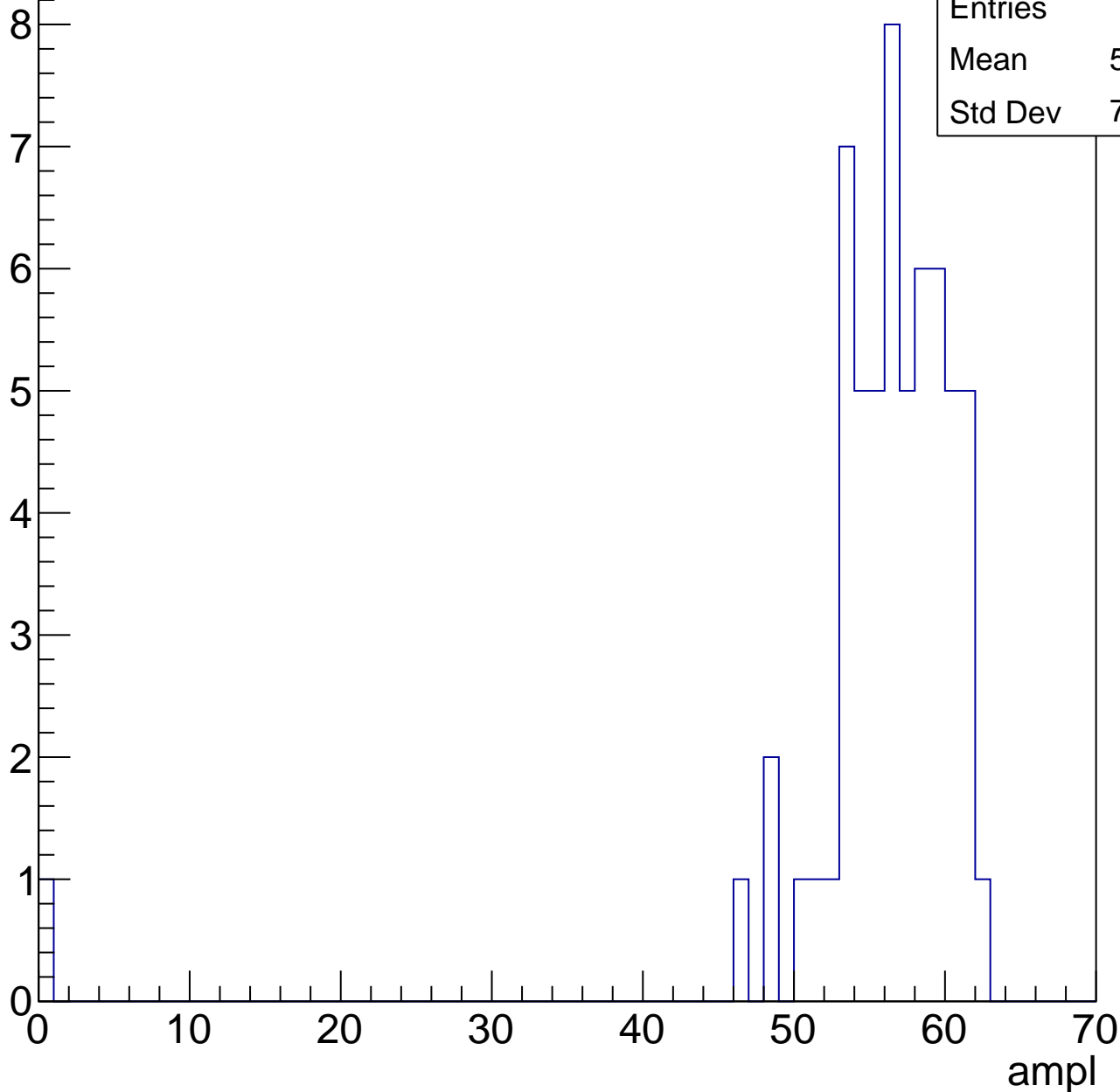


B0L001S, U22-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	55.22
Std Dev	7.975

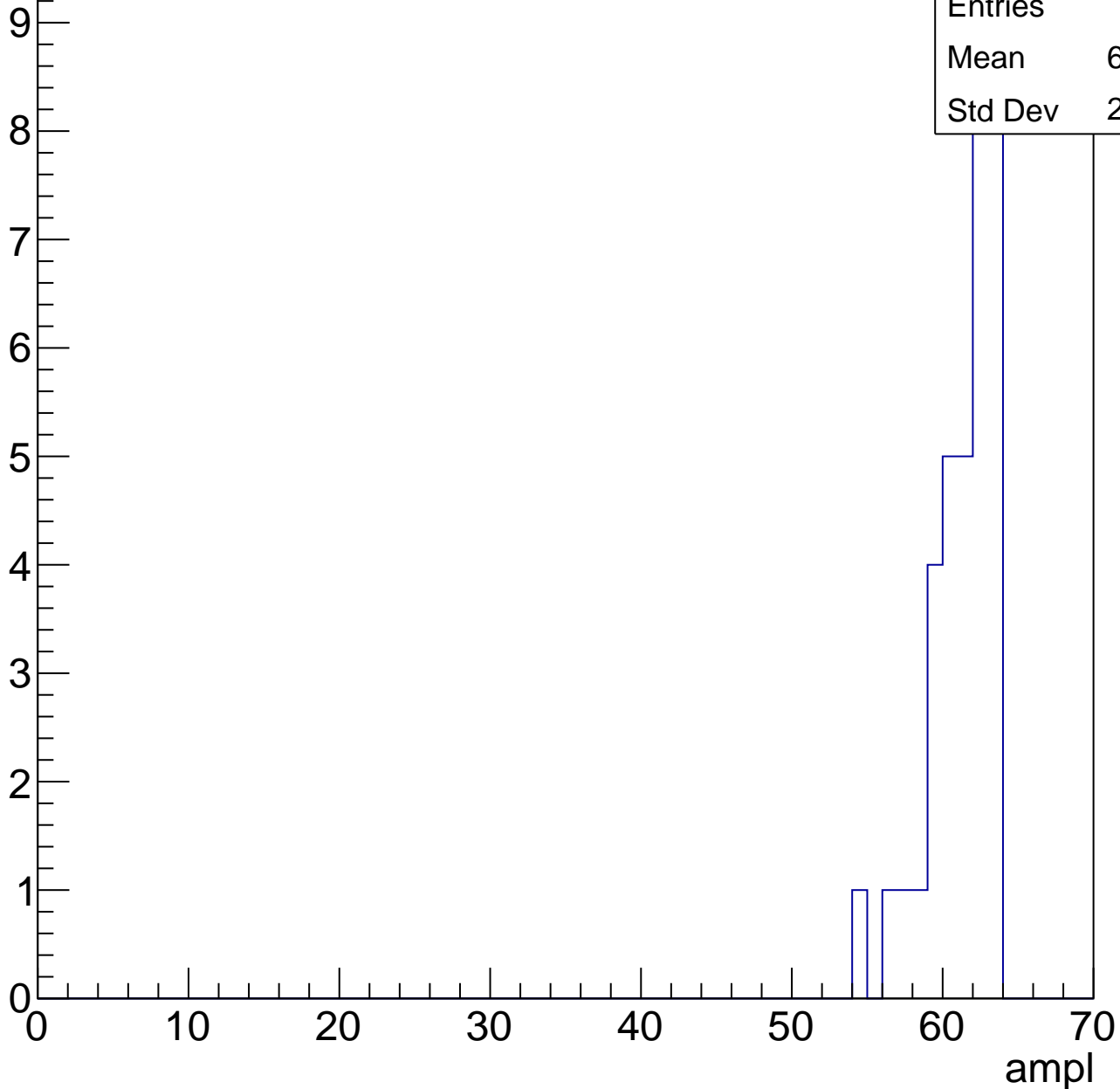


B0L001S, U22-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.83
Std Dev	2.158



B0L001S, U22-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch103, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	30.36
Std Dev	6.858

Gaus mean : 32.1759

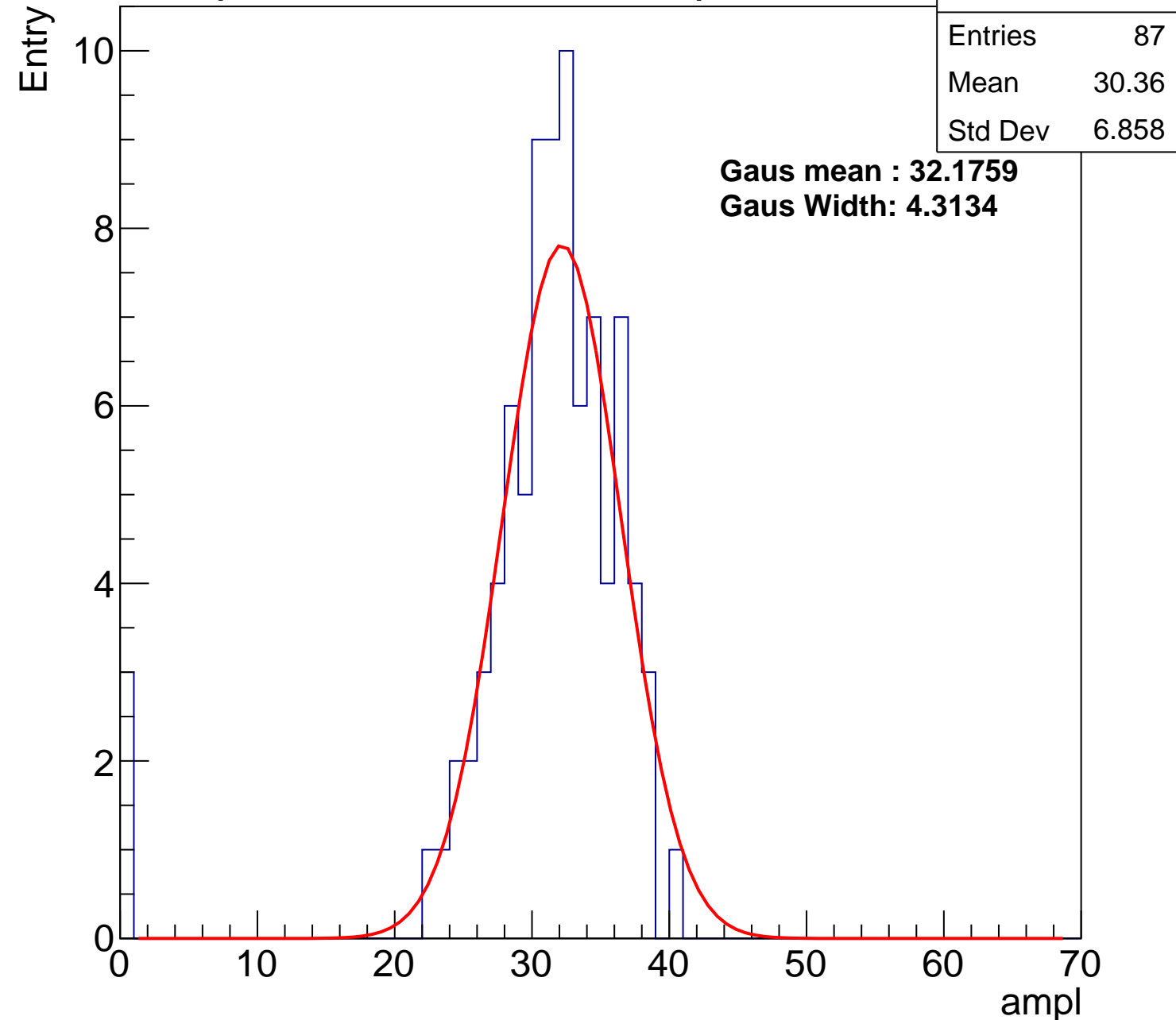
Gaus Width: 4.3134

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch103, adc1

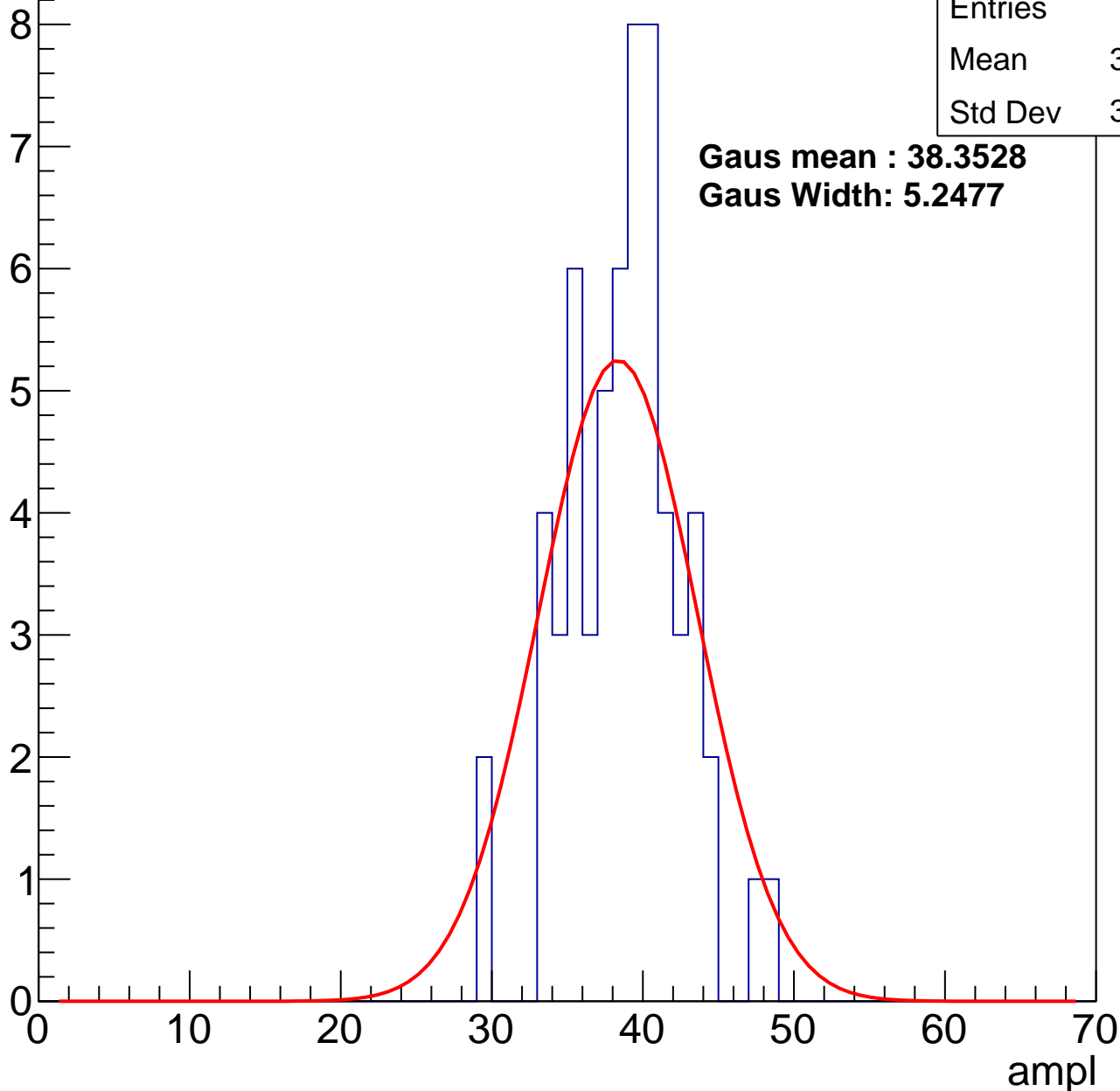
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.33
Std Dev	3.767

Gaus mean : 38.3528

Gaus Width: 5.2477



B0L001S, U22-ch103, adc2

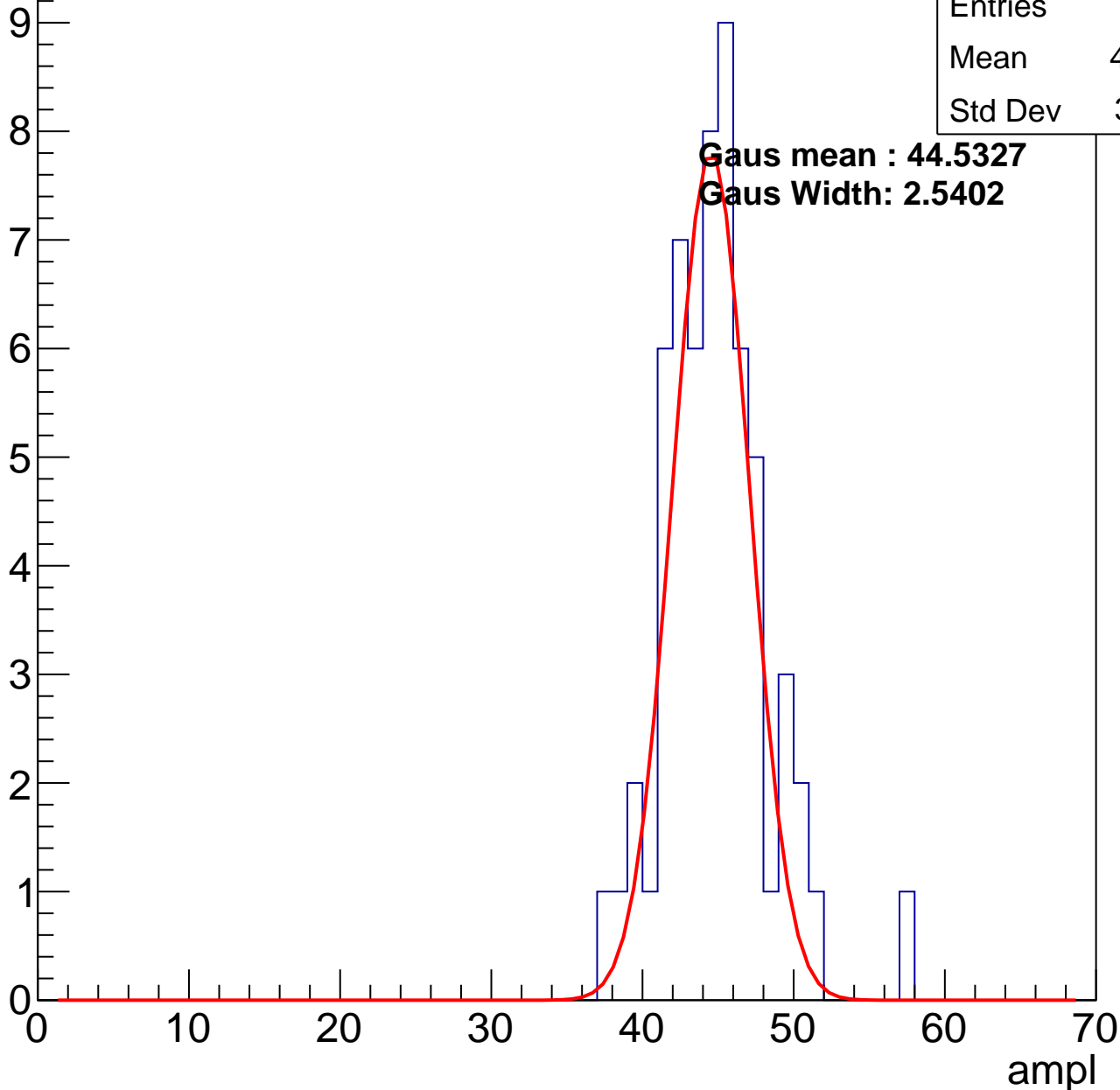
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.37
Std Dev	3.391

Gaus mean : 44.5327

Gaus Width: 2.5402

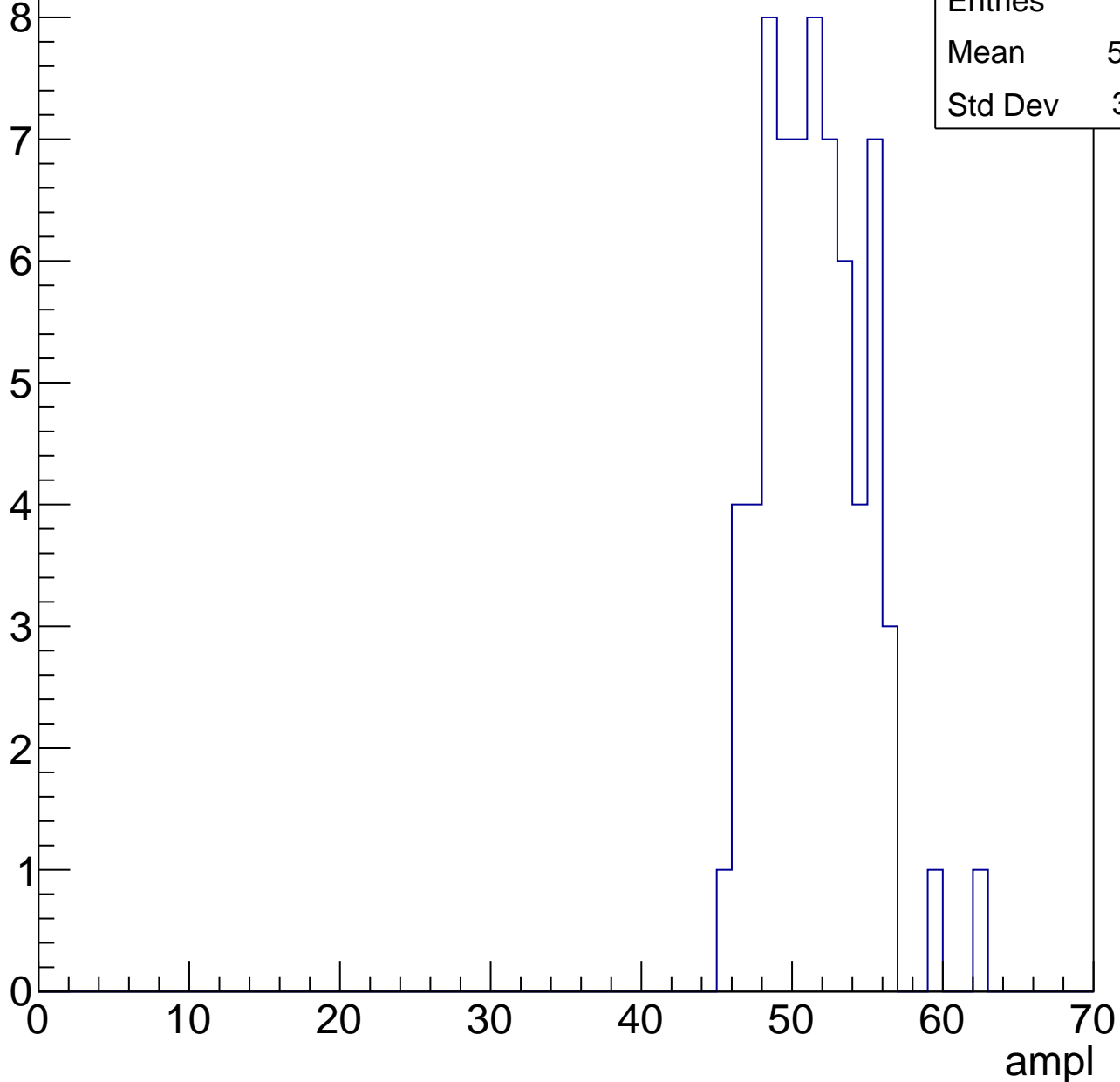


B0L001S, U22-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.09
Std Dev	3.311

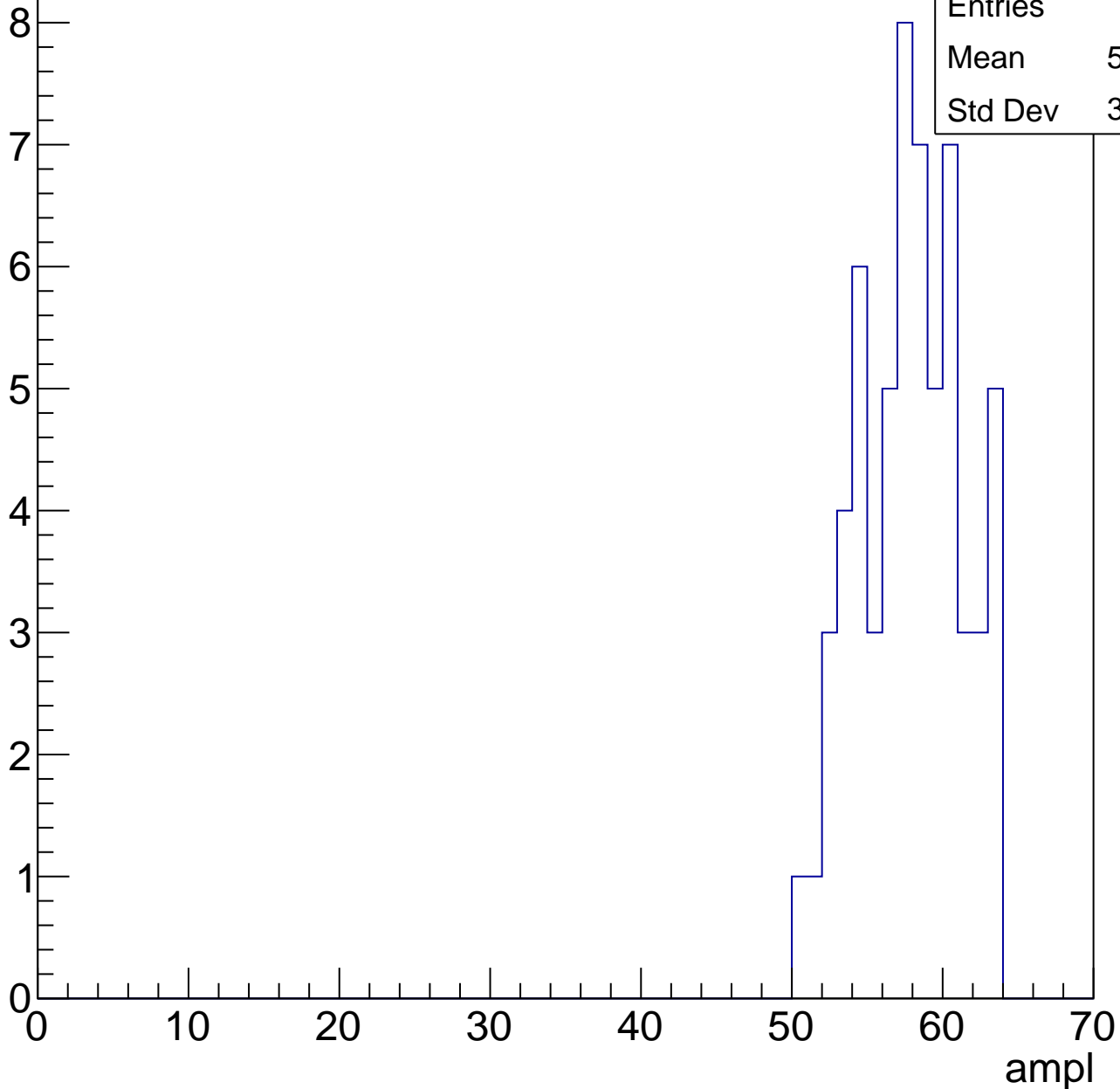


B0L001S, U22-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	57.36
Std Dev	3.339

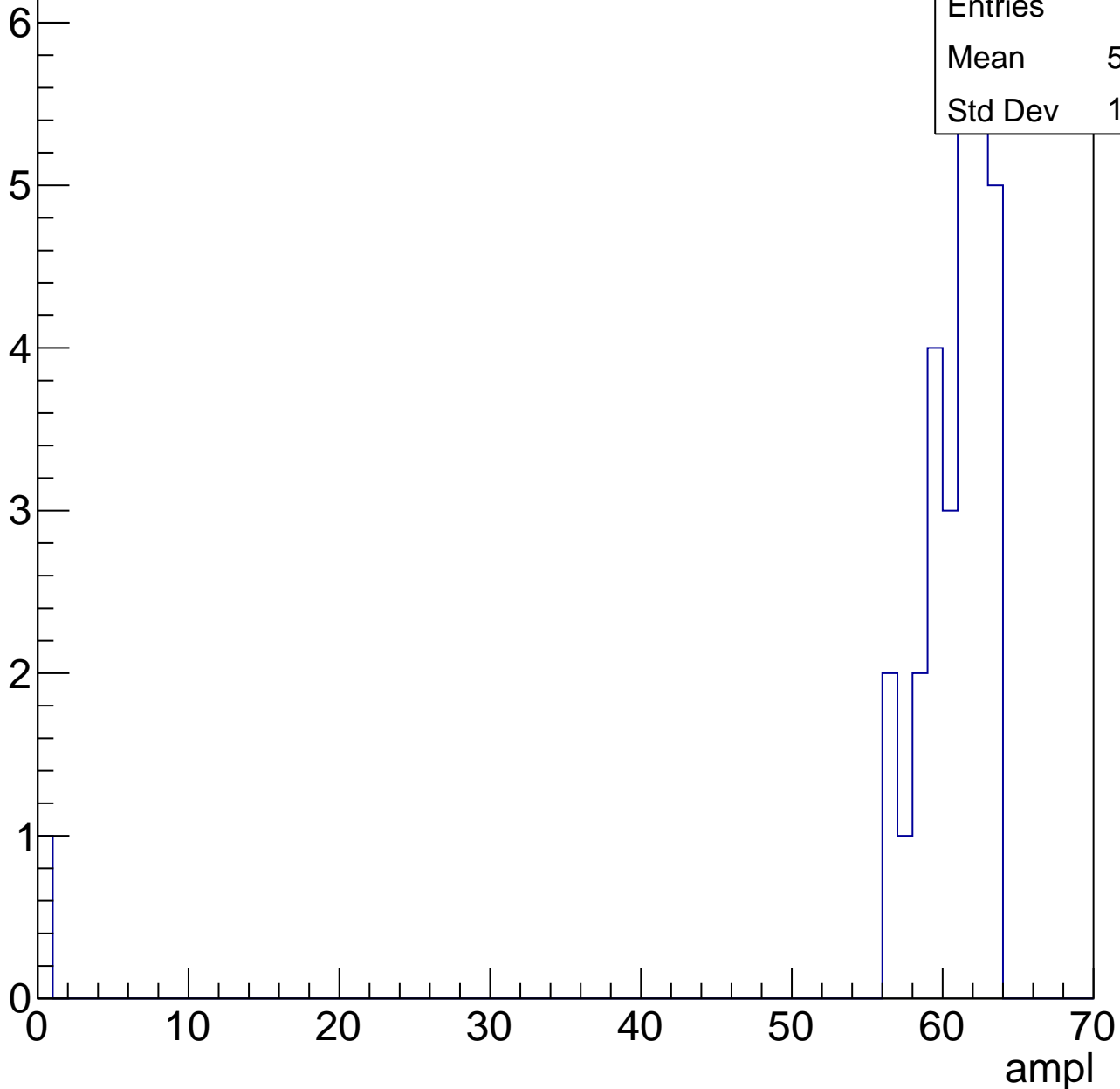


B0L001S, U22-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

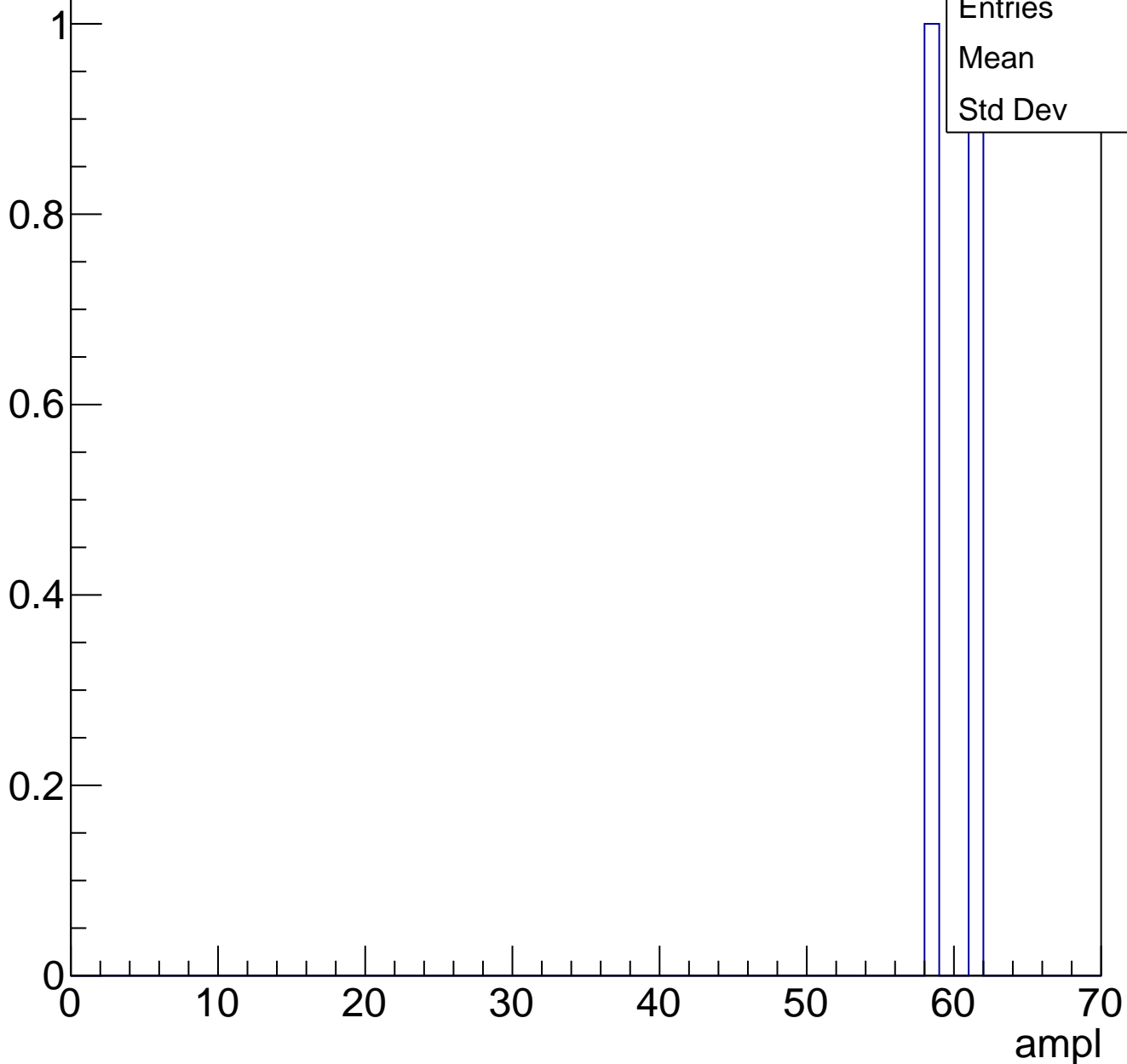
Entries	30
Mean	58.47
Std Dev	11.04



B0L001S, U22-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch104, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	94
Mean	30.94
Std Dev	6.212

Gaus mean : 32.1187

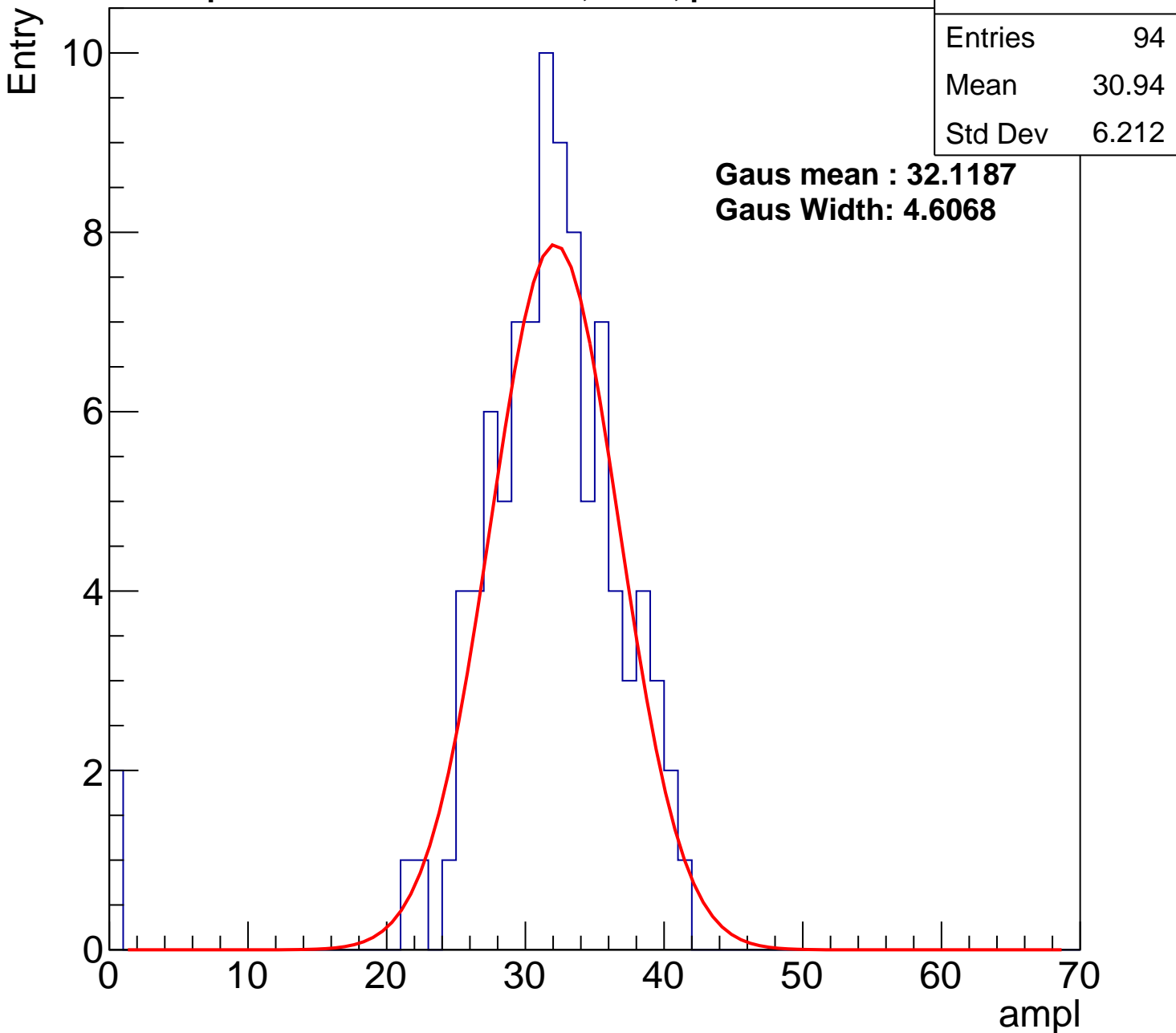
Gaus Width: 4.6068

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch104, adc1

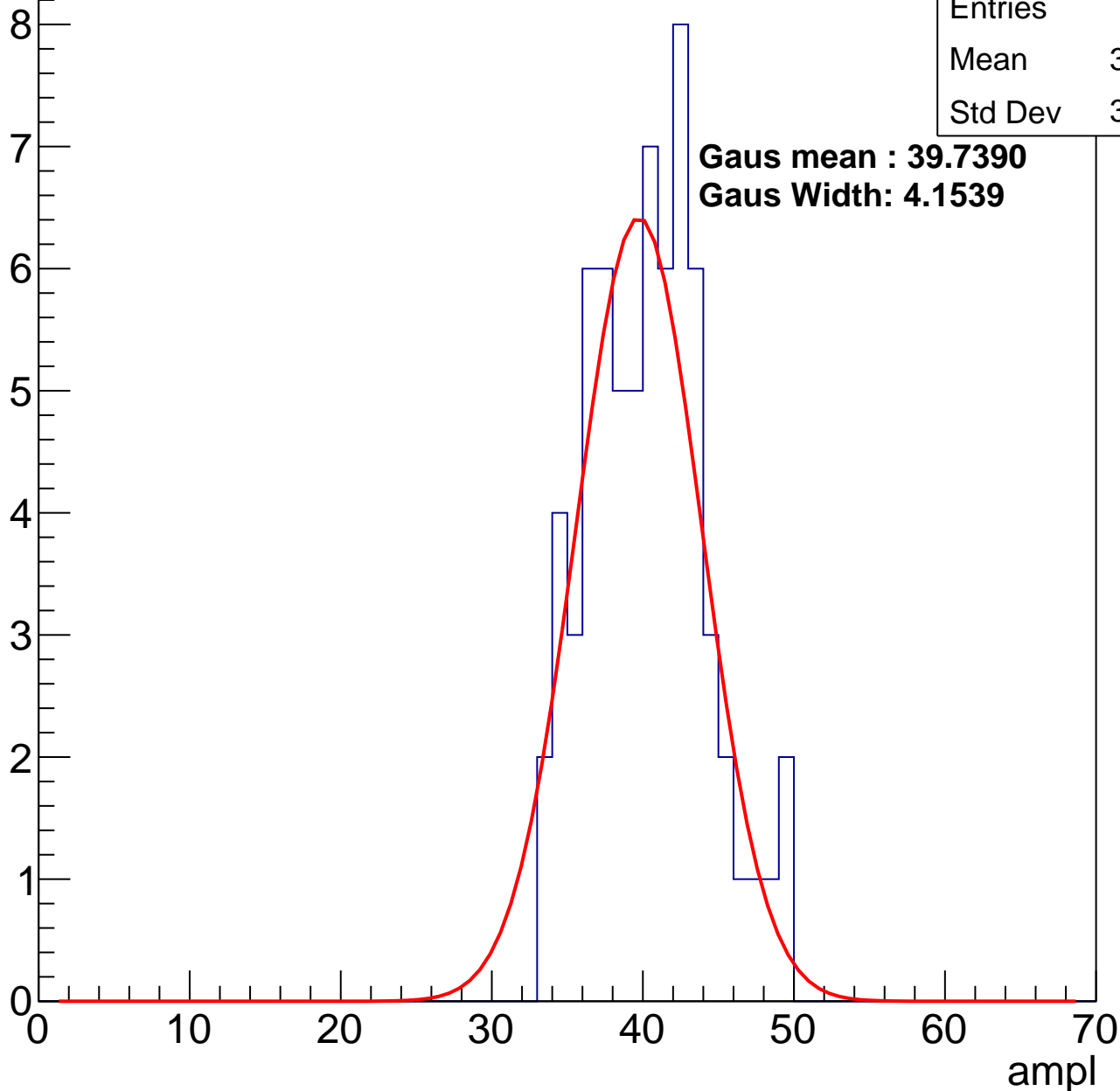
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	39.87
Std Dev	3.819

Gaus mean : 39.7390

Gaus Width: 4.1539



B0L001S, U22-ch104, adc2

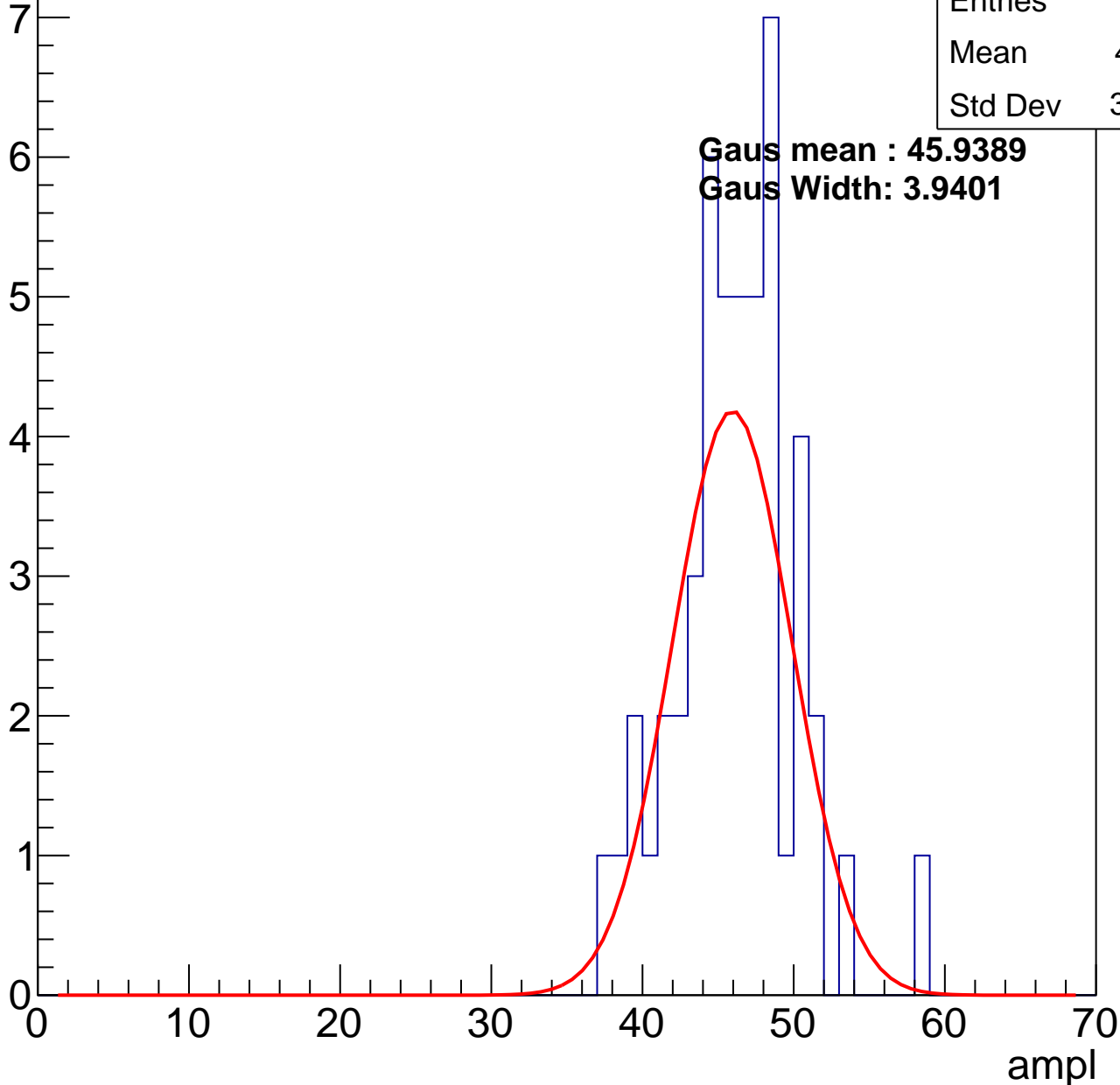
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	45.71
Std Dev	3.943

Gaus mean : 45.9389

Gaus Width: 3.9401

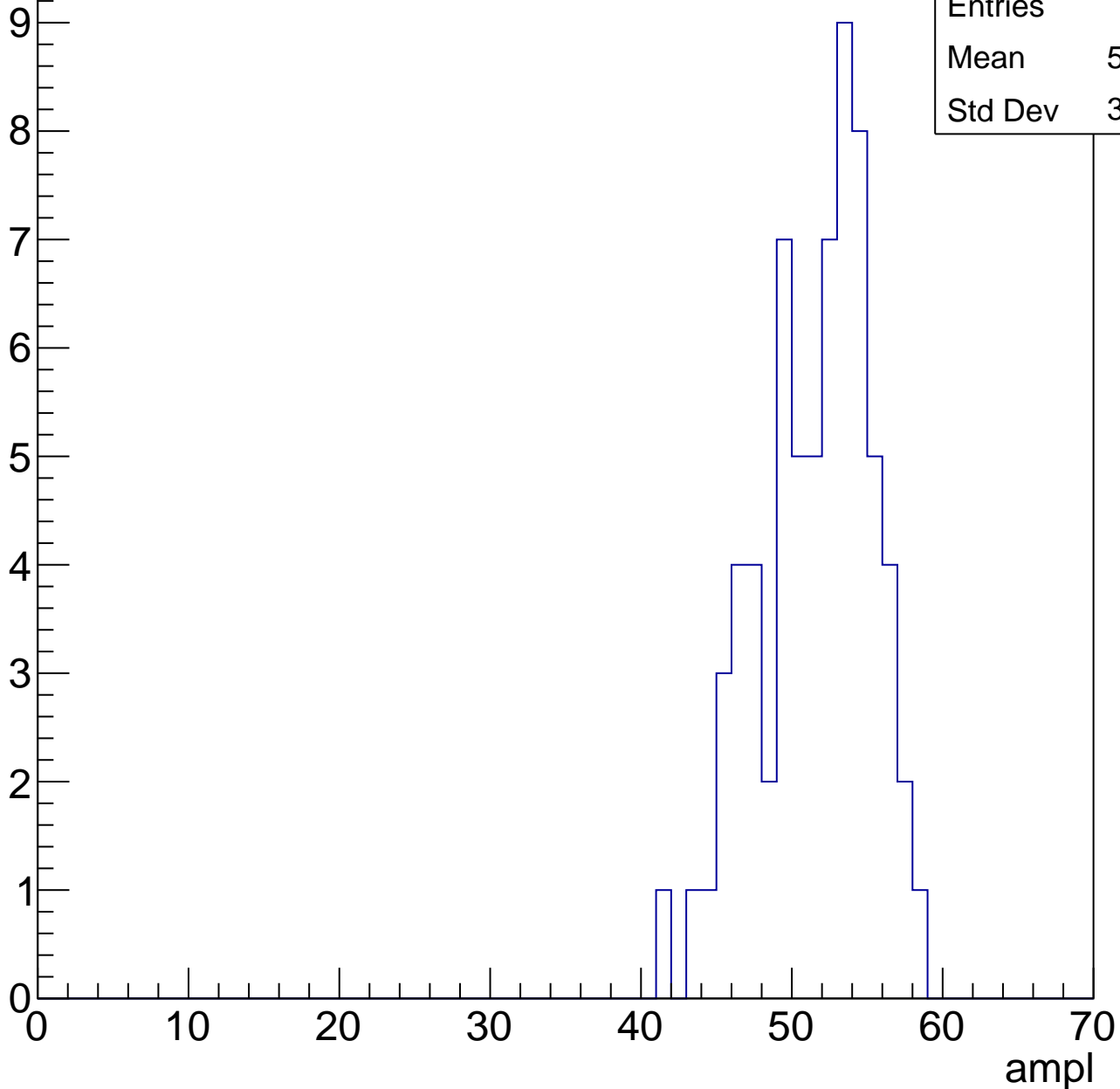


B0L001S, U22-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.06
Std Dev	3.714

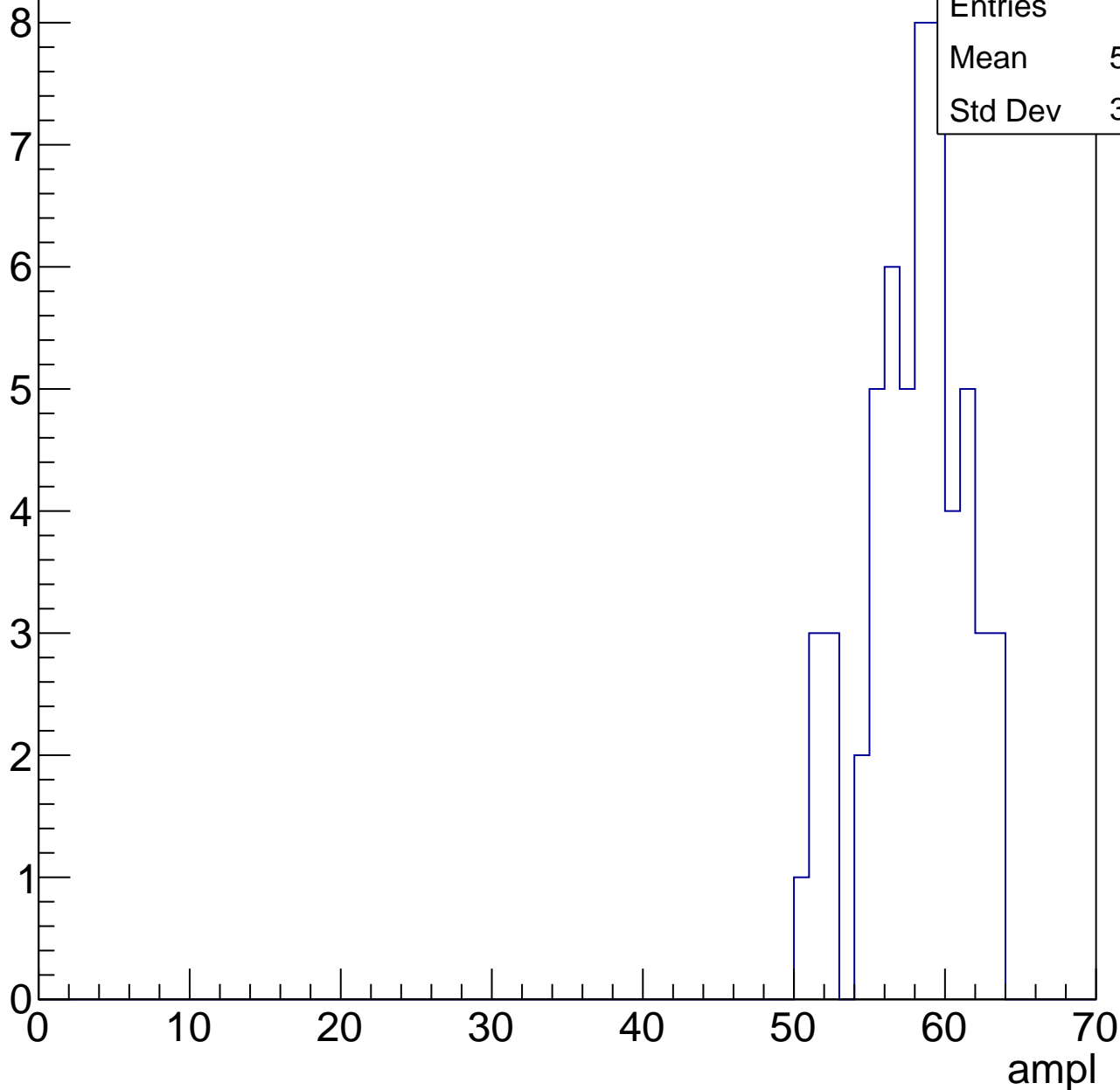


B0L001S, U22-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.48
Std Dev	3.279

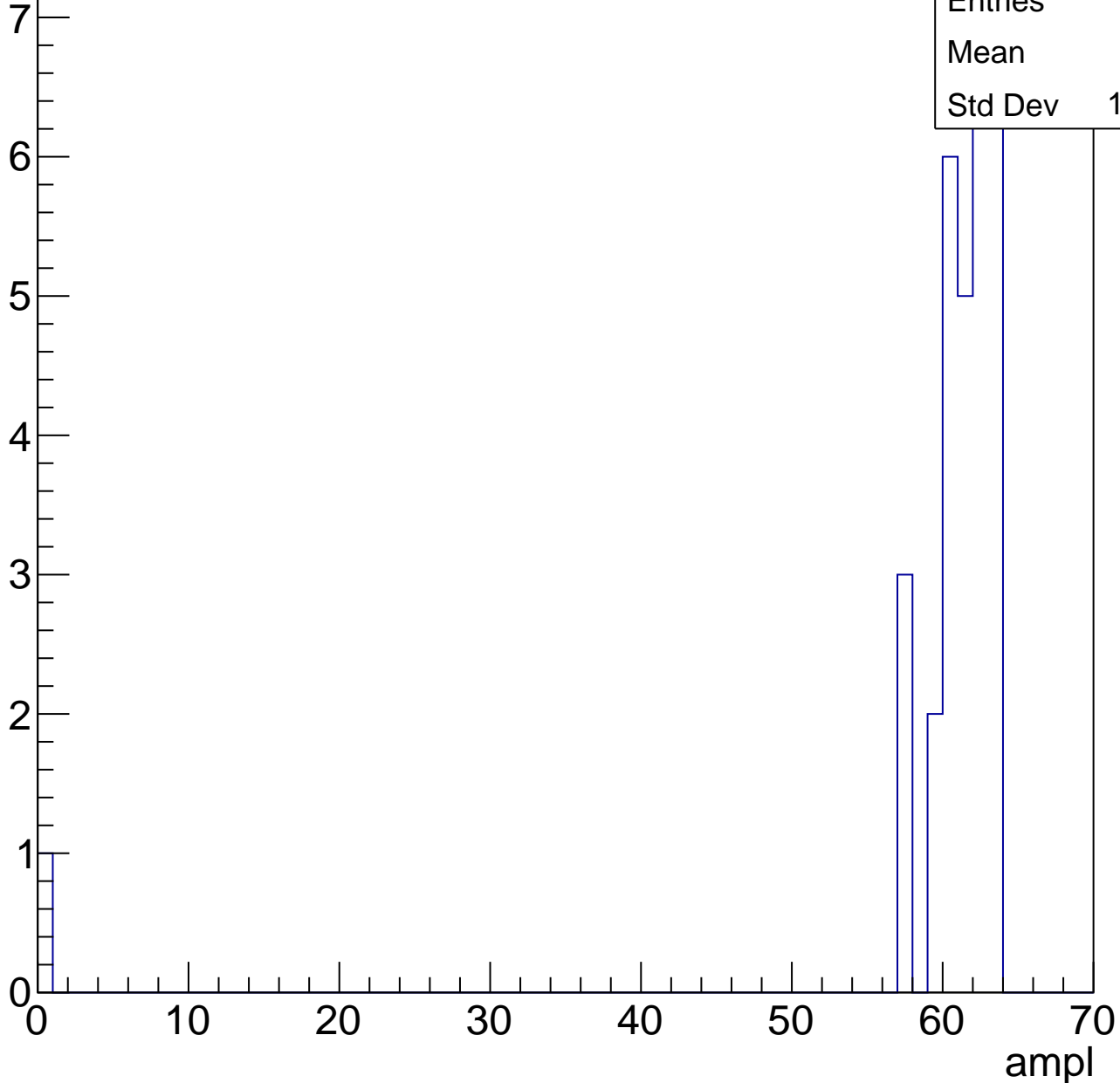


B0L001S, U22-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	31
Mean	59
Std Dev	10.92



B0L001S, U22-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch105, adc0

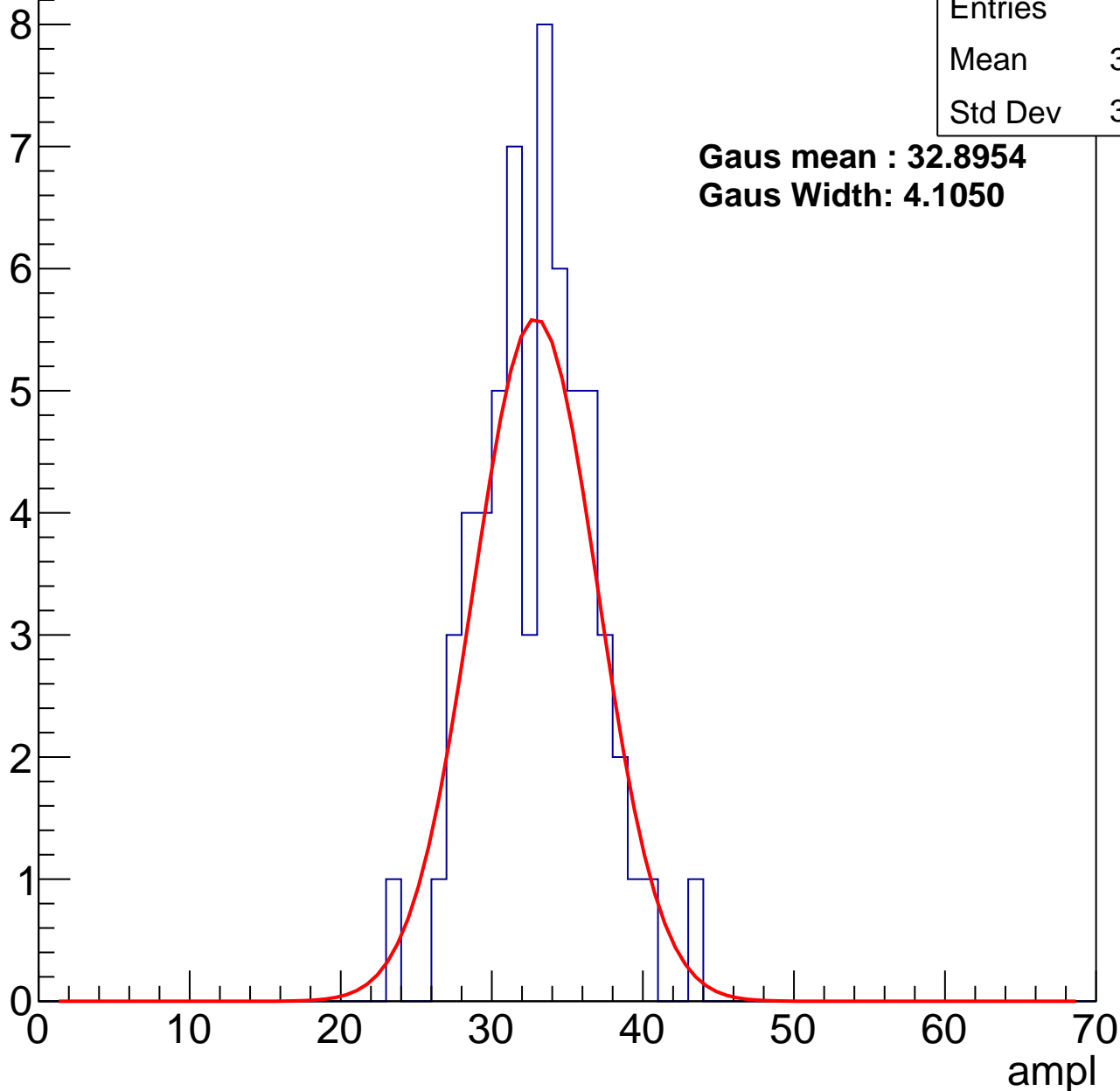
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	32.55
Std Dev	3.739

Gaus mean : 32.8954

Gaus Width: 4.1050



B0L001S, U22-ch105, adc1

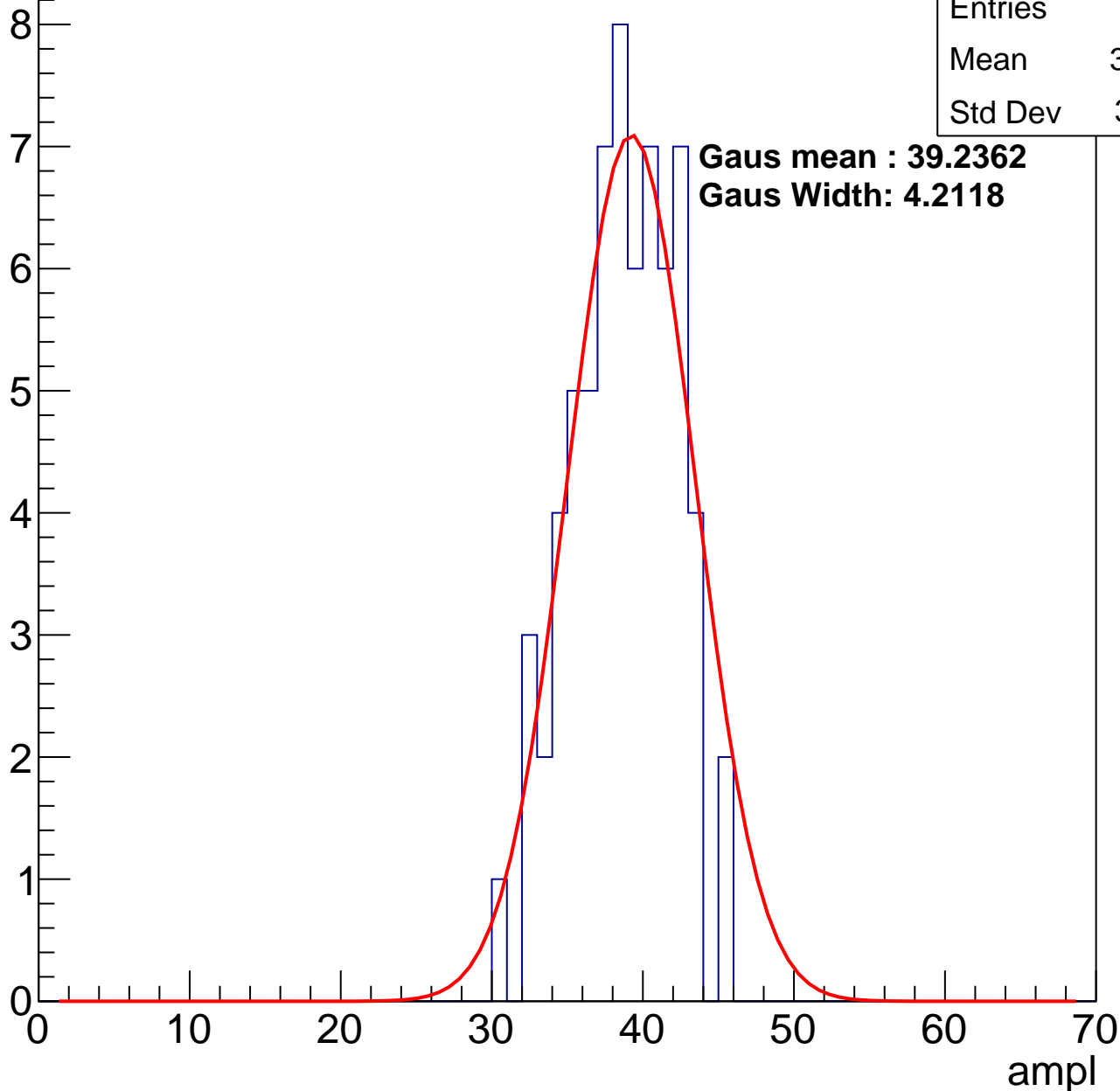
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	38.24
Std Dev	3.351

Gaus mean : 39.2362

Gaus Width: 4.2118



B0L001S, U22-ch105, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	45.47
Std Dev	3.331

Gaus mean : 45.8542

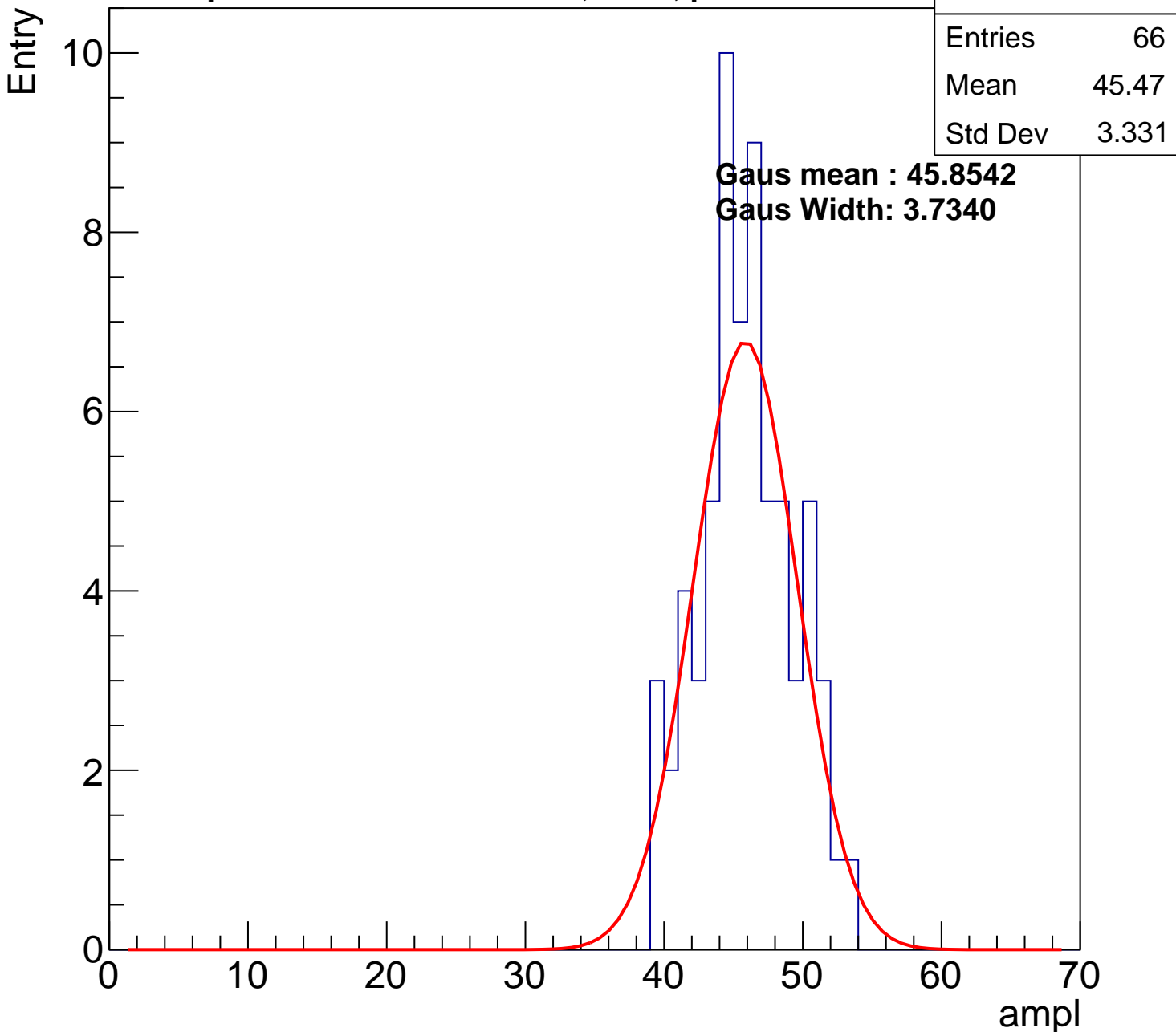
Gaus Width: 3.7340

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

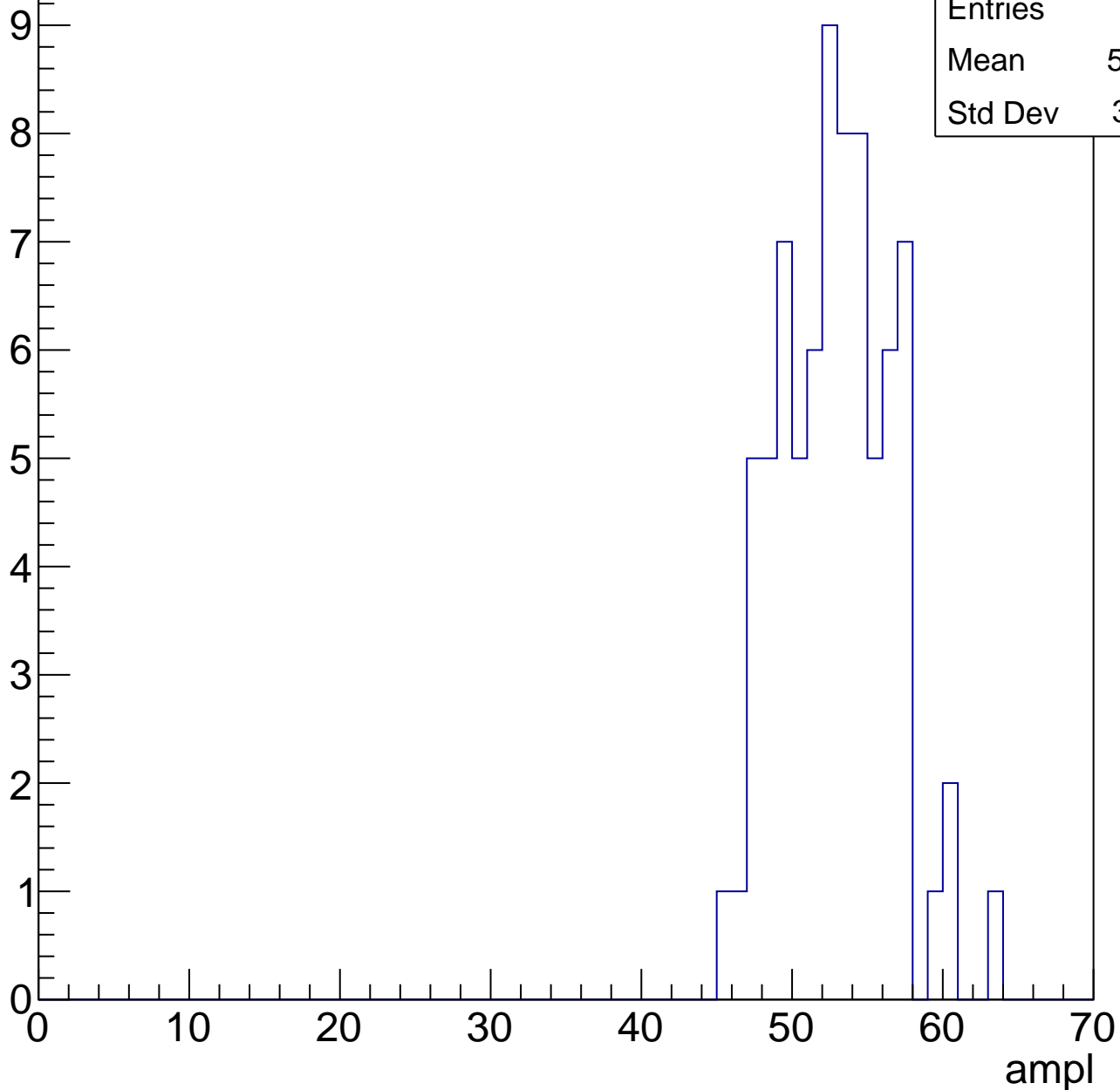


B0L001S, U22-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	52.48
Std Dev	3.631

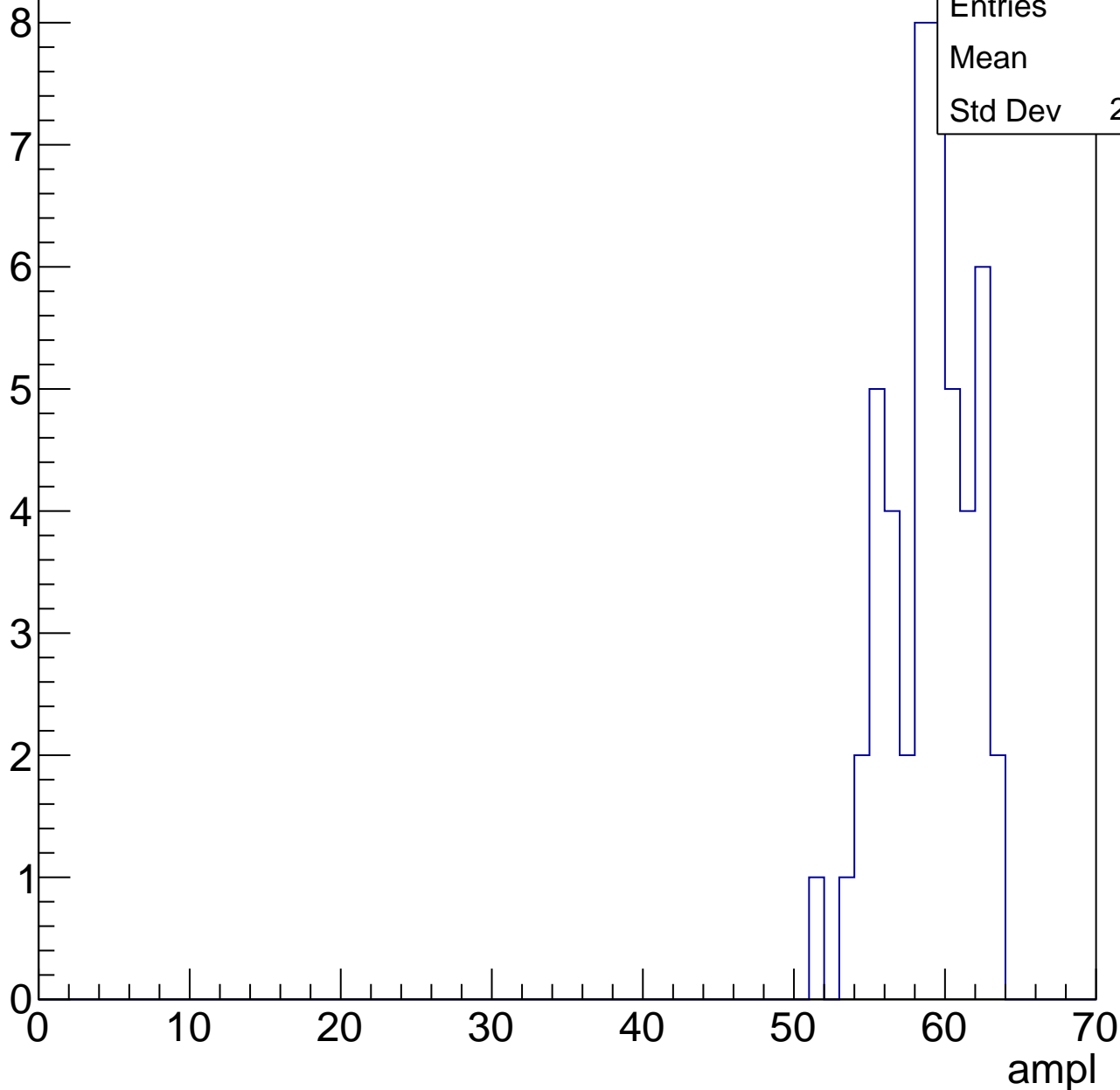


B0L001S, U22-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.4
Std Dev	2.782

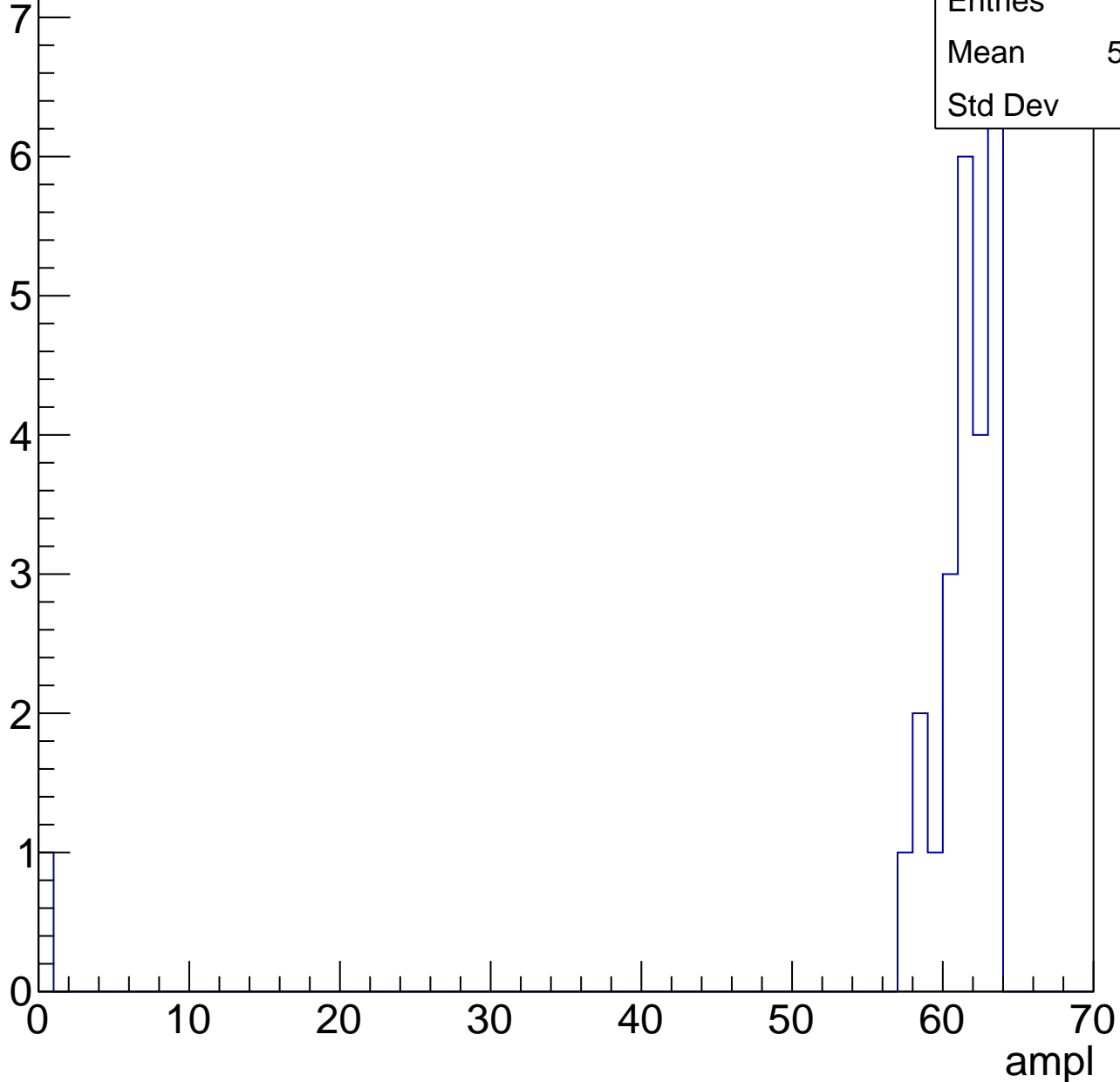


B0L001S, U22-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	25
Mean	58.68
Std Dev	12.1



B0L001S, U22-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch106, adc0

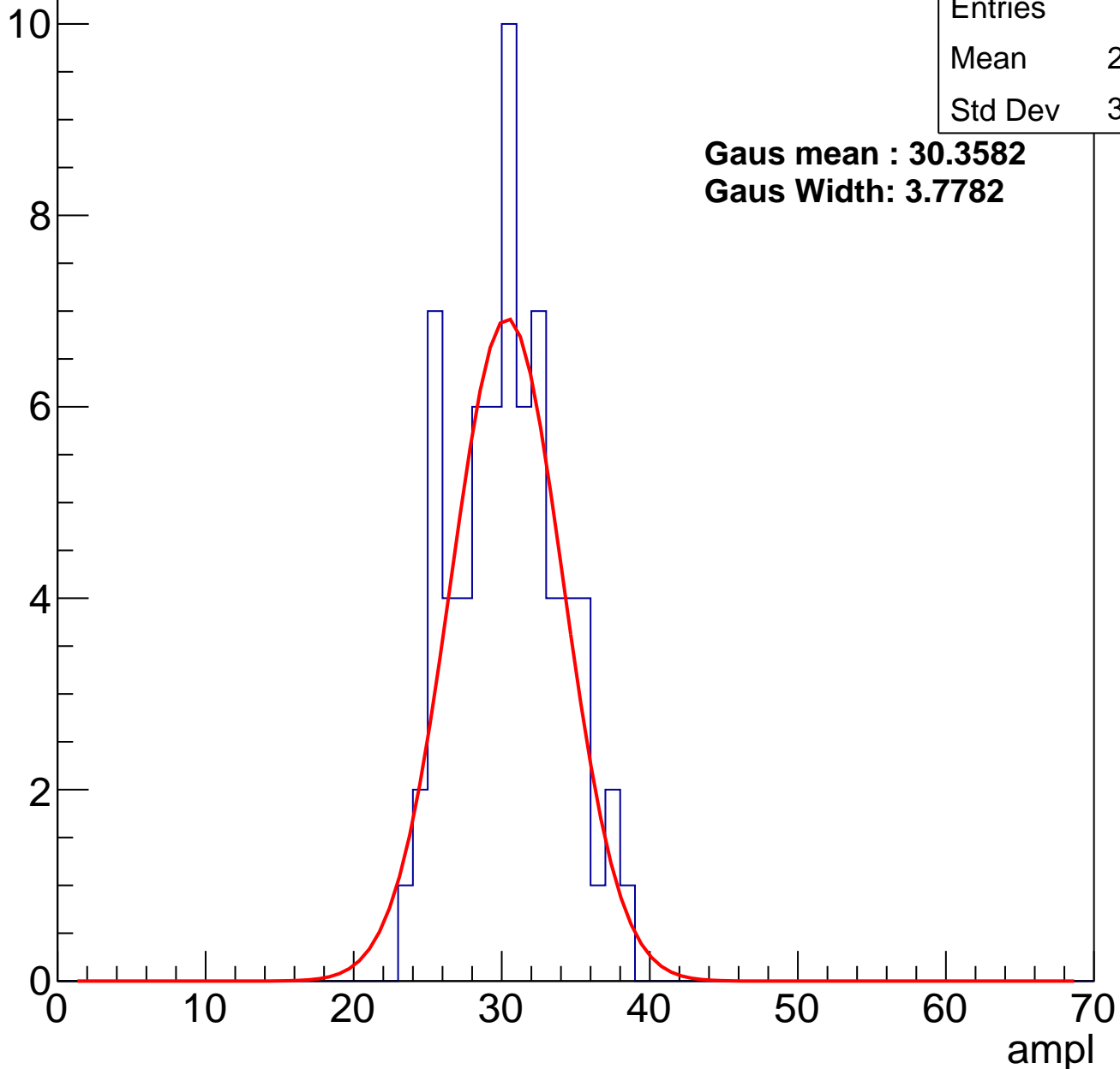
calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	29.94
Std Dev	3.526

Gaus mean : 30.3582

Gaus Width: 3.7782

Entry



B0L001S, U22-ch106, adc1

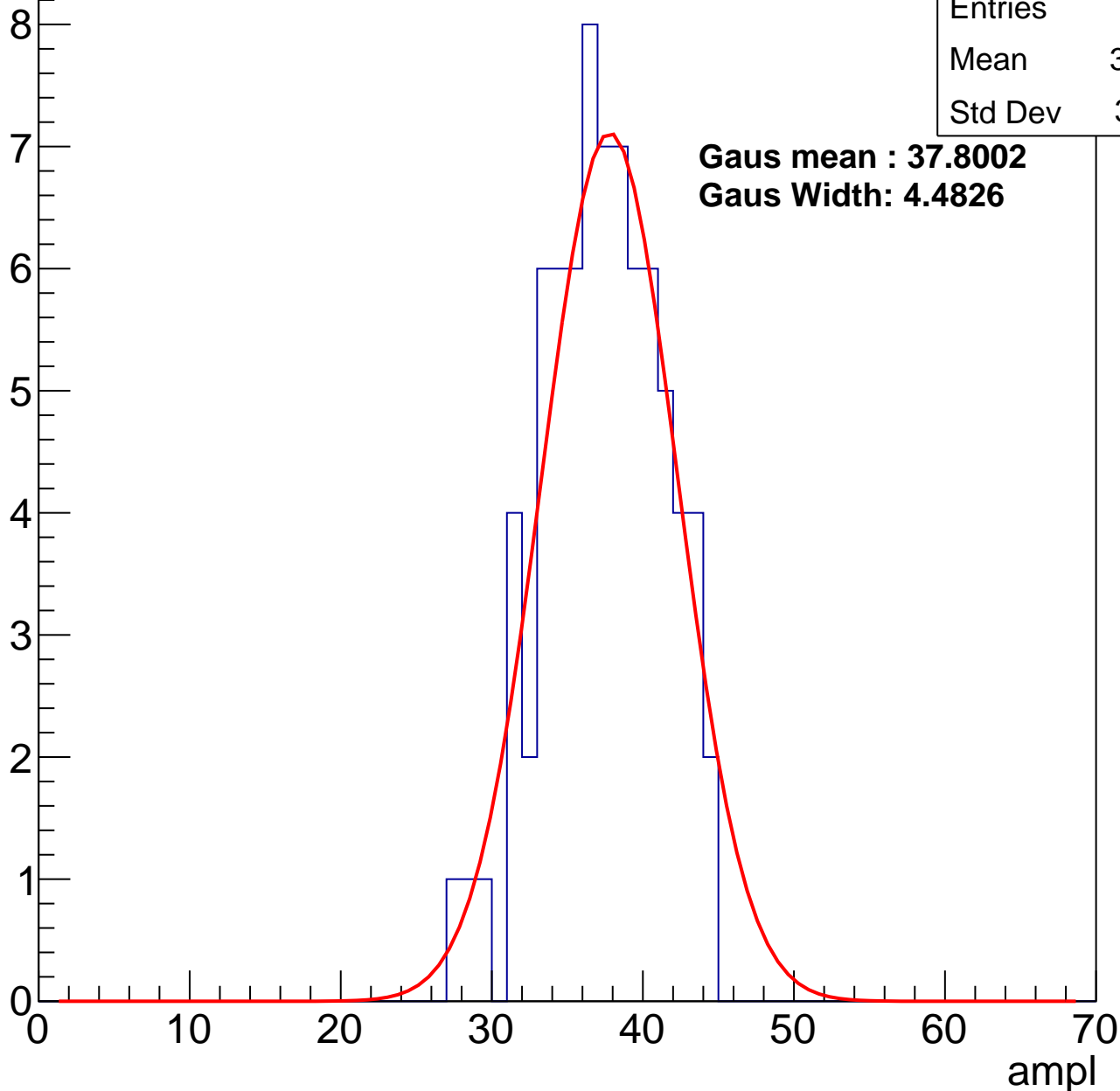
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	36.89
Std Dev	3.851

Gaus mean : 37.8002

Gaus Width: 4.4826



B0L001S, U22-ch106, adc2

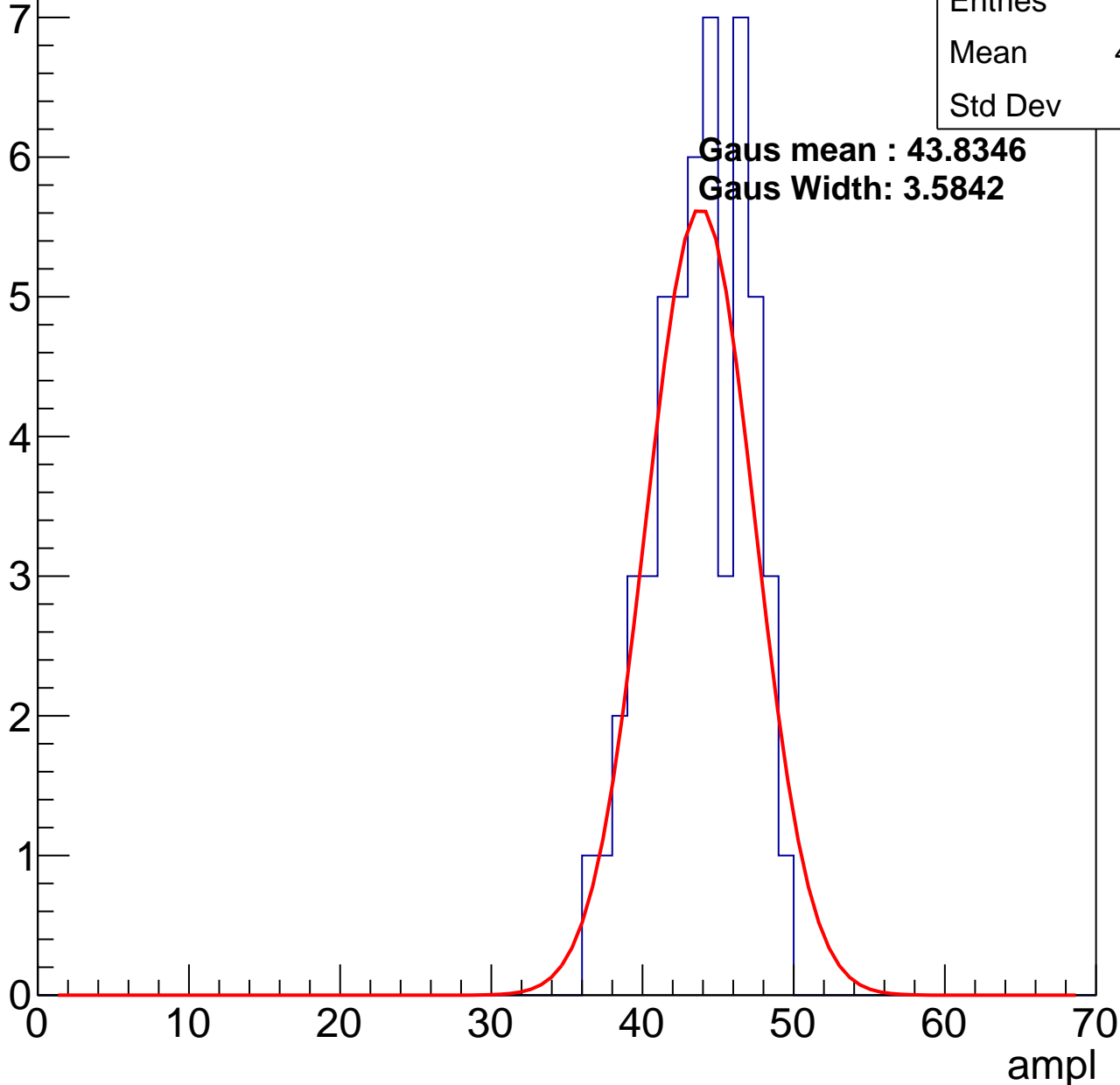
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.31
Std Dev	3.11

Gaus mean : 43.8346

Gaus Width: 3.5842

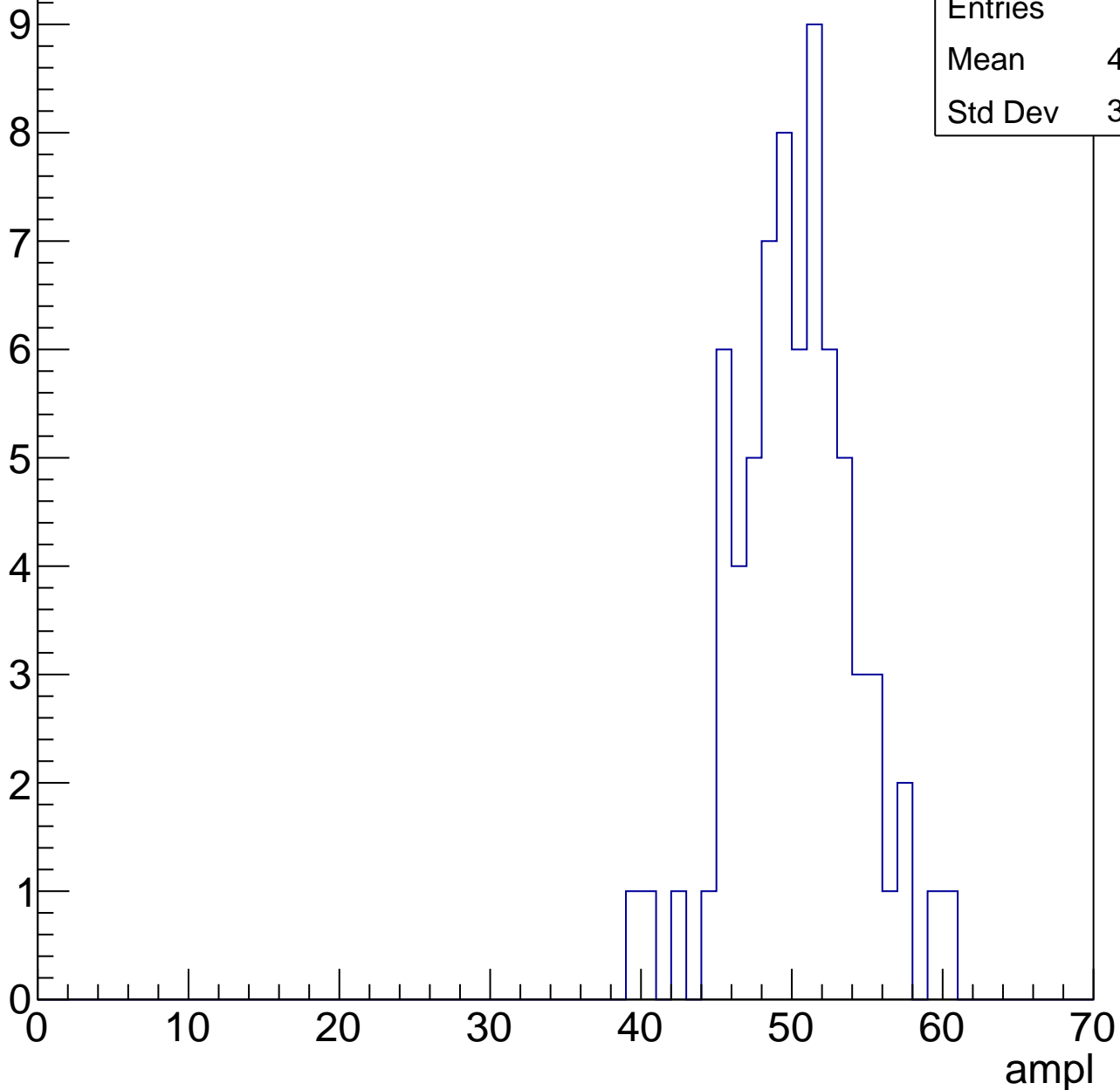


B0L001S, U22-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.77
Std Dev	3.987

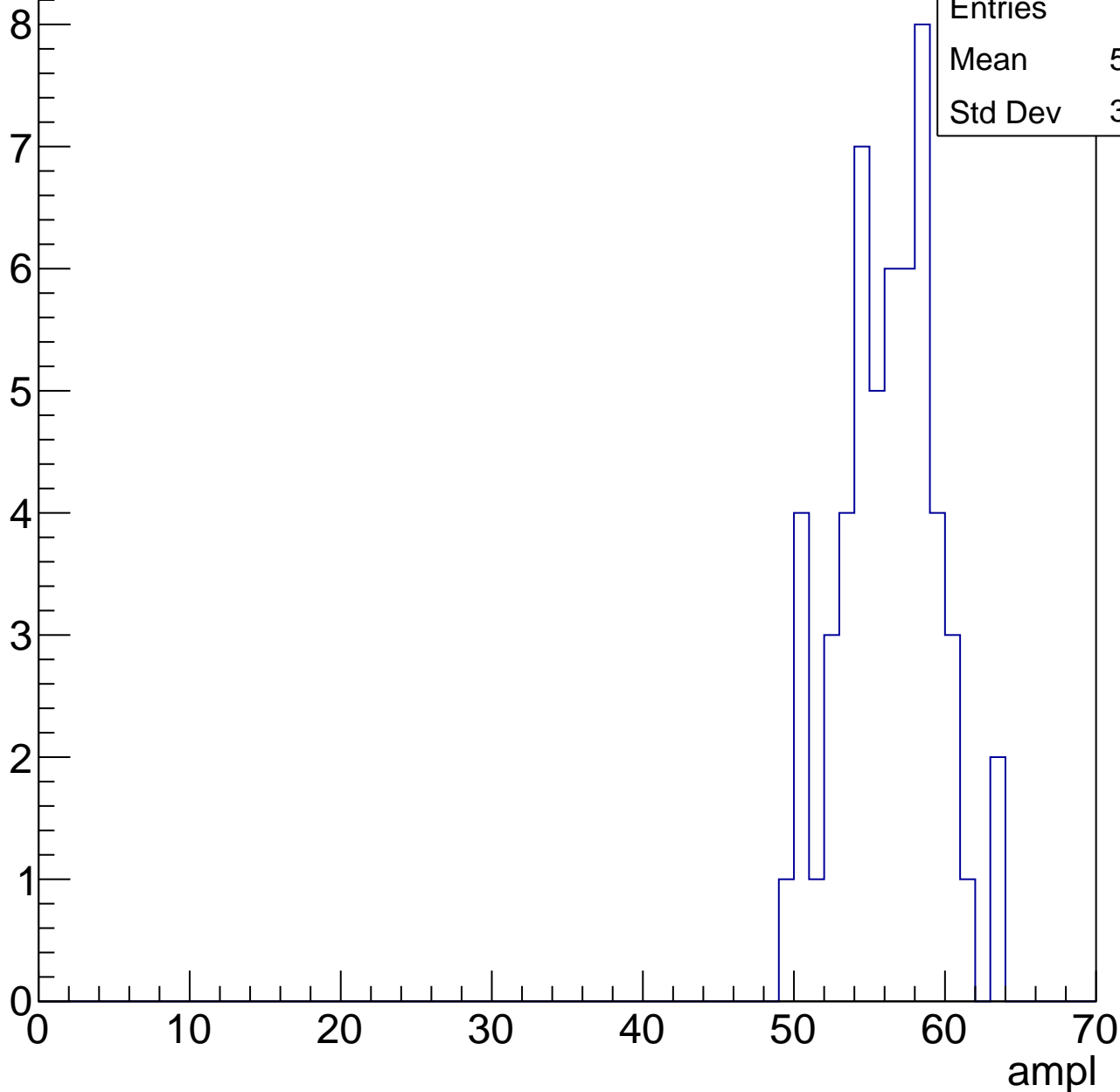


B0L001S, U22-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	55.75
Std Dev	3.237

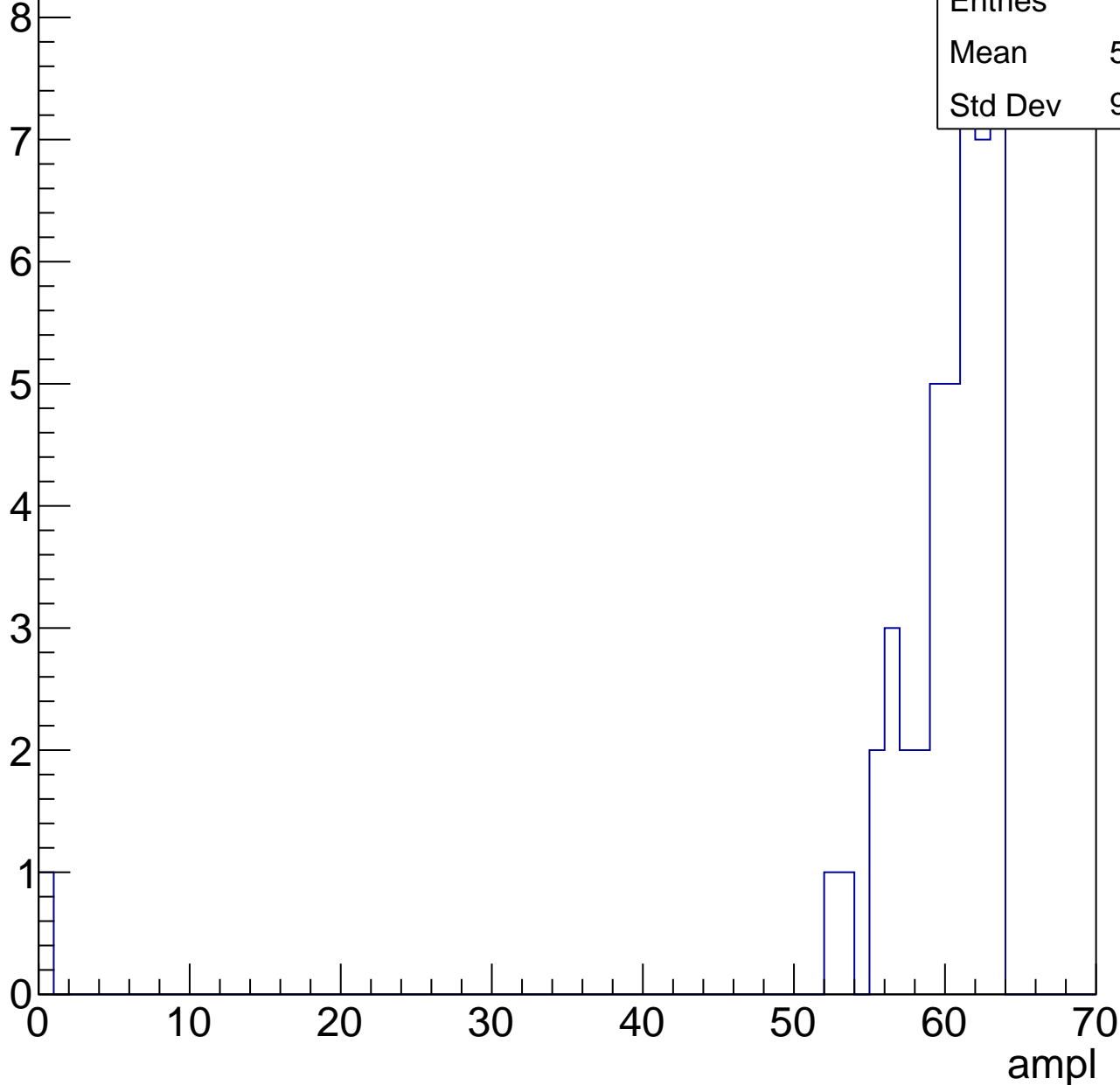


B0L001S, U22-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

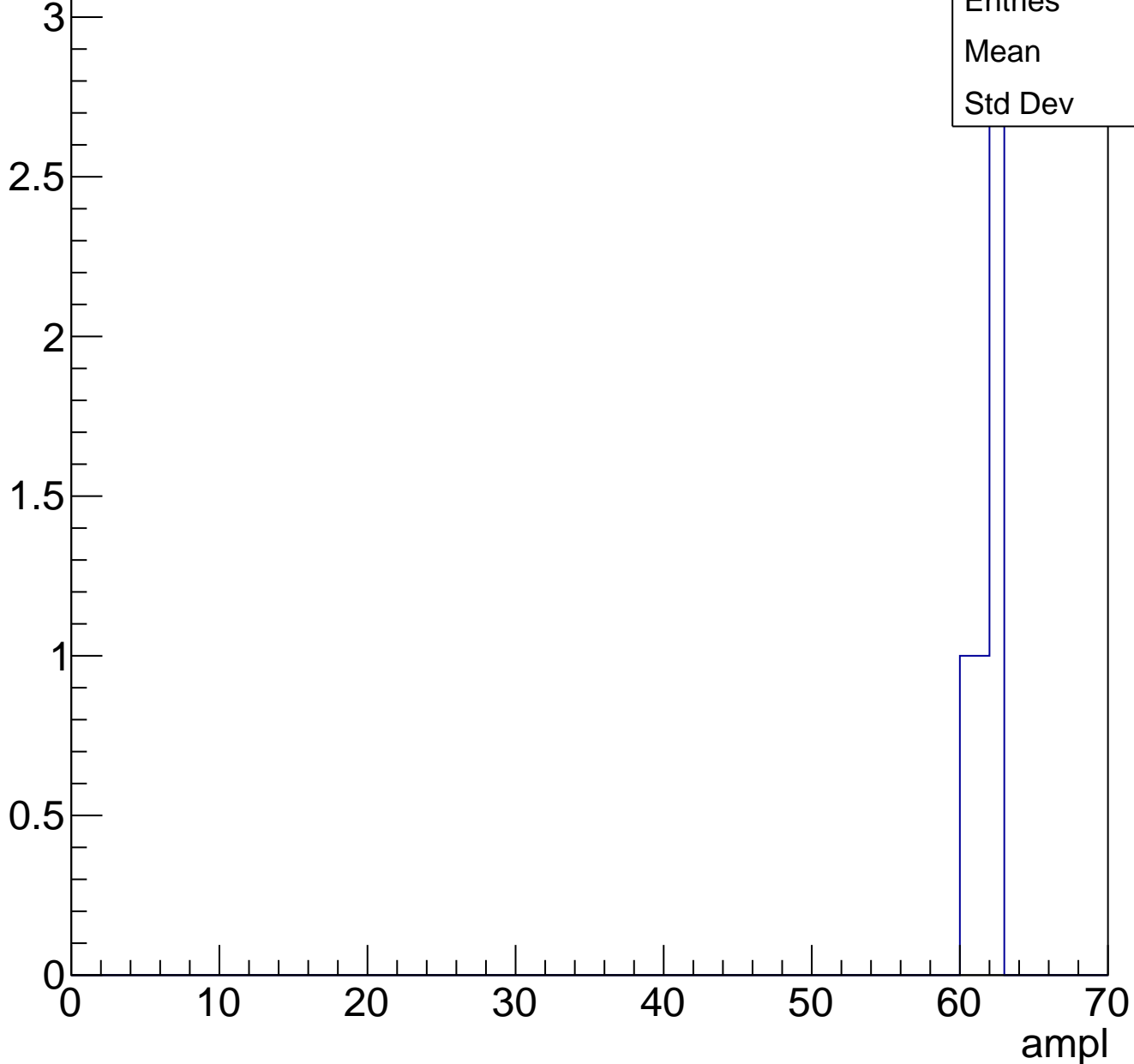
Entries	45
Mean	58.53
Std Dev	9.253



B0L001S, U22-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch107, adc0

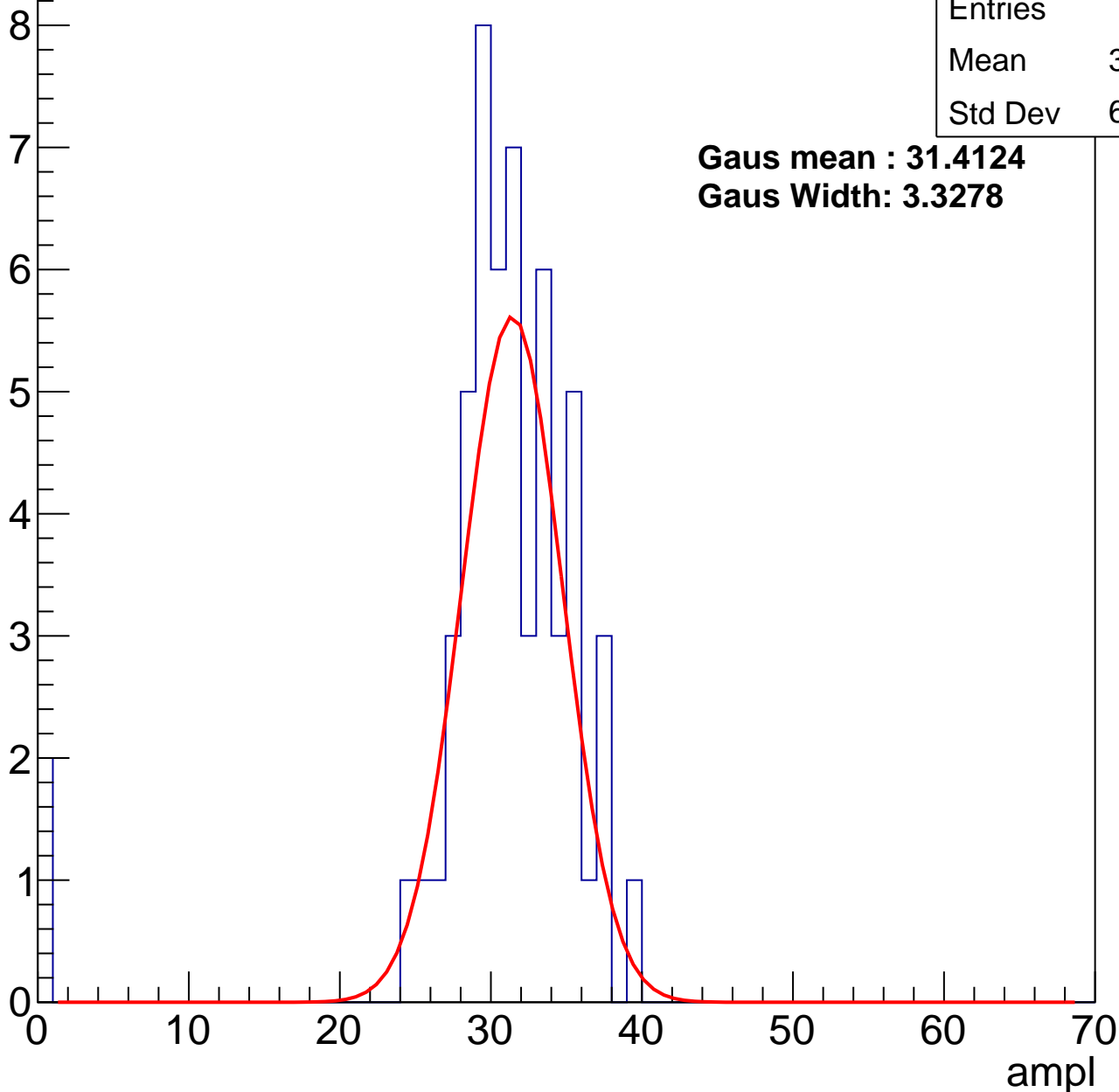
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.04
Std Dev	6.603

Gaus mean : 31.4124

Gaus Width: 3.3278



B0L001S, U22-ch107, adc1

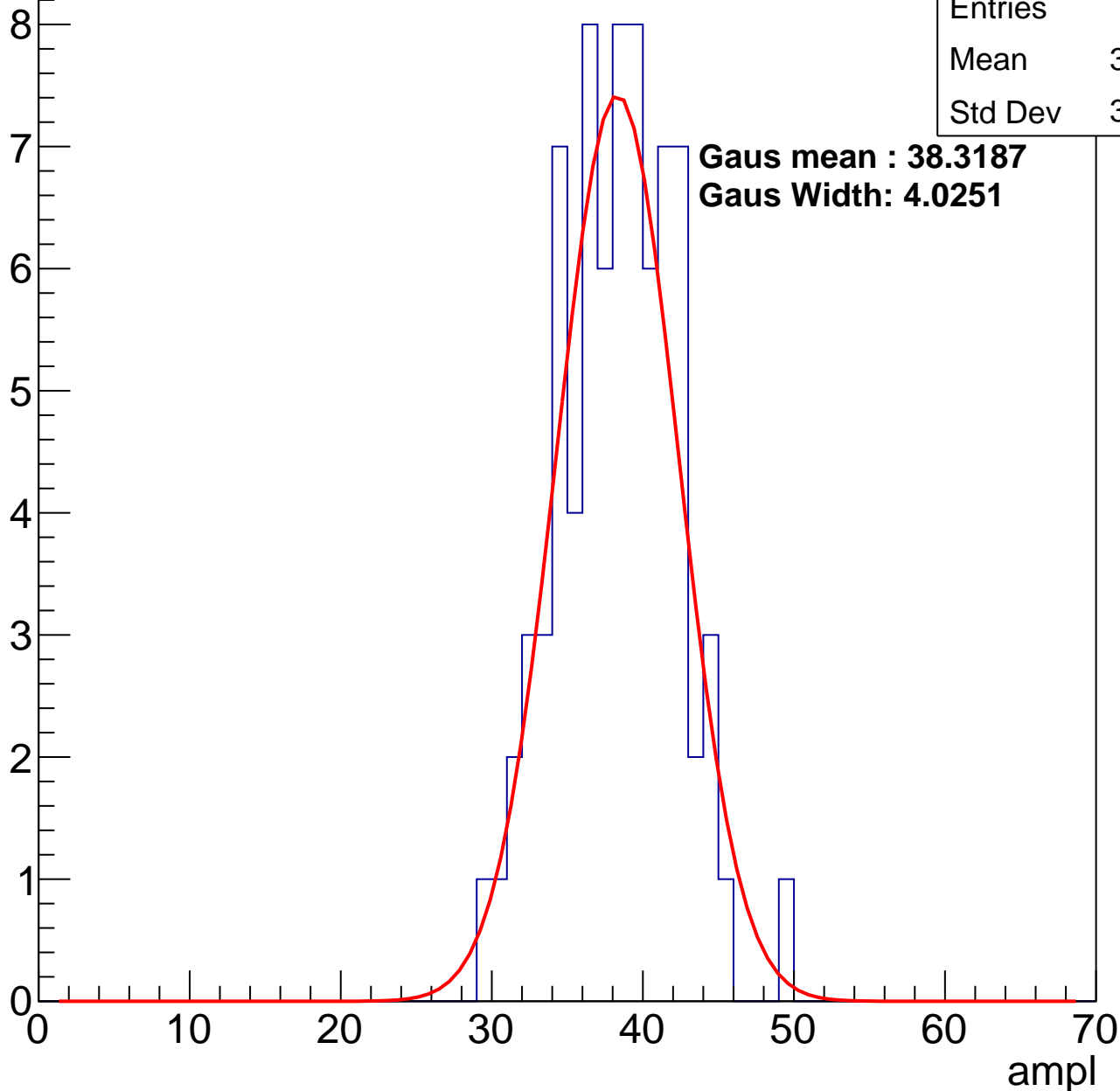
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.86
Std Dev	3.832

Gaus mean : 38.3187

Gaus Width: 4.0251



B0L001S, U22-ch107, adc2

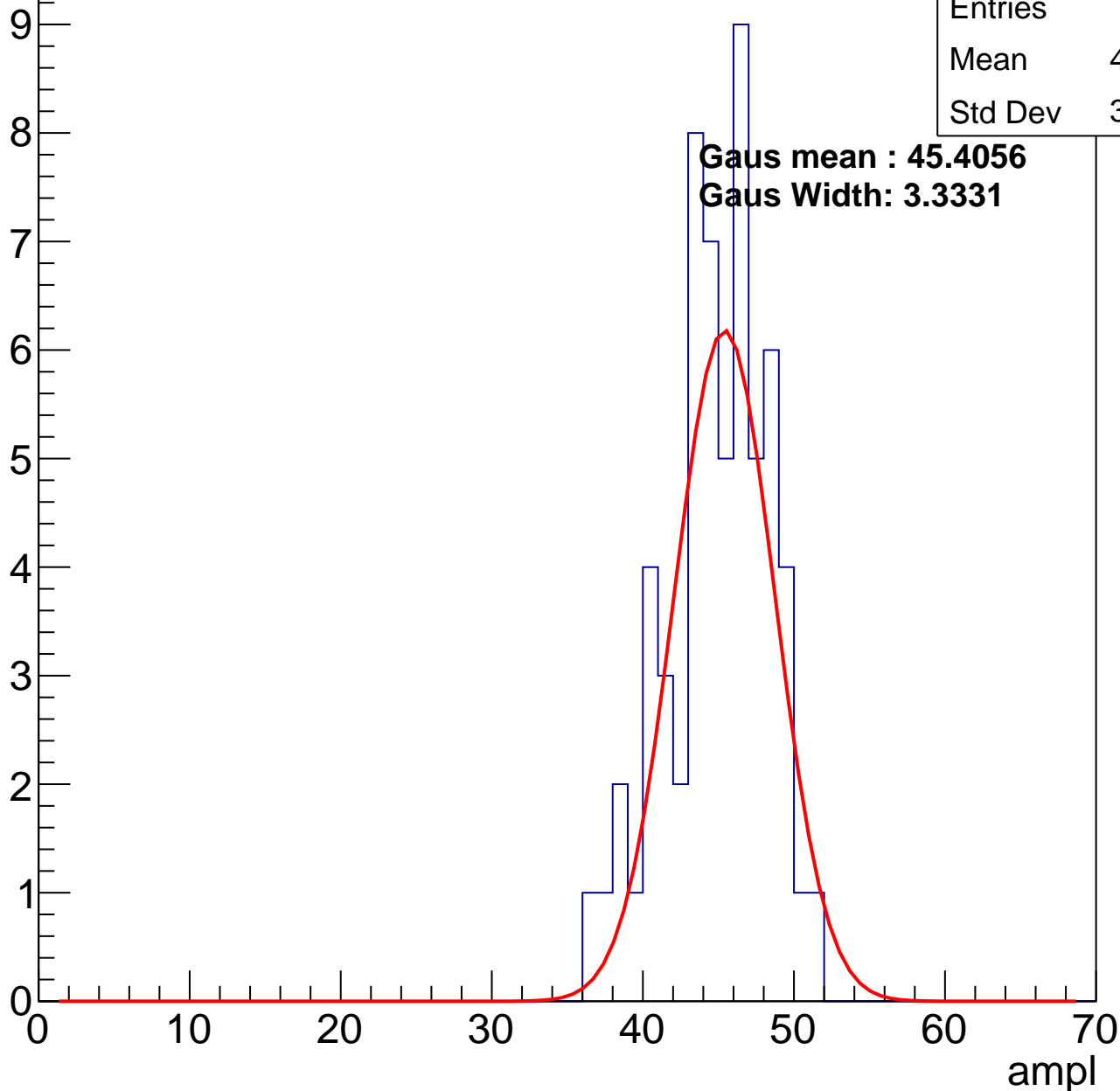
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.43
Std Dev	3.348

Gaus mean : 45.4056

Gaus Width: 3.3331



B0L001S, U22-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	51.7
Std Dev	3.652

Entry

10

8

6

4

2

0

0

10

20

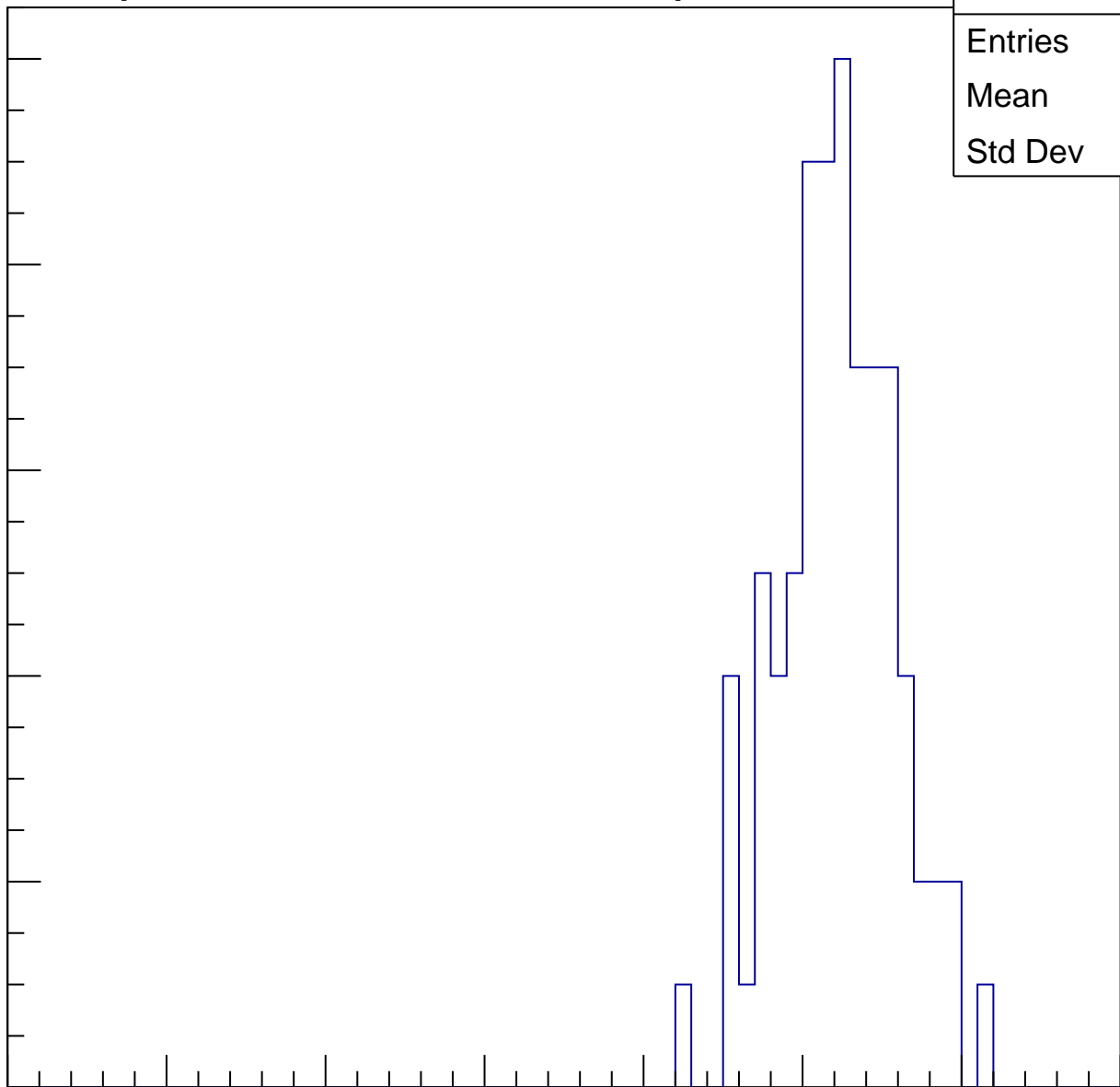
30

40

50

60

ampl



B0L001S, U22-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

6

5

4

3

2

1

0

Entries	43
Mean	57.91
Std Dev	2.802

10

20

30

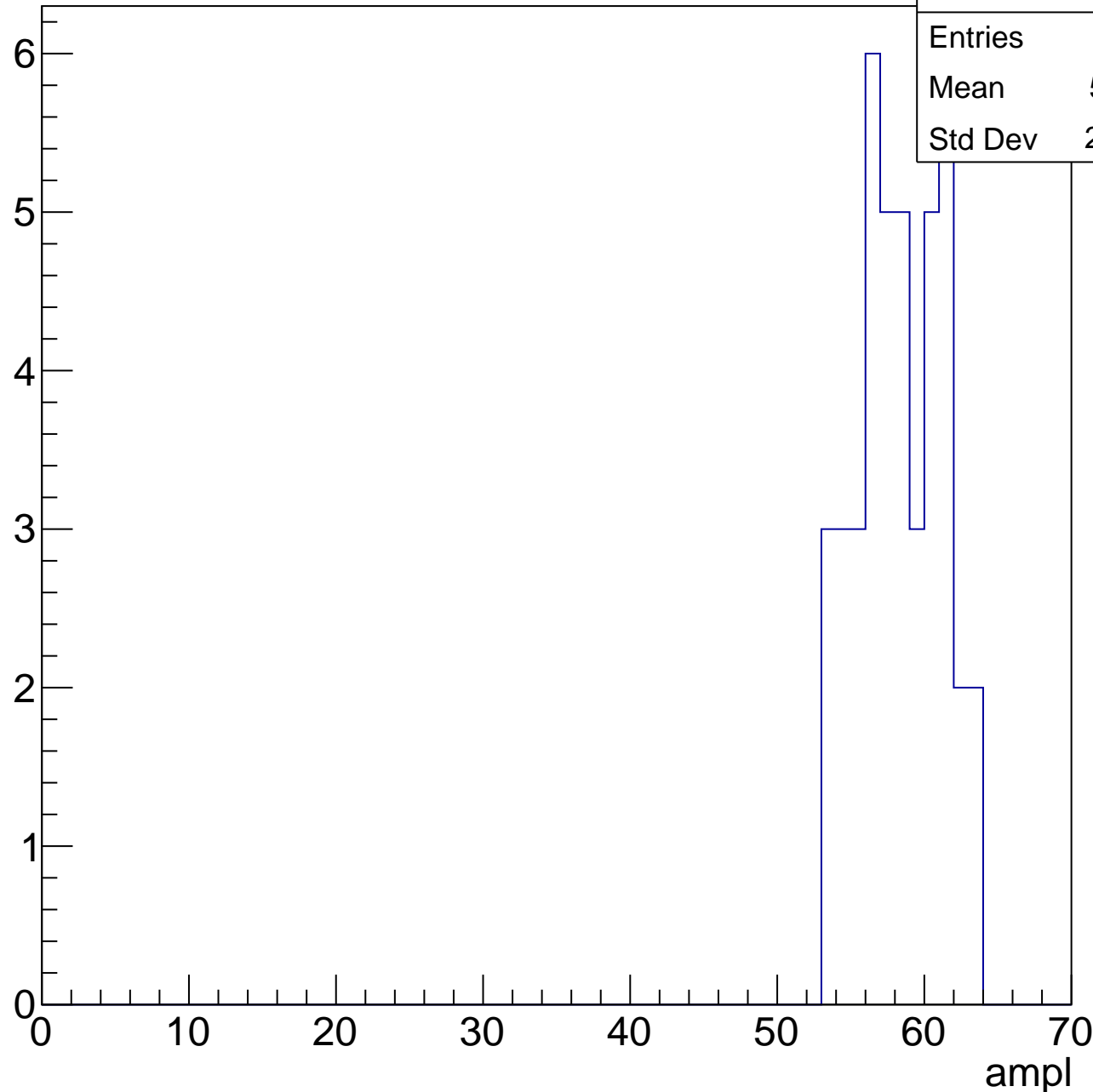
40

50

60

70

ampl

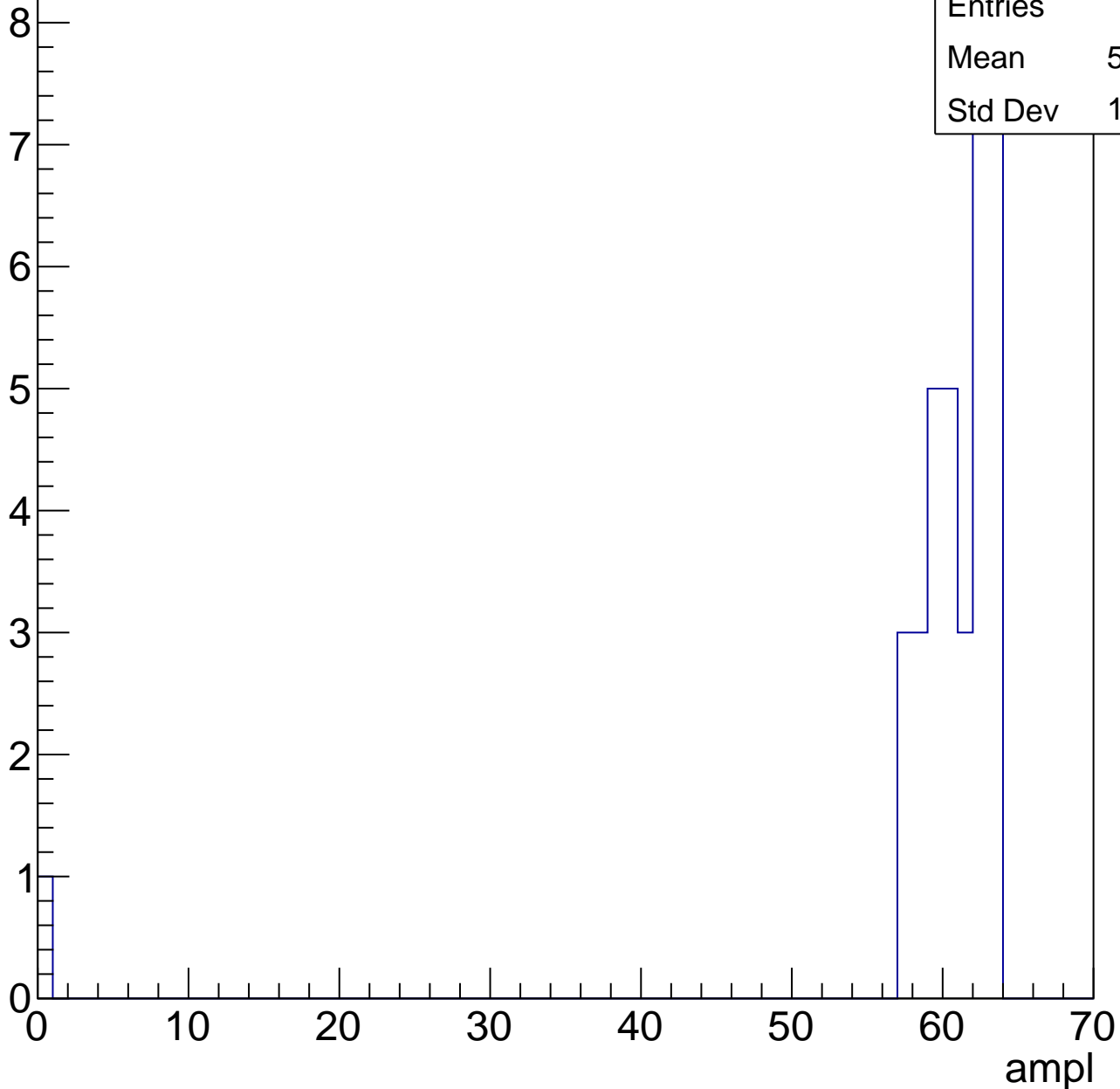


B0L001S, U22-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.97
Std Dev	10.16



B0L001S, U22-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch108, adc0

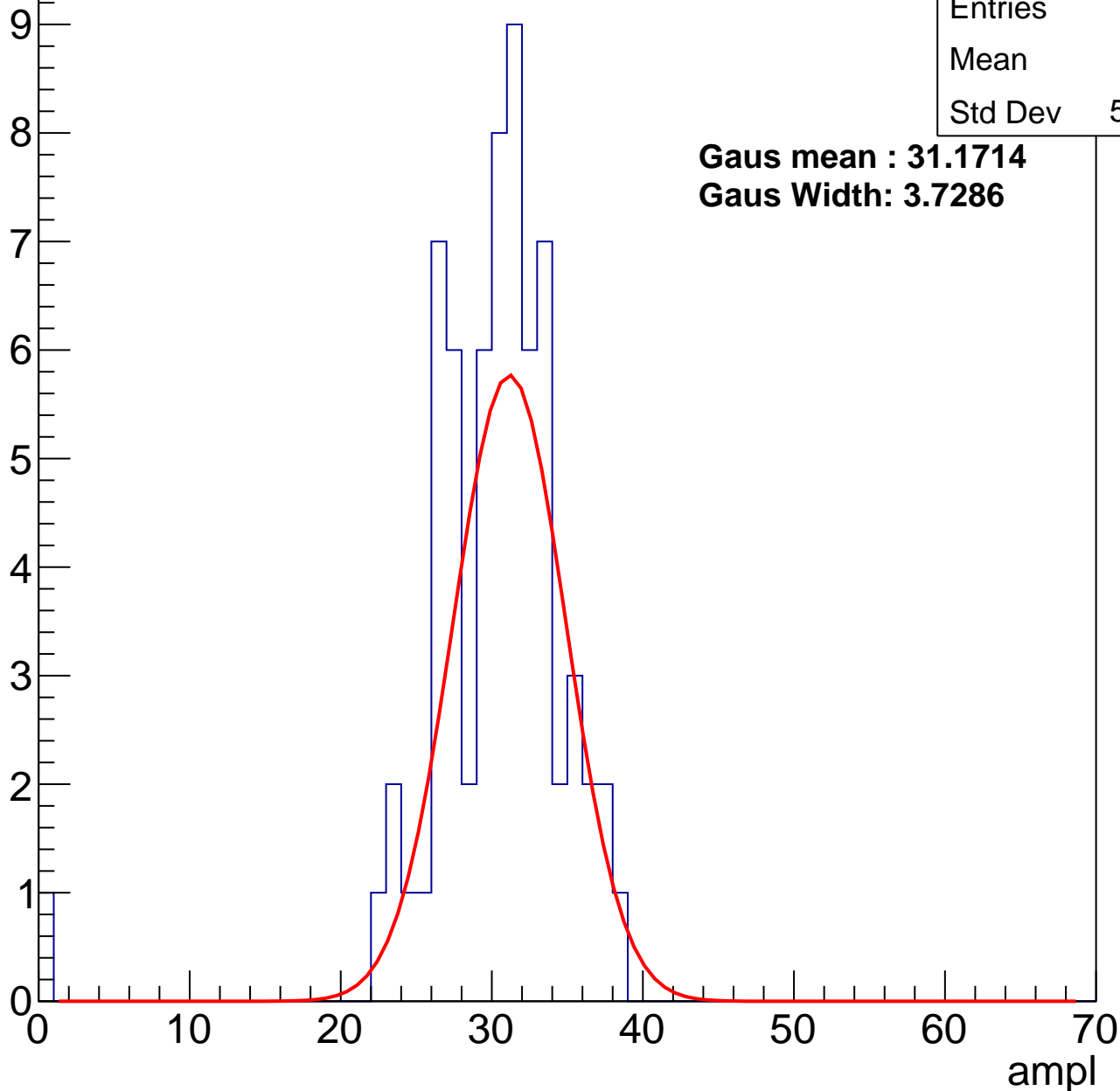
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.7
Std Dev	5.087

Gaus mean : 31.1714

Gaus Width: 3.7286



B0L001S, U22-ch108, adc1

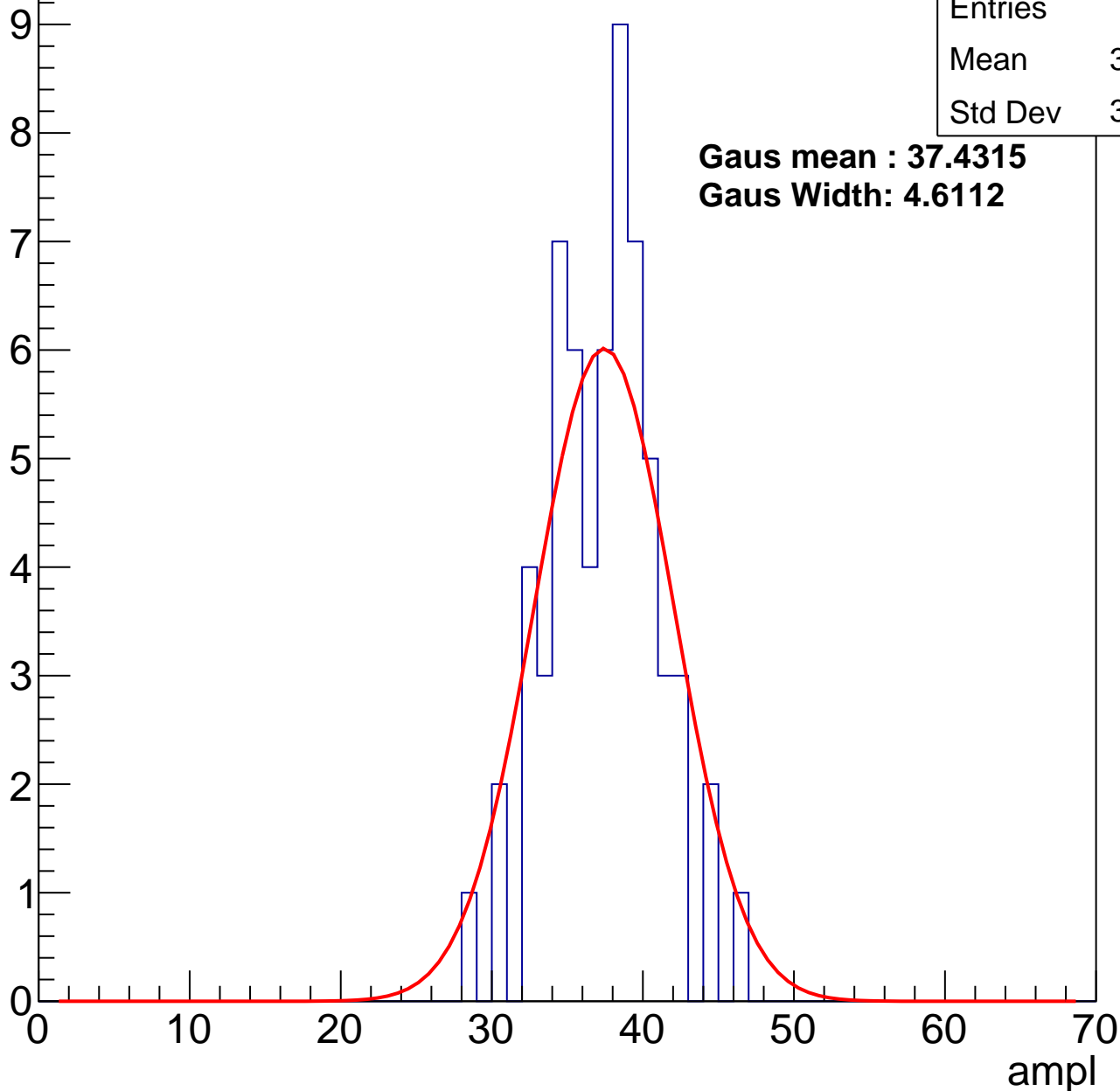
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	36.94
Std Dev	3.567

Gaus mean : 37.4315

Gaus Width: 4.6112



B0L001S, U22-ch108, adc2

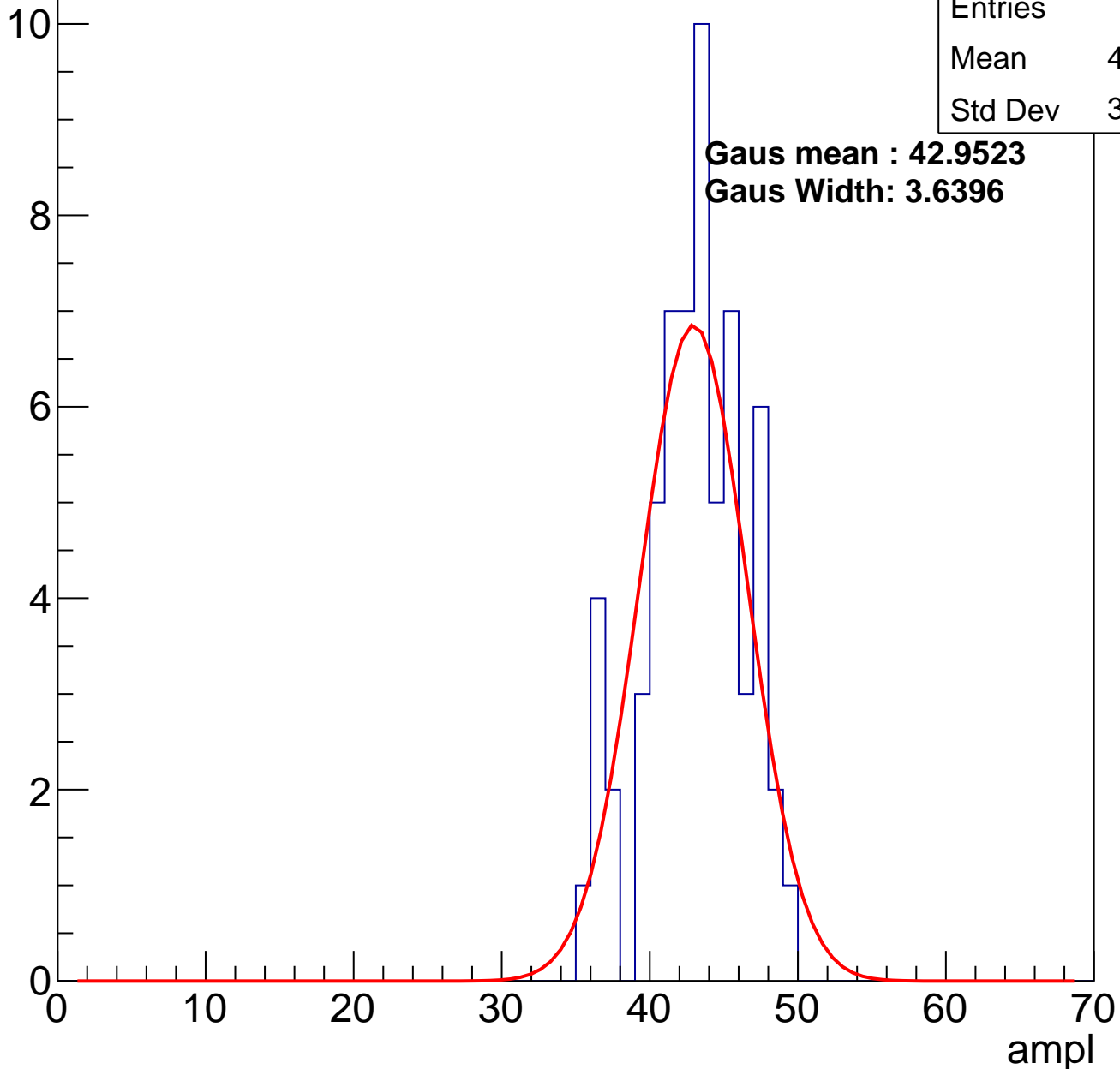
calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	42.56
Std Dev	3.308

Gaus mean : 42.9523

Gaus Width: 3.6396

Entry

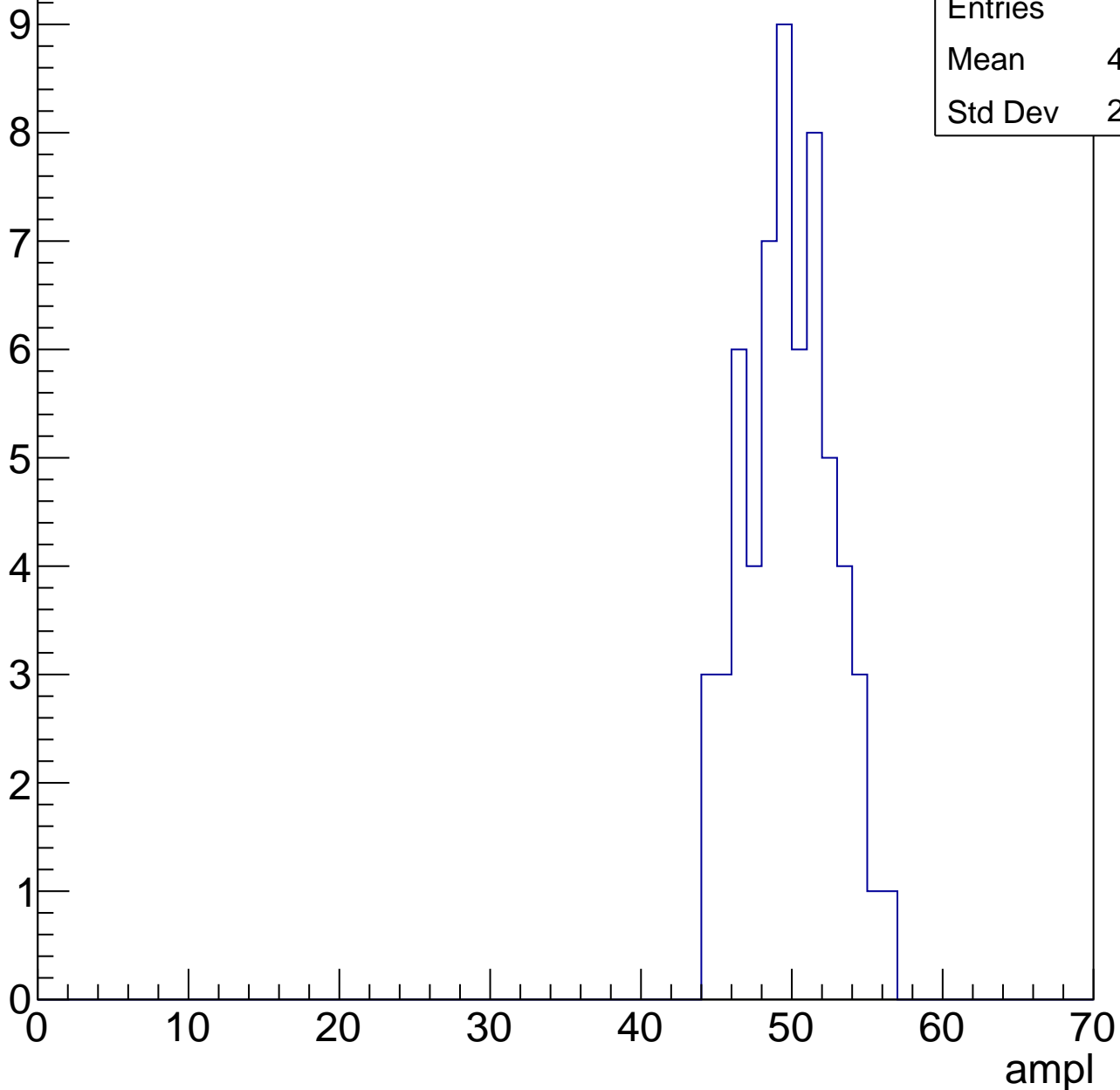


B0L001S, U22-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

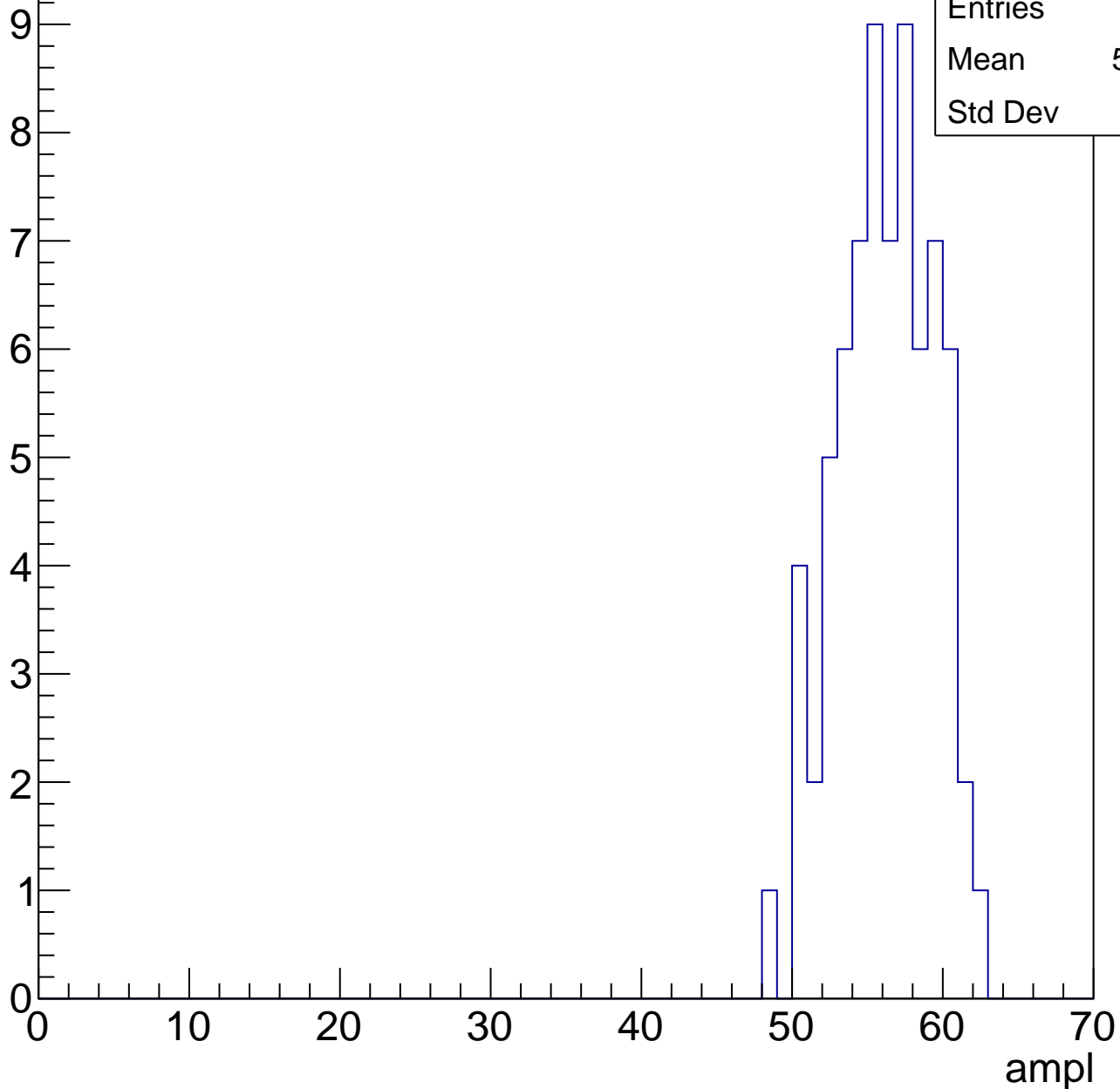
Entries	60
Mean	49.35
Std Dev	2.886



B0L001S, U22-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

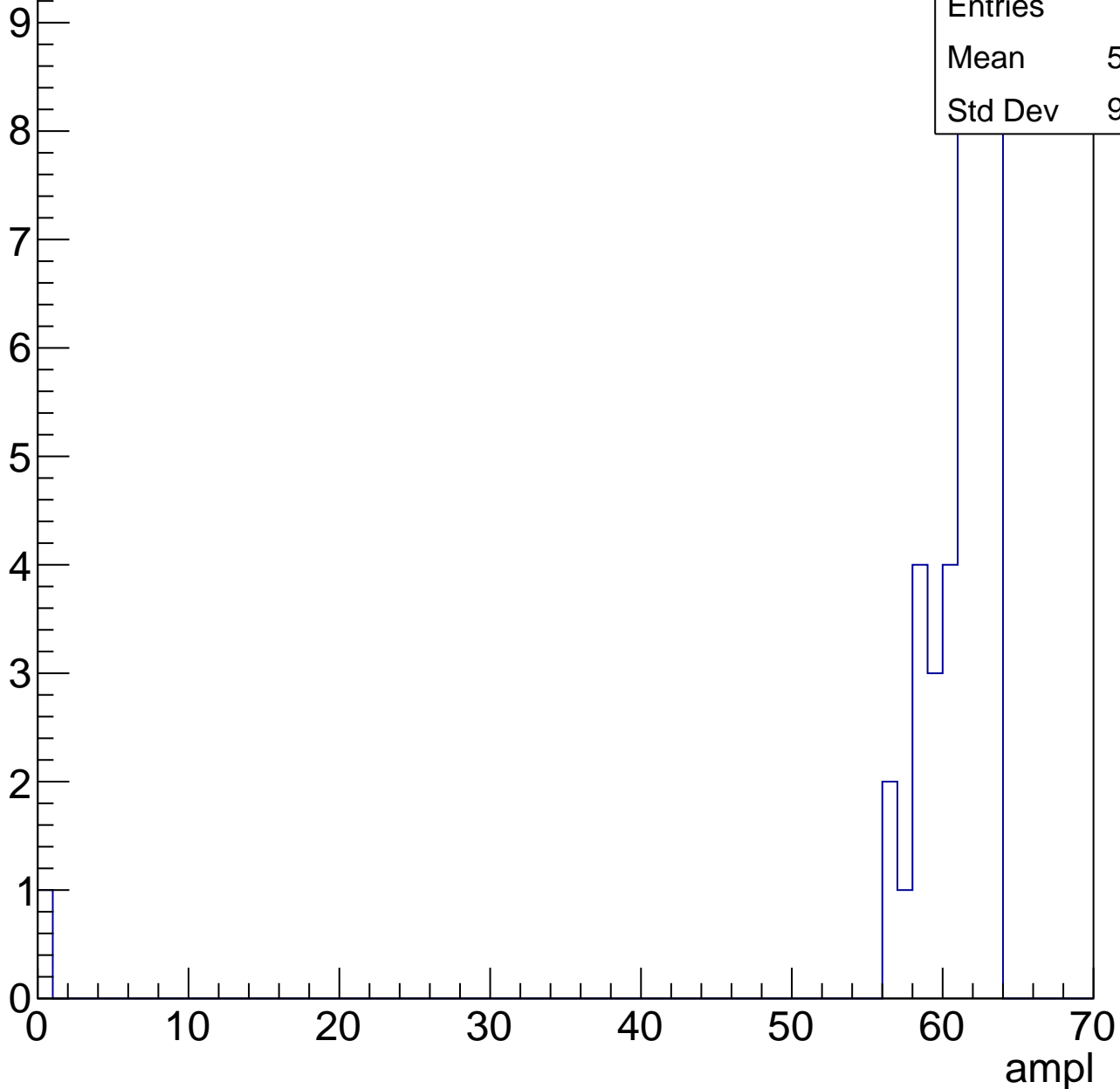


B0L001S, U22-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	59.29
Std Dev	9.582



B0L001S, U22-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch109, adc0

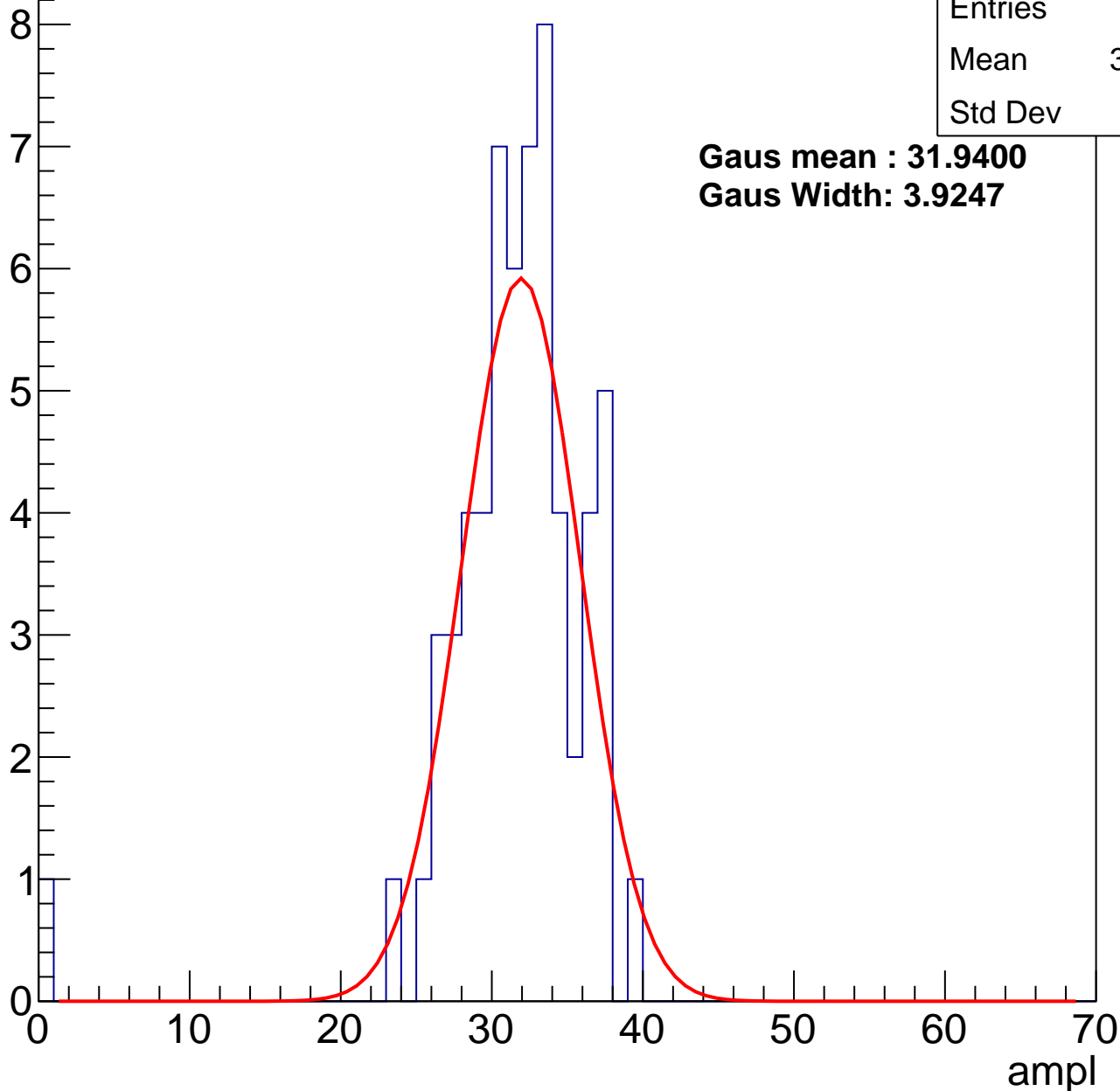
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.03
Std Dev	5.27

Gaus mean : 31.9400

Gaus Width: 3.9247



B0L001S, U22-ch109, adc1

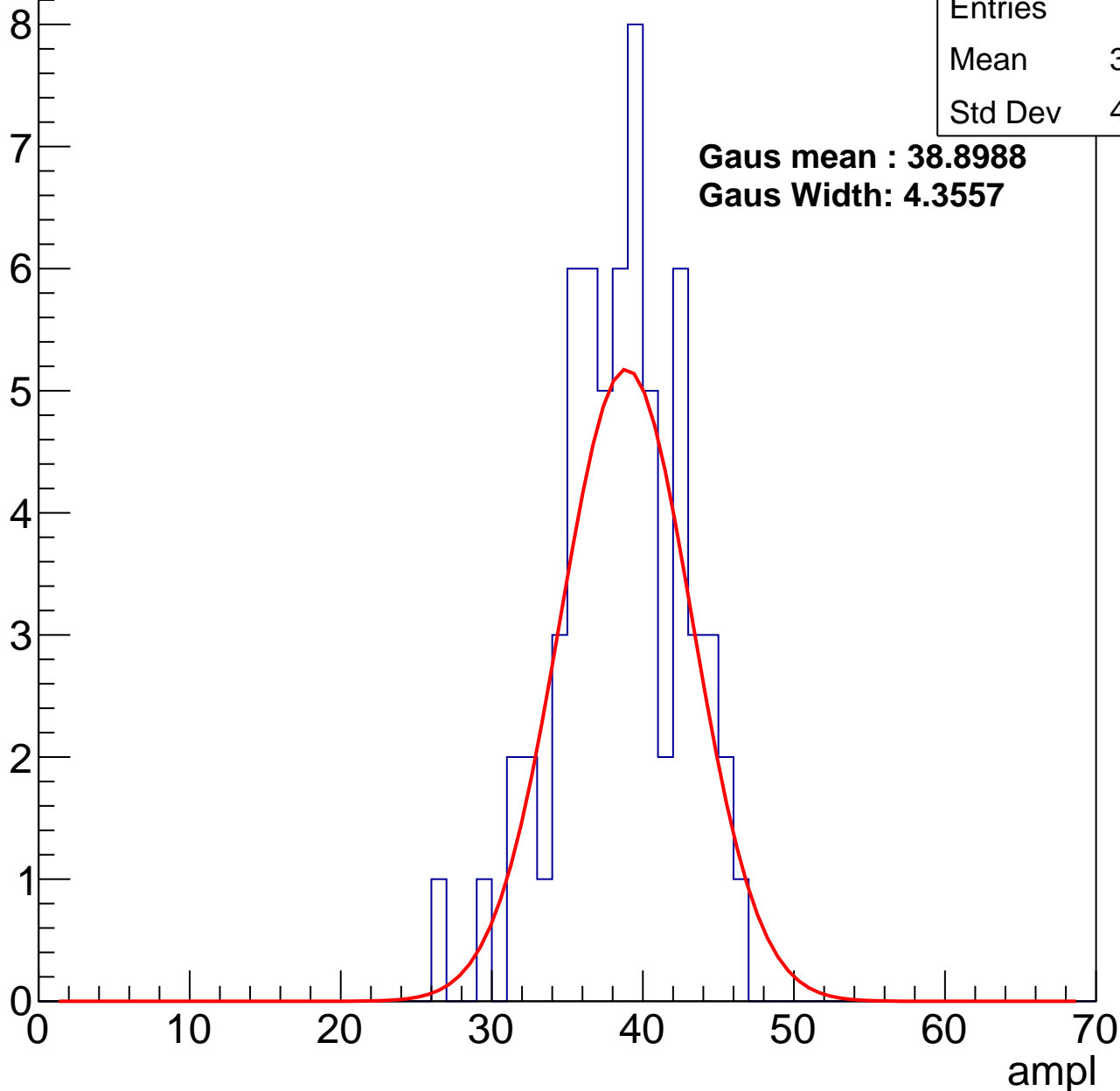
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.06
Std Dev	4.059

Gaus mean : 38.8988

Gaus Width: 4.3557



B0L001S, U22-ch109, adc2

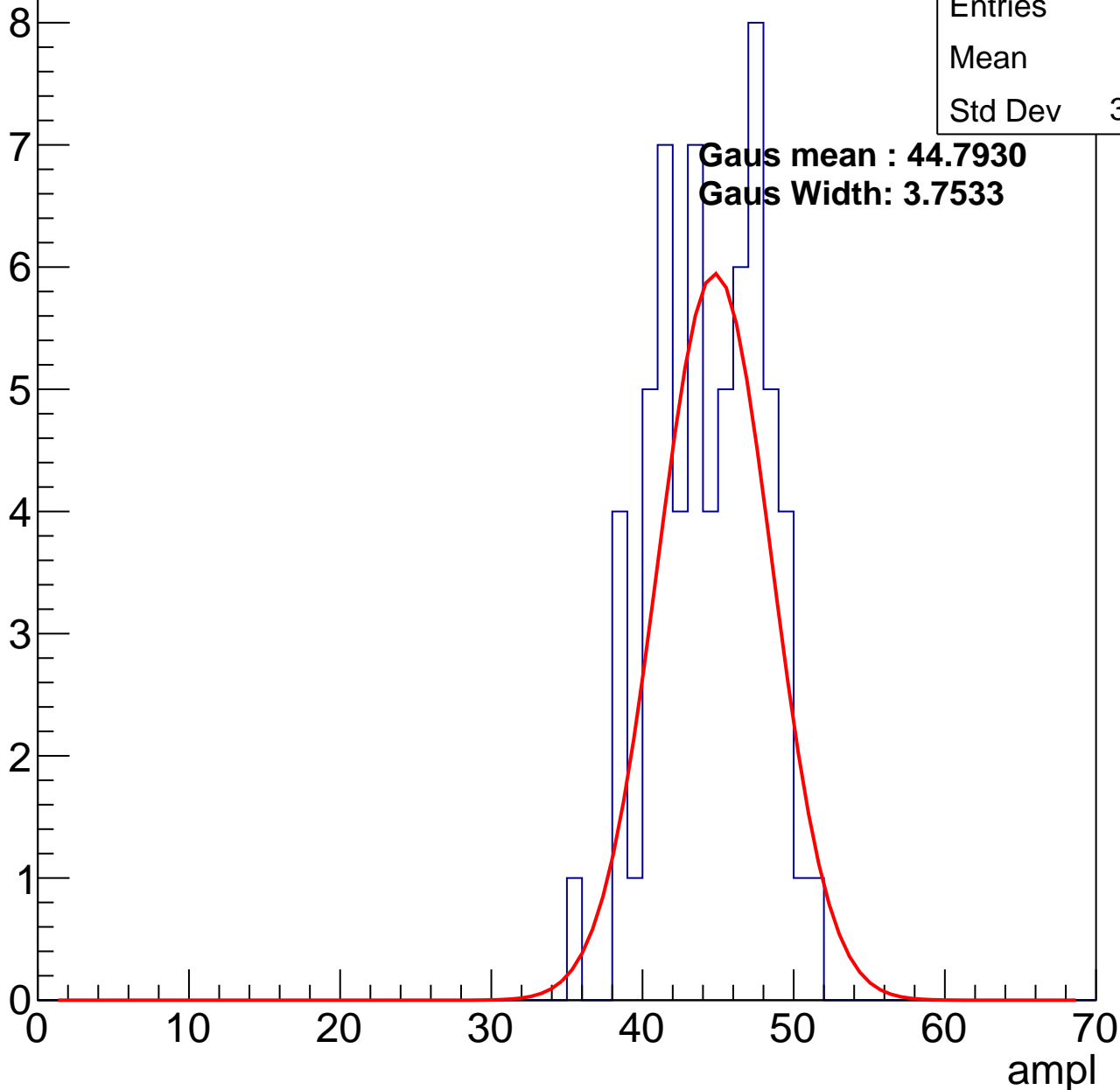
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44
Std Dev	3.523

Gaus mean : 44.7930

Gaus Width: 3.7533

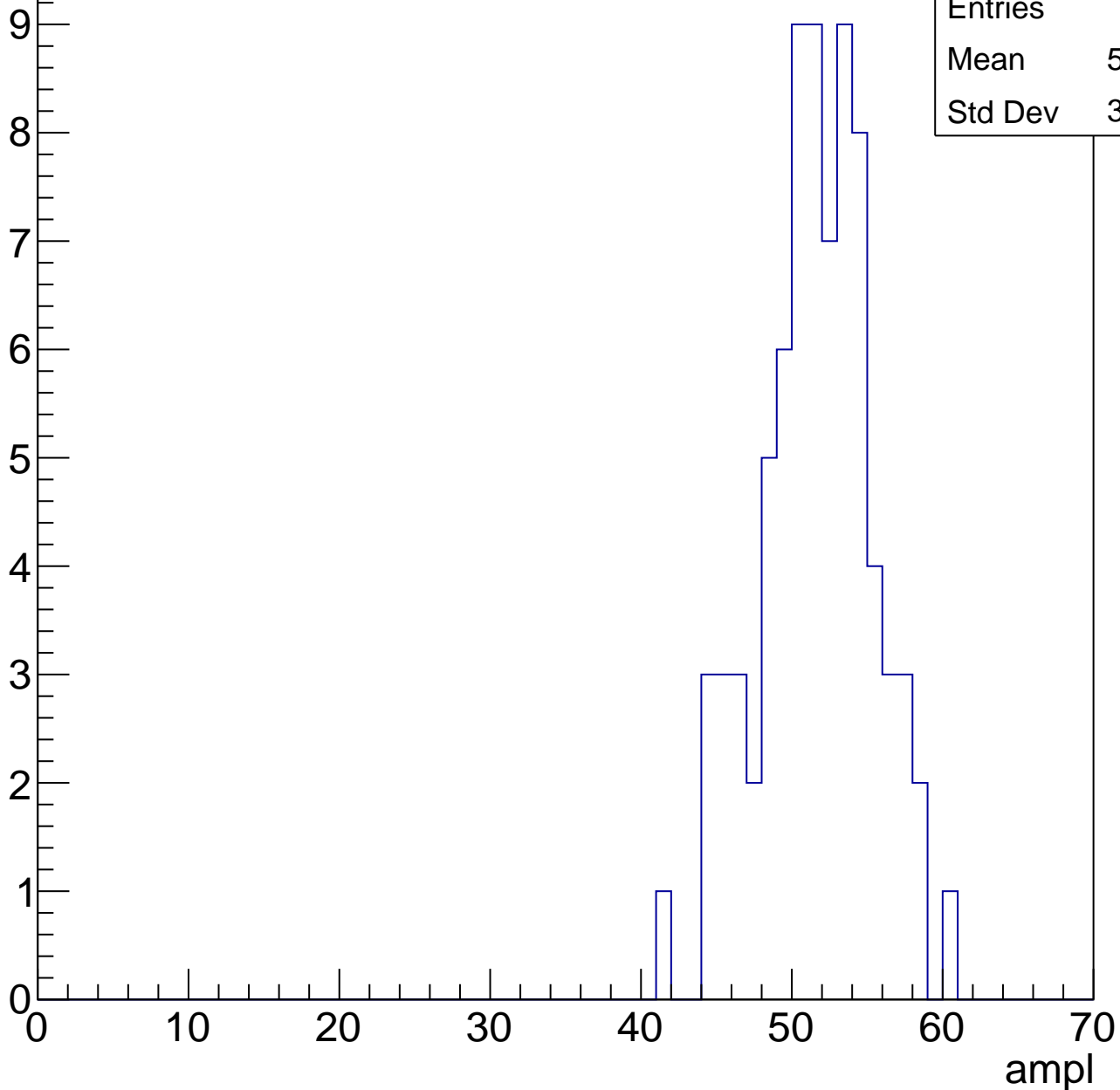


B0L001S, U22-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	51.17
Std Dev	3.719

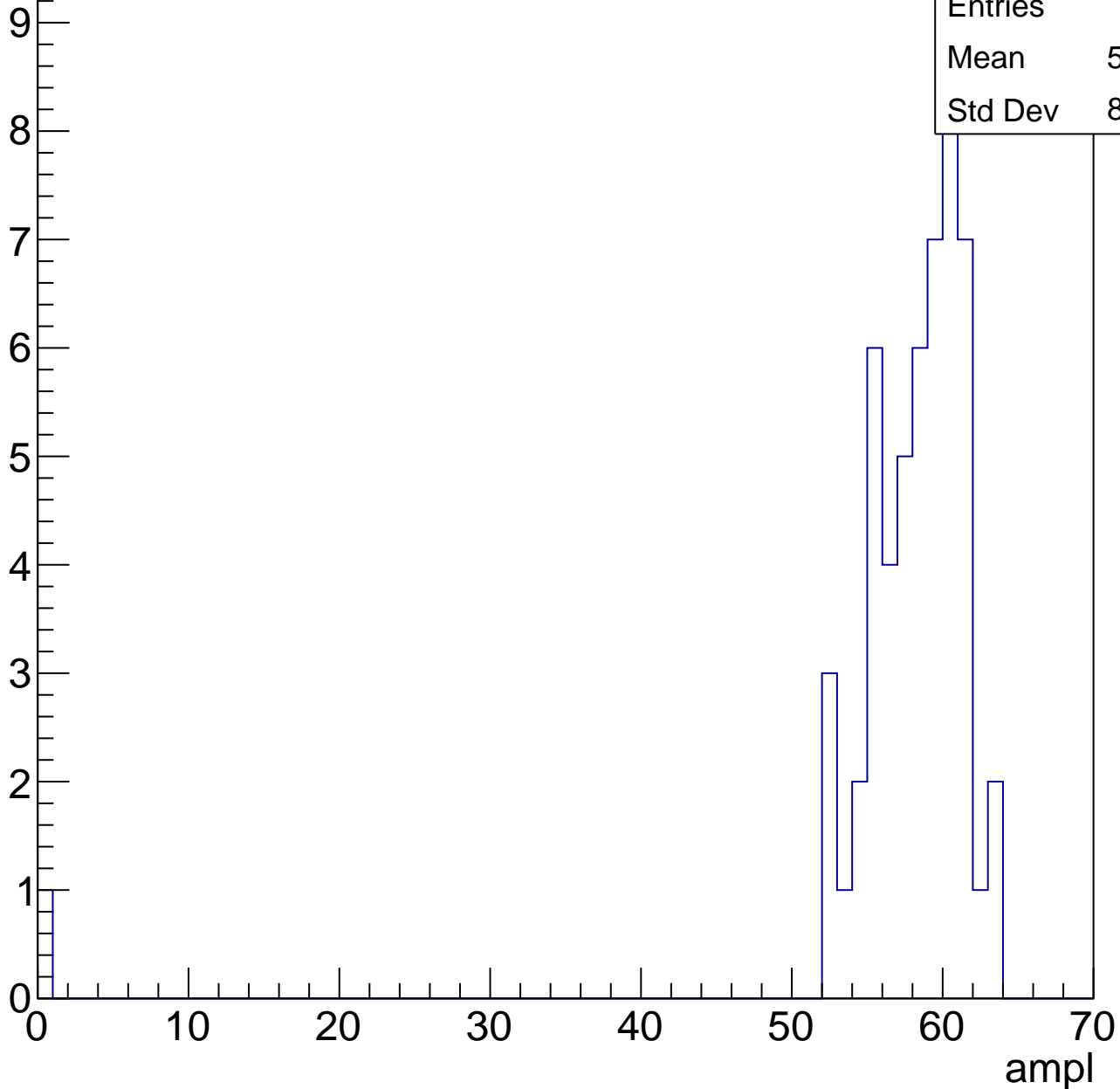


B0L001S, U22-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.89
Std Dev	8.288

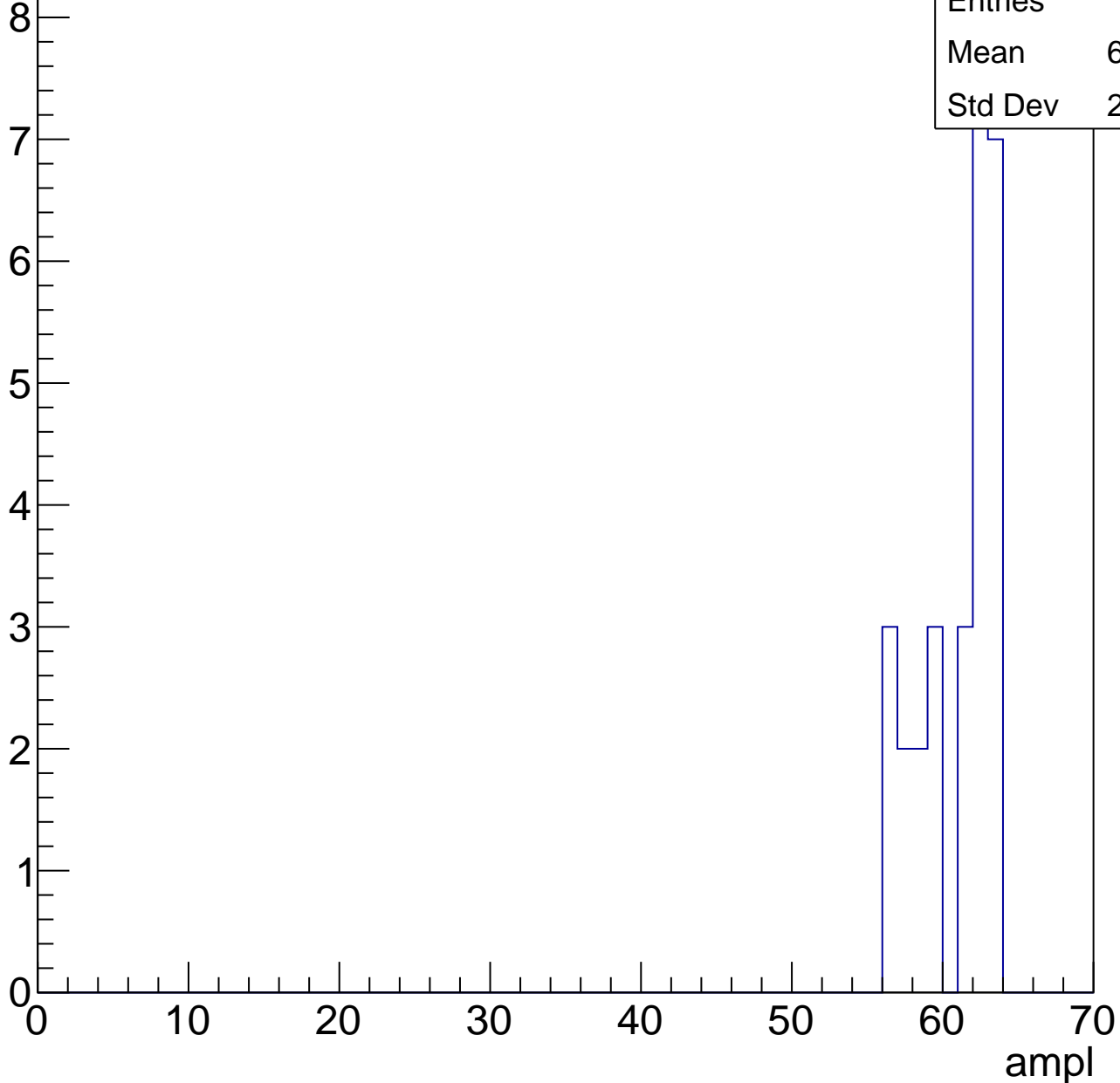


B0L001S, U22-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	60.54
Std Dev	2.442



B0L001S, U22-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U22-ch110, adc0

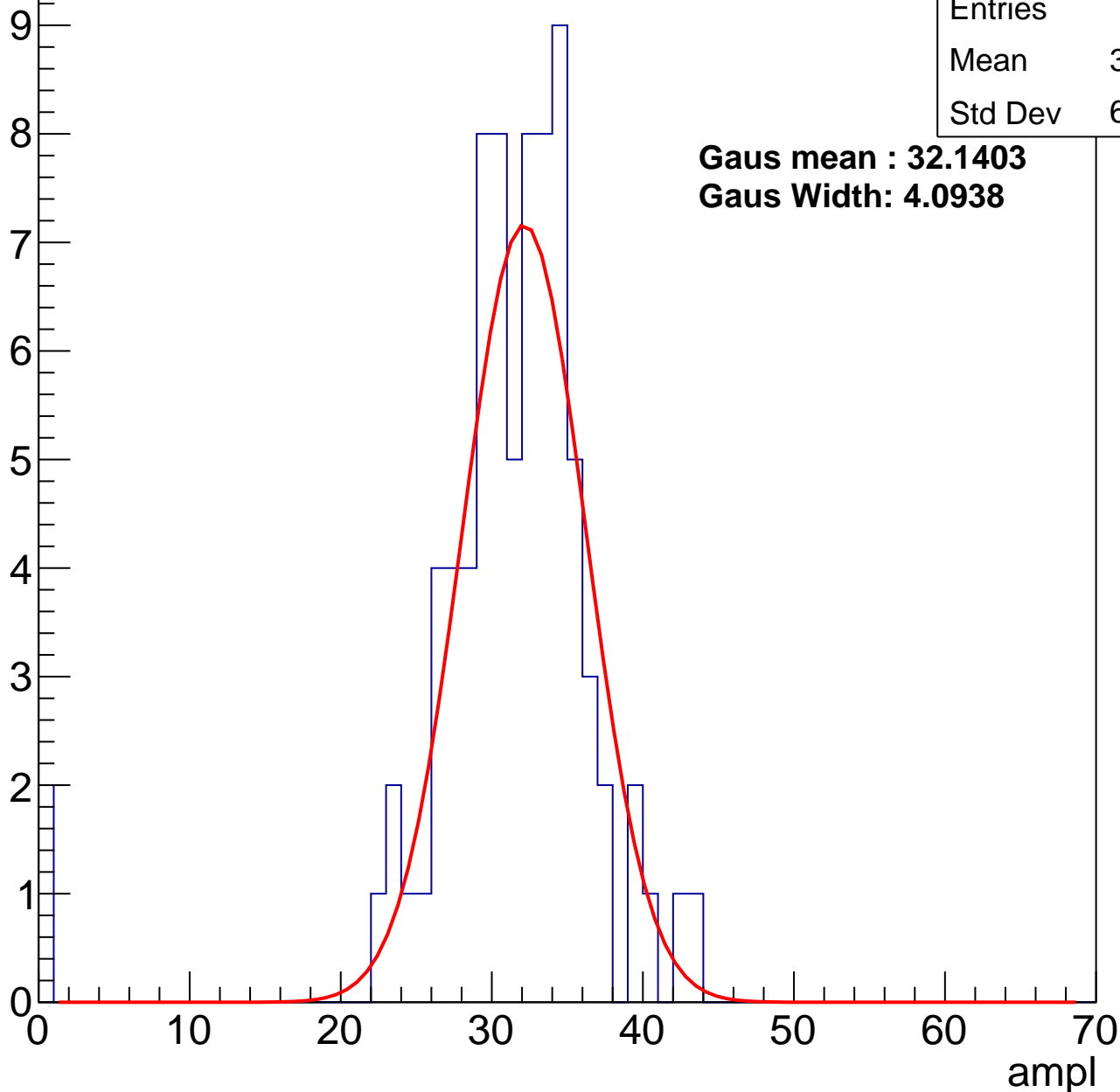
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	30.68
Std Dev	6.393

Gaus mean : 32.1403

Gaus Width: 4.0938



B0L001S, U22-ch110, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	61
Mean	39.02
Std Dev	3.541

Gaus mean : 38.5185

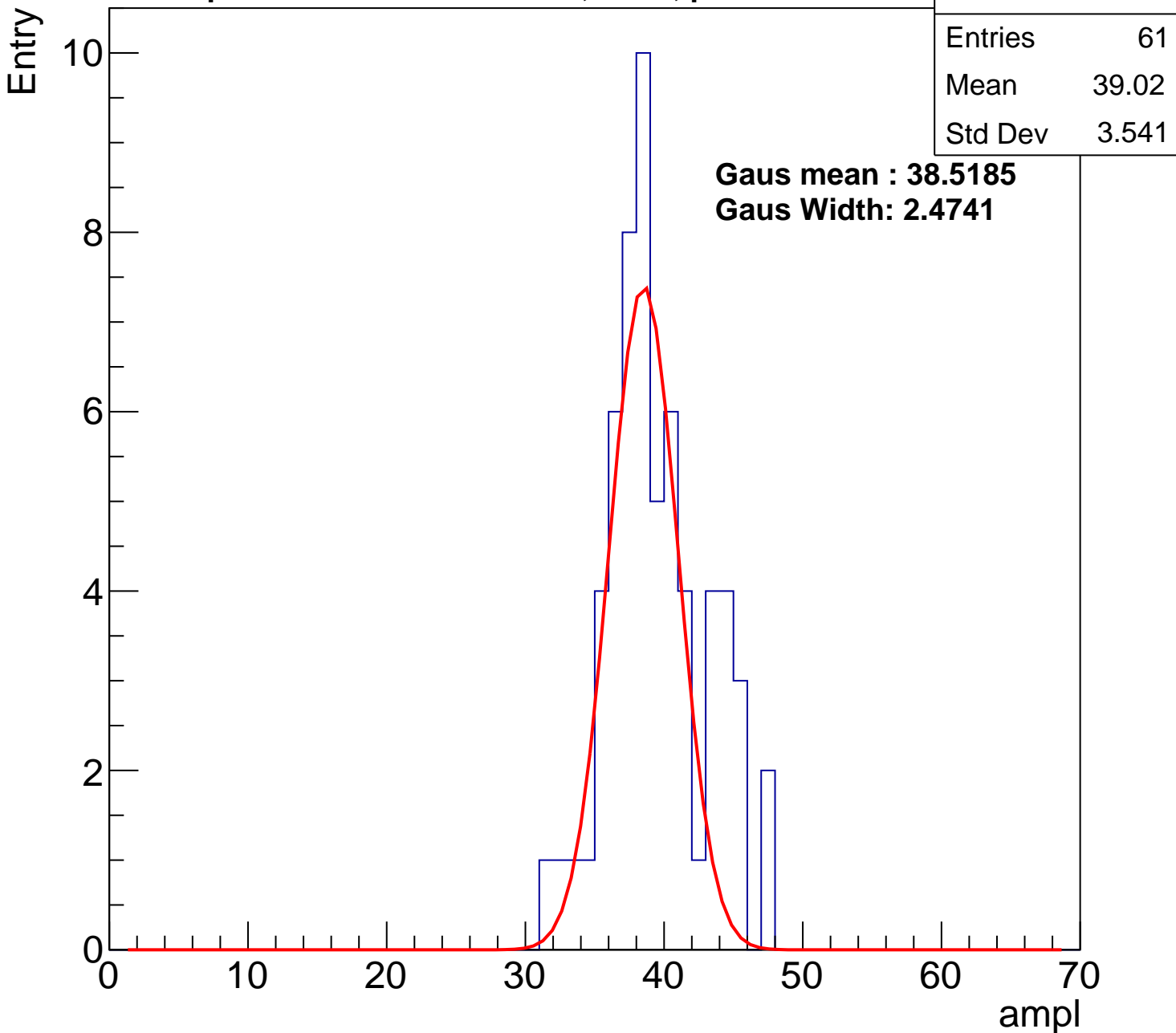
Gaus Width: 2.4741

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch110, adc2

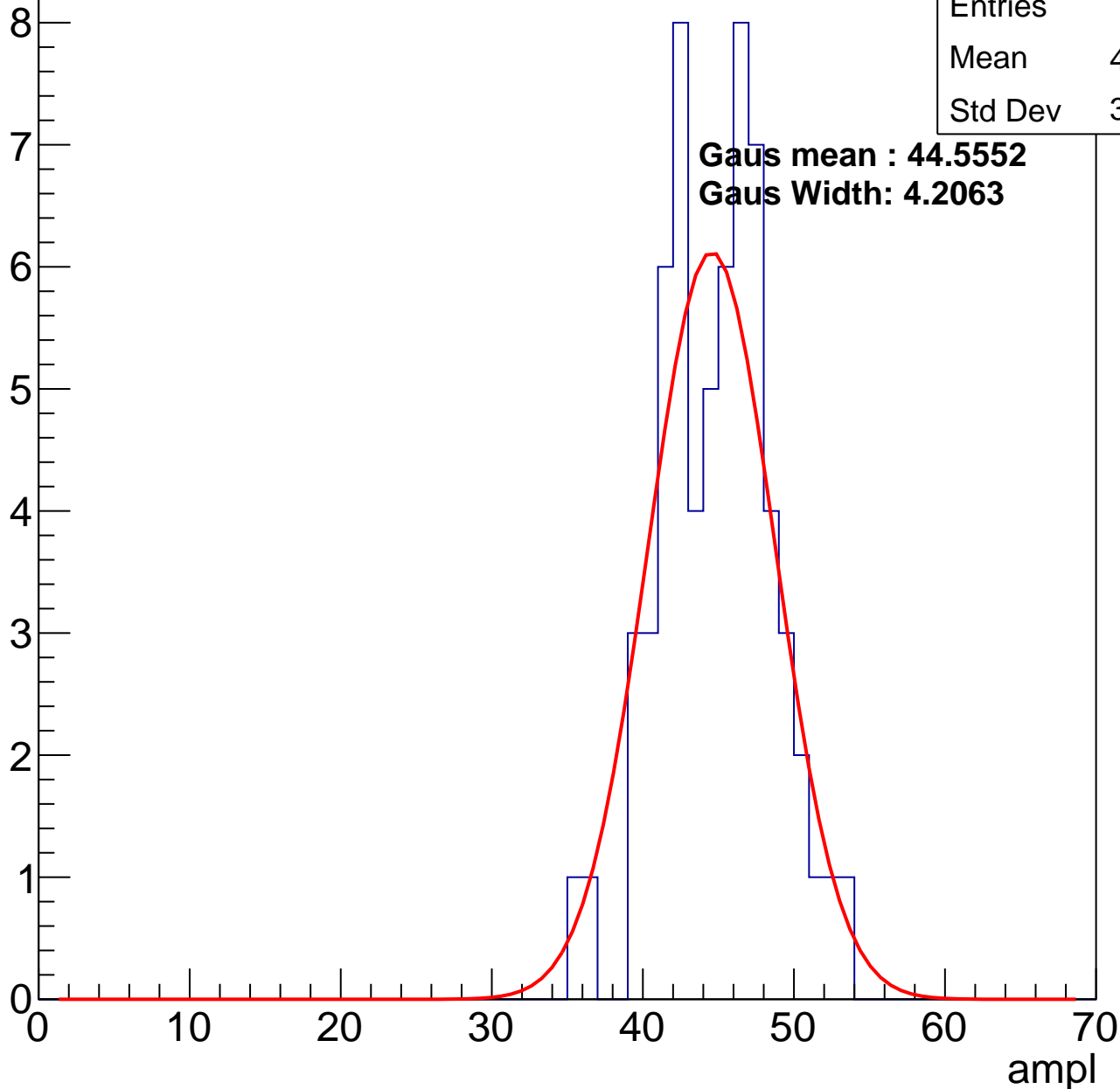
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	44.44
Std Dev	3.648

Gaus mean : 44.5552

Gaus Width: 4.2063

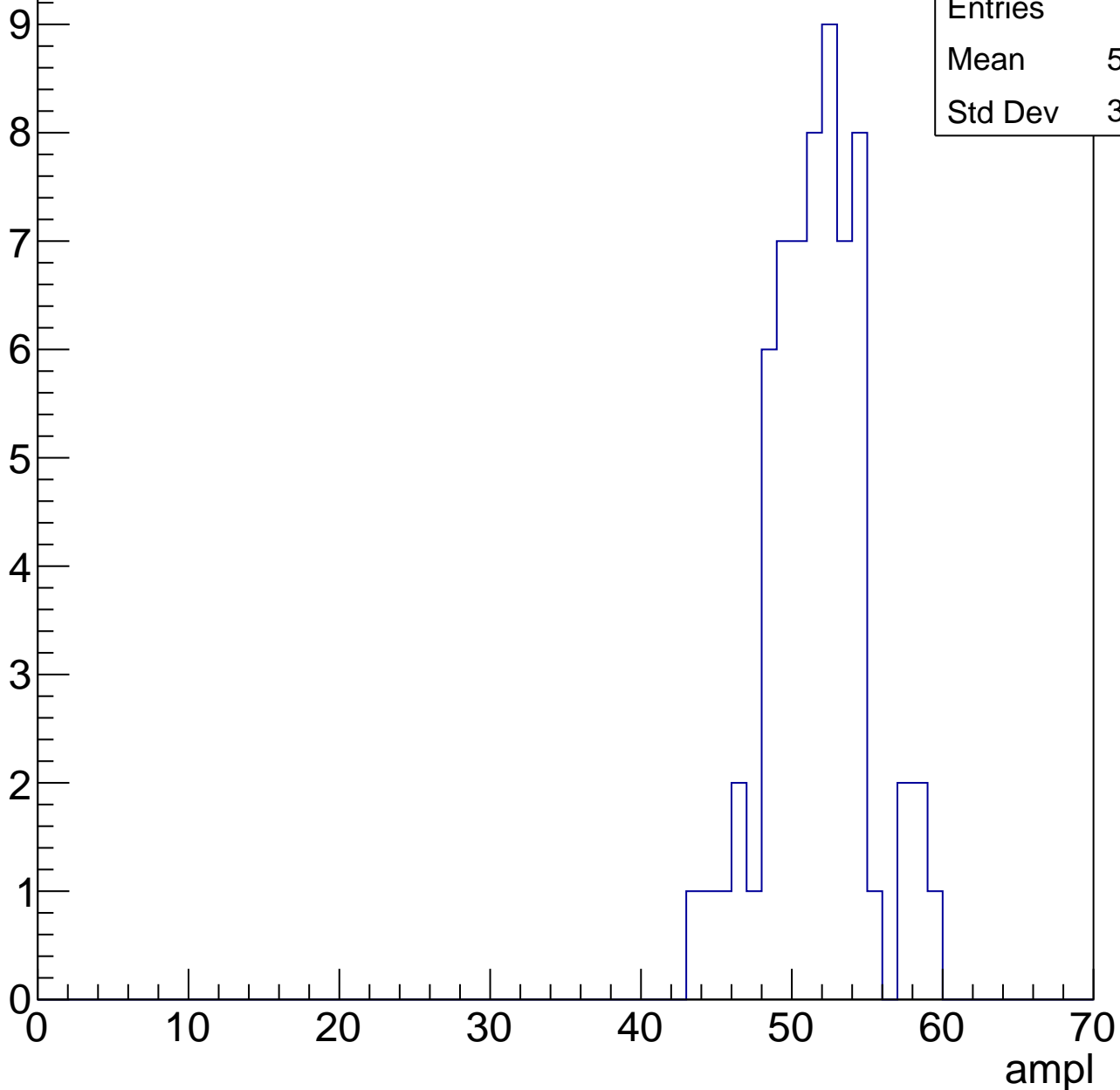


B0L001S, U22-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.17
Std Dev	3.214

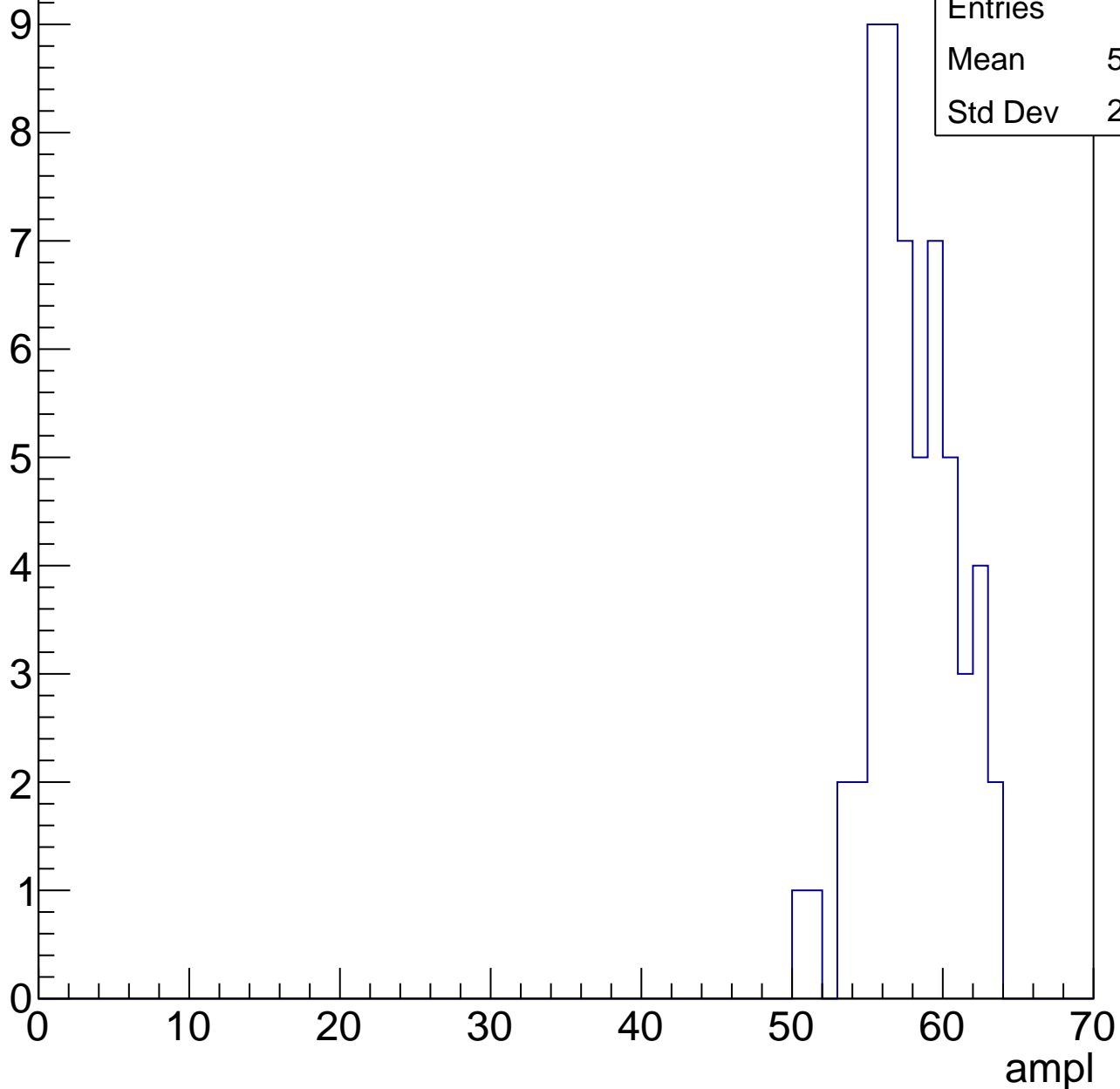


B0L001S, U22-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	57.42
Std Dev	2.865

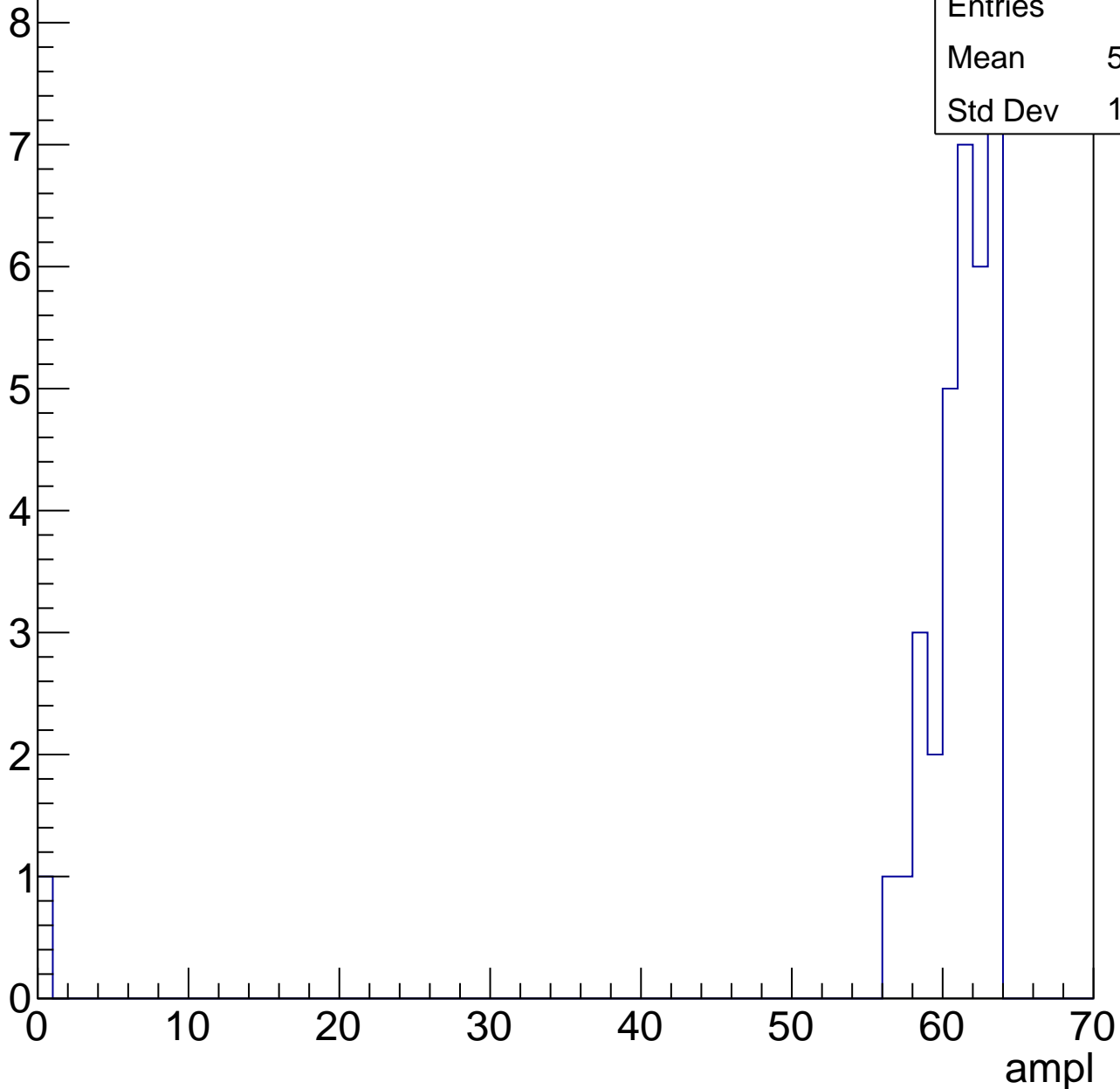


B0L001S, U22-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	59.06
Std Dev	10.45



B0L001S, U22-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch111, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	58
Mean	30.45
Std Dev	3.715

Gaus mean : 31.0499

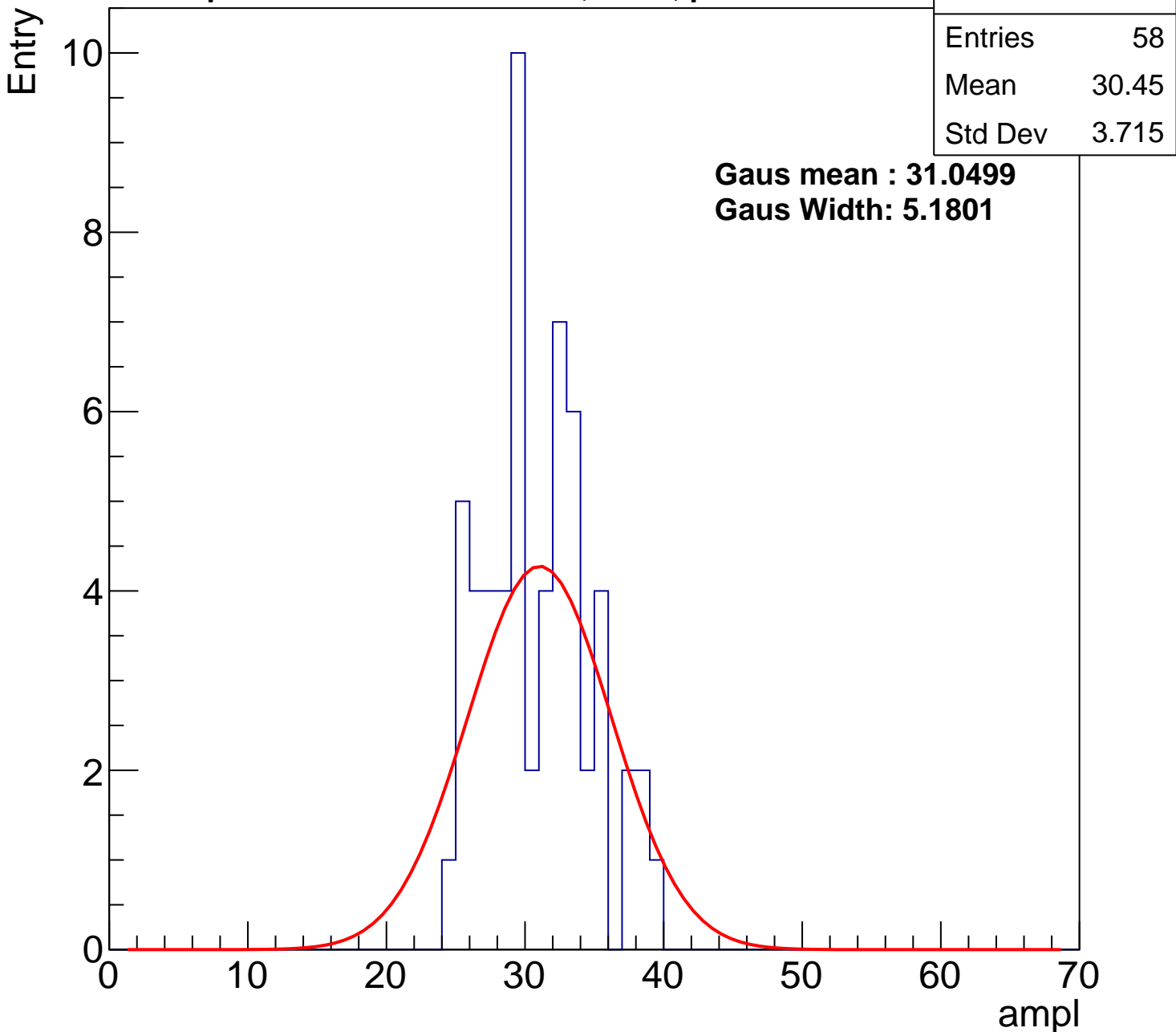
Gaus Width: 5.1801

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch111, adc1

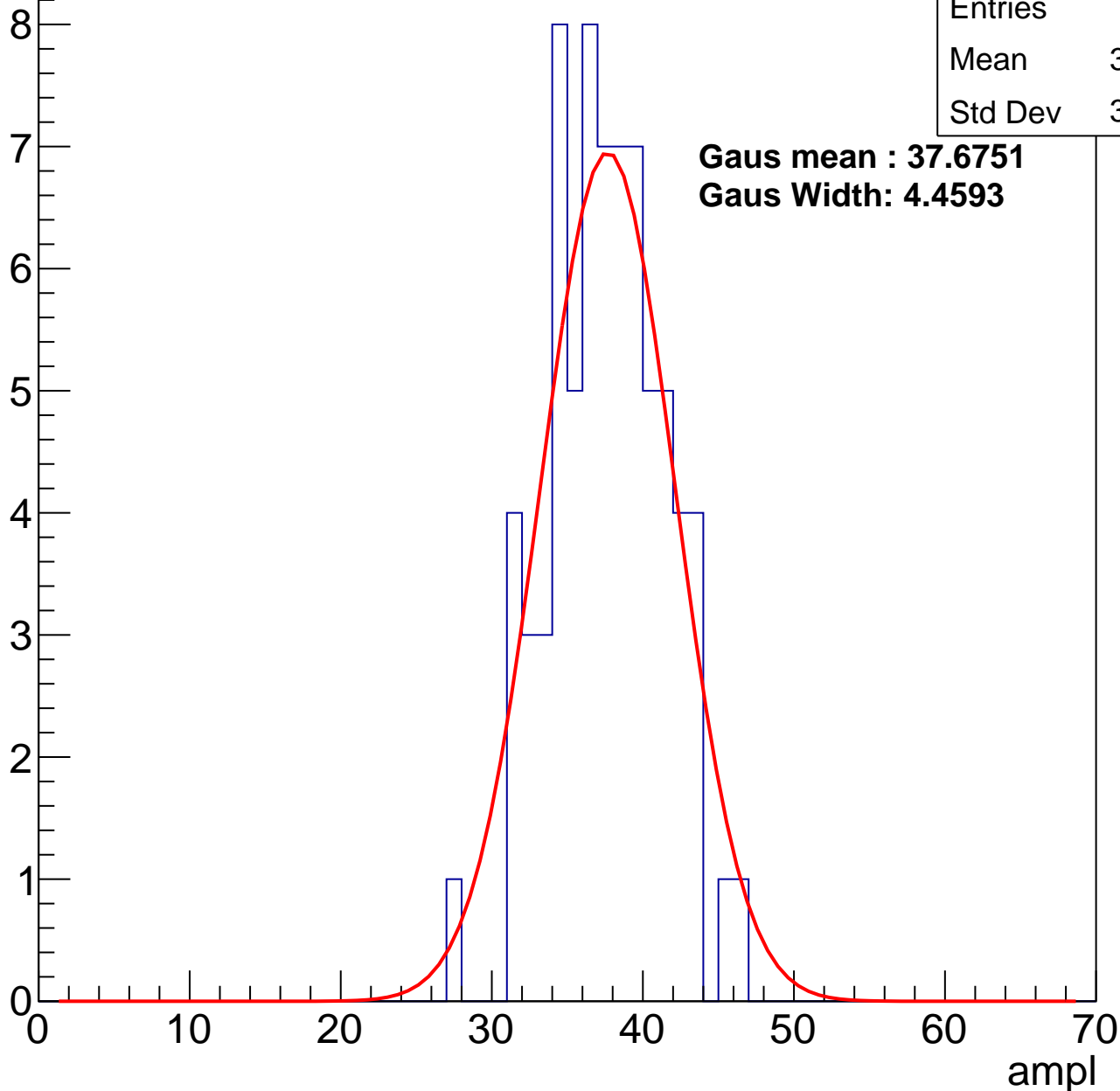
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	37.19
Std Dev	3.726

Gaus mean : 37.6751

Gaus Width: 4.4593



B0L001S, U22-ch111, adc2

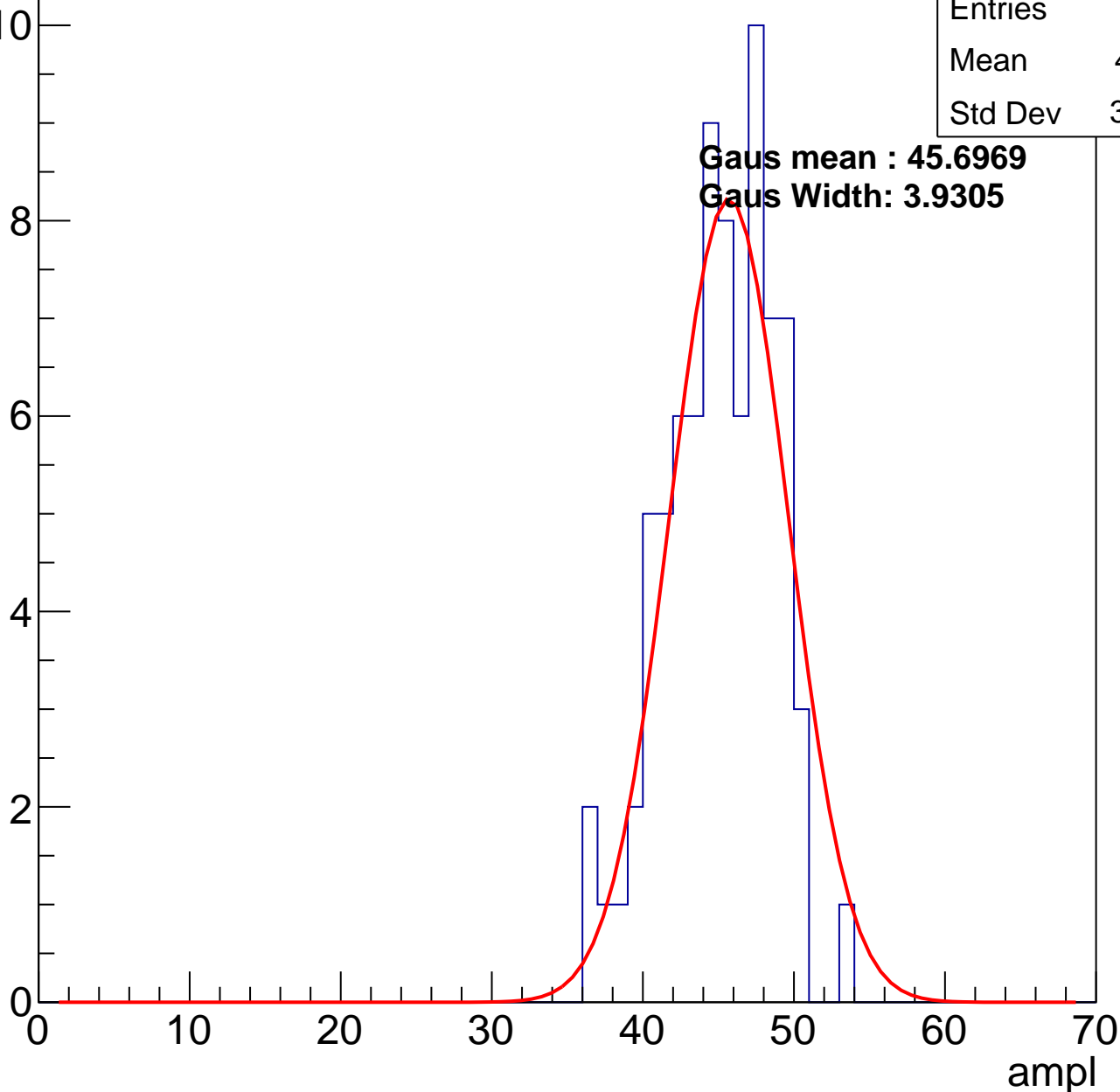
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	44.61
Std Dev	3.534

Gaus mean : 45.6969

Gaus Width: 3.9305

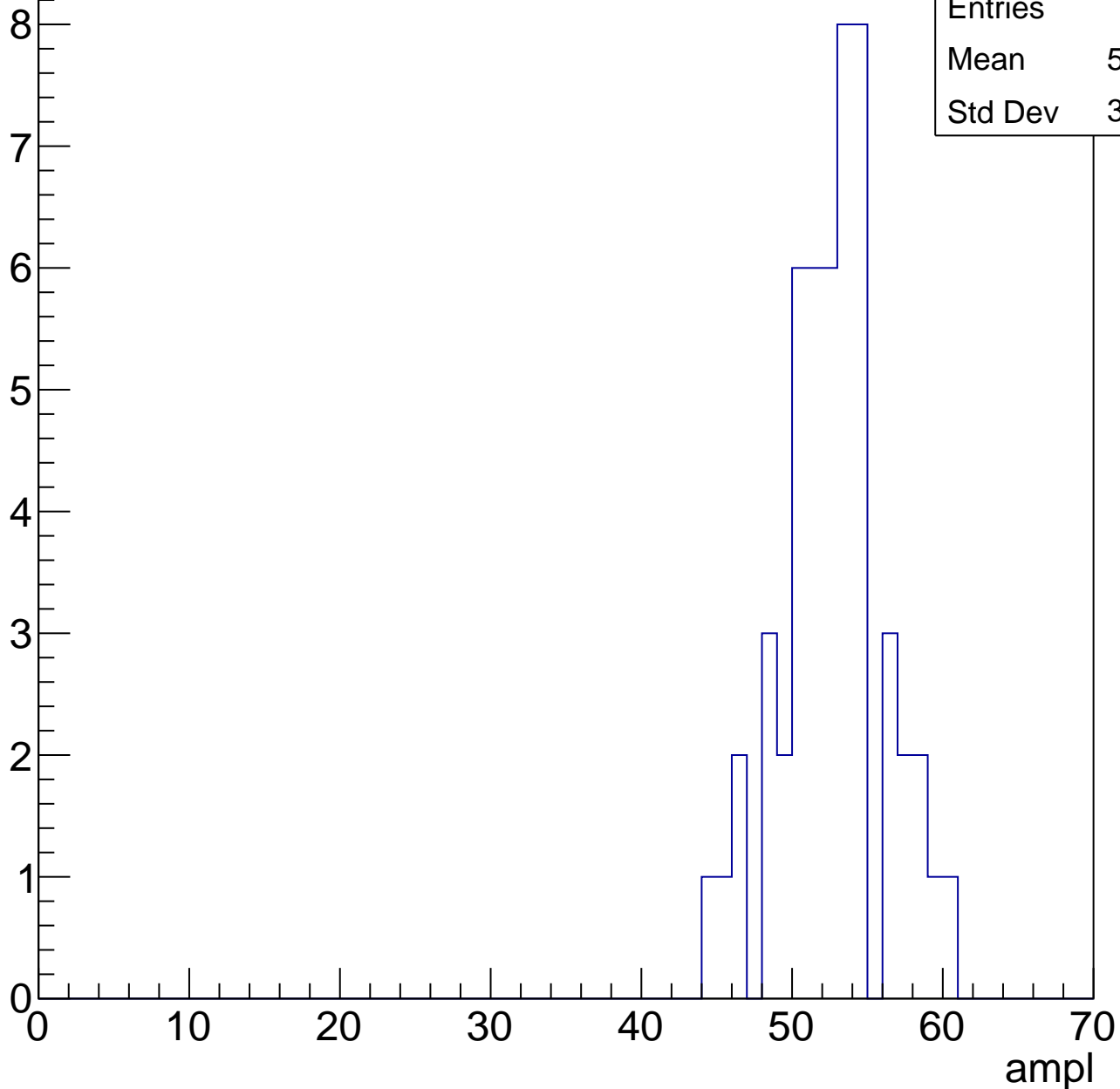


B0L001S, U22-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	52.19
Std Dev	3.403

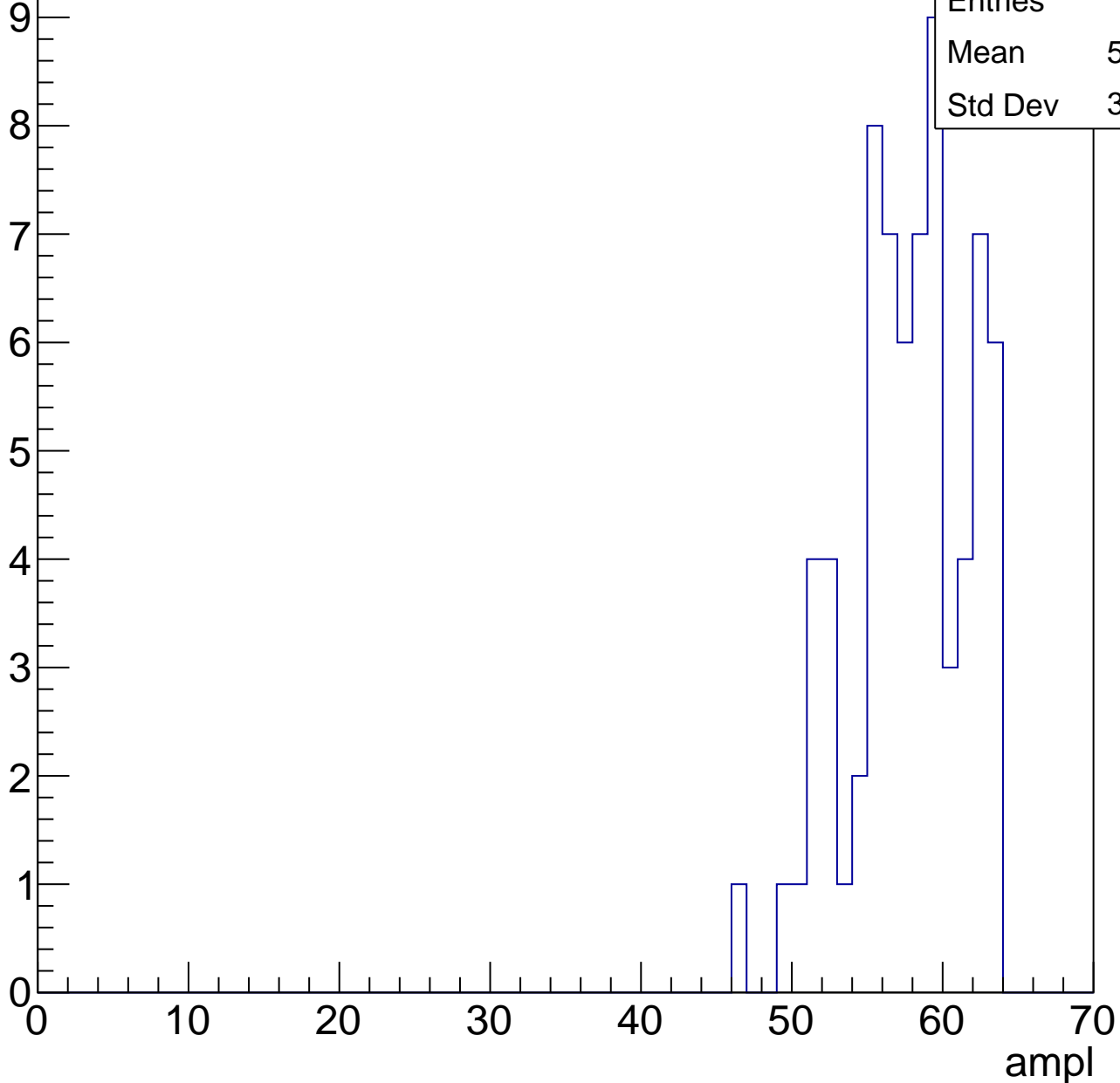


B0L001S, U22-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	57.25
Std Dev	3.885

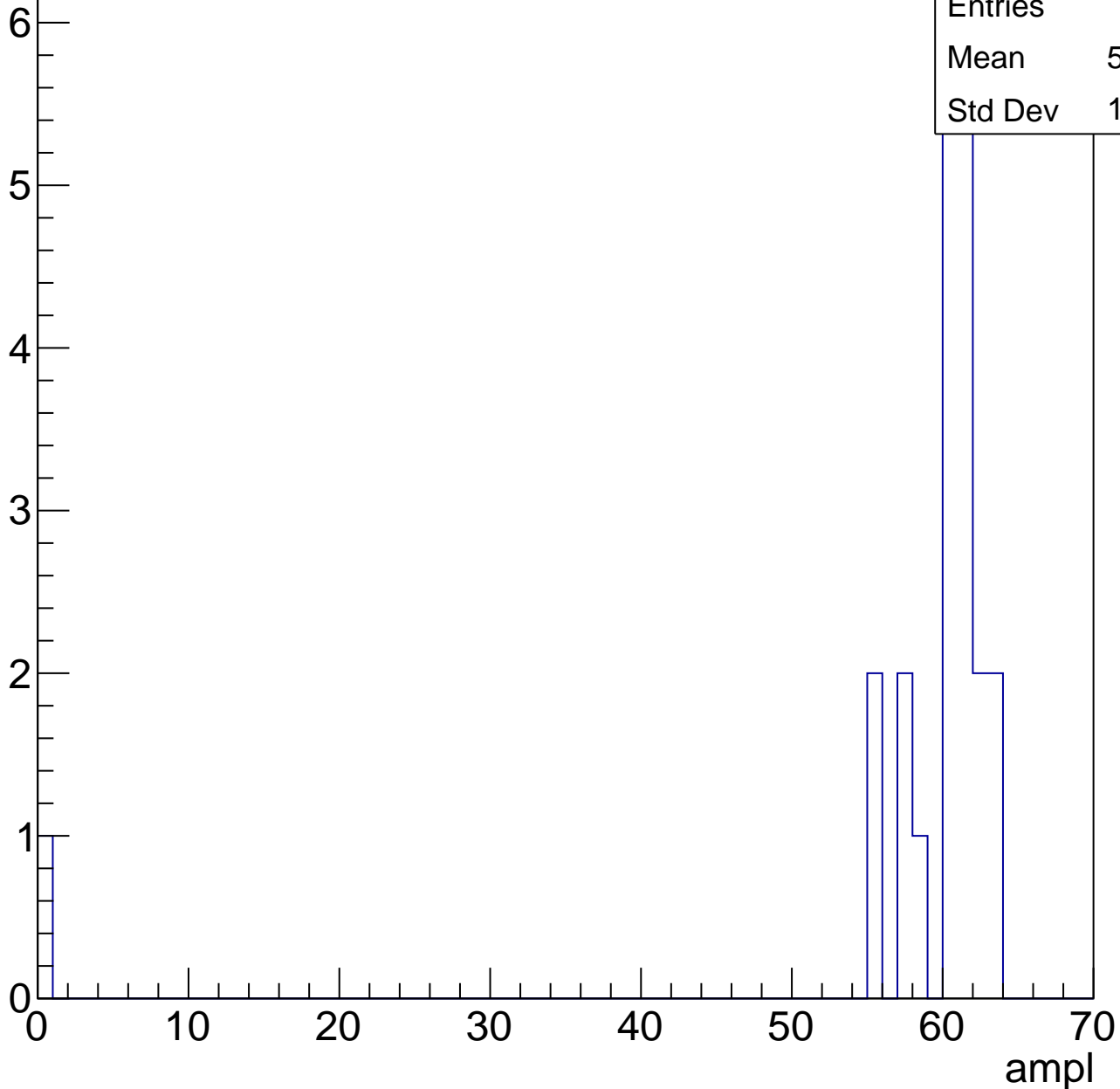


B0L001S, U22-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	57.18
Std Dev	12.67



B0L001S, U22-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	62.67
Std Dev	0.4714

B0L001S, U22-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch112, adc0

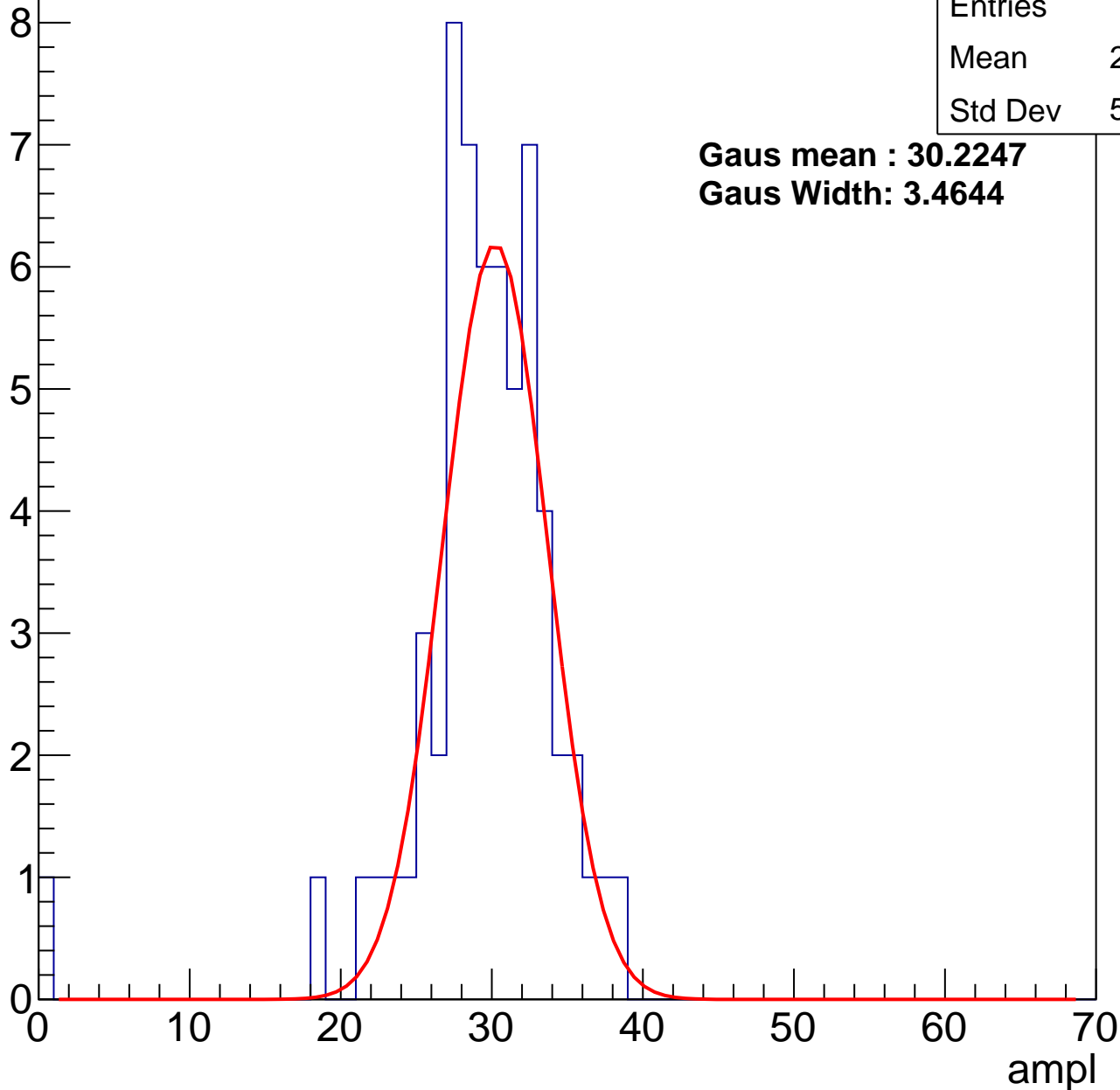
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	28.87
Std Dev	5.293

Gaus mean : 30.2247

Gaus Width: 3.4644



B0L001S, U22-ch112, adc1

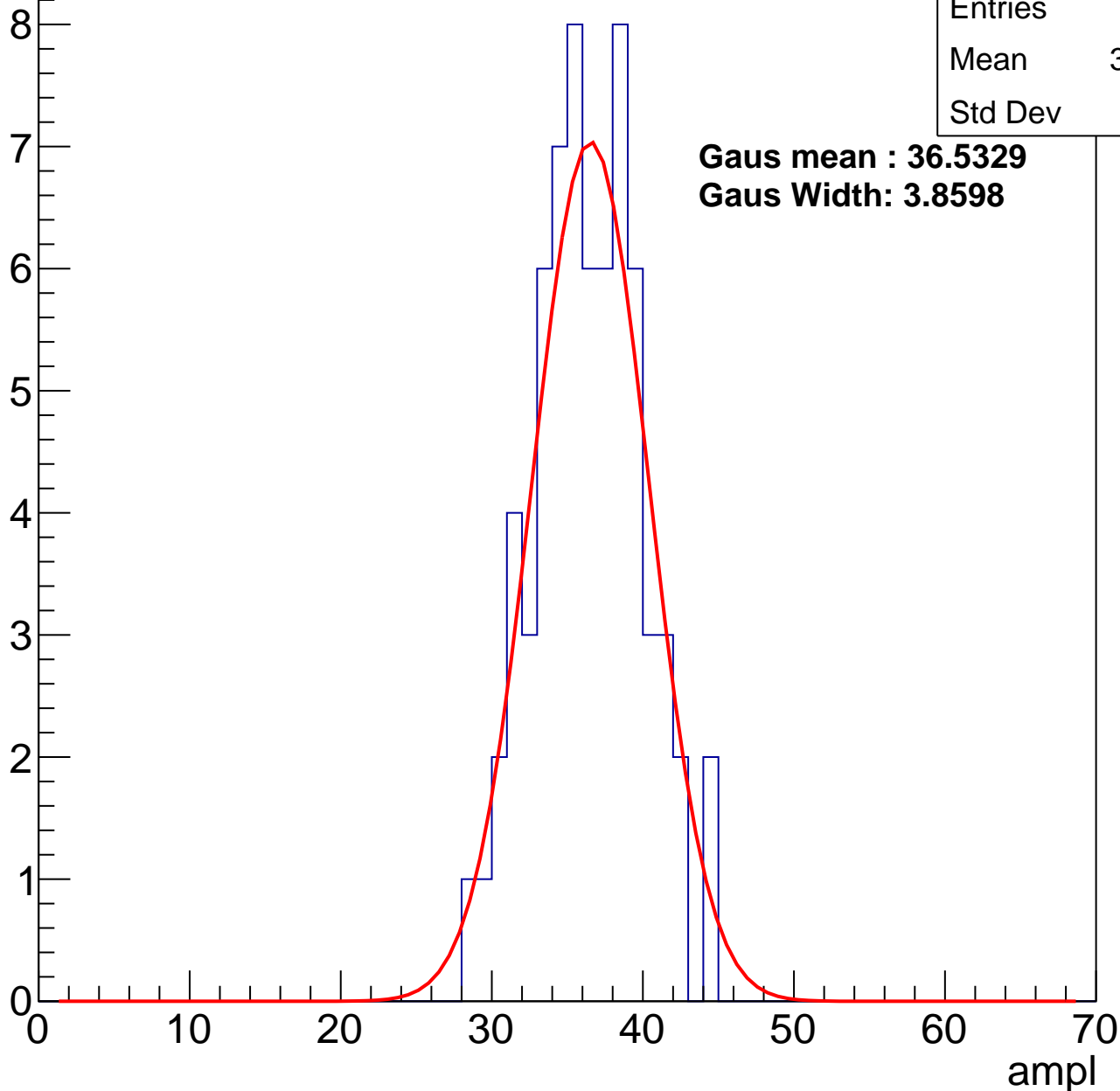
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	35.94
Std Dev	3.51

Gaus mean : 36.5329

Gaus Width: 3.8598



B0L001S, U22-ch112, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	42.77
Std Dev	3.579

Gaus mean : 43.8365

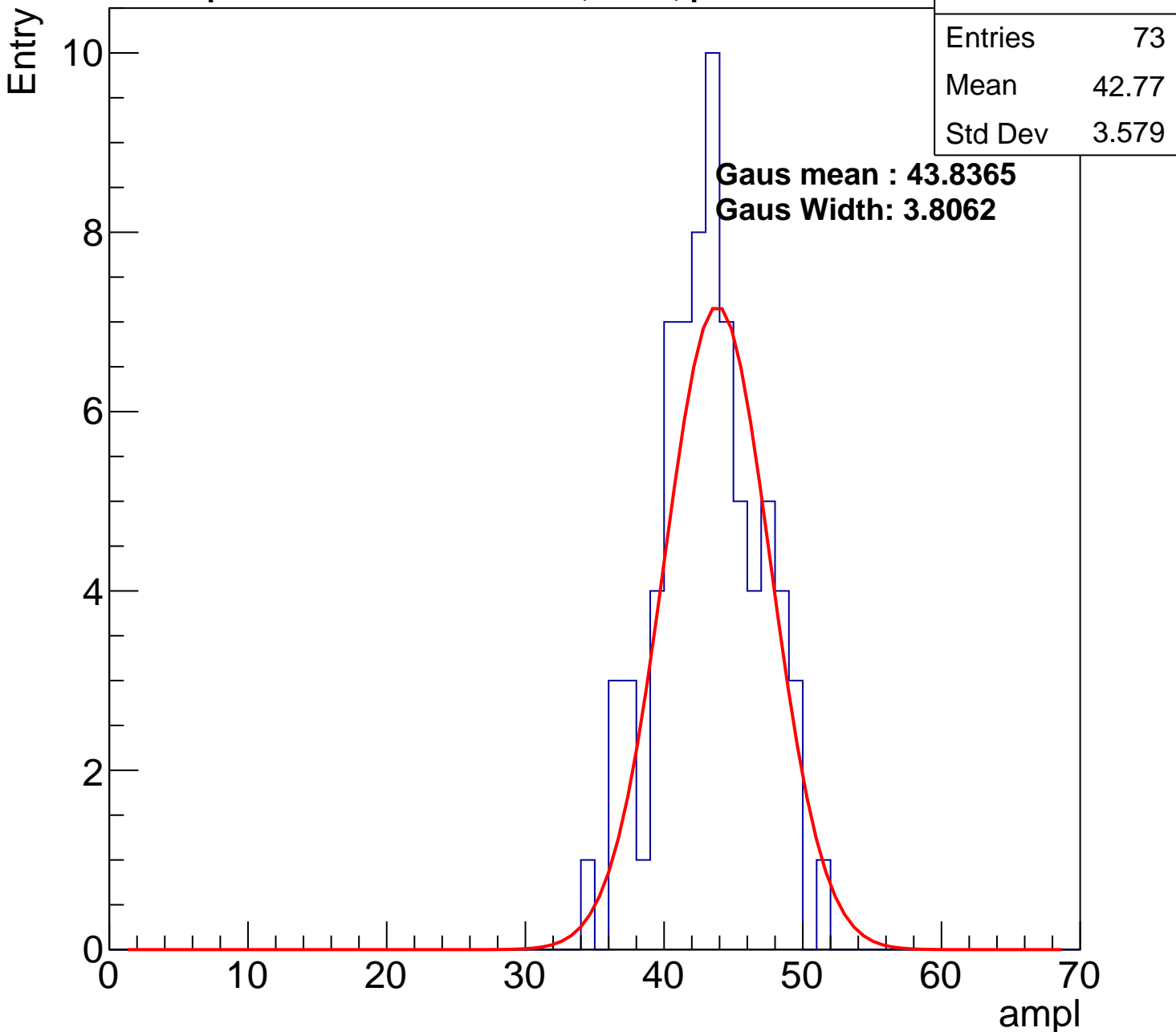
Gaus Width: 3.8062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

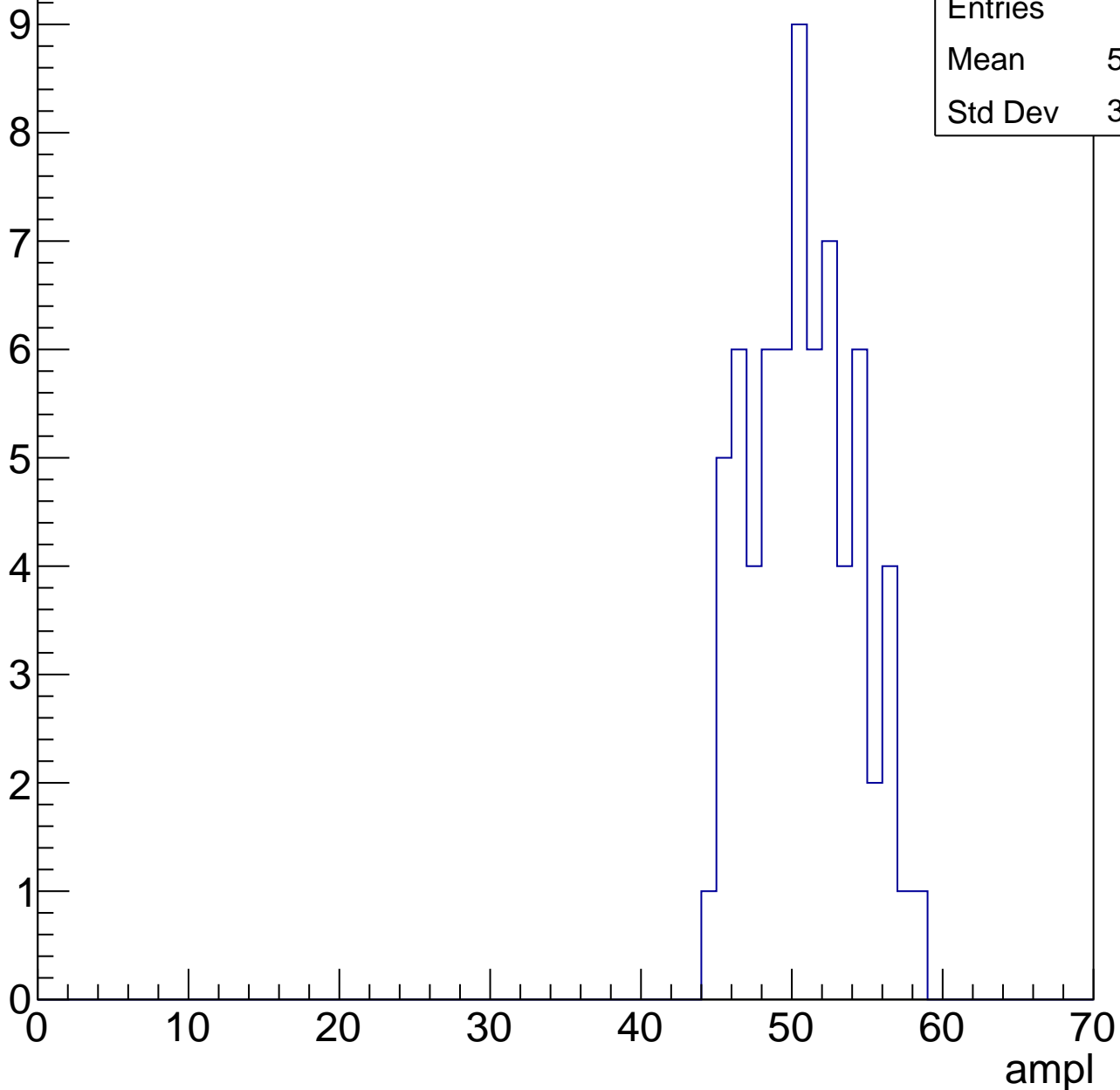


B0L001S, U22-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	50.29
Std Dev	3.409

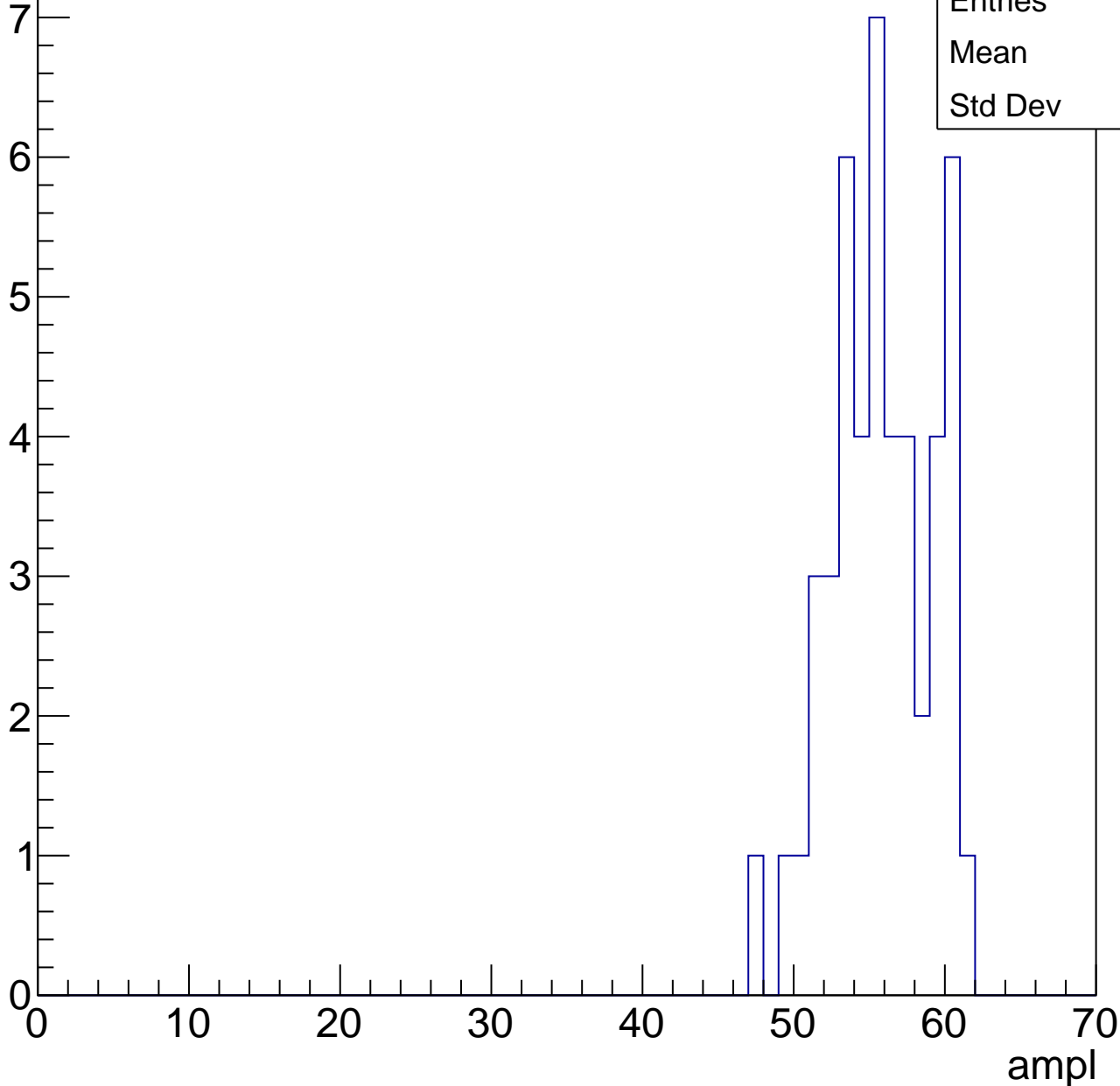


B0L001S, U22-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	55.3
Std Dev	3.3

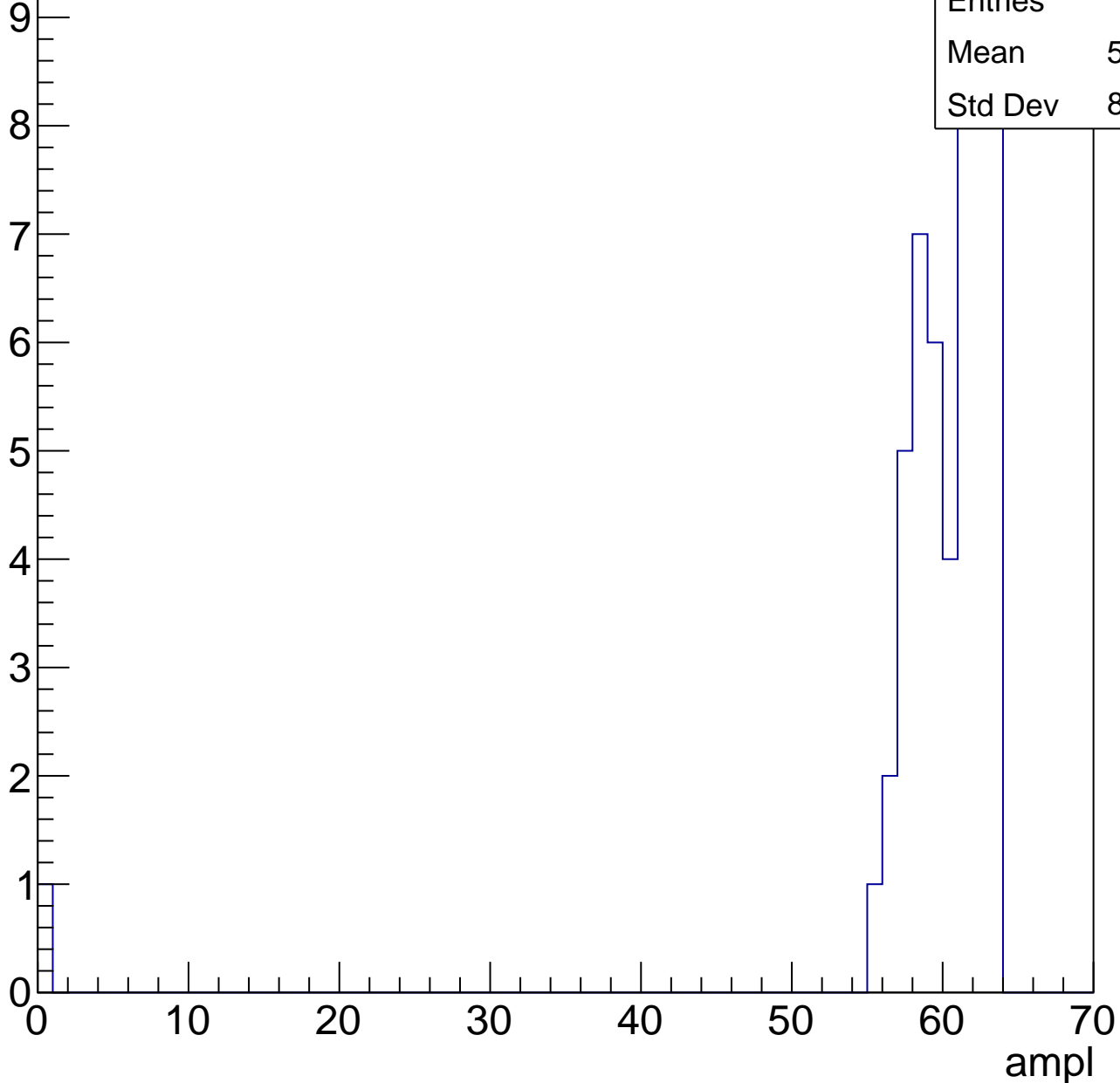


B0L001S, U22-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.98
Std Dev	8.475



B0L001S, U22-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch113, adc0

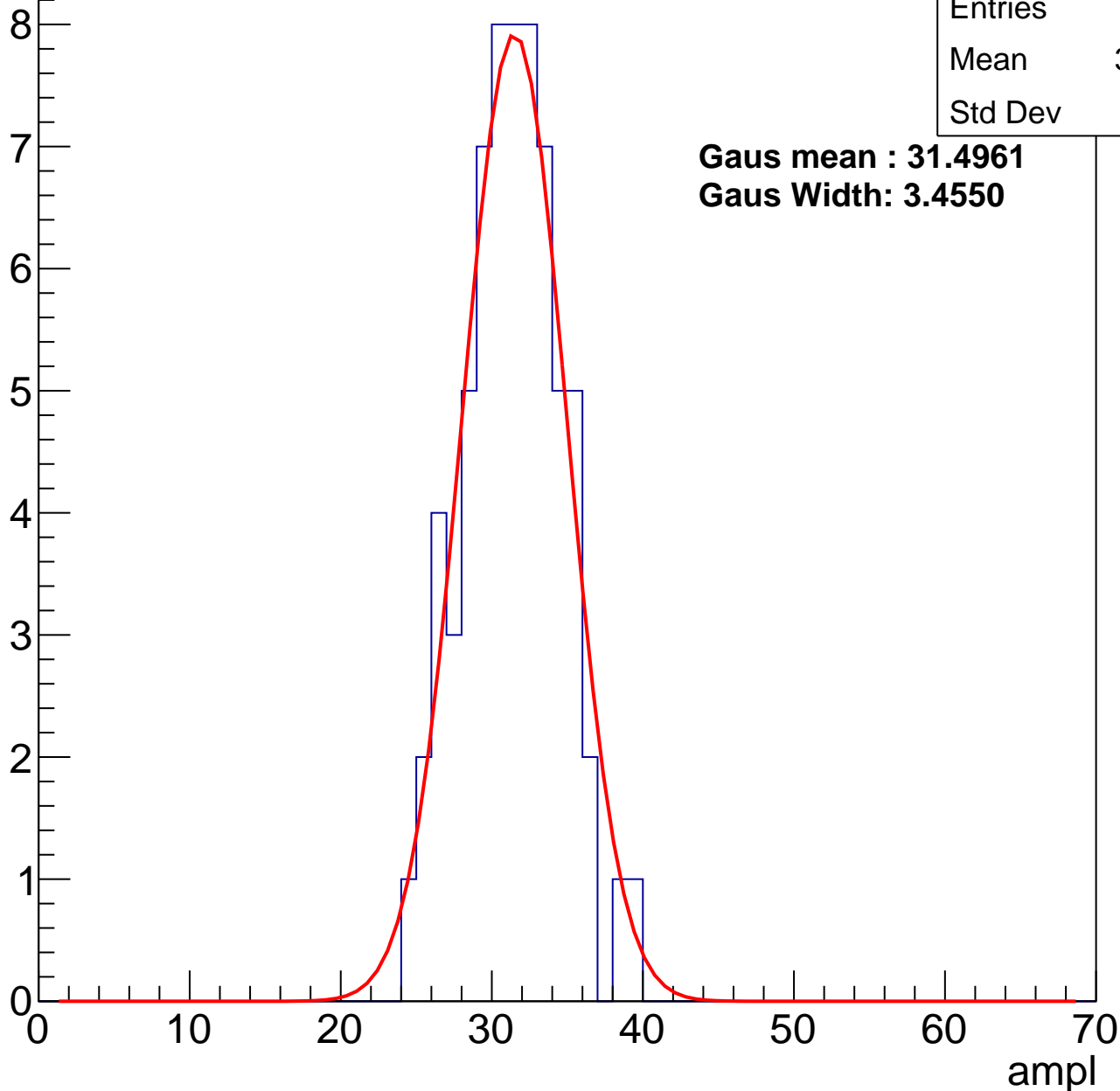
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.91
Std Dev	3.17

Gaus mean : 31.4961

Gaus Width: 3.4550



B0L001S, U22-ch113, adc1

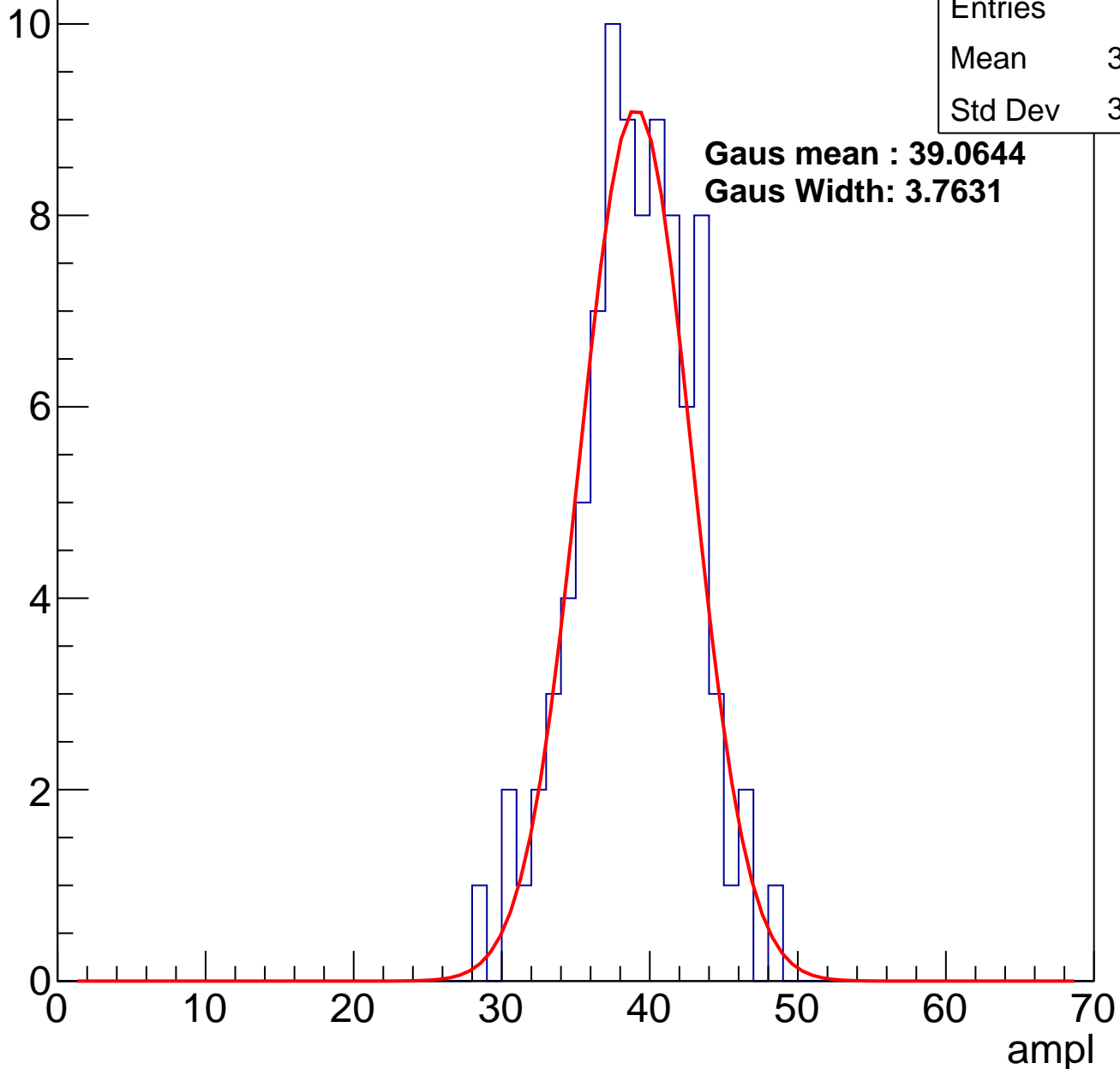
calib_packv5_042523_0143.root, FC#9, port A1

Entries	90
Mean	38.56
Std Dev	3.859

Gaus mean : 39.0644

Gaus Width: 3.7631

Entry



B0L001S, U22-ch113, adc2

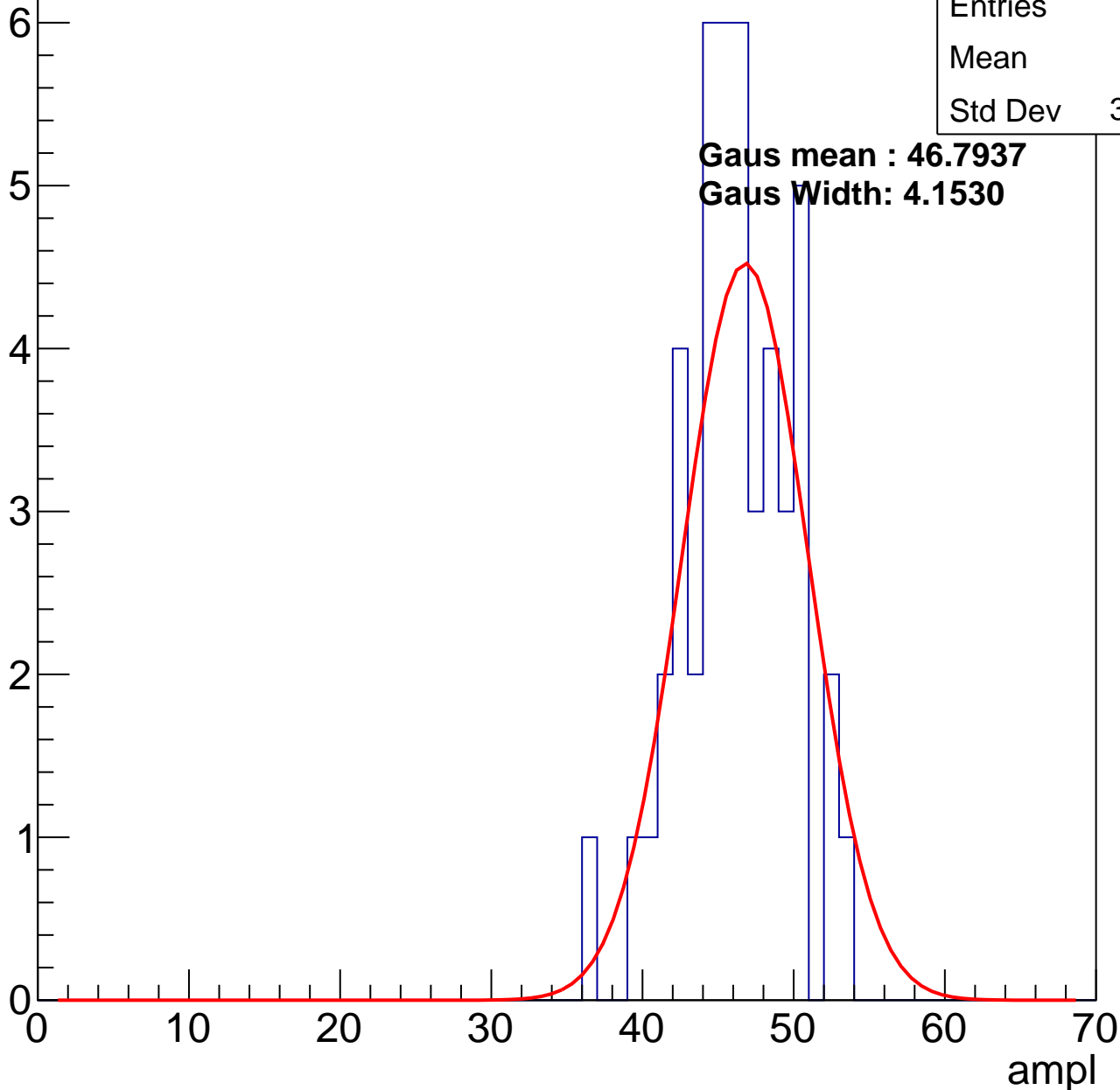
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	45.7
Std Dev	3.543

Gaus mean : 46.7937

Gaus Width: 4.1530

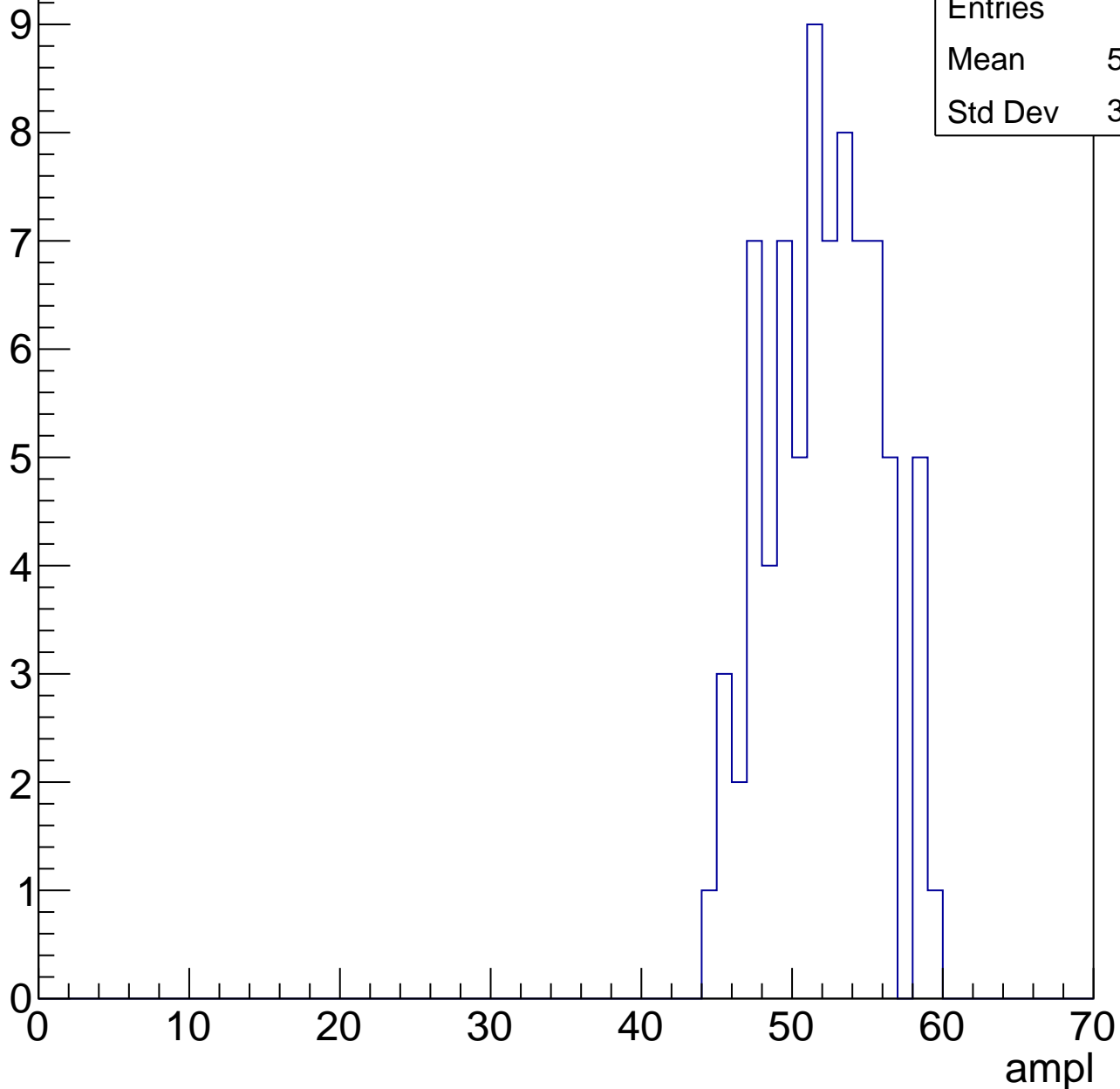


B0L001S, U22-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	51.59
Std Dev	3.596

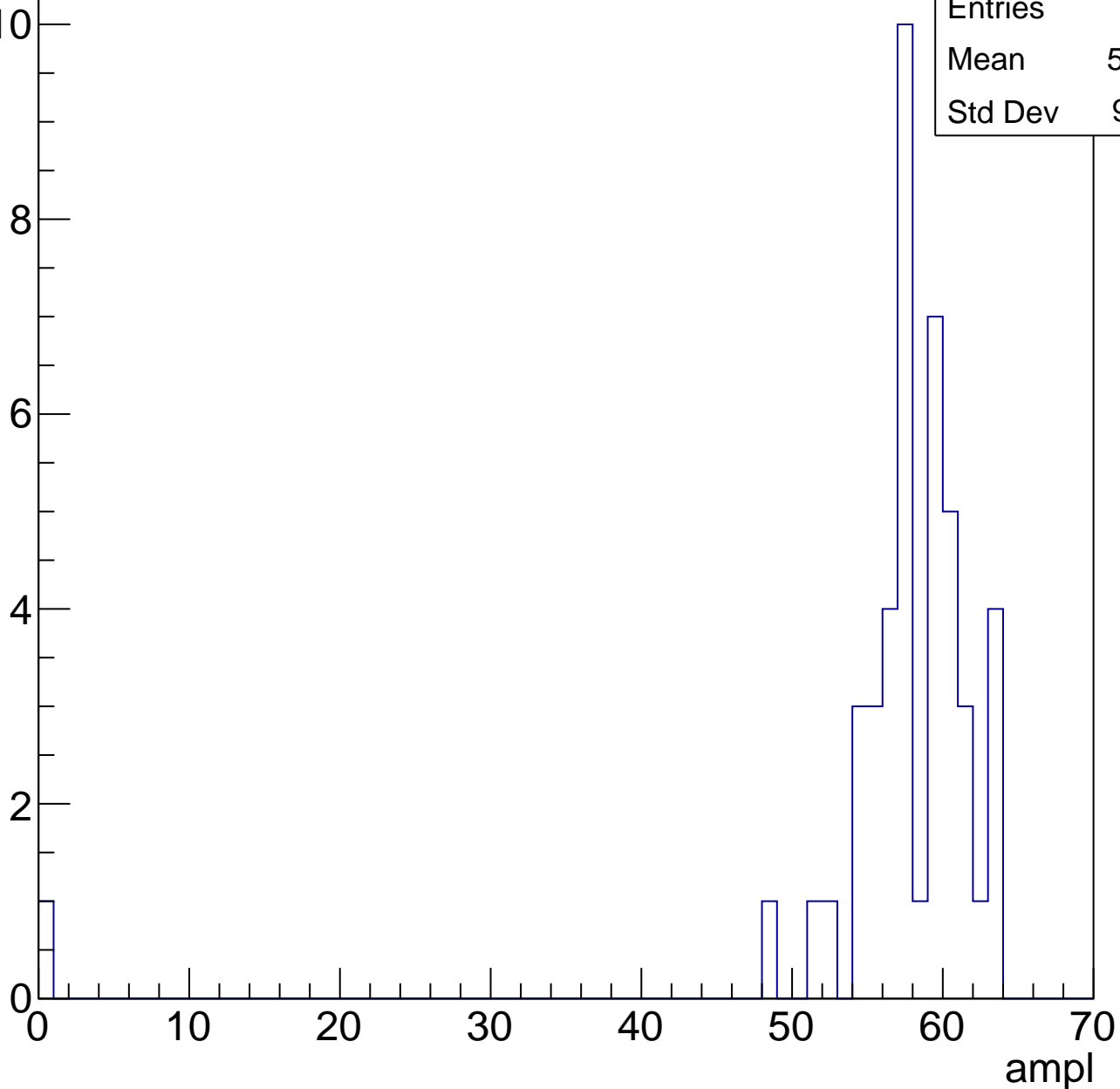


B0L001S, U22-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	56.44
Std Dev	9.081

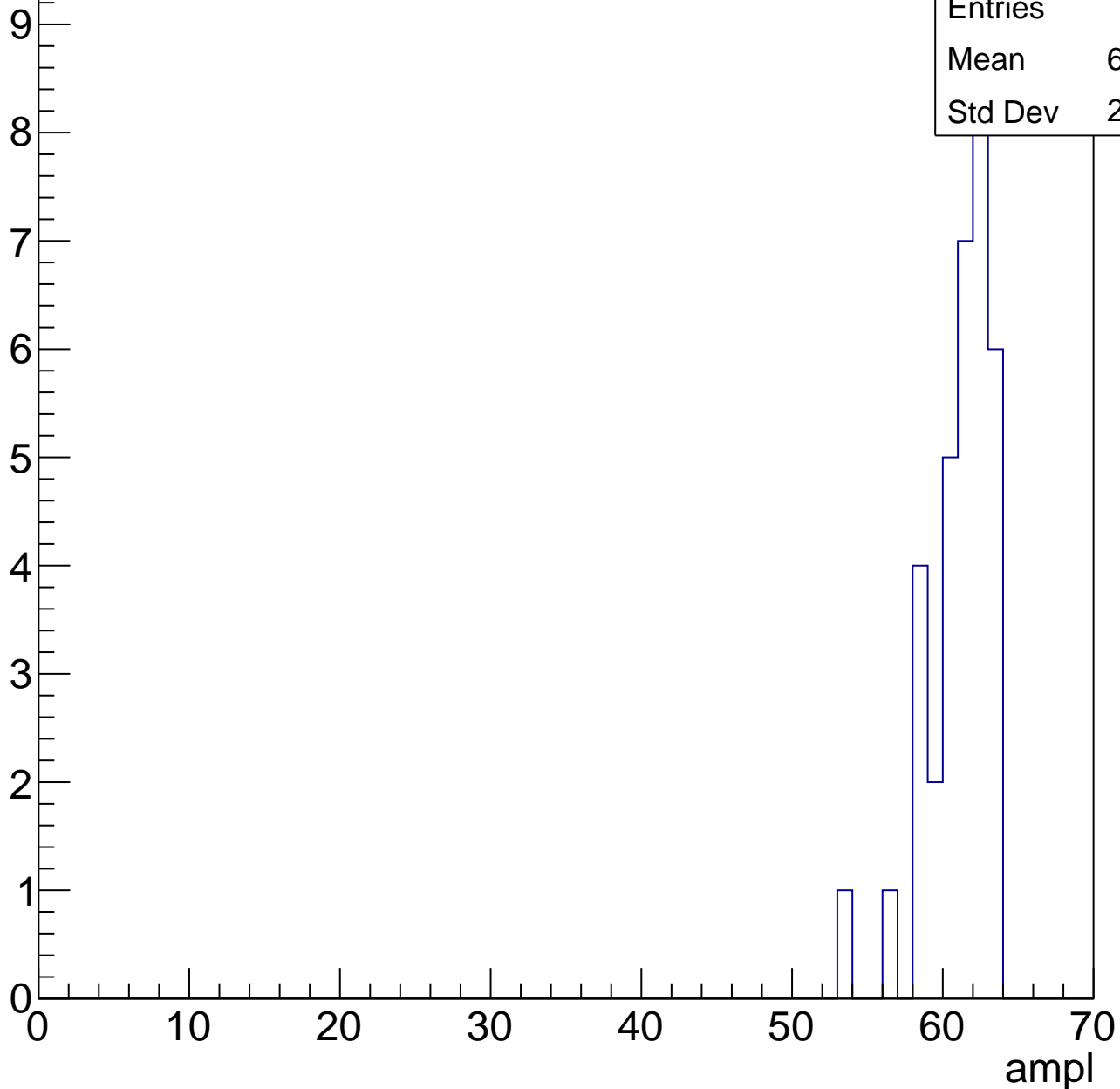


B0L001S, U22-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.63
Std Dev	2.179



B0L001S, U22-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch114, adc0

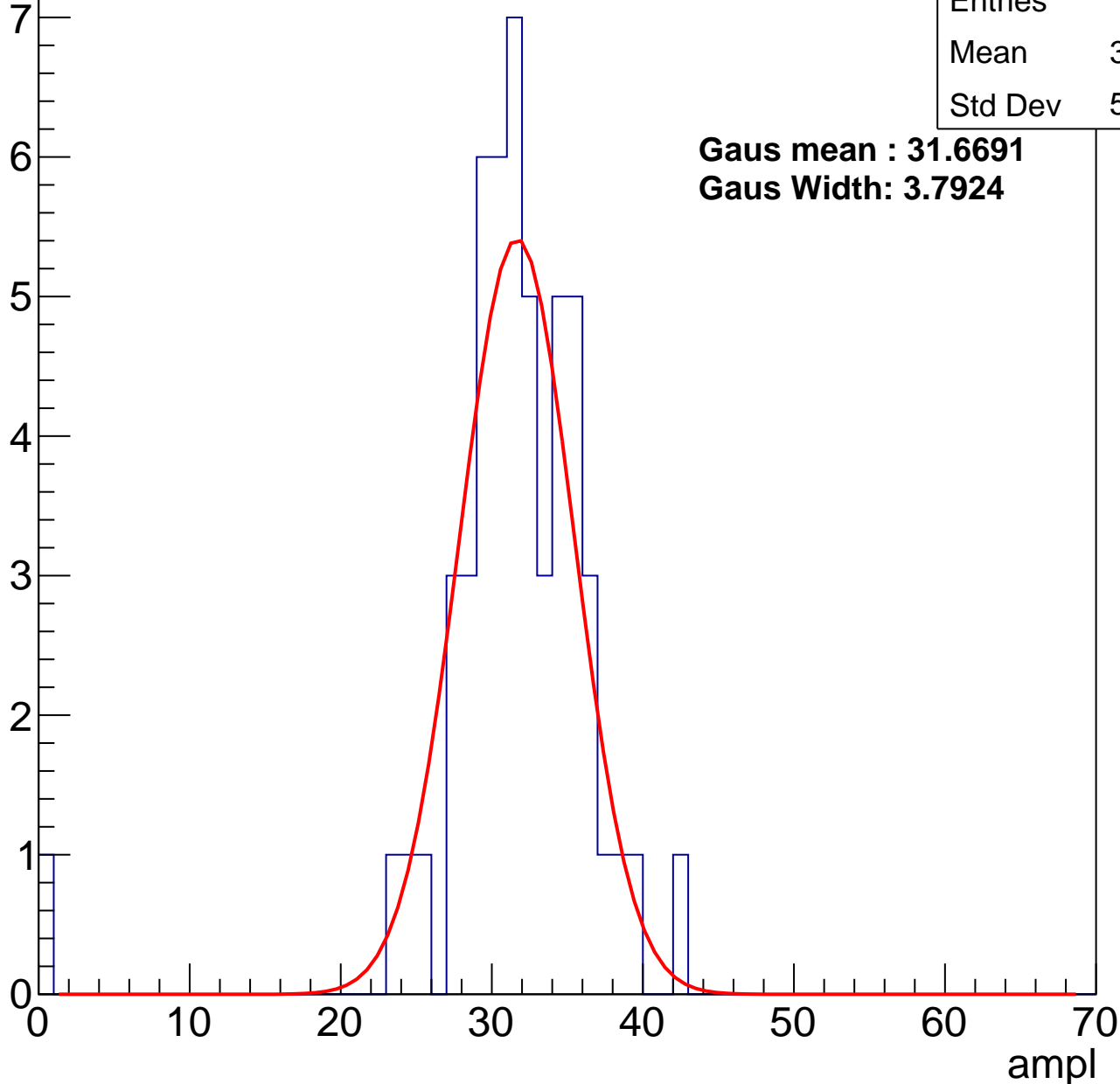
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	31.04
Std Dev	5.614

Gaus mean : 31.6691

Gaus Width: 3.7924



B0L001S, U22-ch114, adc1

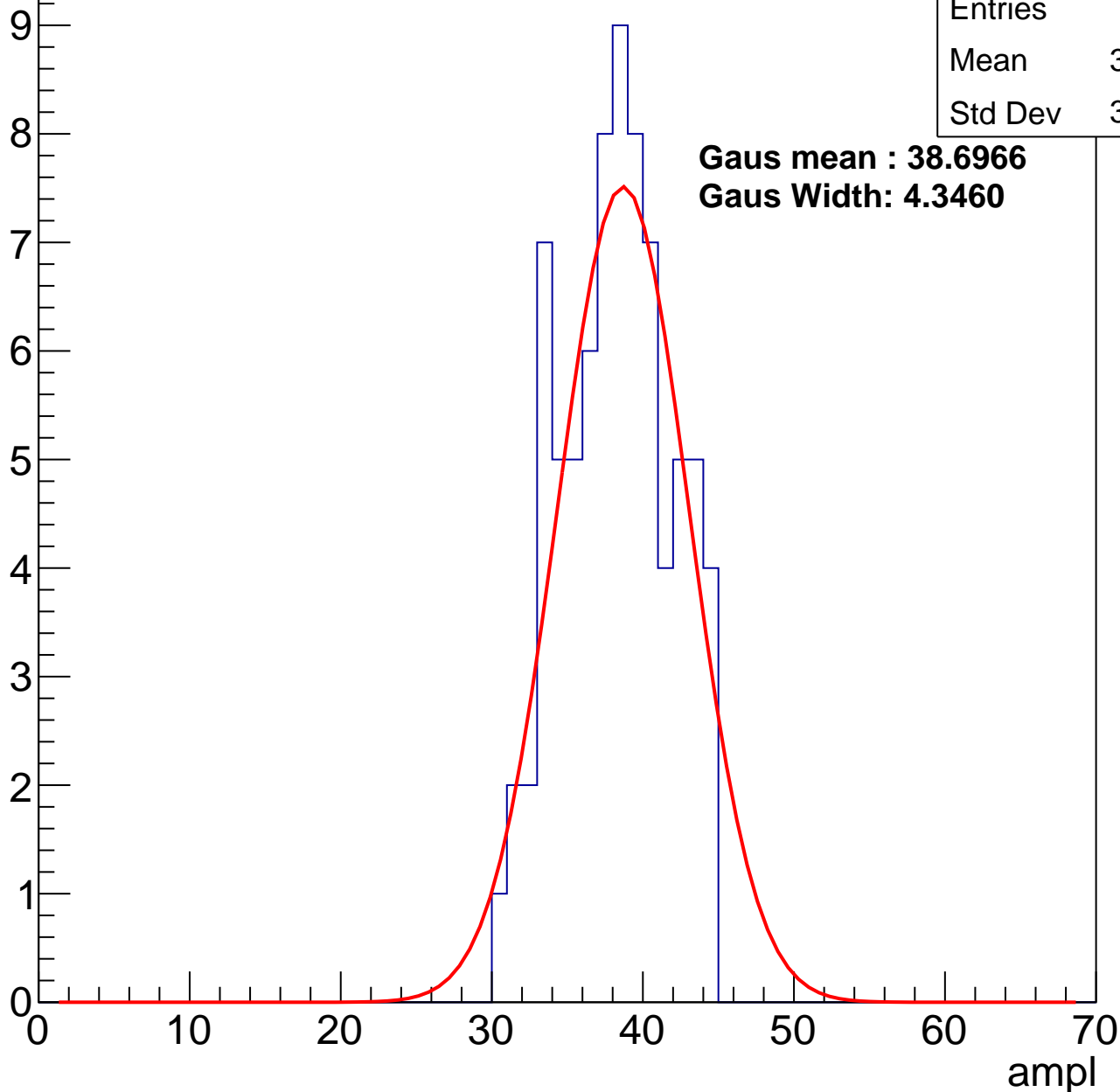
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.73
Std Dev	3.547

Gaus mean : 38.6966

Gaus Width: 4.3460



B0L001S, U22-ch114, adc2

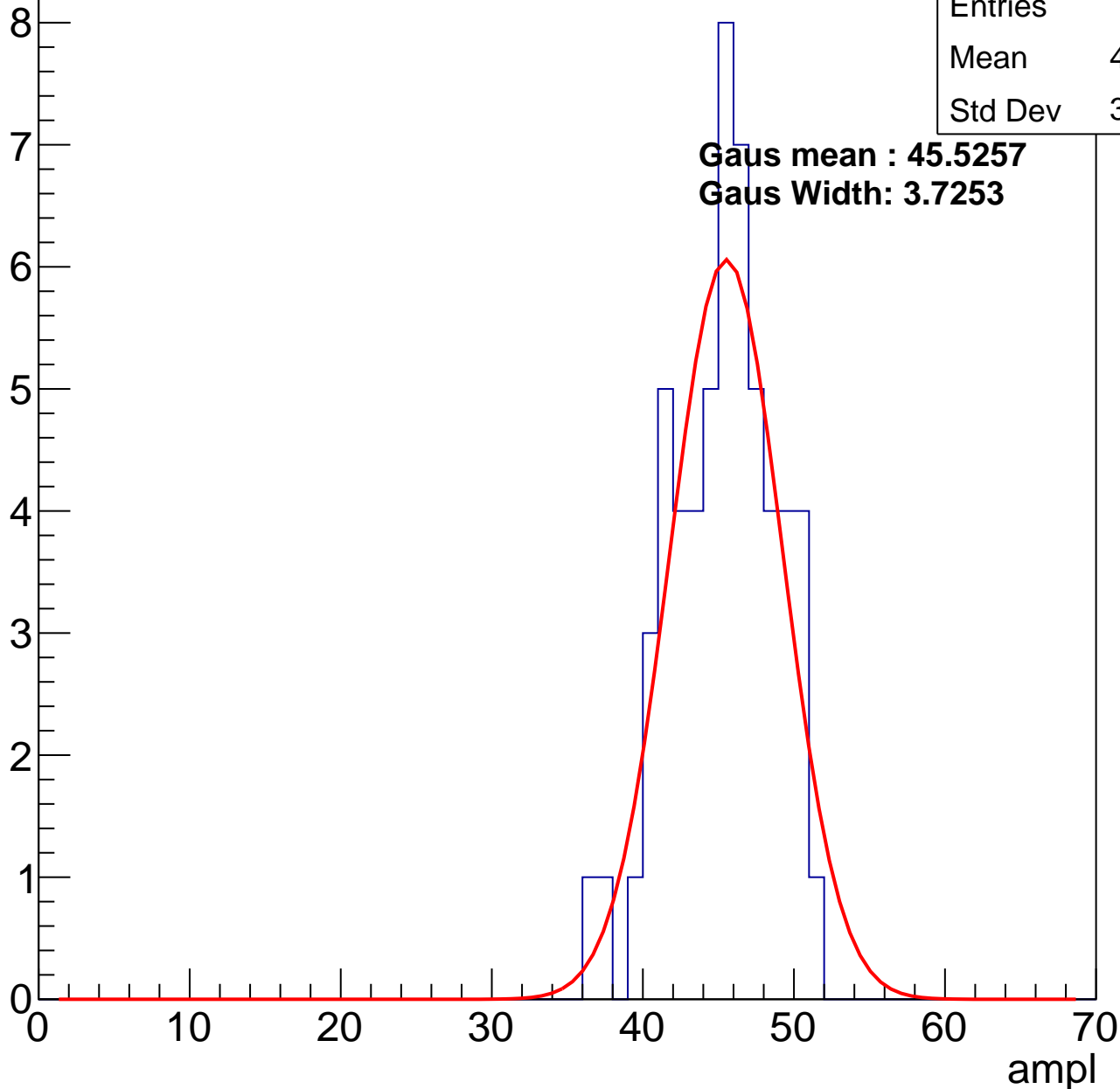
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	44.79
Std Dev	3.386

Gaus mean : 45.5257

Gaus Width: 3.7253

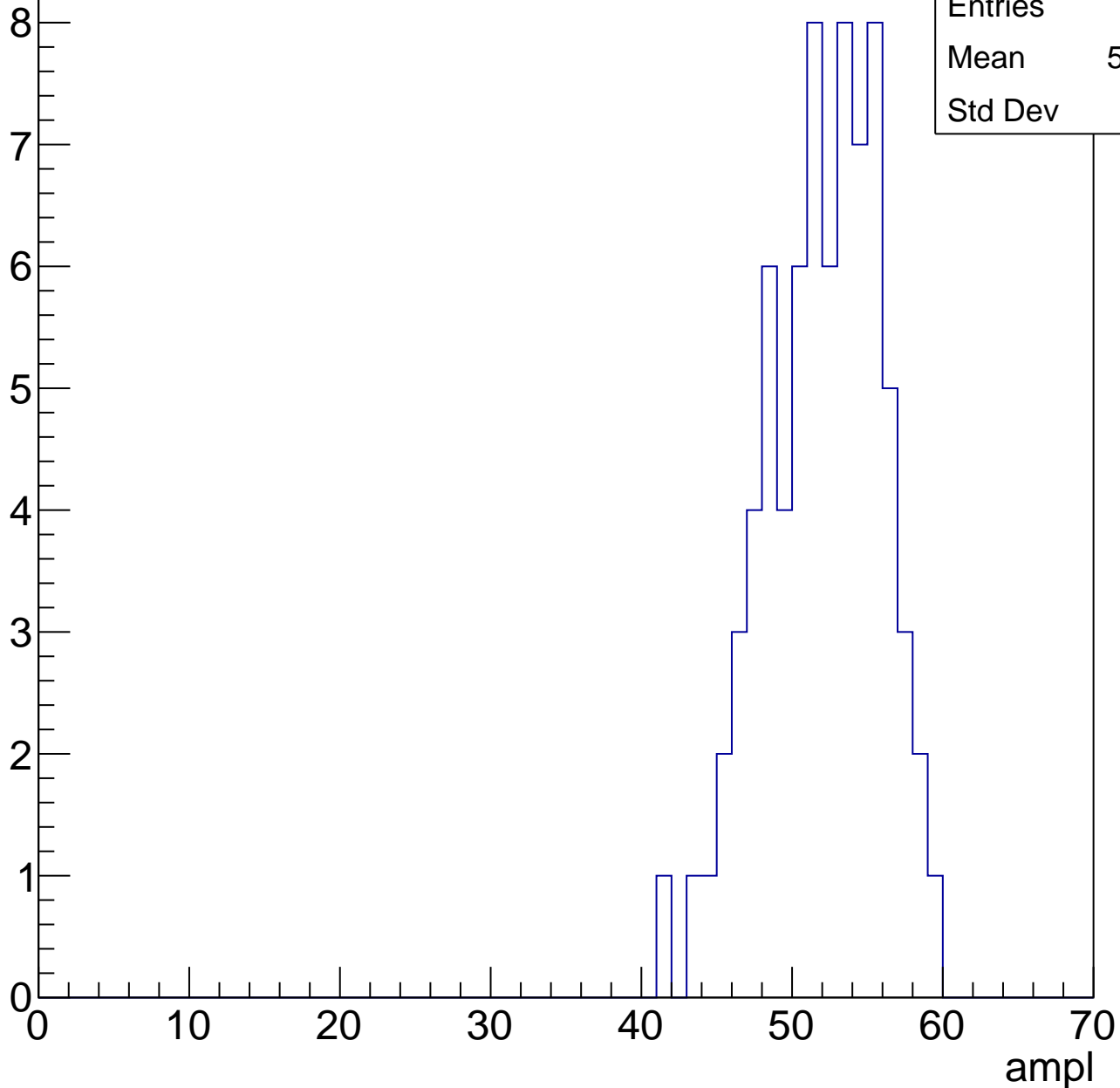


B0L001S, U22-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	51.53
Std Dev	3.81

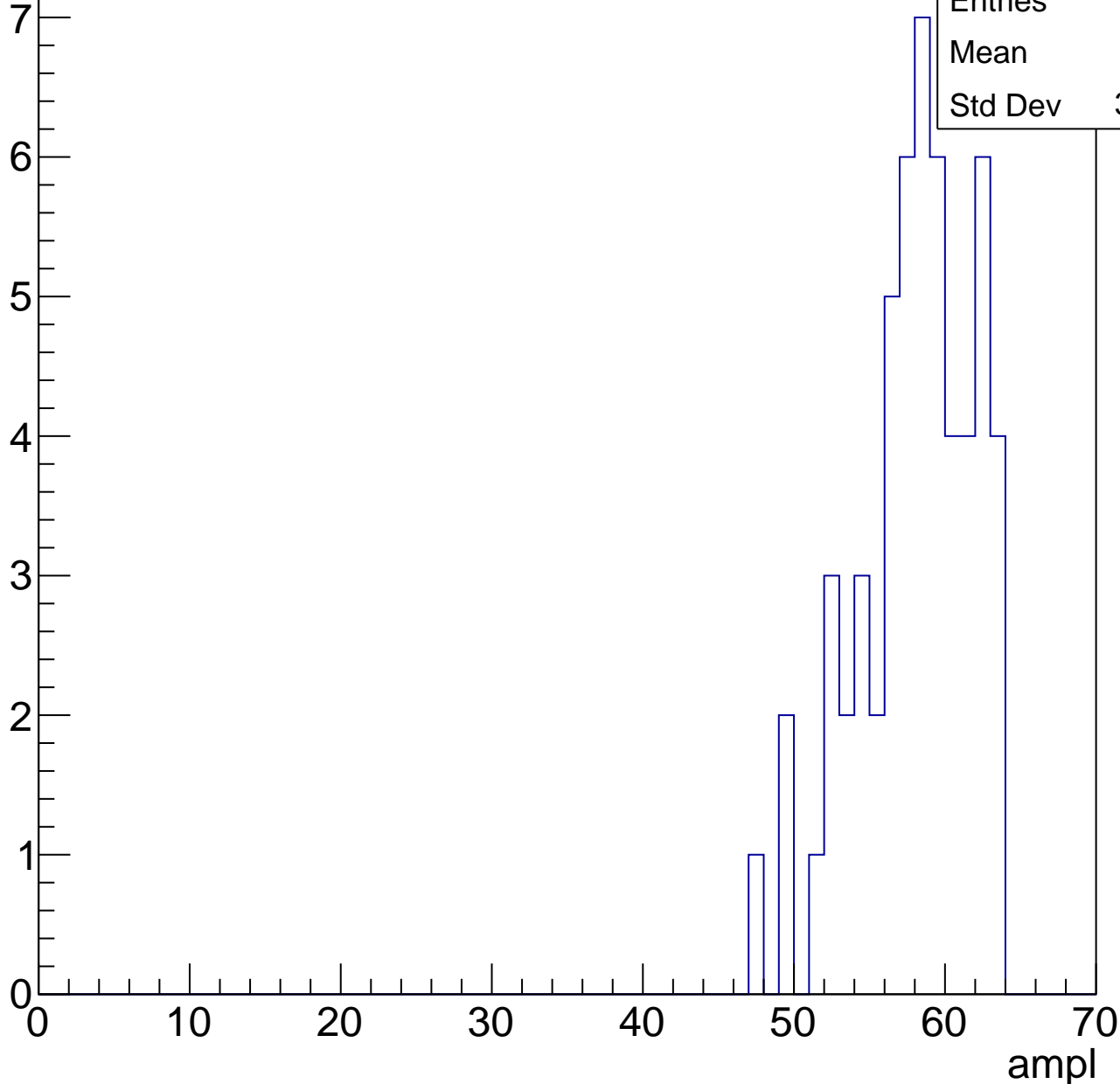


B0L001S, U22-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	57.5
Std Dev	3.831

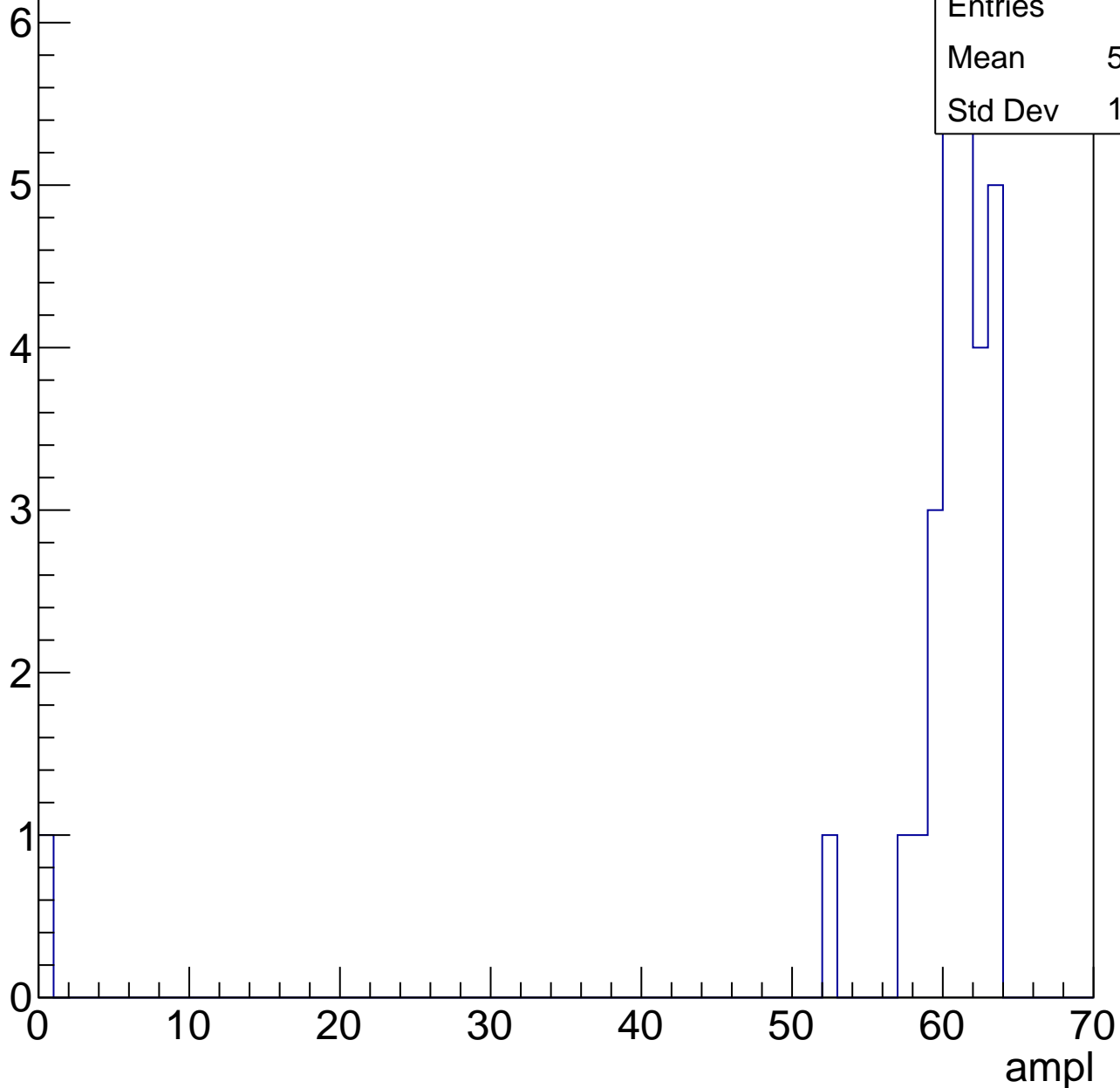


B0L001S, U22-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	58.32
Std Dev	11.45



B0L001S, U22-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

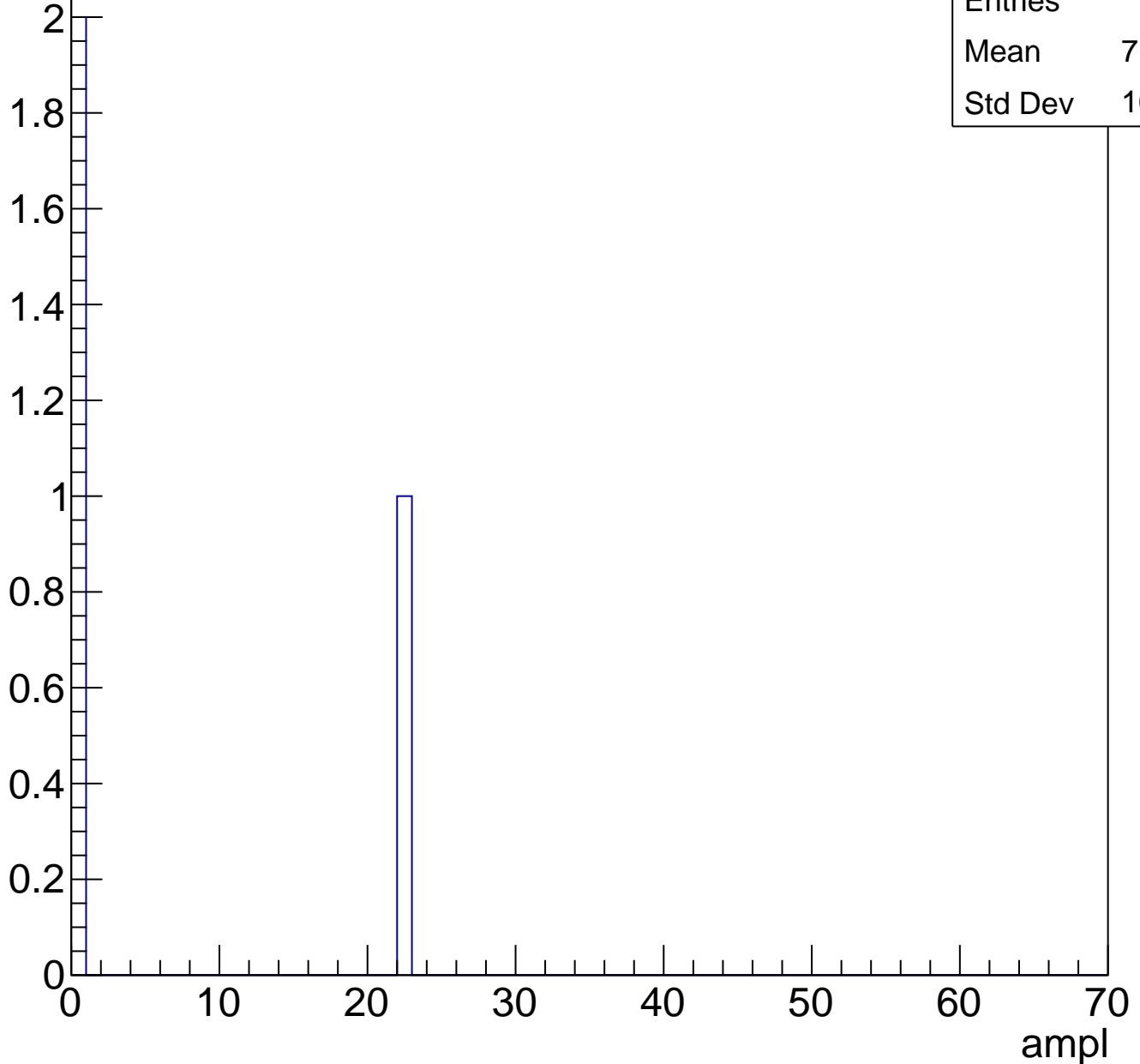
Entry



B0L001S, U22-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L001S, U22-ch115, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	31.8
Std Dev	3.723

Gaus mean : 32.7003

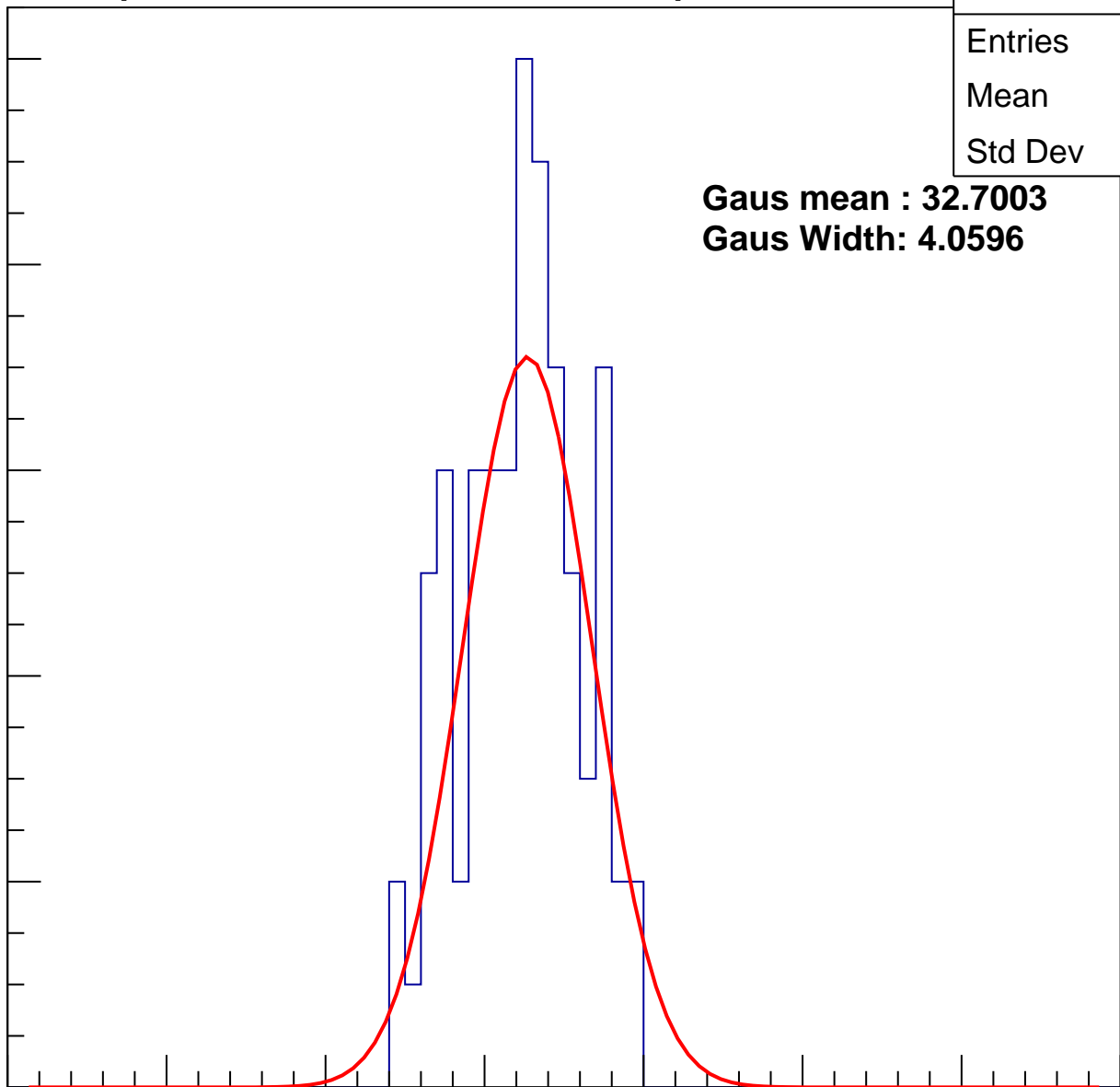
Gaus Width: 4.0596

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch115, adc1

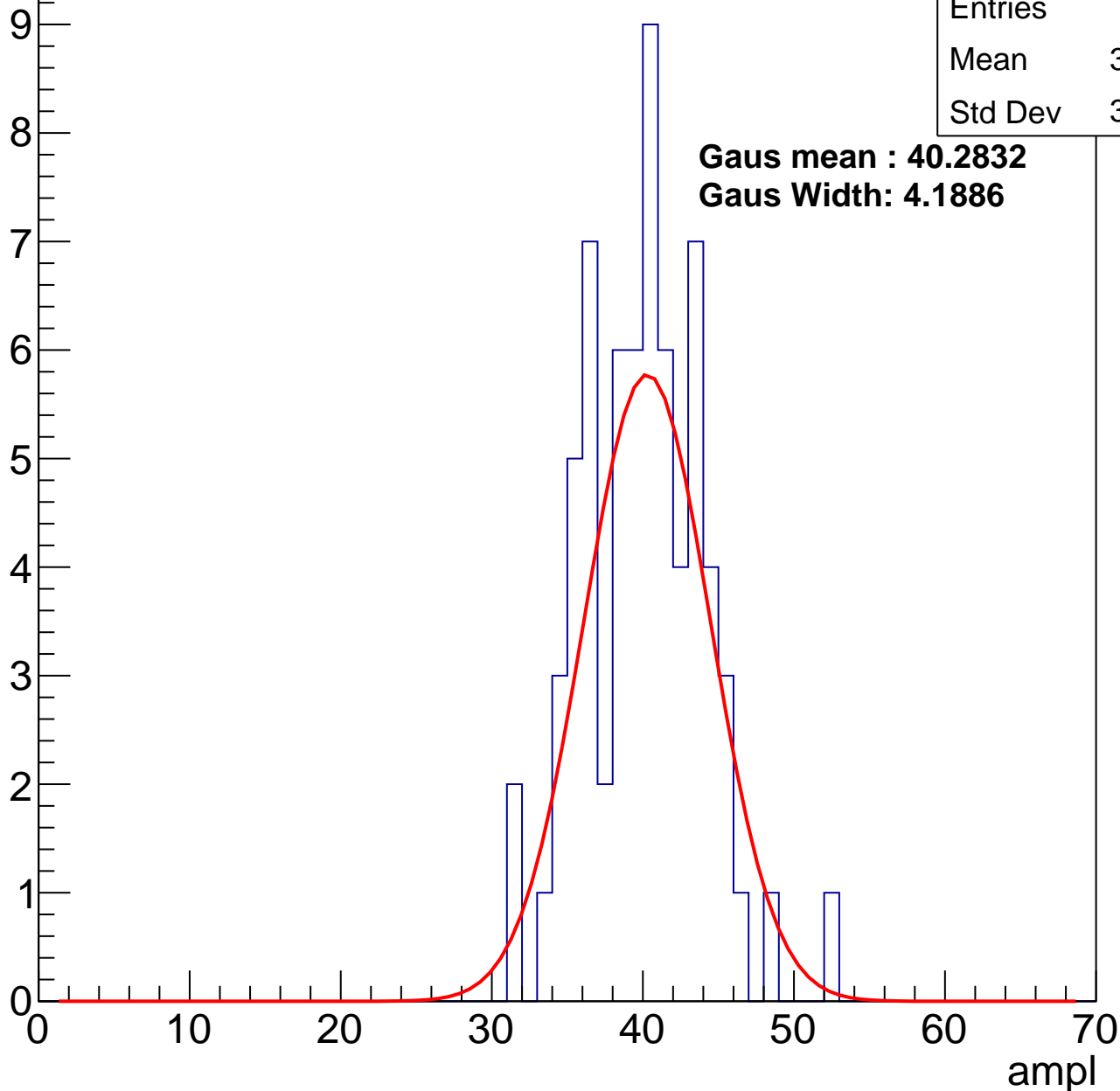
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	39.59
Std Dev	3.949

Gaus mean : 40.2832

Gaus Width: 4.1886



B0L001S, U22-ch115, adc2

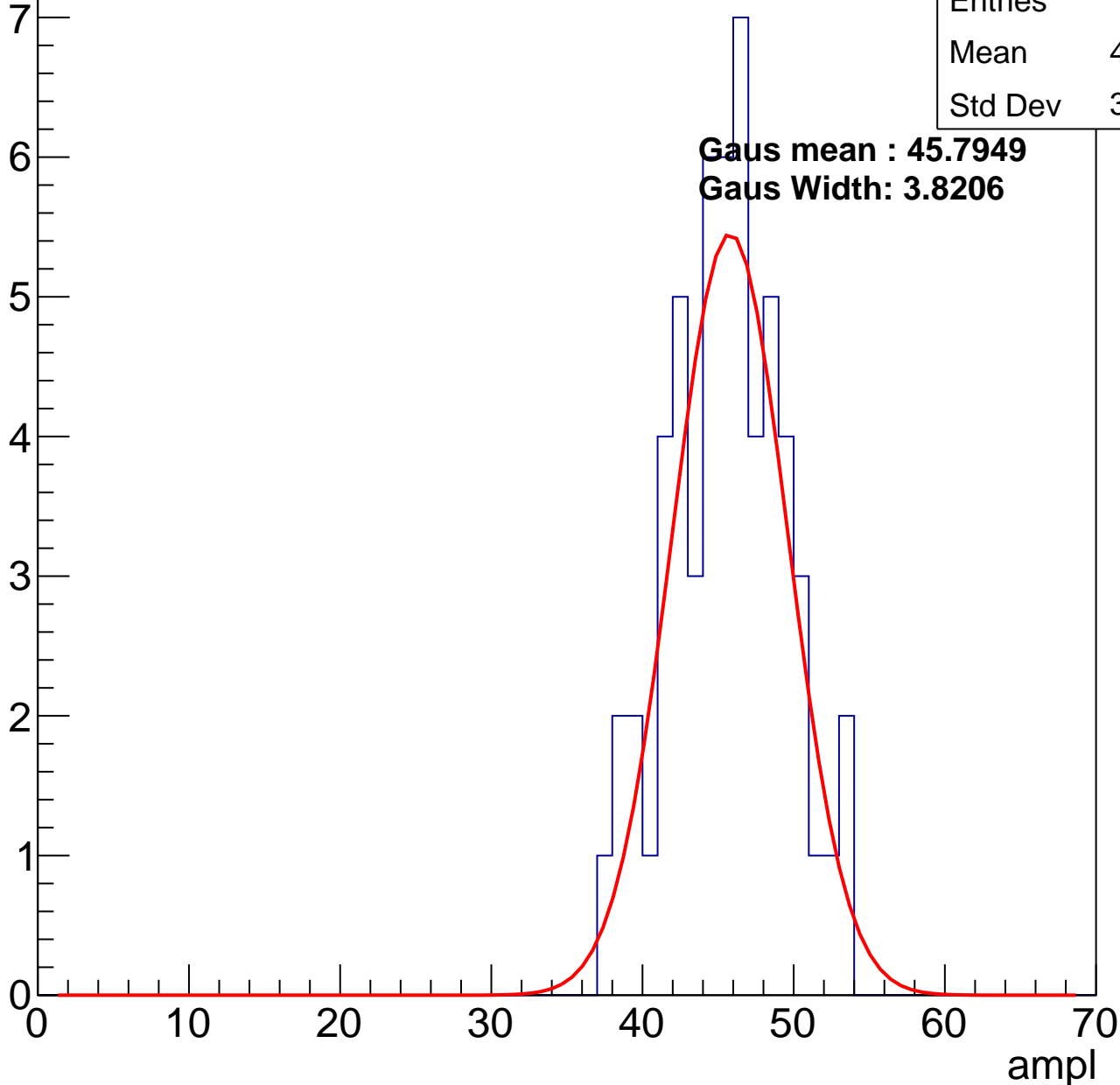
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	45.14
Std Dev	3.758

Gaus mean : 45.7949

Gaus Width: 3.8206

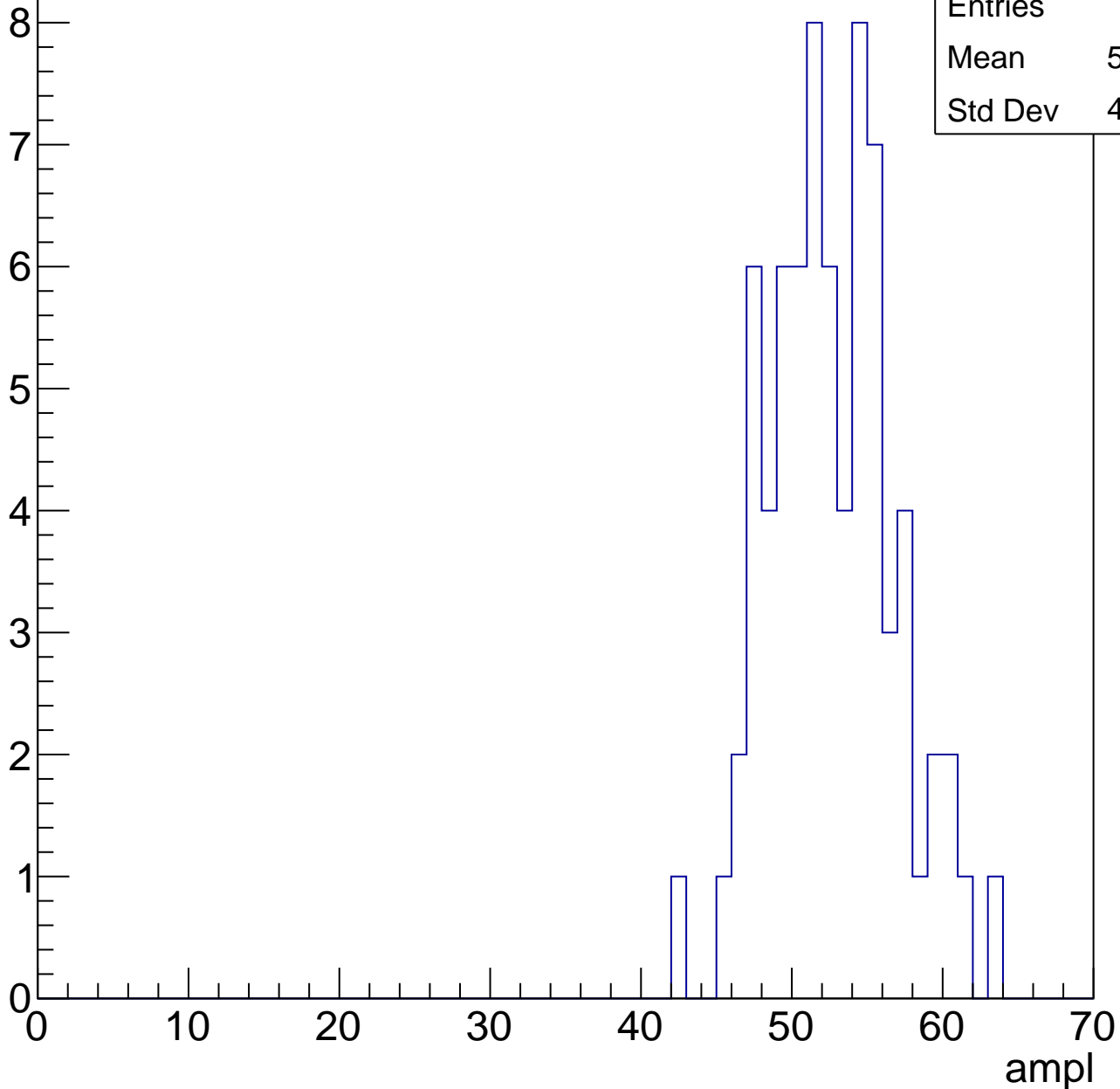


B0L001S, U22-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	52.22
Std Dev	4.102

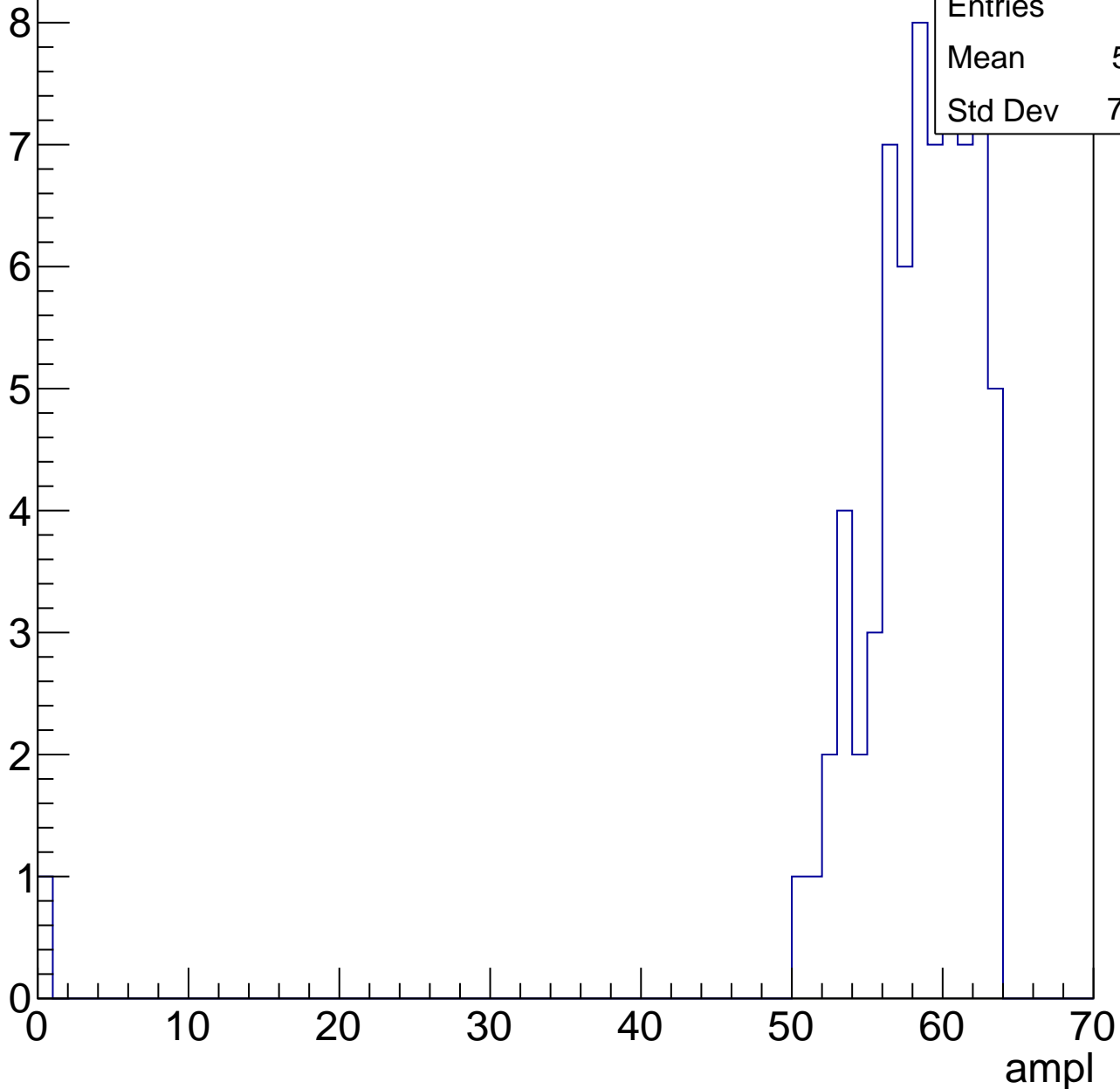


B0L001S, U22-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

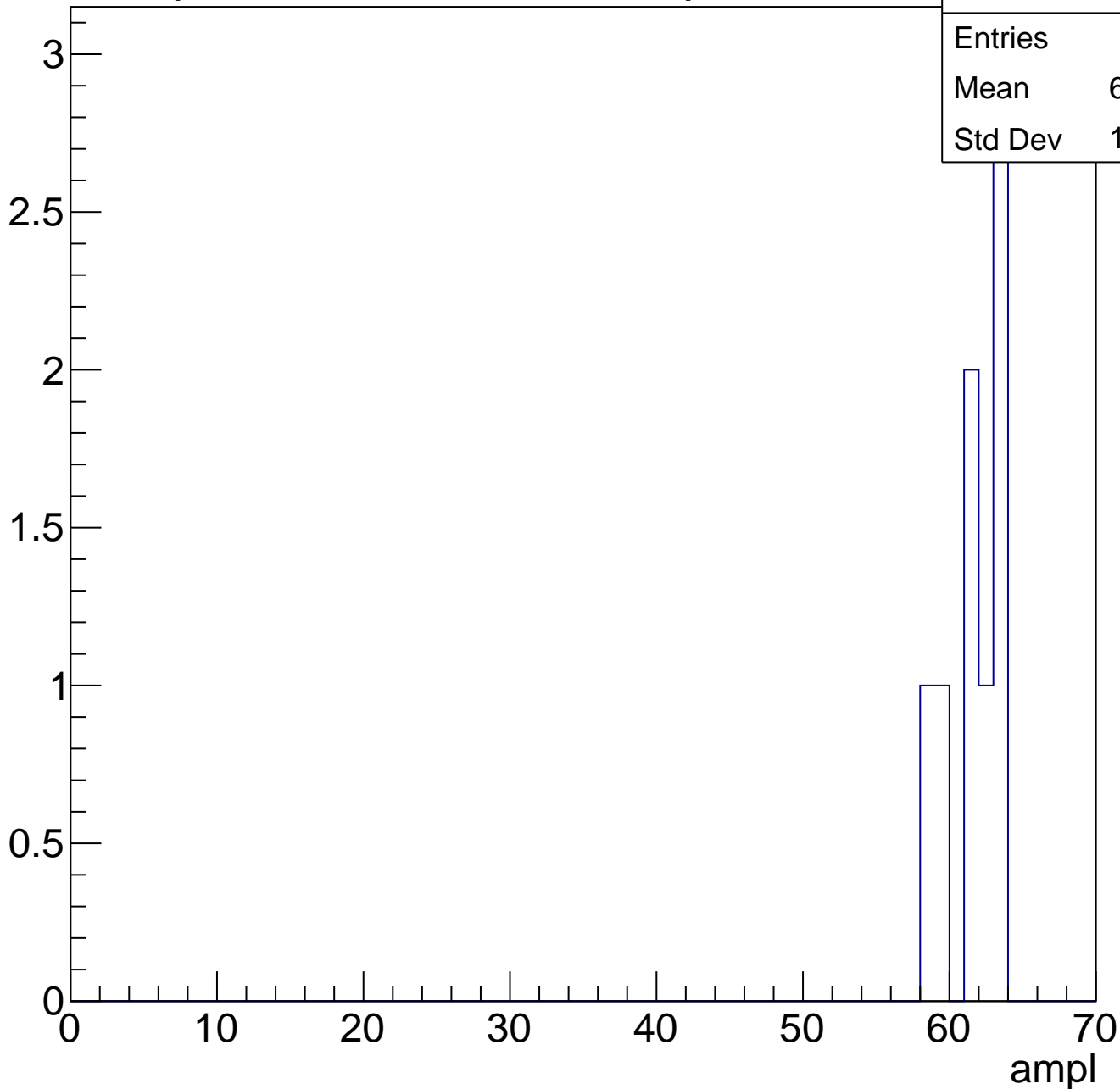
Entries	70
Mean	57.41
Std Dev	7.628



B0L001S, U22-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

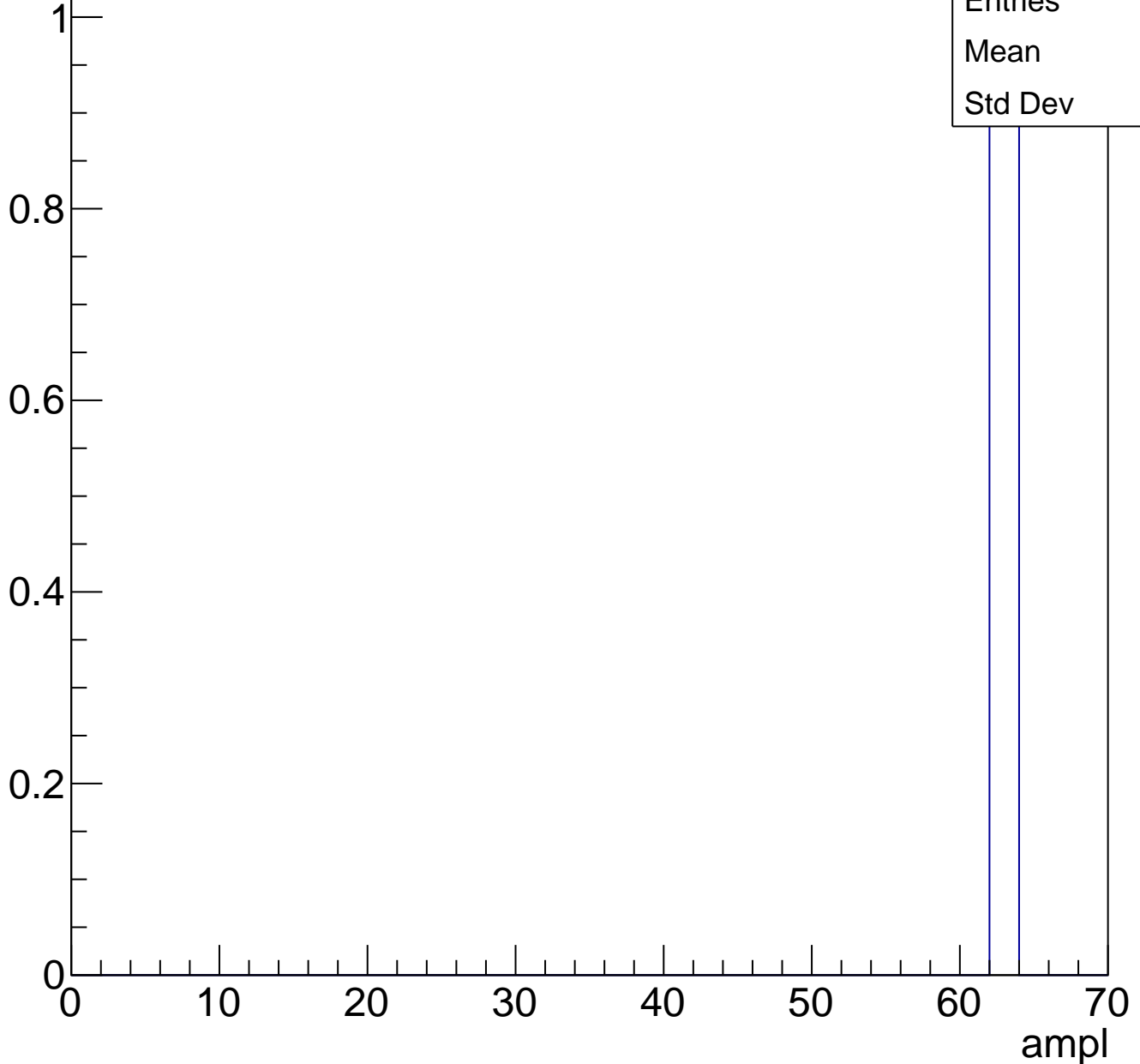
Entry



B0L001S, U22-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch116, adc0

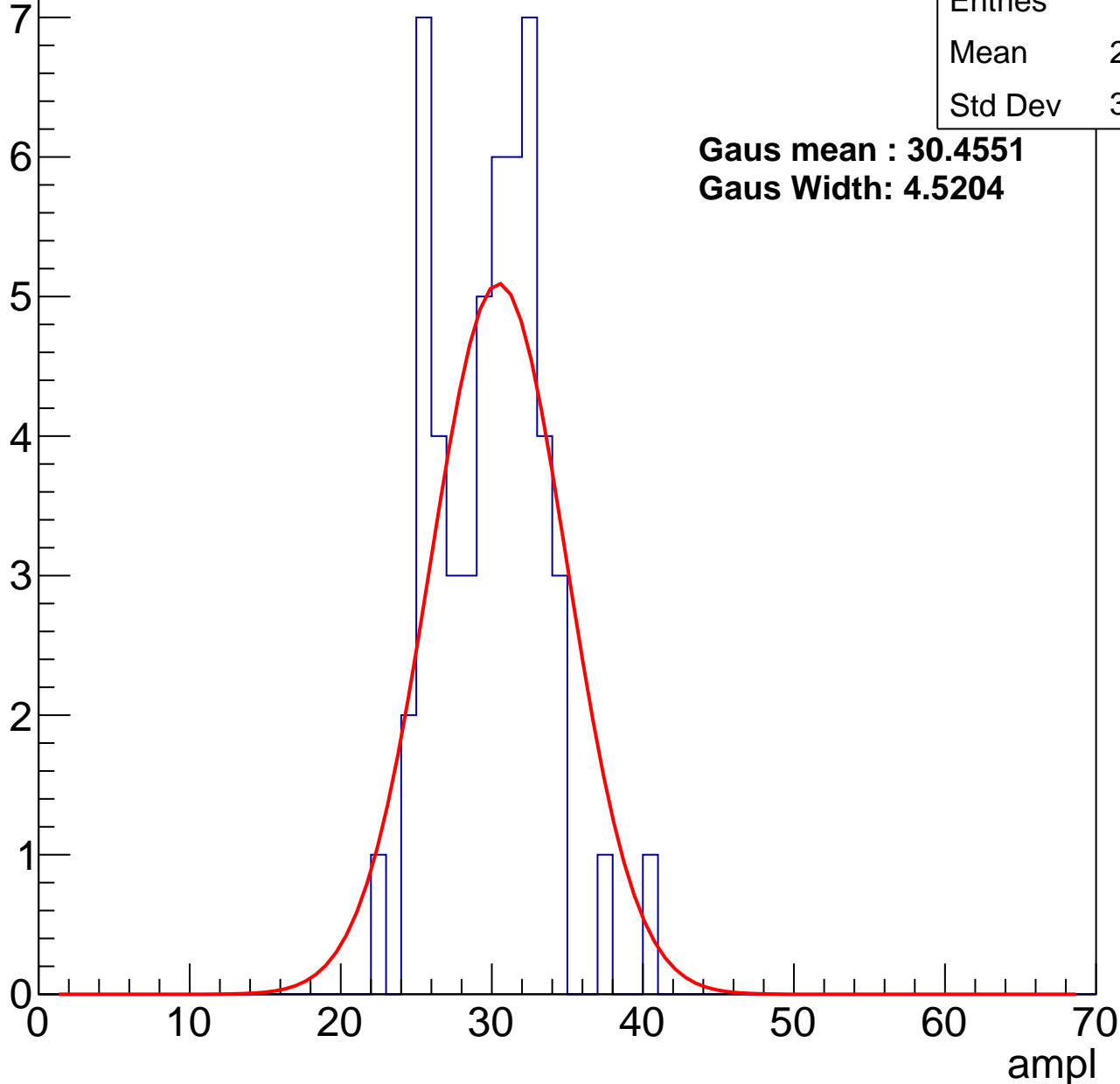
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	29.43
Std Dev	3.553

Gaus mean : 30.4551

Gaus Width: 4.5204



B0L001S, U22-ch116, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	35.8
Std Dev	3.555

Gaus mean : 36.0928

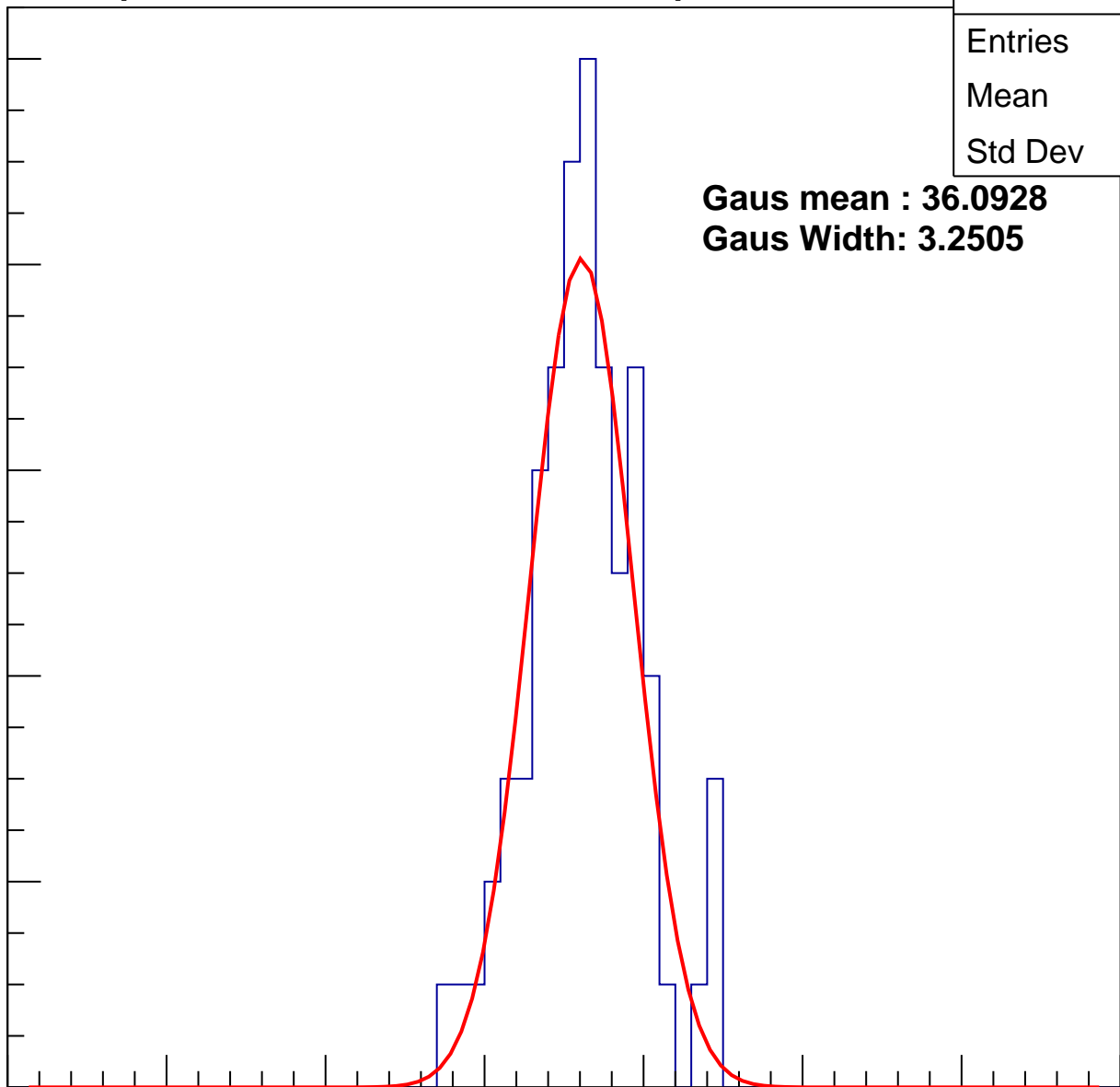
Gaus Width: 3.2505

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch116, adc2

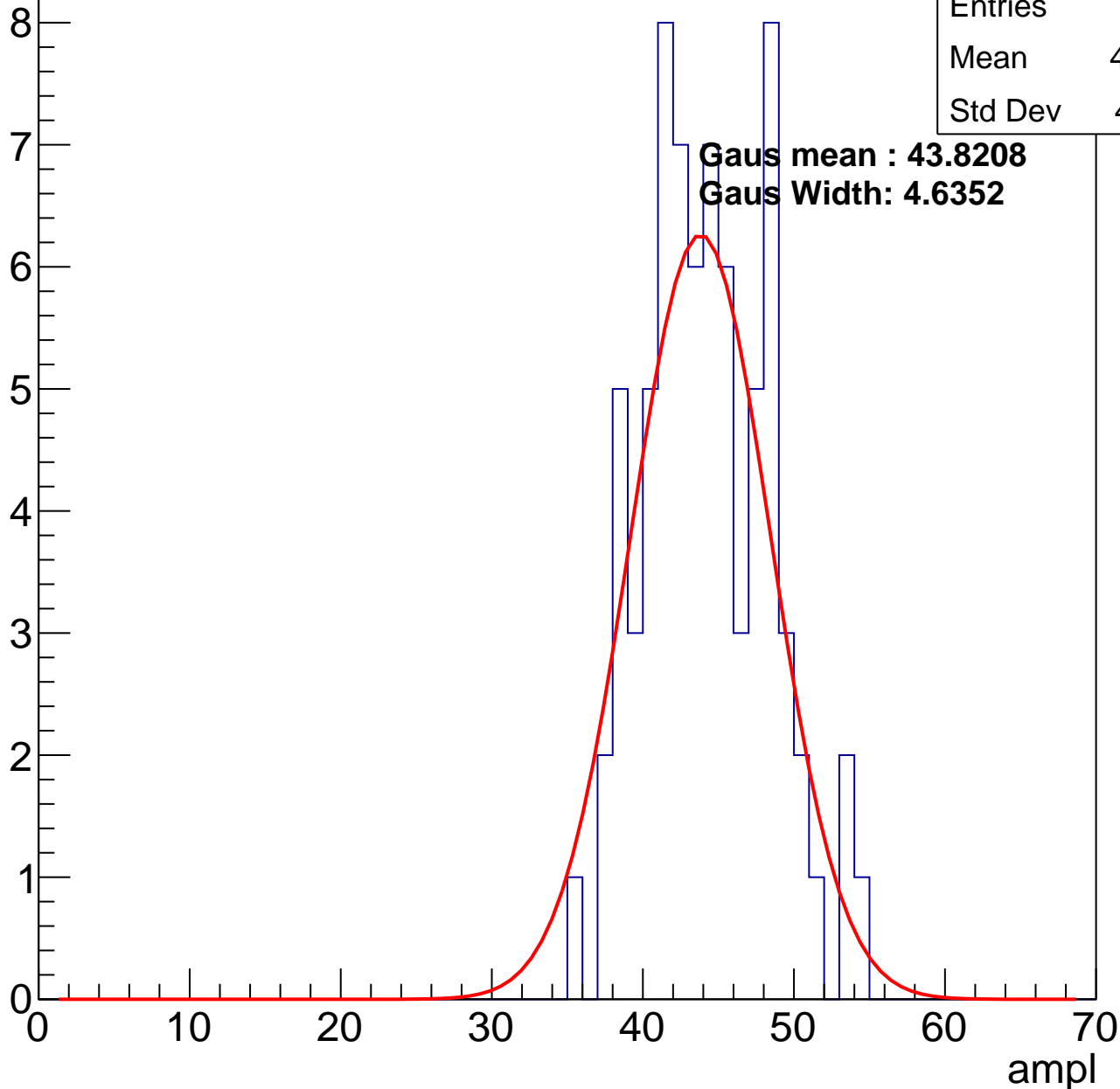
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.85
Std Dev	4.111

Gaus mean : 43.8208

Gaus Width: 4.6352

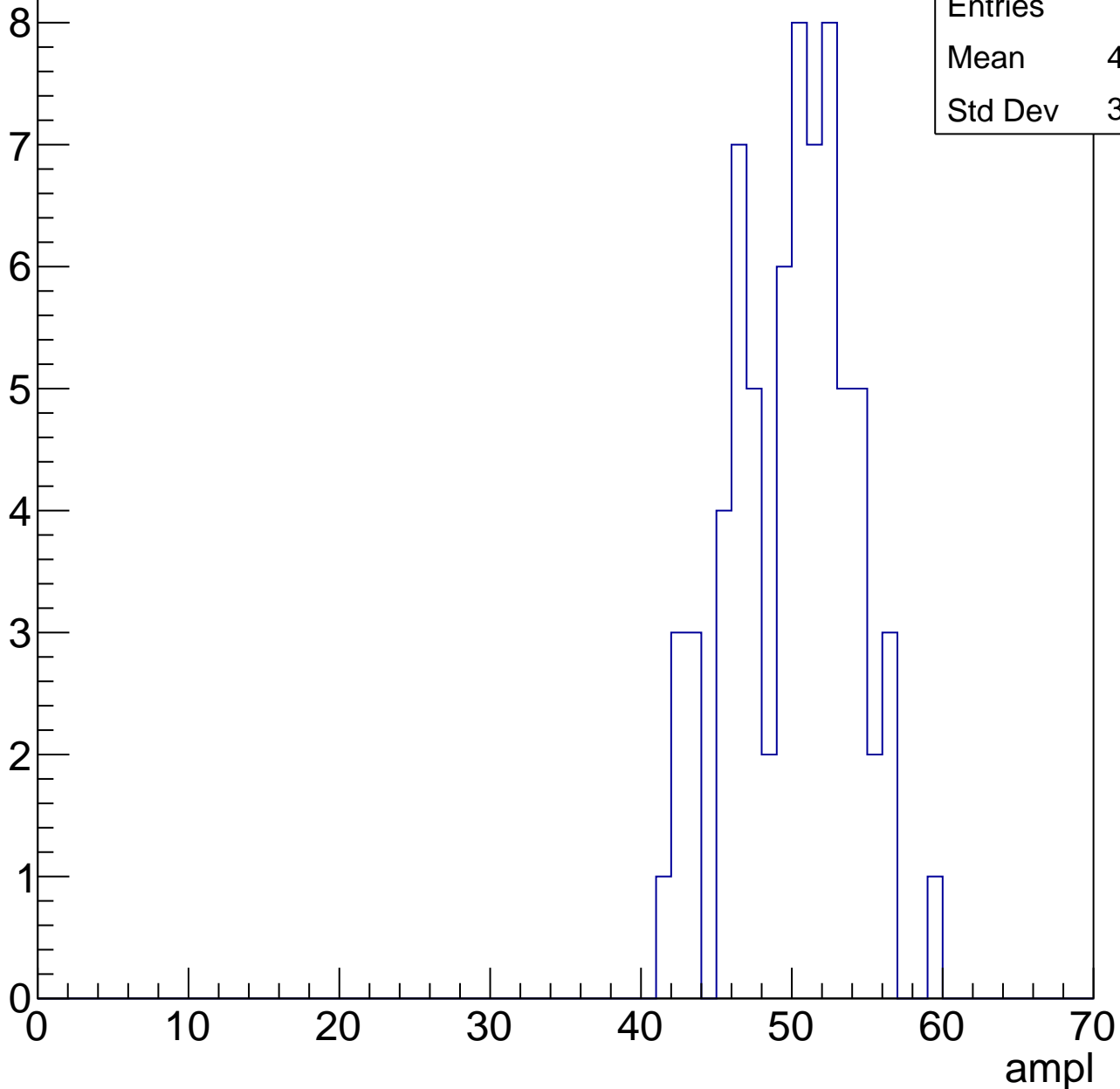


B0L001S, U22-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.54
Std Dev	3.927

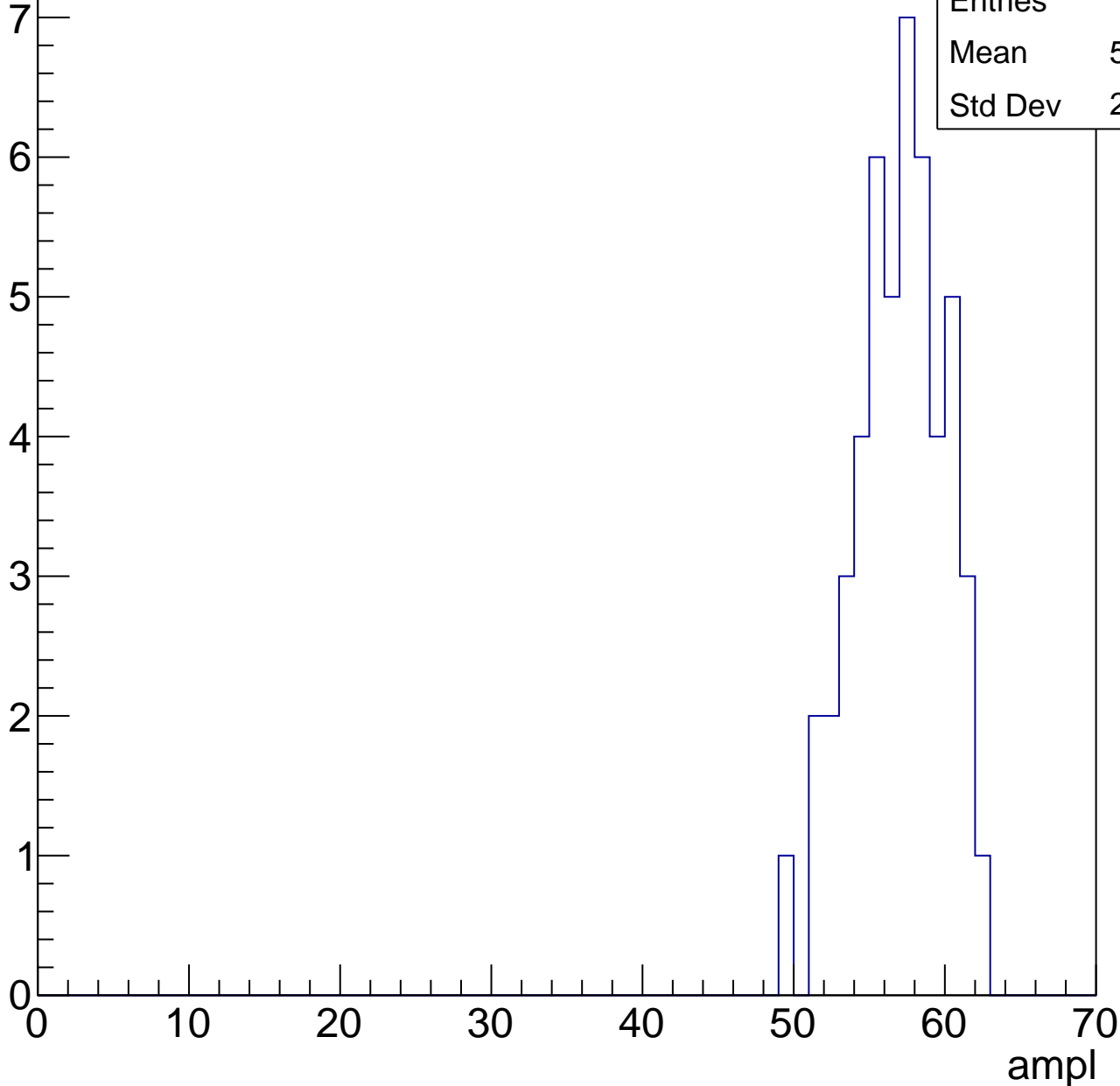


B0L001S, U22-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.49
Std Dev	2.949

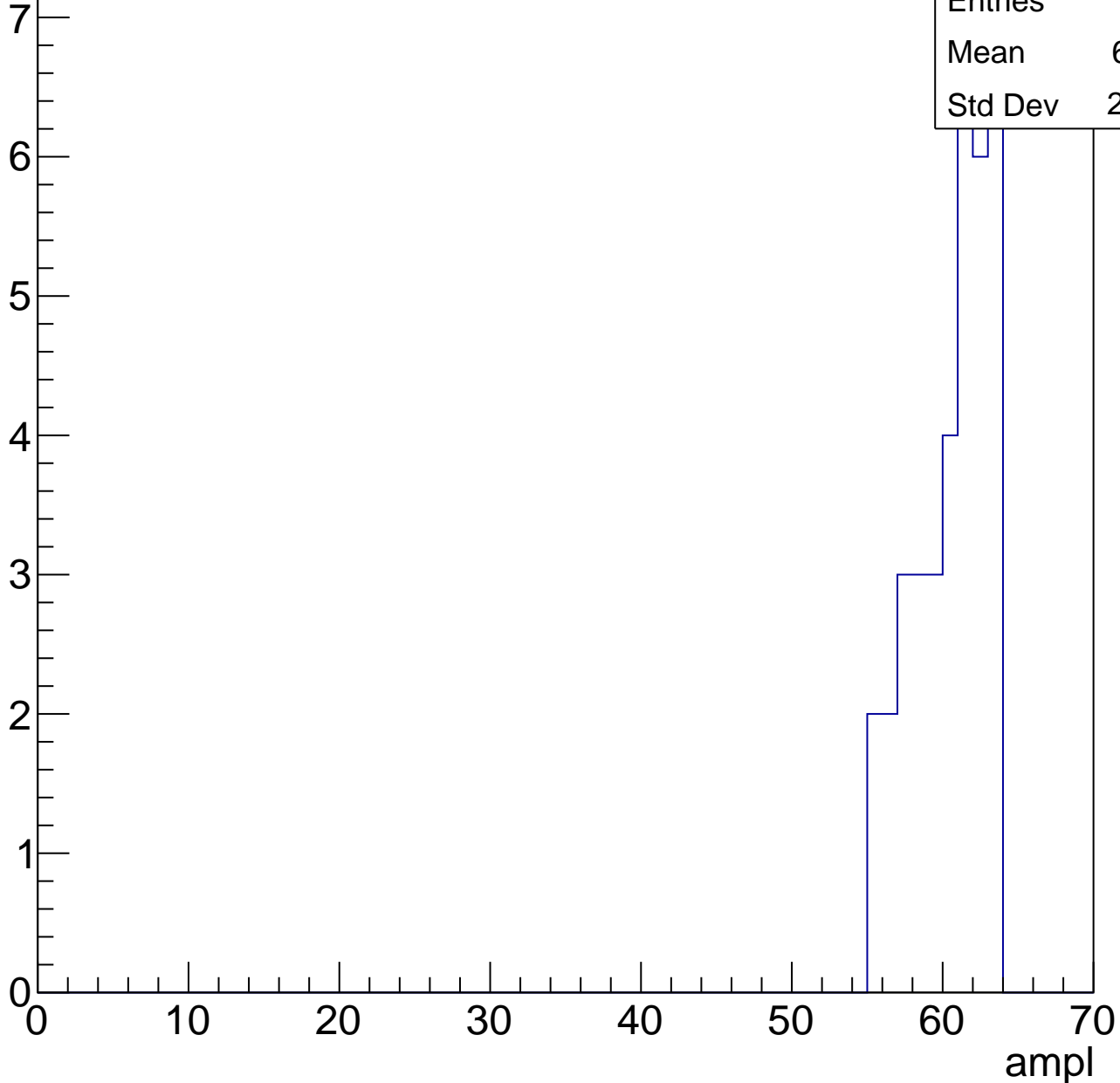


B0L001S, U22-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

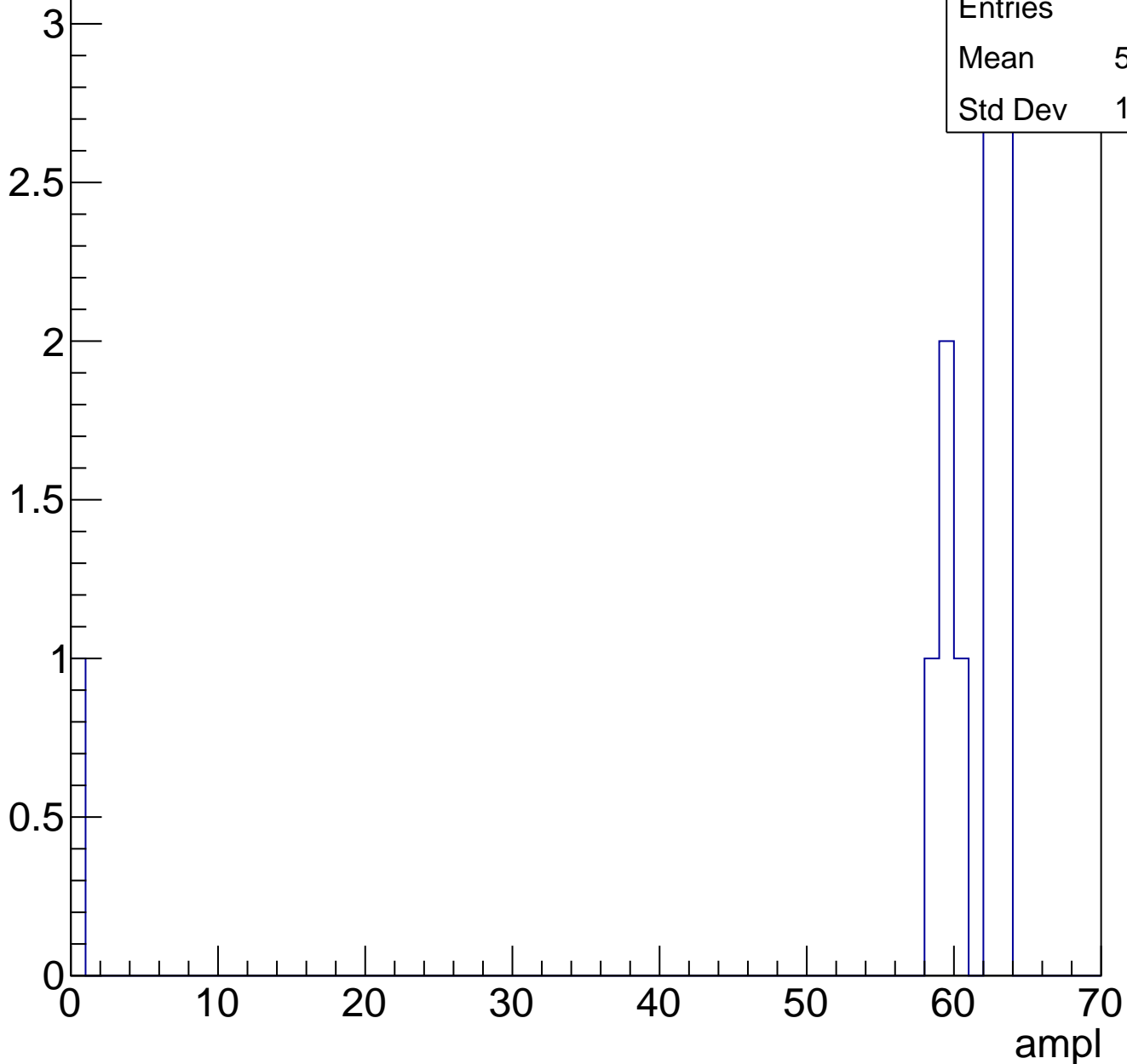
Entries	37
Mean	60.11
Std Dev	2.425



B0L001S, U22-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	11
Mean	55.55
Std Dev	17.65

B0L001S, U22-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch117, adc0

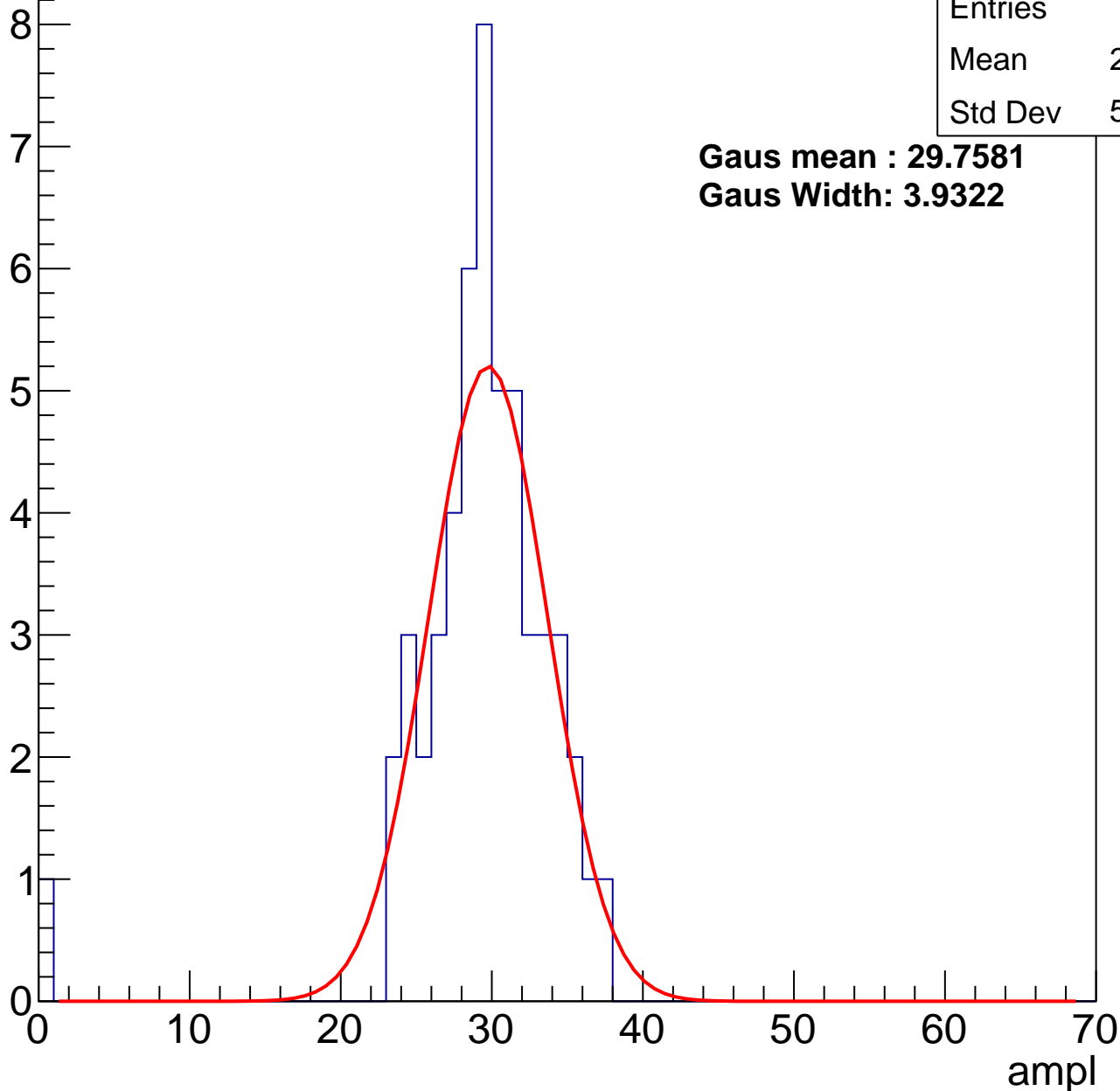
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	28.83
Std Dev	5.239

Gaus mean : 29.7581

Gaus Width: 3.9322



B0L001S, U22-ch117, adc1

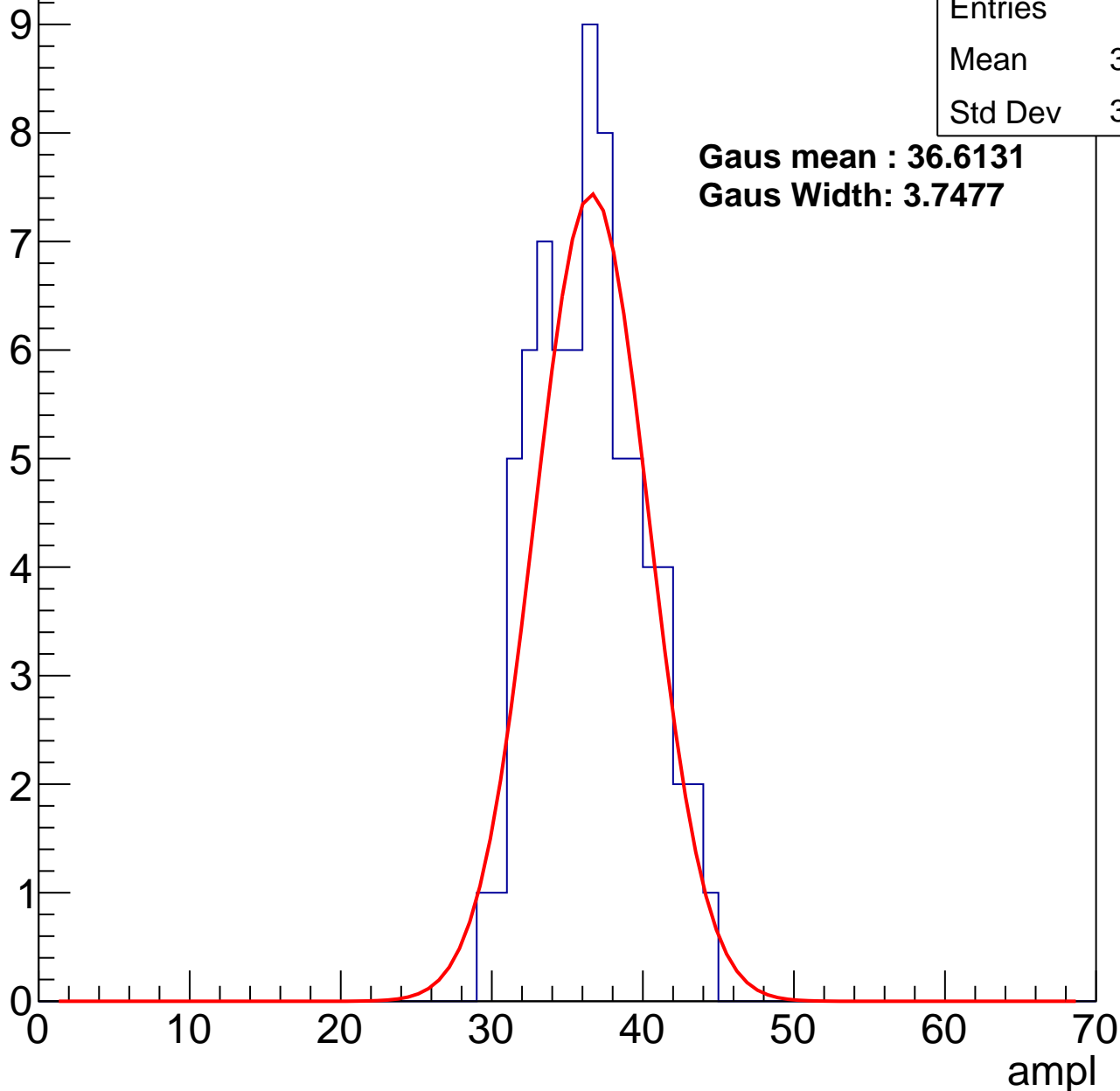
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.03
Std Dev	3.476

Gaus mean : 36.6131

Gaus Width: 3.7477



B0L001S, U22-ch117, adc2

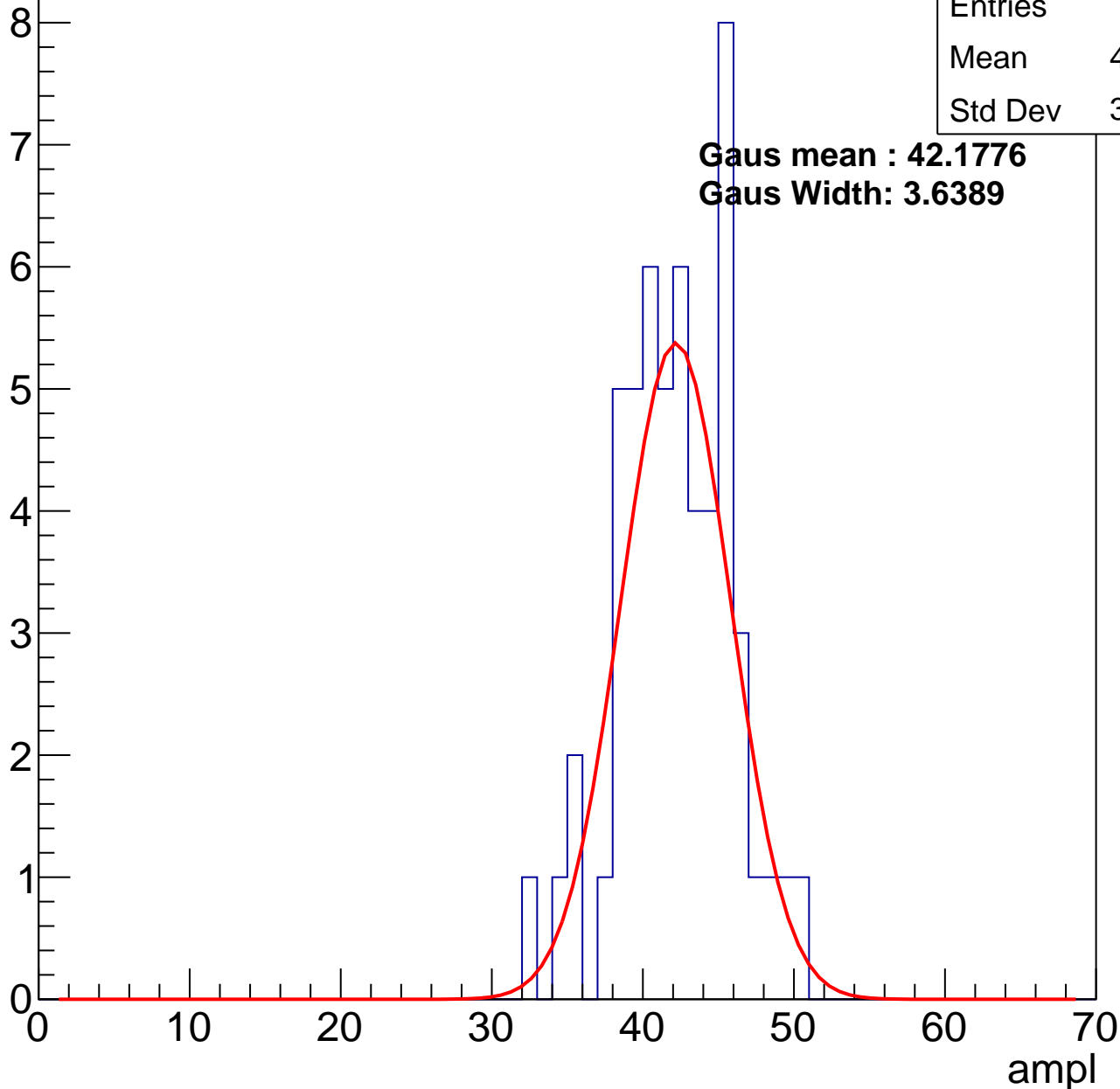
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	41.73
Std Dev	3.695

Gaus mean : 42.1776

Gaus Width: 3.6389

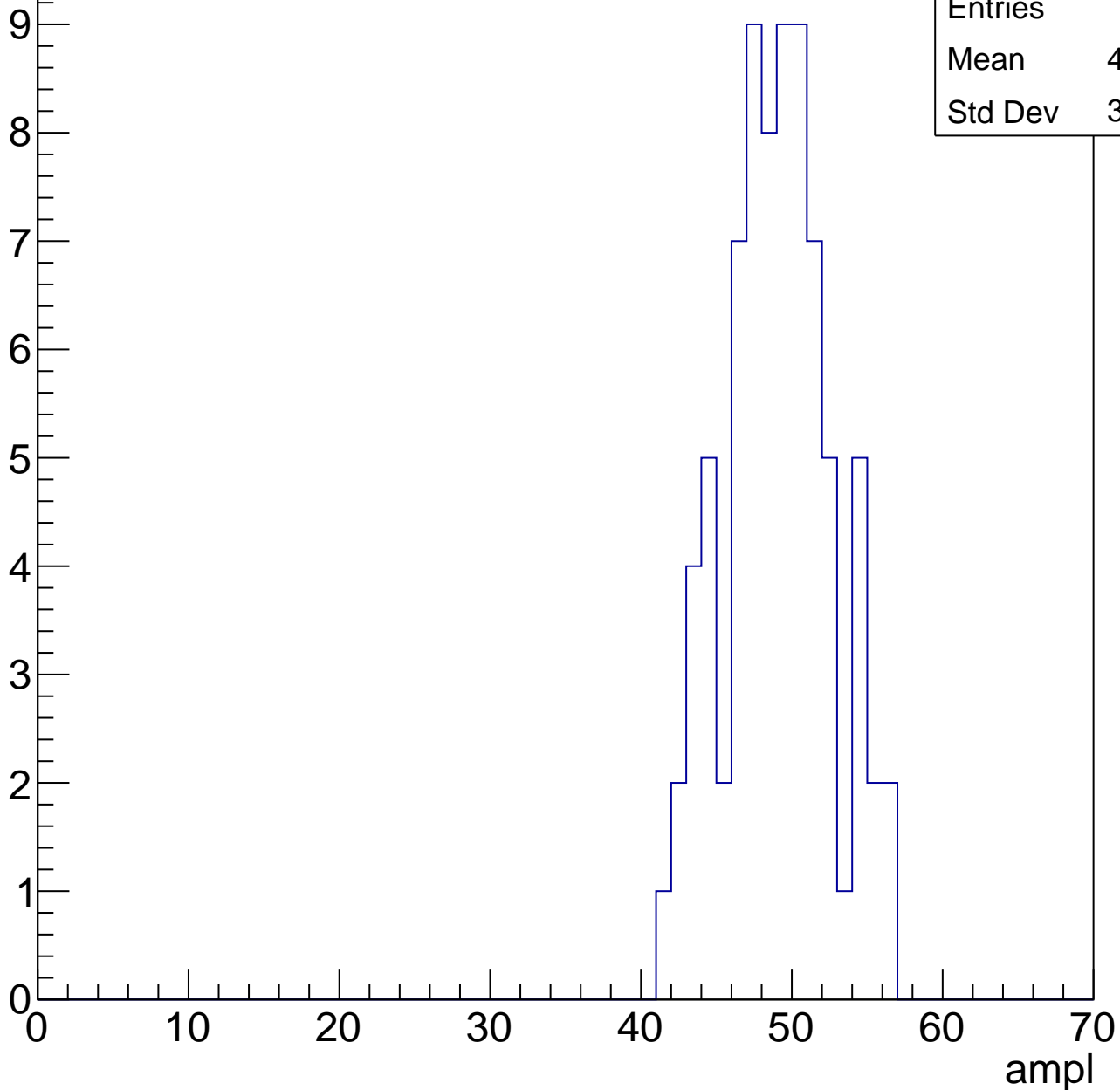


B0L001S, U22-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	48.58
Std Dev	3.488

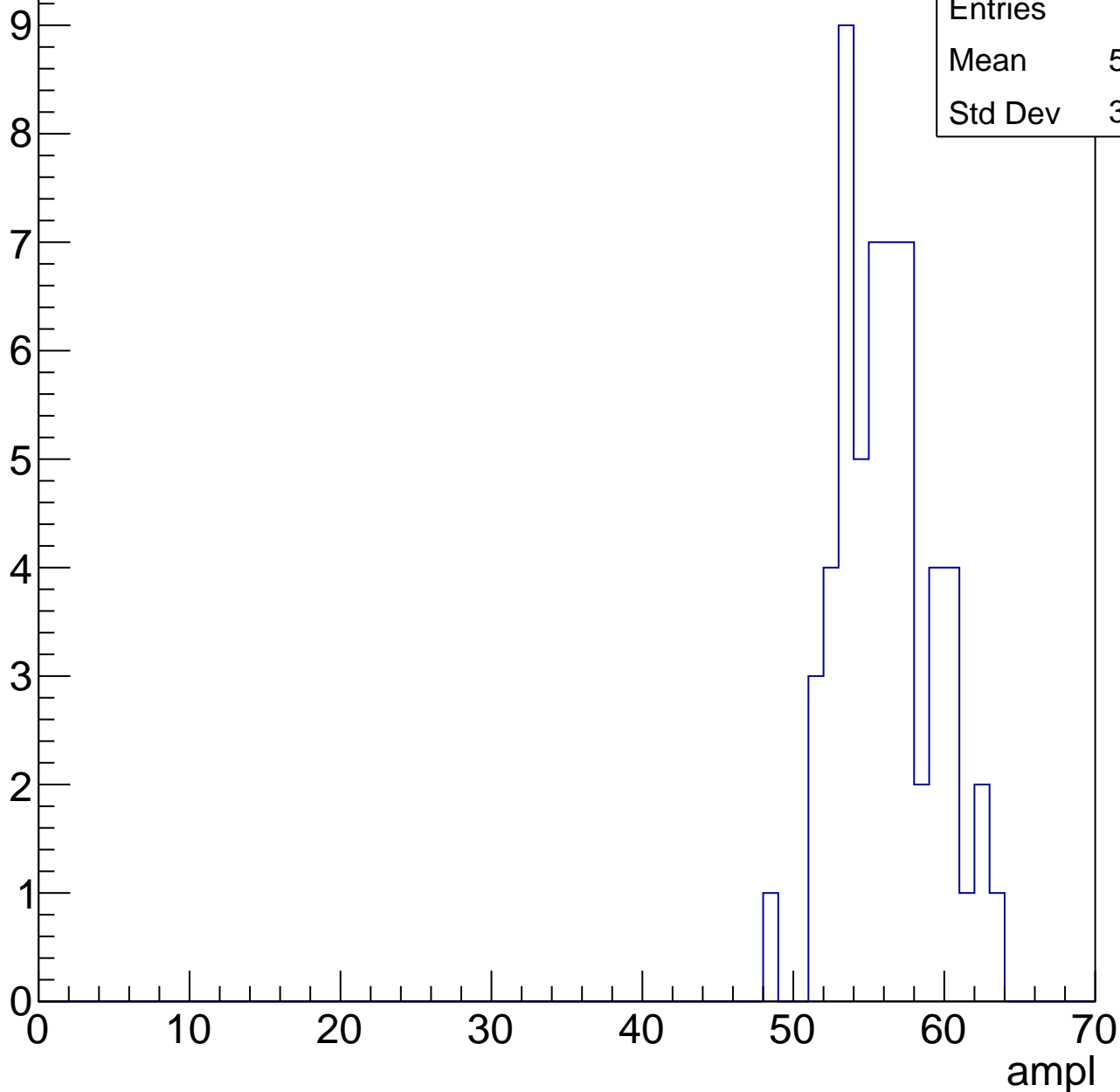


B0L001S, U22-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.65
Std Dev	3.148

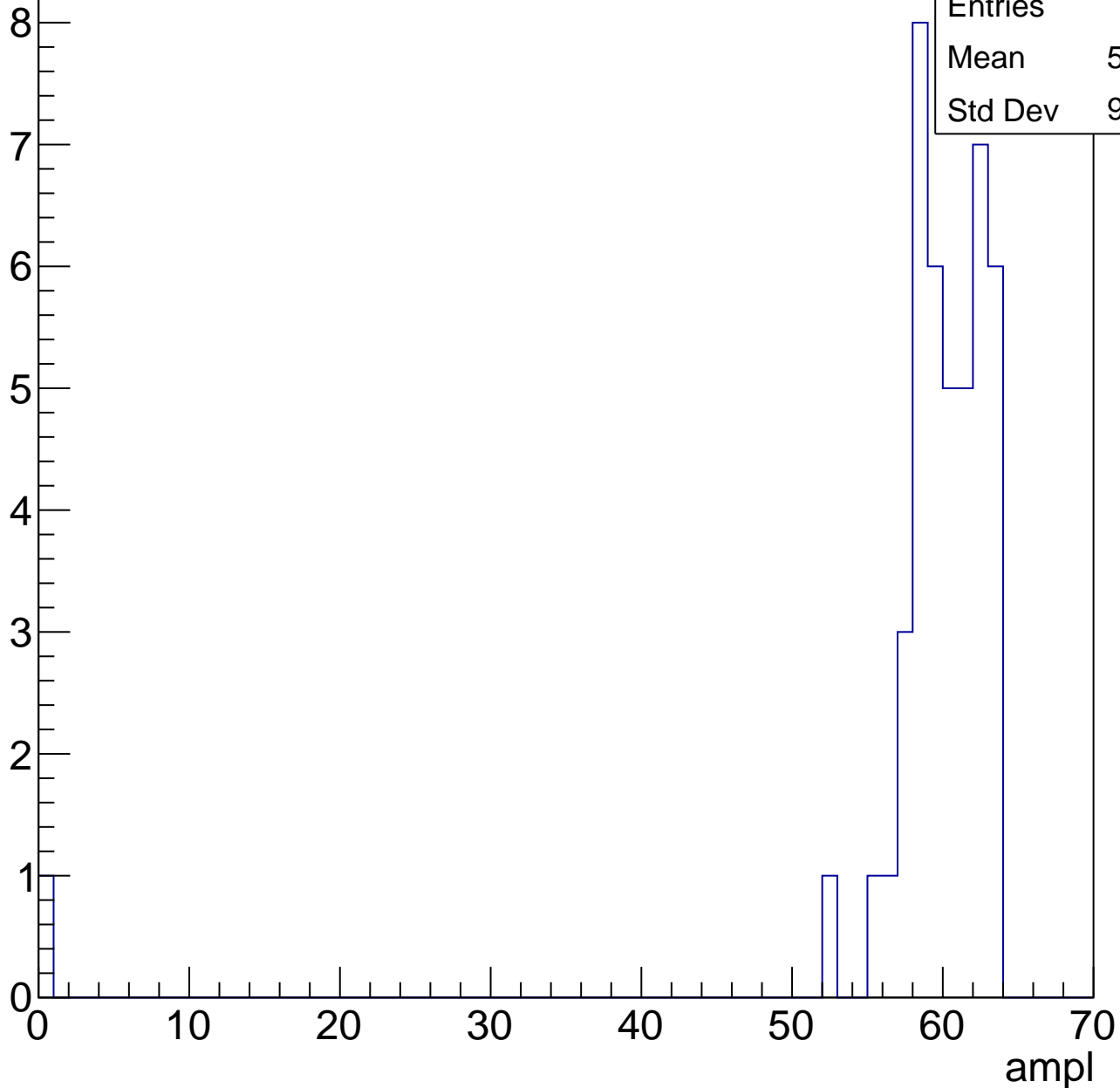


B0L001S, U22-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.39
Std Dev	9.223



B0L001S, U22-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

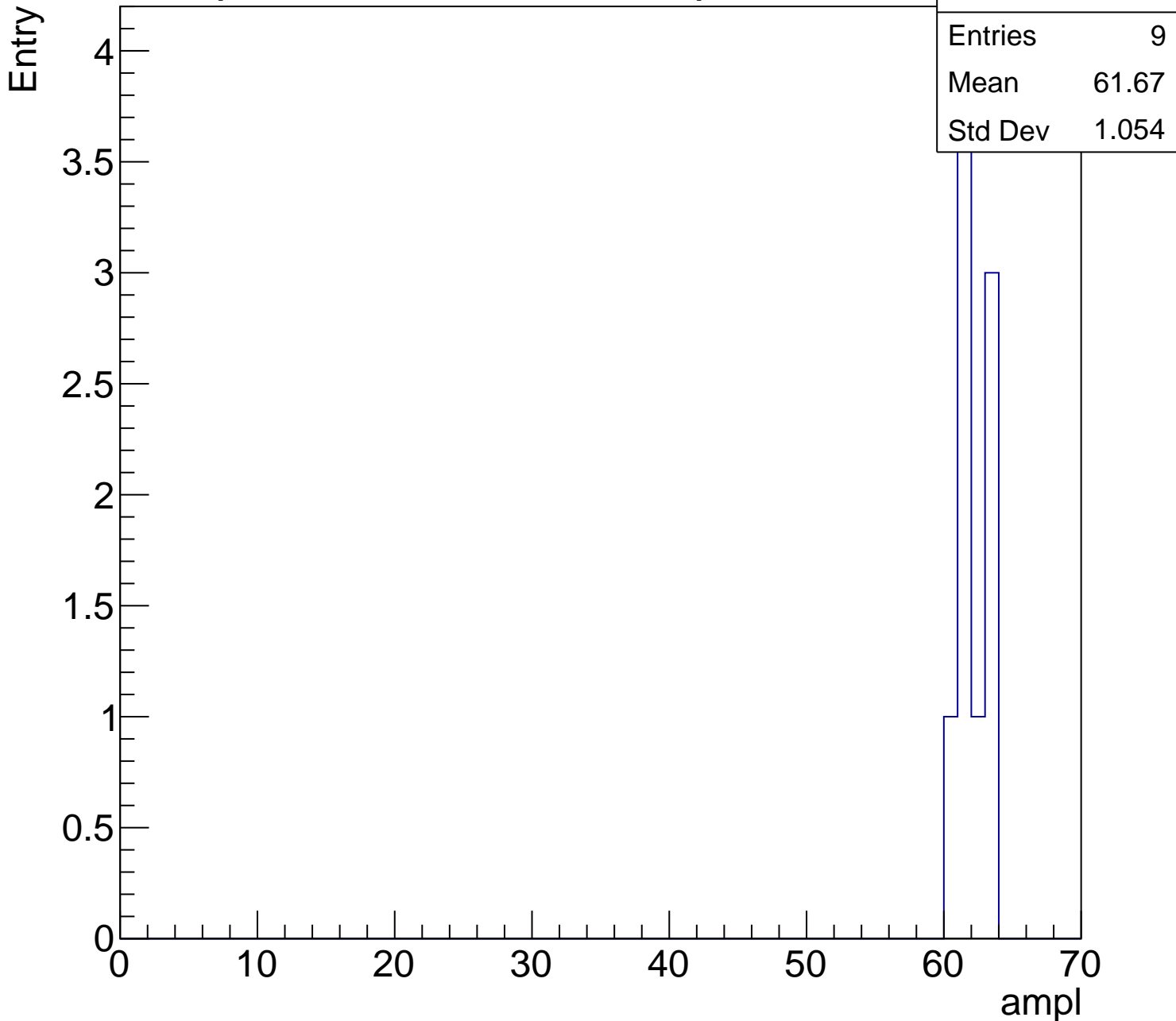
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.67
Std Dev	1.054

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch118, adc0

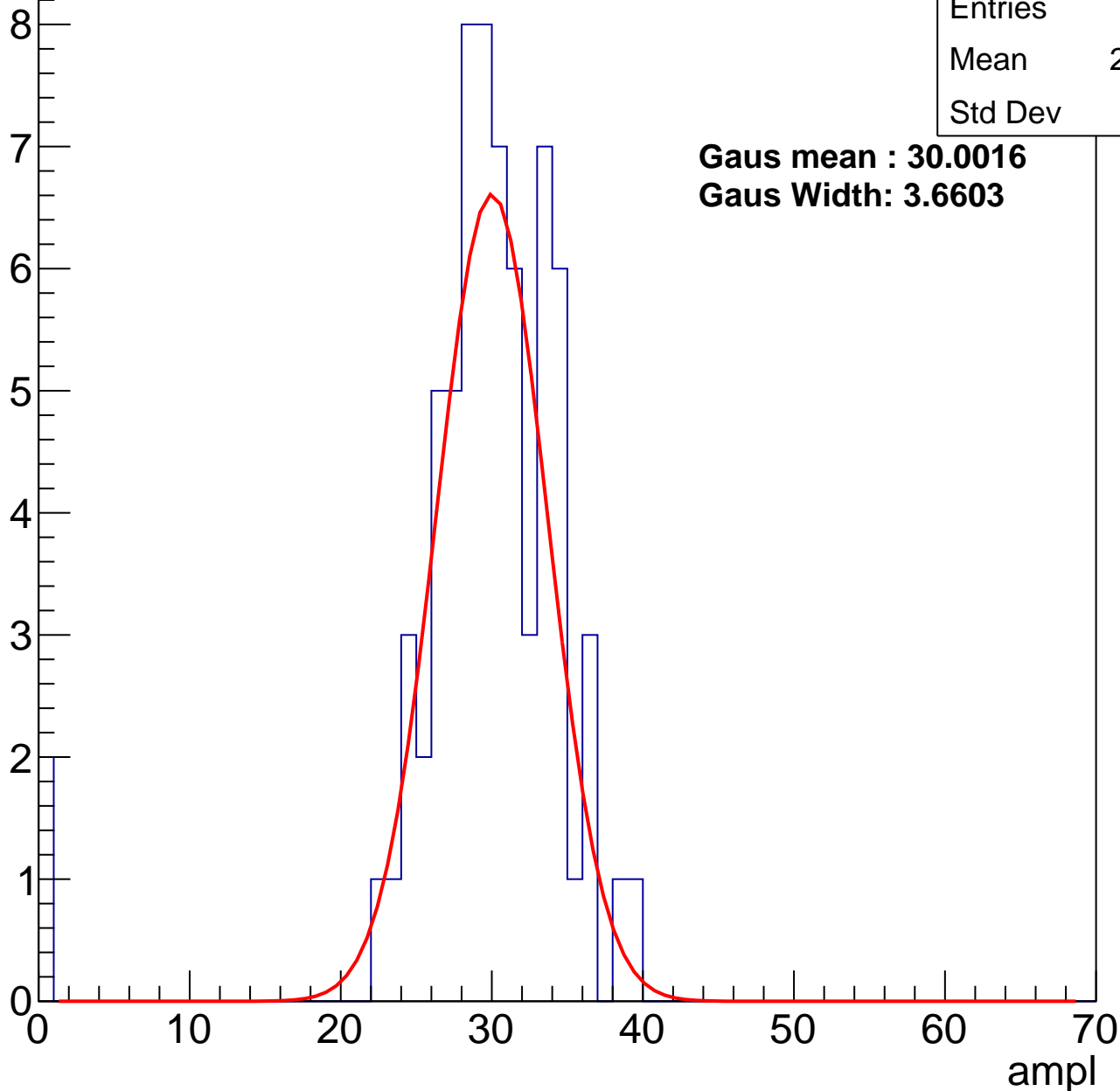
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.07
Std Dev	6.13

Gaus mean : 30.0016

Gaus Width: 3.6603



B0L001S, U22-ch118, adc1

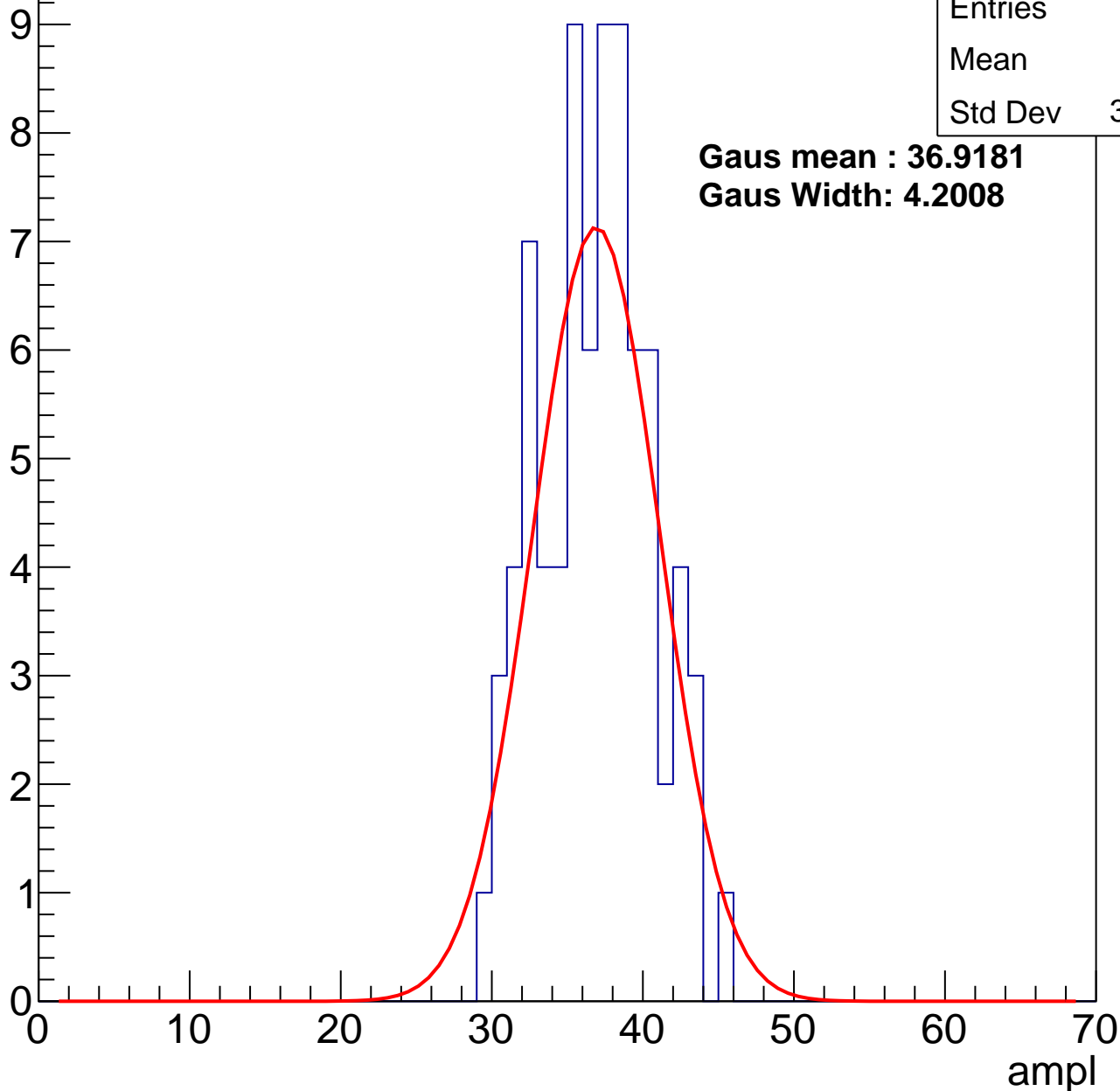
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	36.4
Std Dev	3.642

Gaus mean : 36.9181

Gaus Width: 4.2008



B0L001S, U22-ch118, adc2

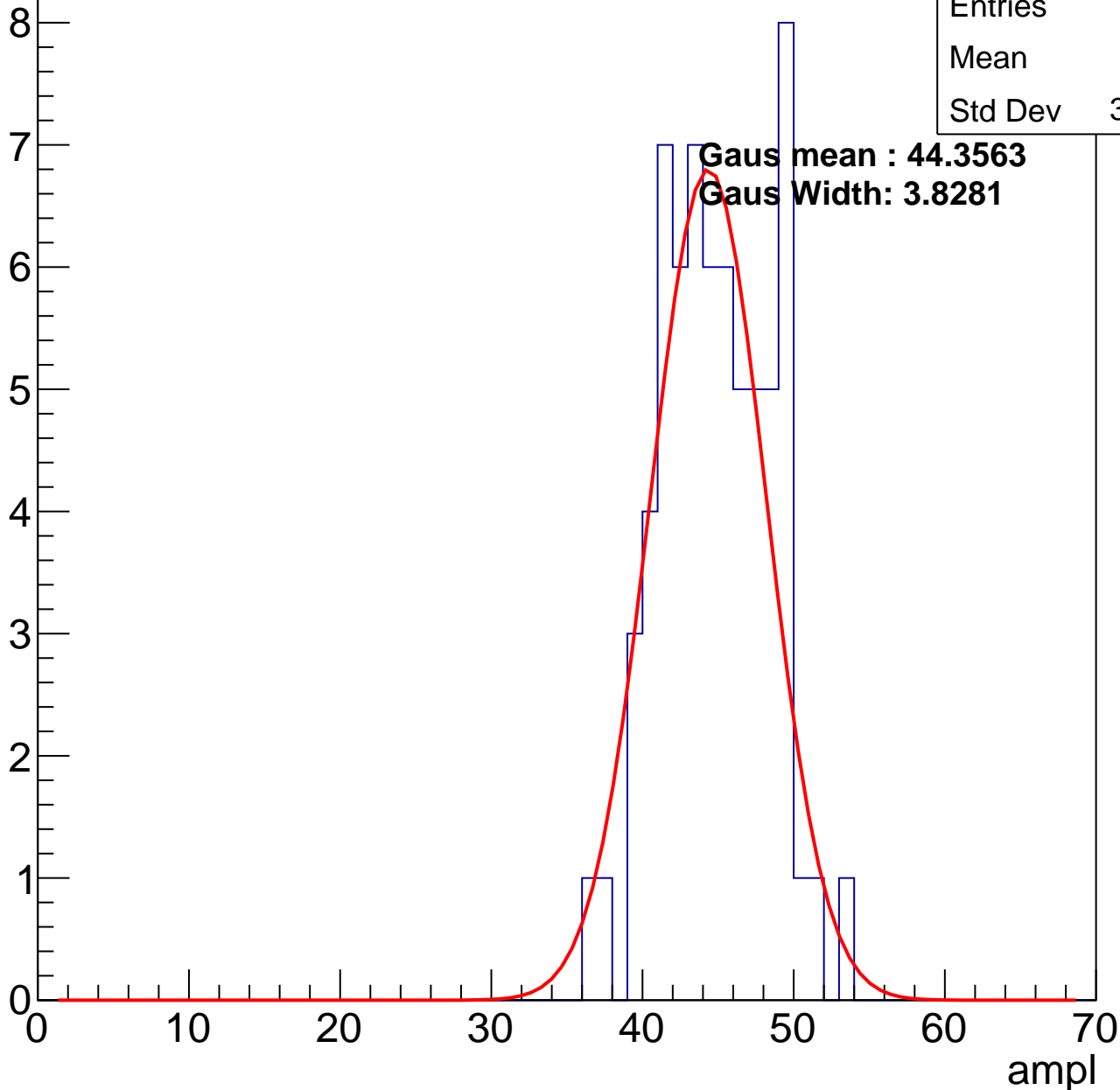
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.4
Std Dev	3.566

Gaus mean : 44.3563

Gaus Width: 3.8281

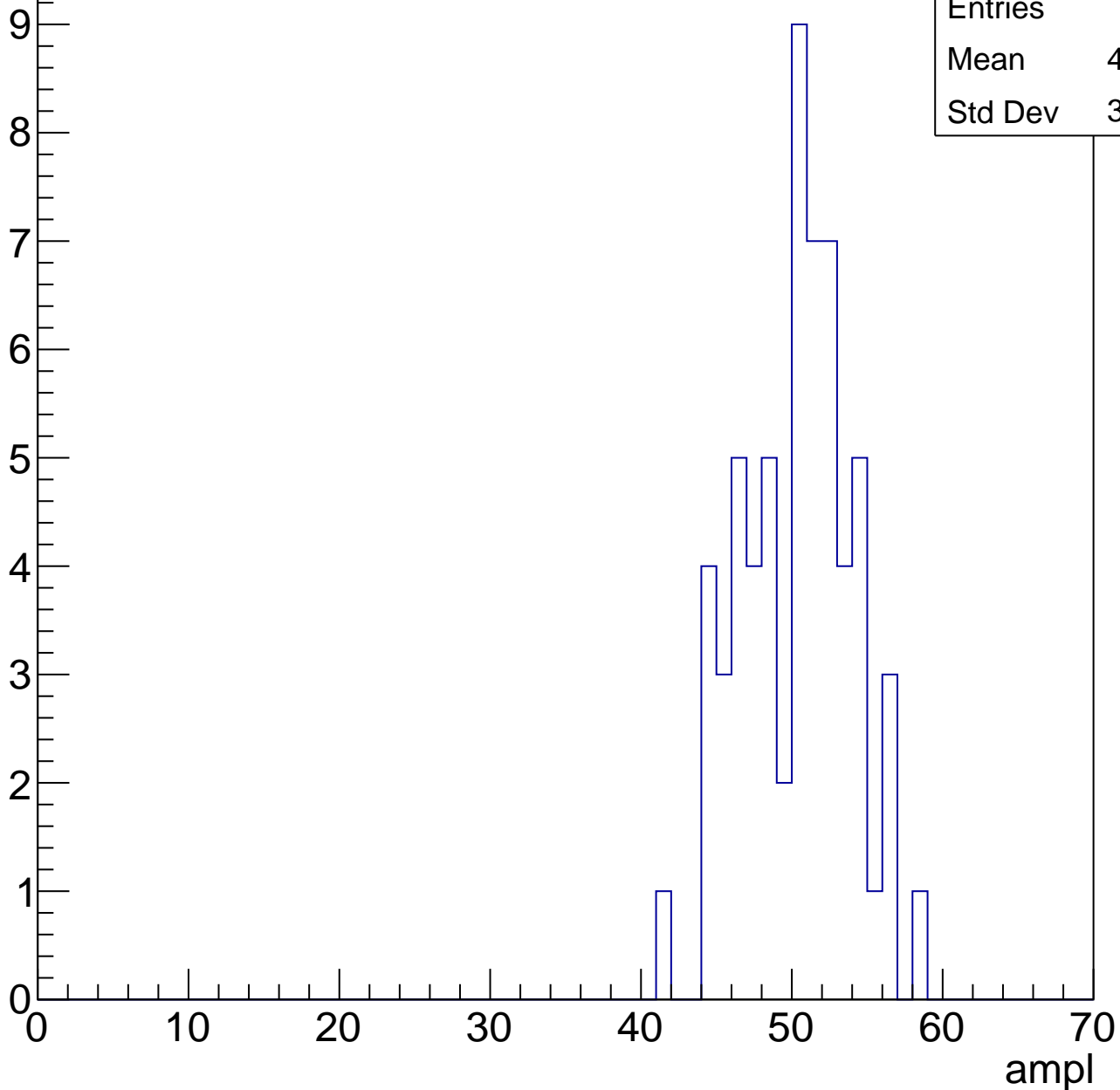


B0L001S, U22-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	49.87
Std Dev	3.587

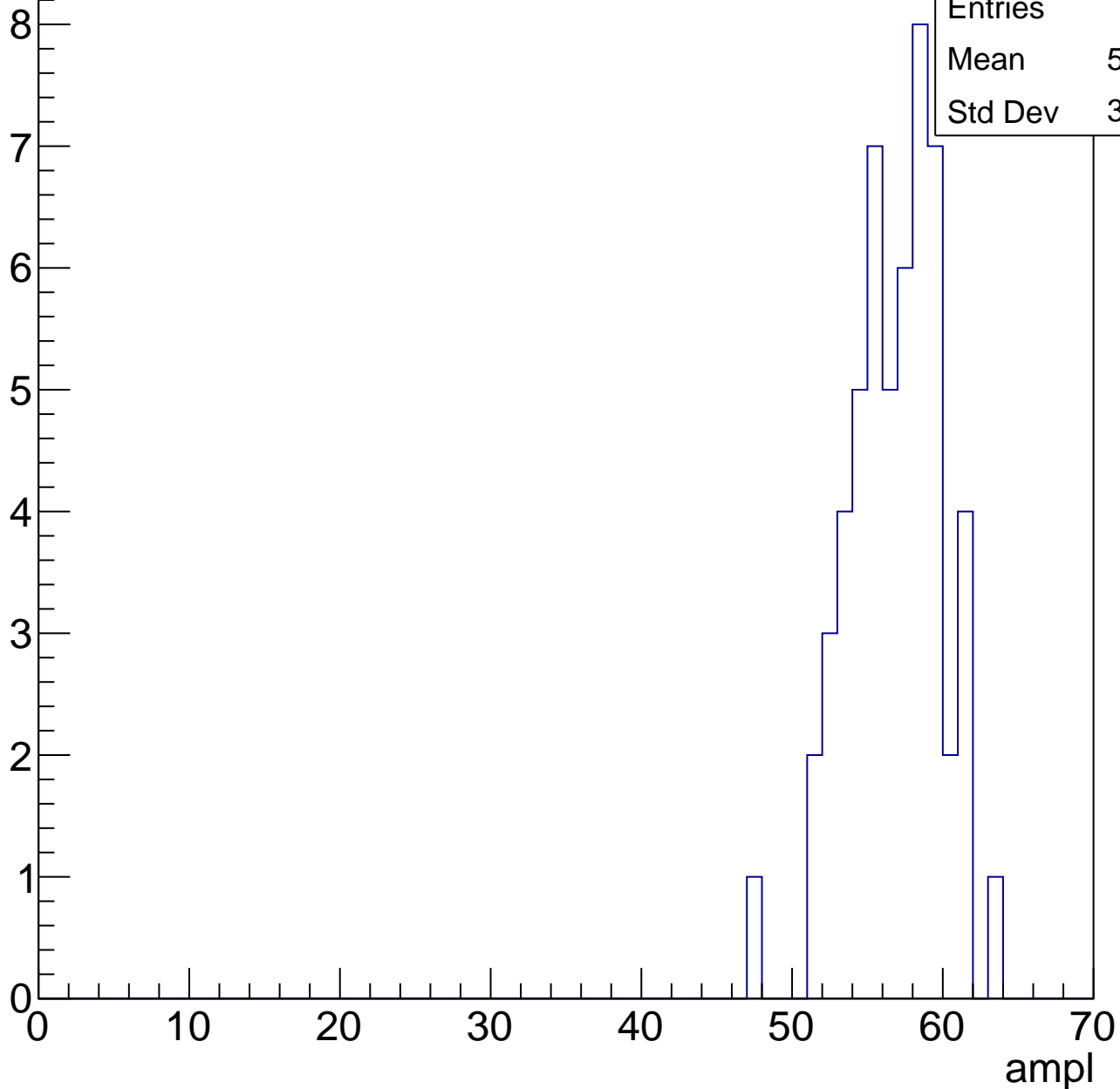


B0L001S, U22-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.33
Std Dev	3.069

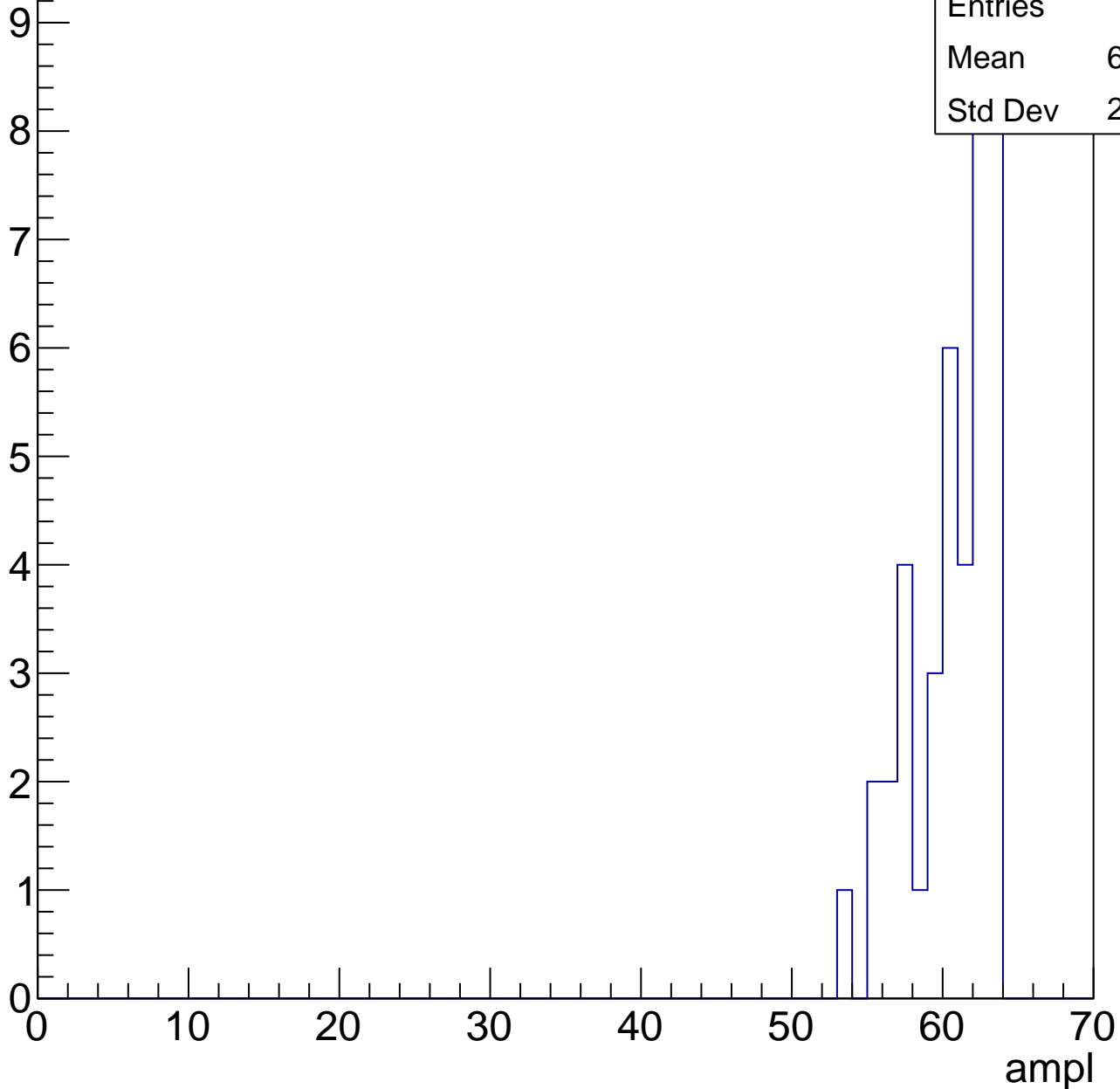


B0L001S, U22-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

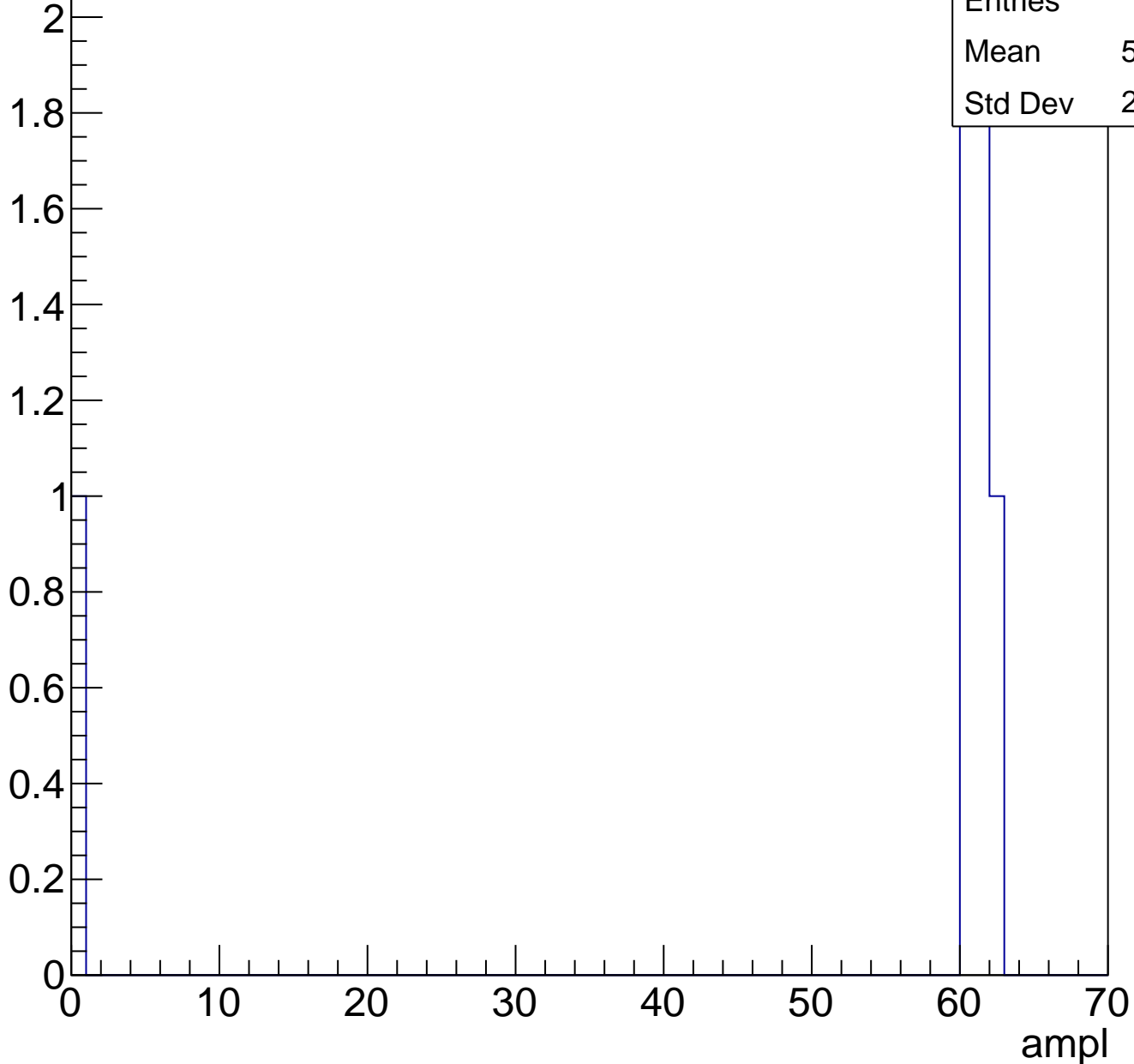
Entries	41
Mean	60.17
Std Dev	2.677



B0L001S, U22-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch119, adc0

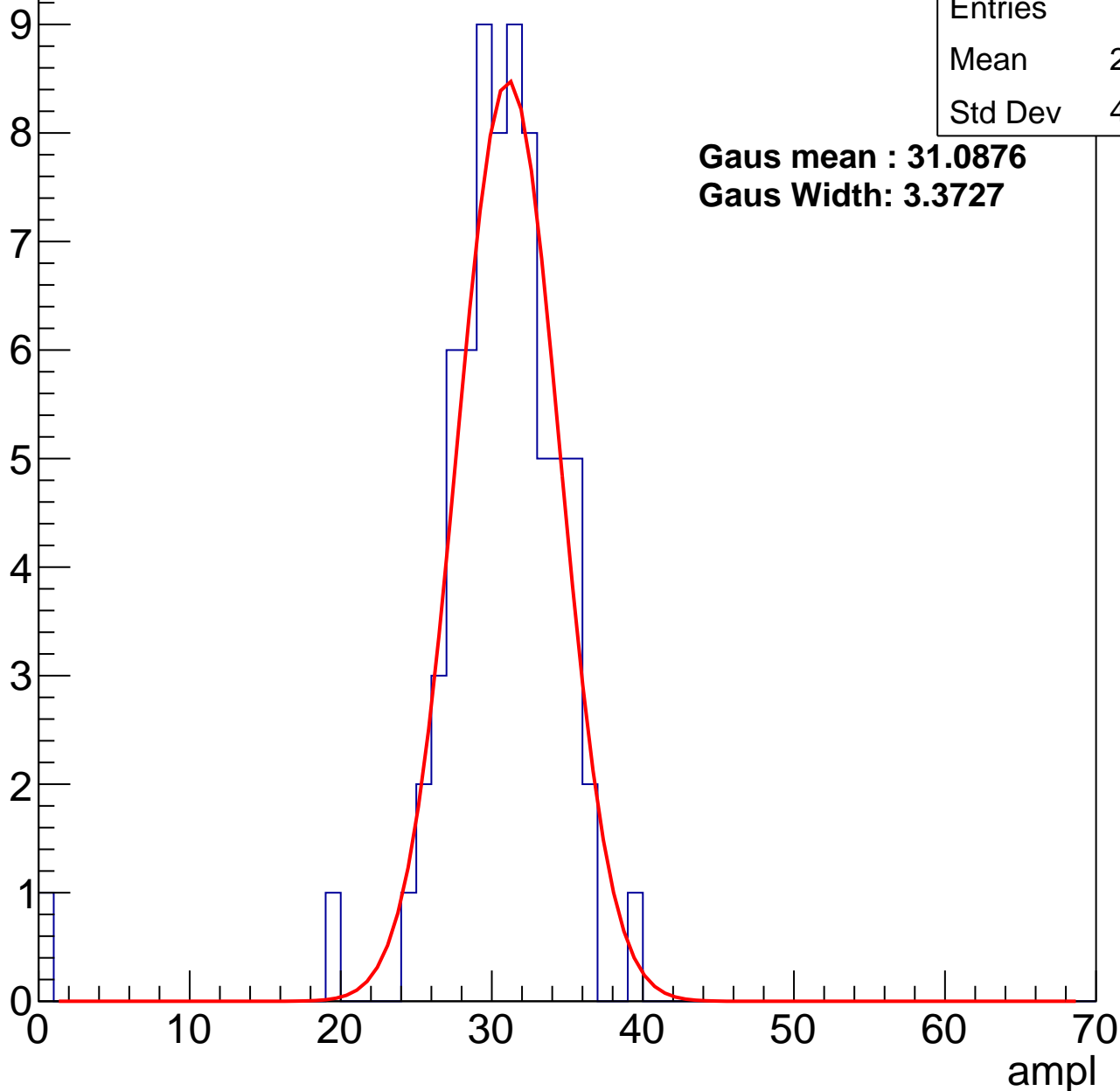
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.97
Std Dev	4.839

Gaus mean : 31.0876

Gaus Width: 3.3727



B0L001S, U22-ch119, adc1

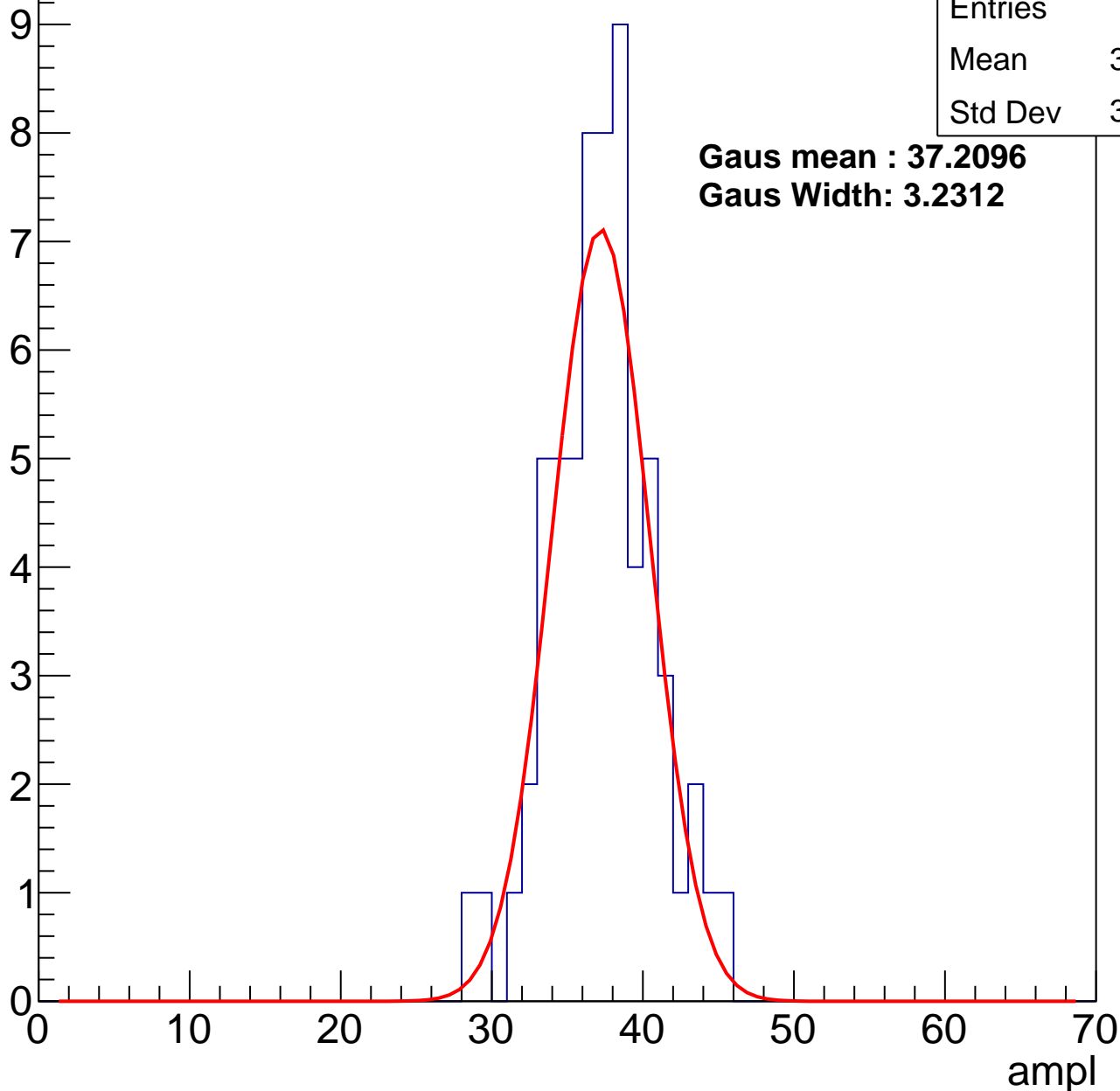
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	36.84
Std Dev	3.385

Gaus mean : 37.2096

Gaus Width: 3.2312



B0L001S, U22-ch119, adc2

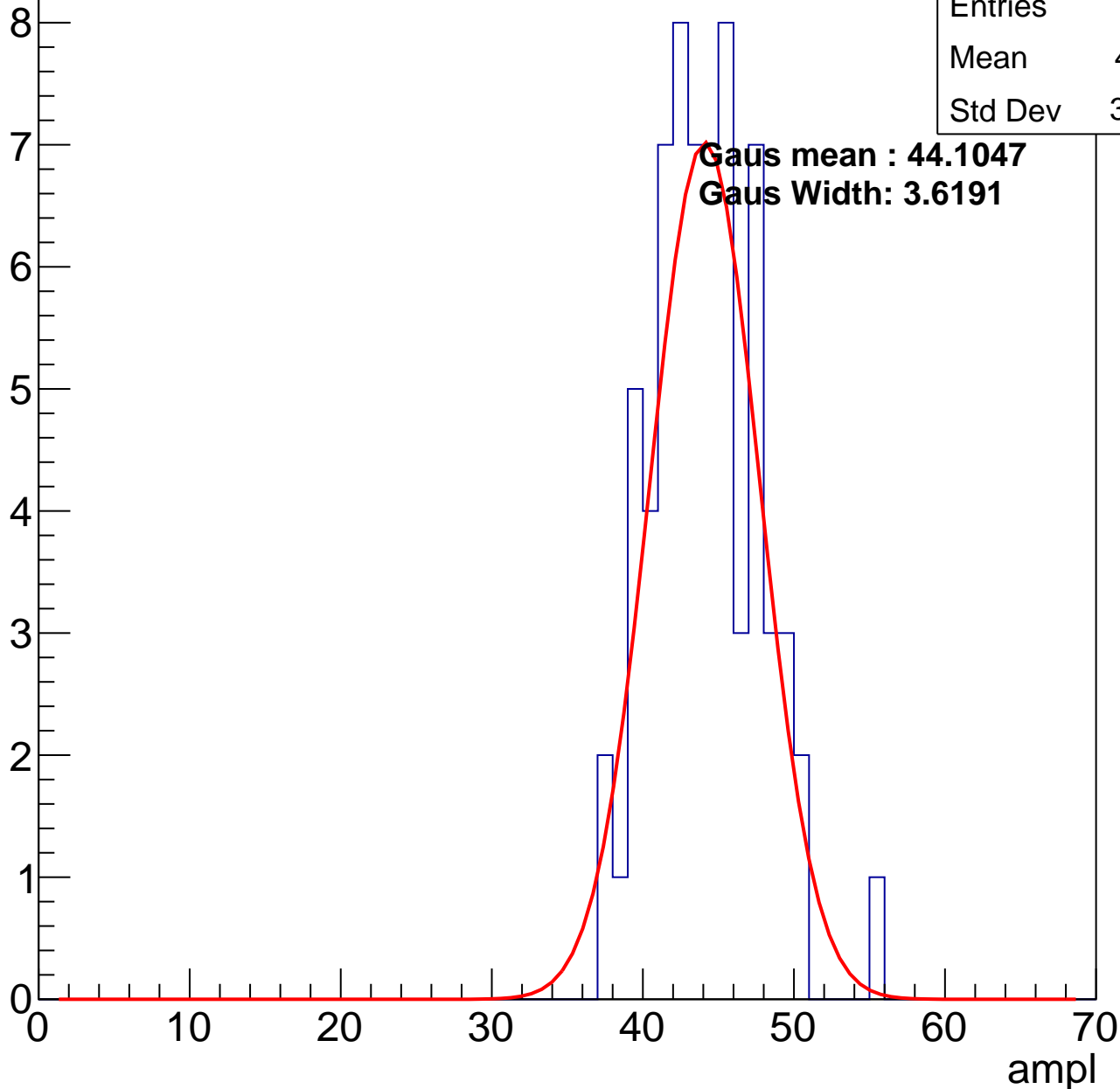
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	43.71
Std Dev	3.469

Gaus mean : 44.1047

Gaus Width: 3.6191

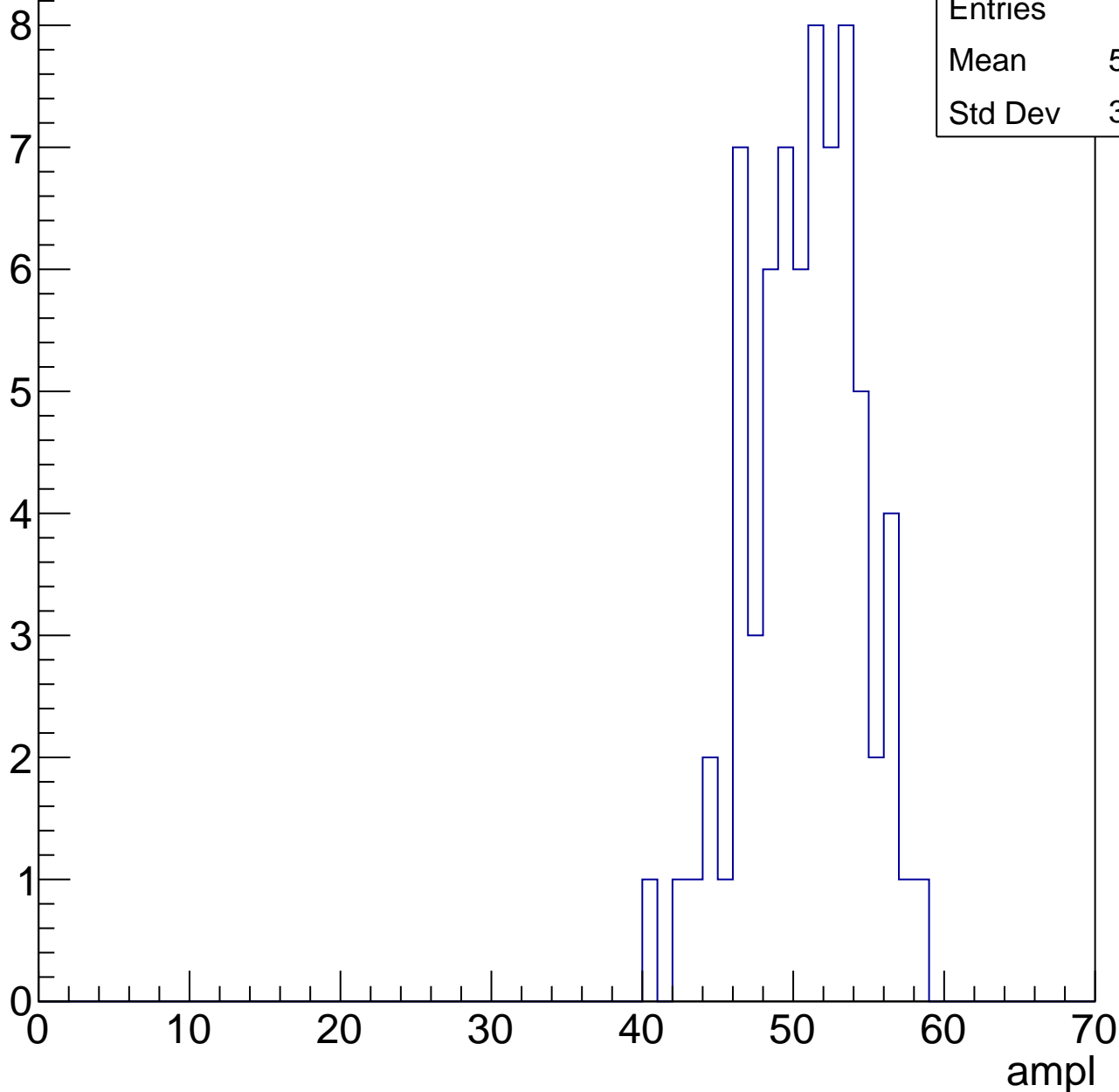


B0L001S, U22-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	50.24
Std Dev	3.698

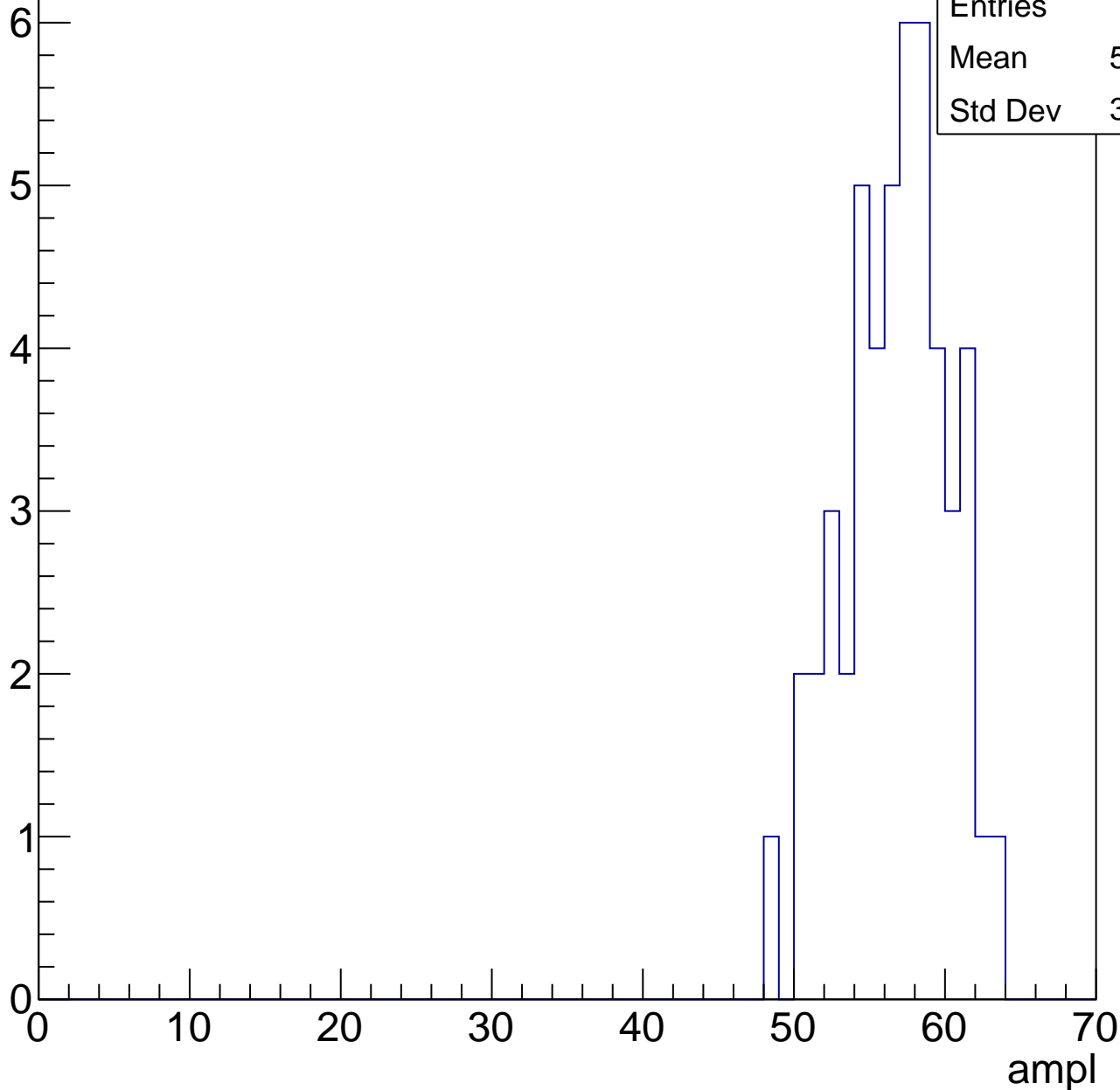


B0L001S, U22-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.27
Std Dev	3.415

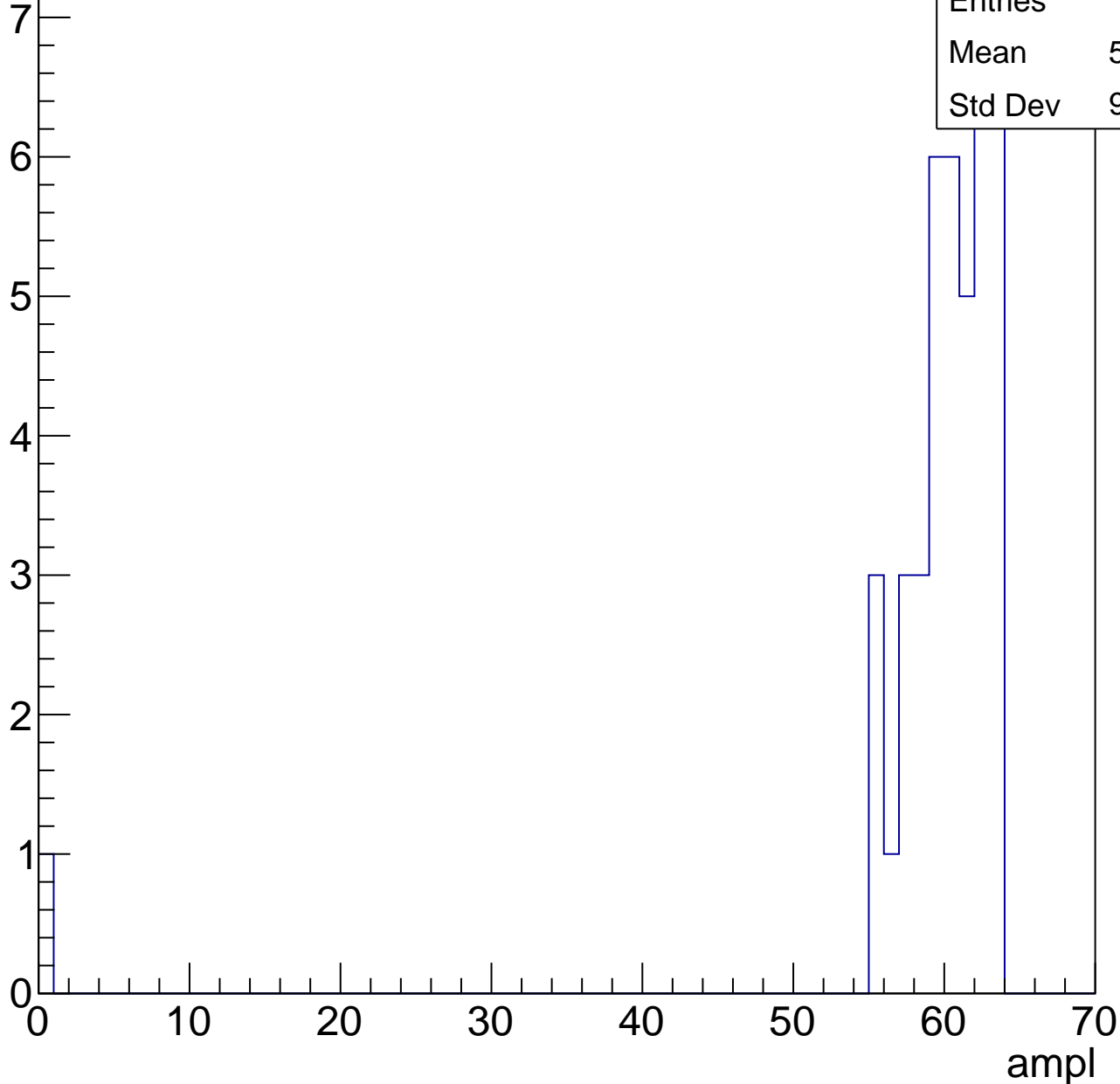


B0L001S, U22-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.57
Std Dev	9.444



B0L001S, U22-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70

B0L001S, U22-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U22-ch120, adc0

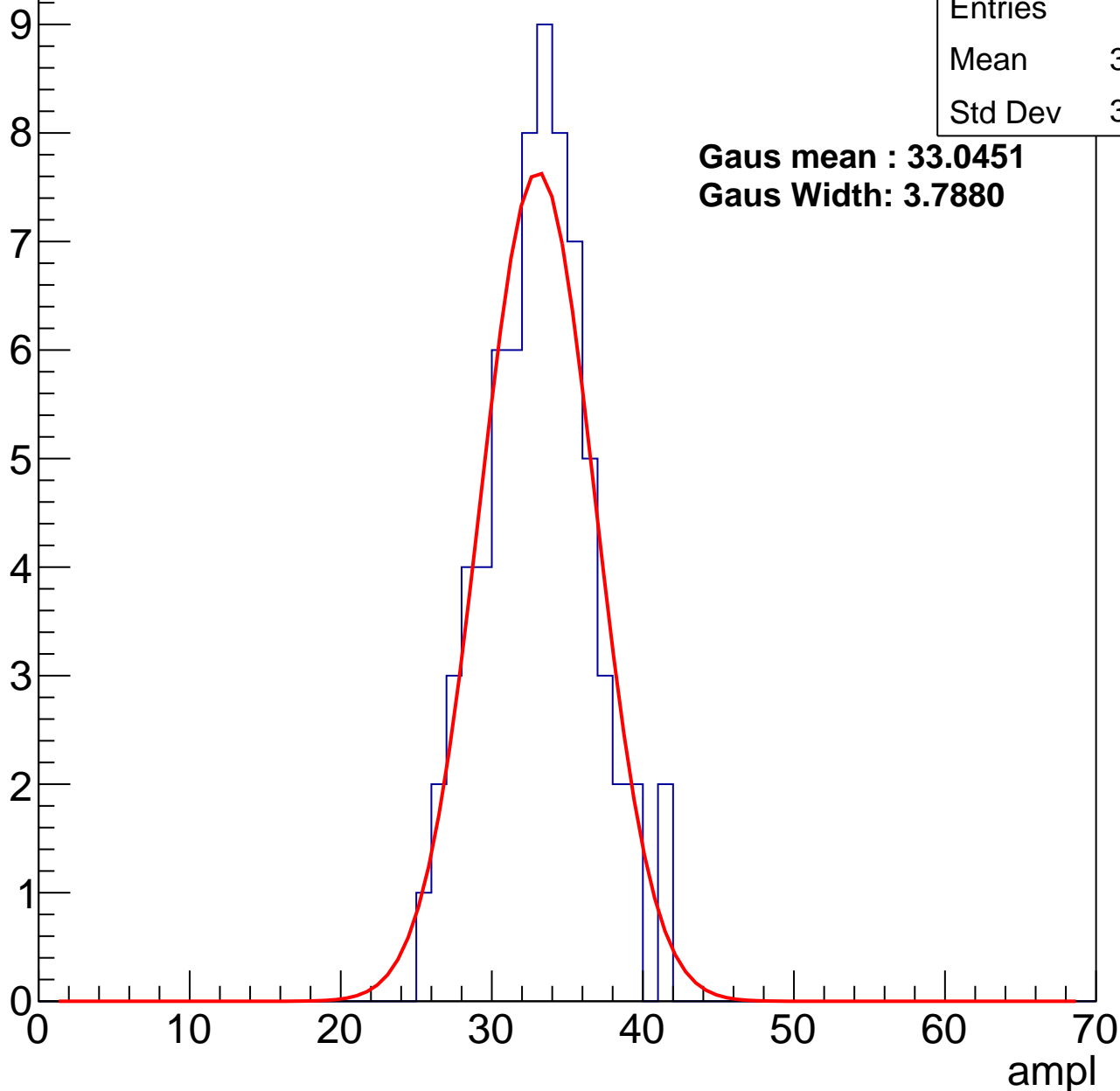
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	32.62
Std Dev	3.514

Gaus mean : 33.0451

Gaus Width: 3.7880



B0L001S, U22-ch120, adc1

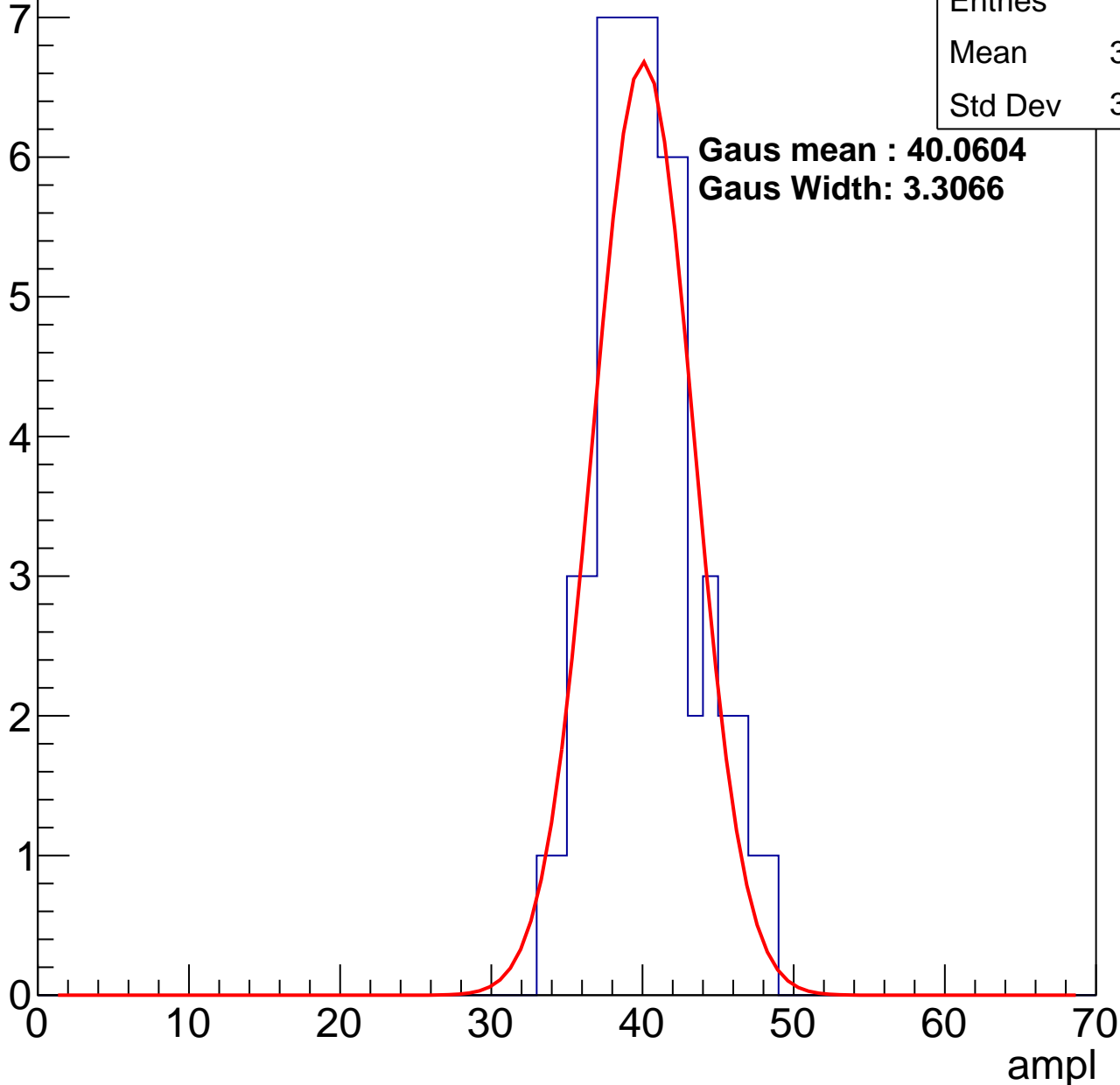
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	39.85
Std Dev	3.282

Gaus mean : 40.0604

Gaus Width: 3.3066



B0L001S, U22-ch120, adc2

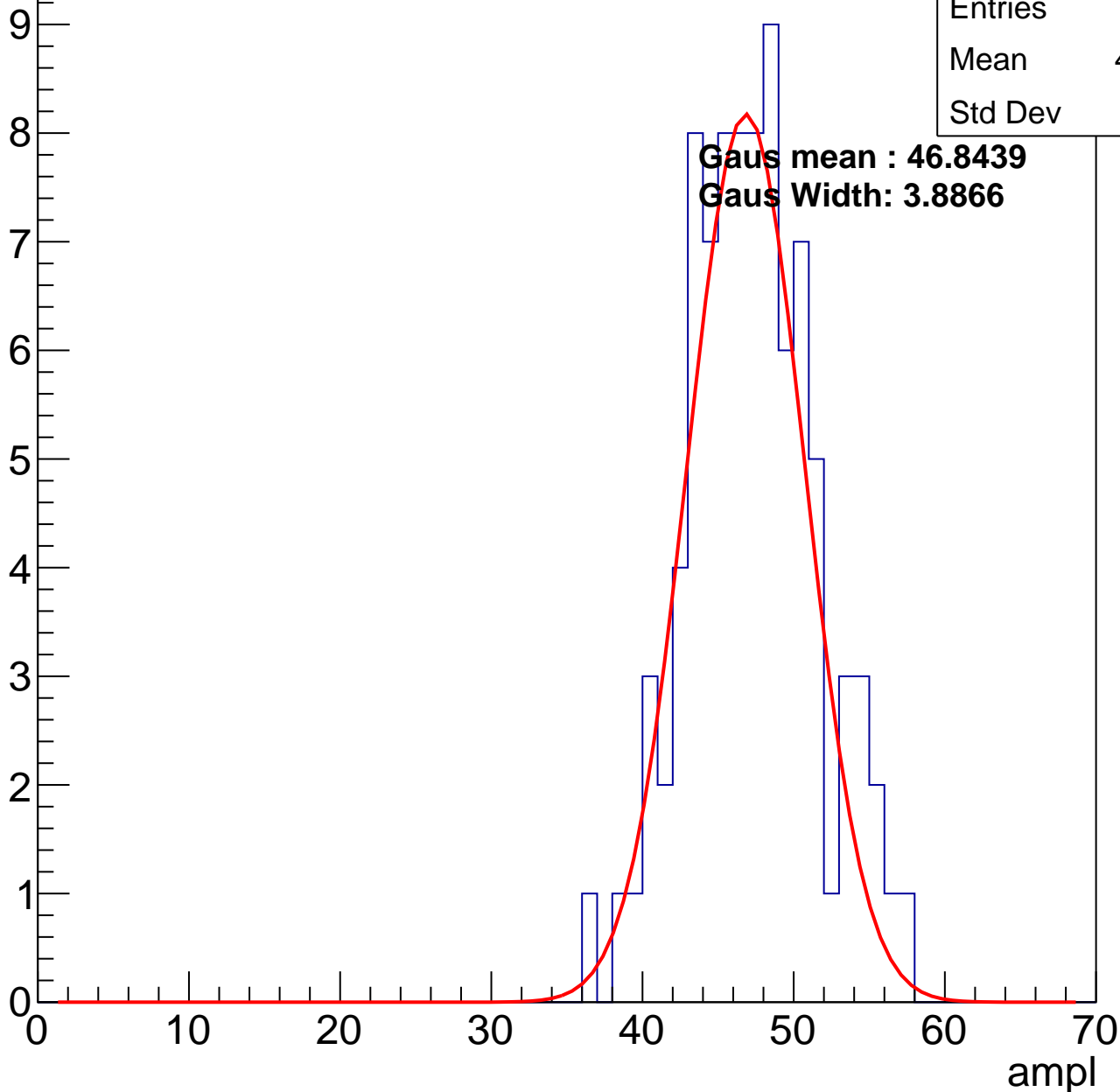
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	89
Mean	46.81
Std Dev	4.21

Gaus mean : 46.8439

Gaus Width: 3.8866

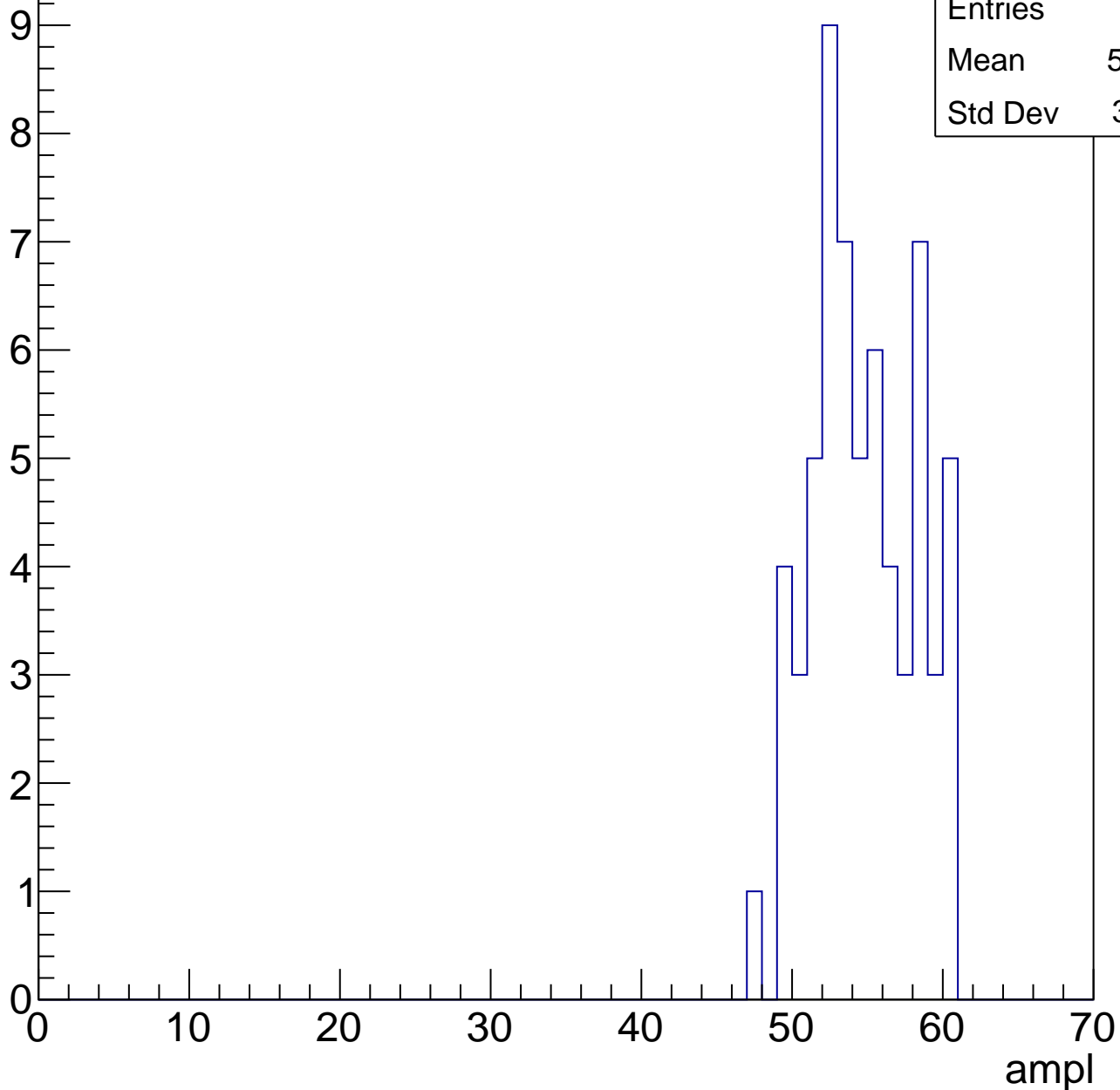


B0L001S, U22-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	54.27
Std Dev	3.351

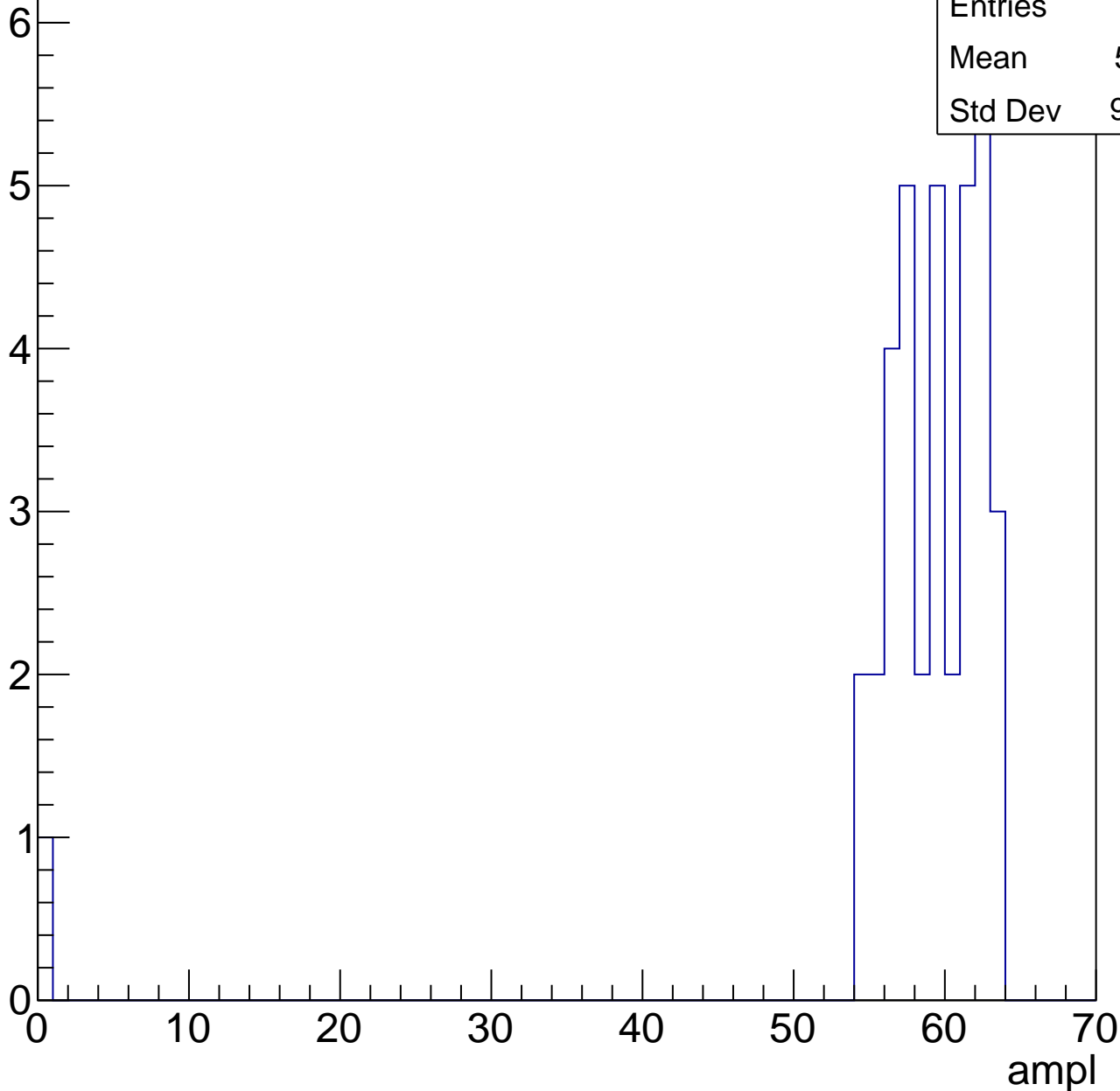


B0L001S, U22-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	57.41
Std Dev	9.933

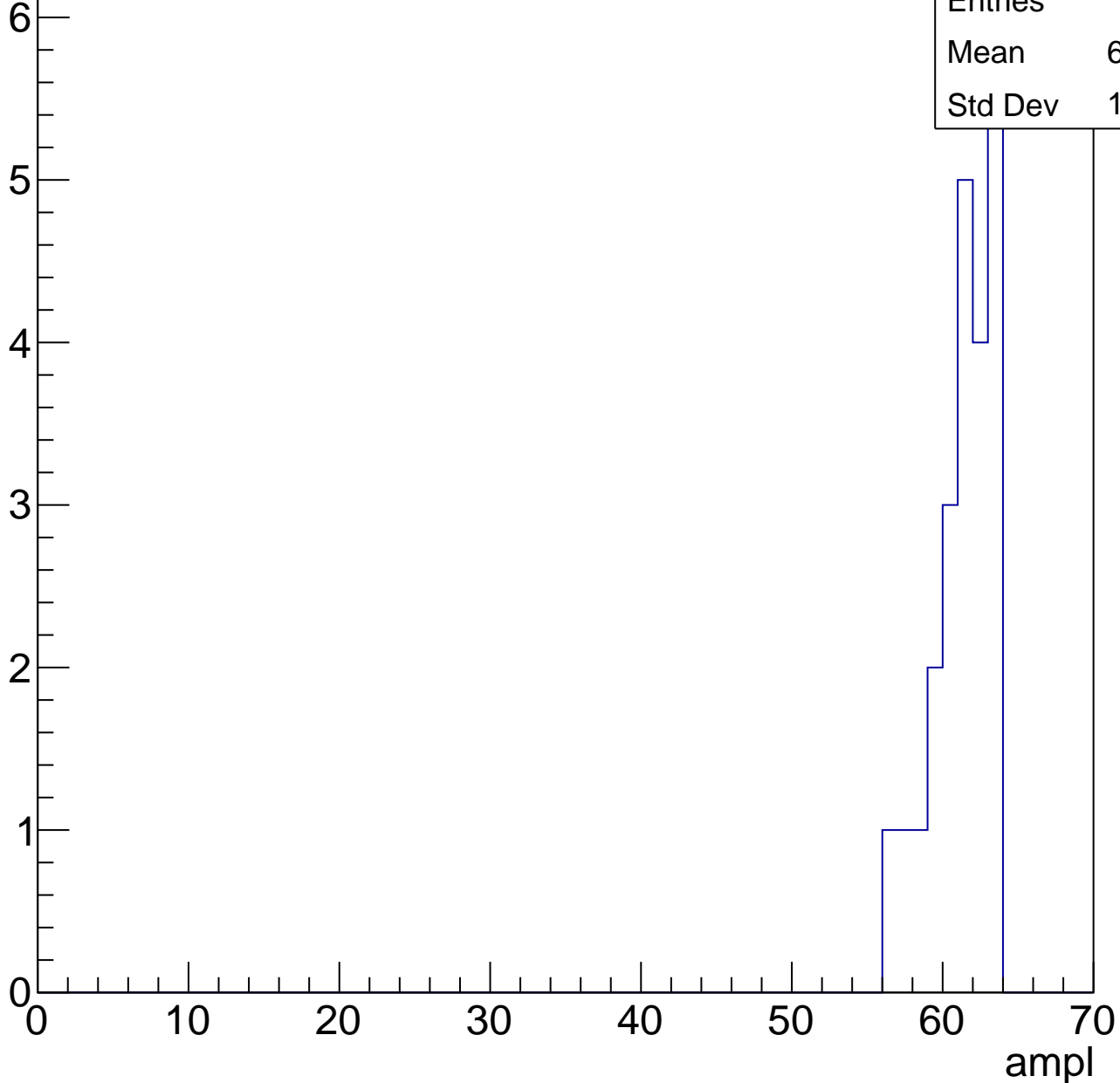


B0L001S, U22-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	23
Mean	60.87
Std Dev	1.963



B0L001S, U22-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

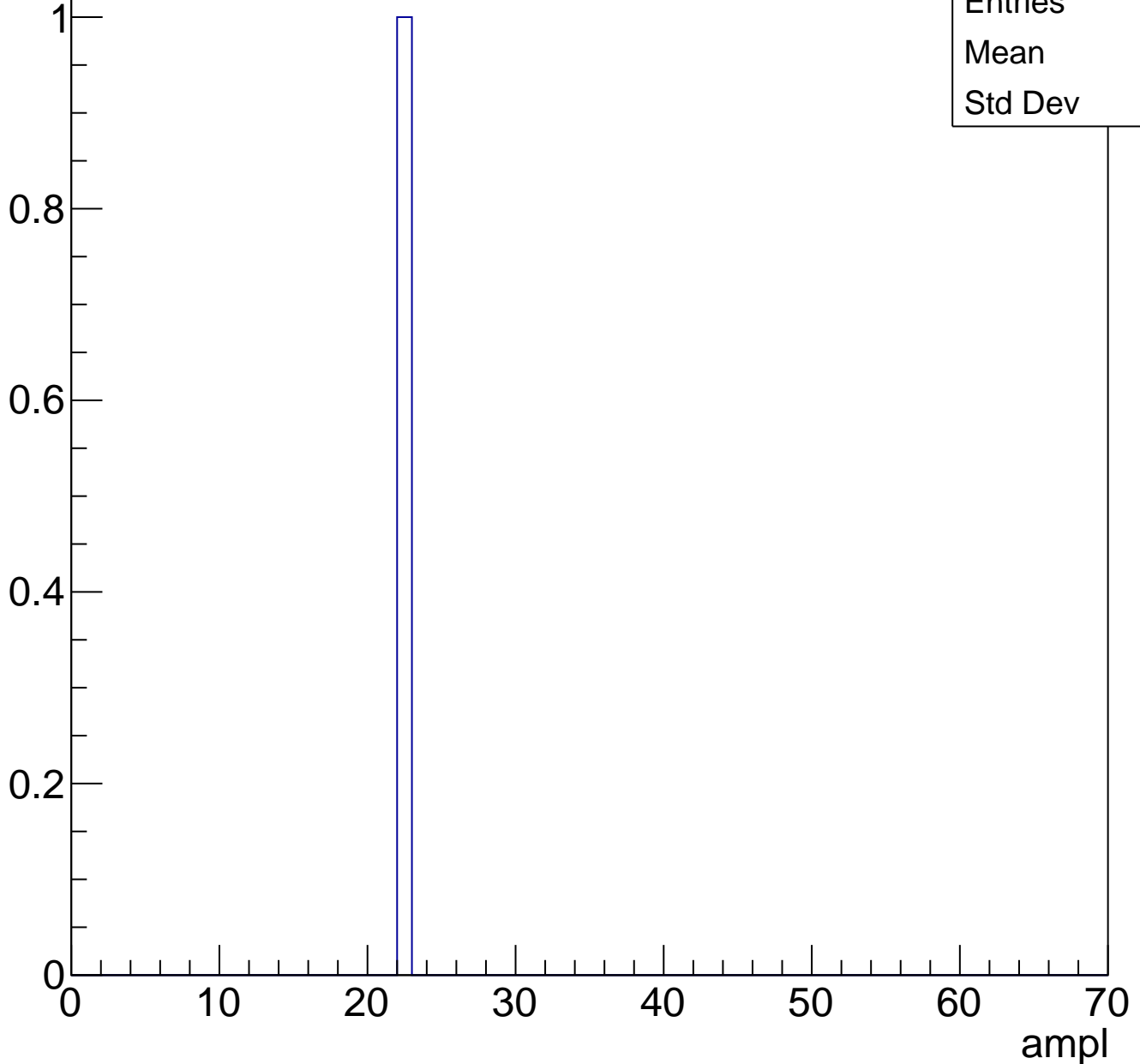
Entry



B0L001S, U22-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	22
Std Dev	0

B0L001S, U22-ch121, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	31.21
Std Dev	5.422

Gaus mean : 31.9375

Gaus Width: 3.6237

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L001S, U22-ch121, adc1

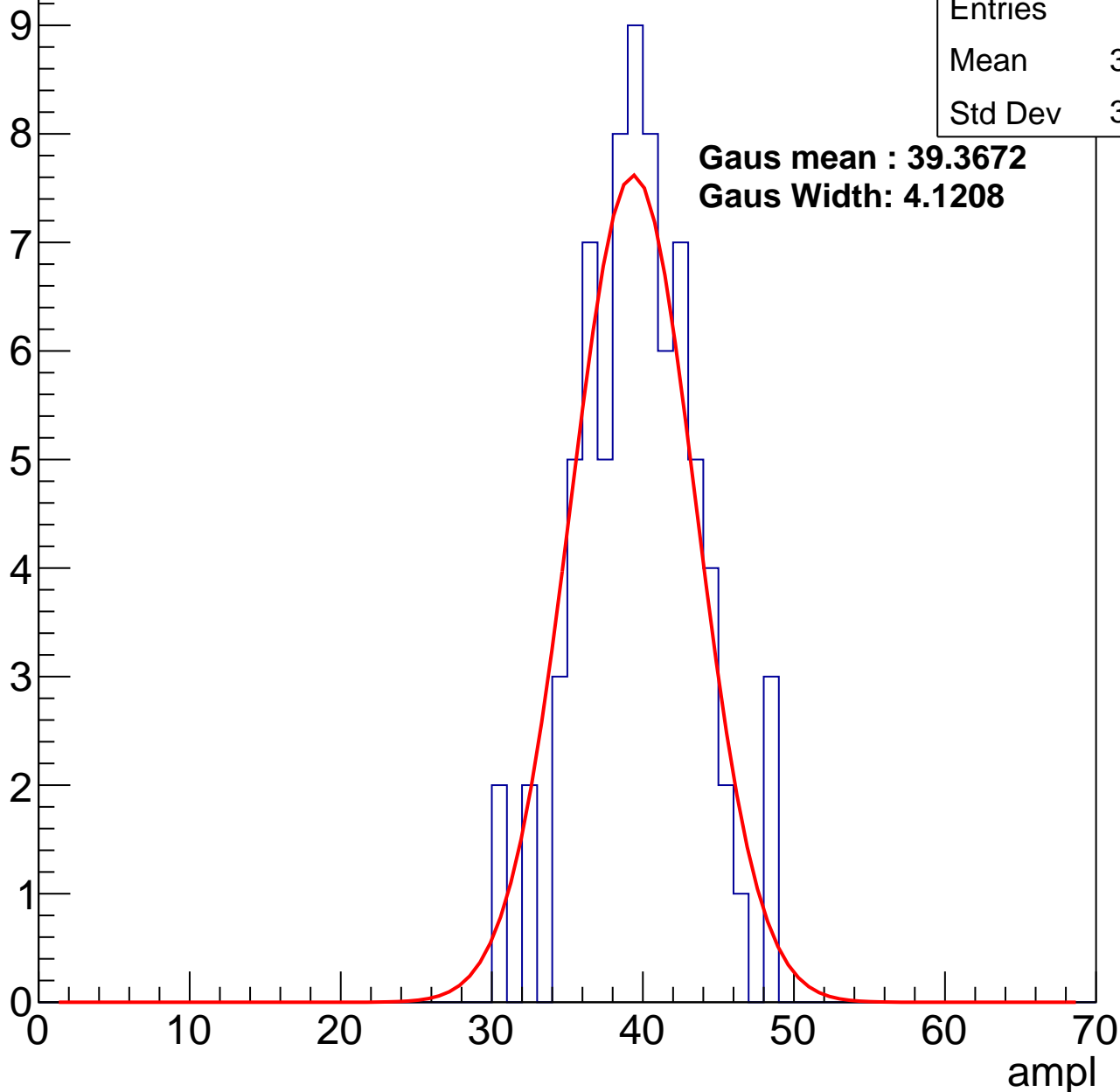
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	39.27
Std Dev	3.847

Gaus mean : 39.3672

Gaus Width: 4.1208



B0L001S, U22-ch121, adc2

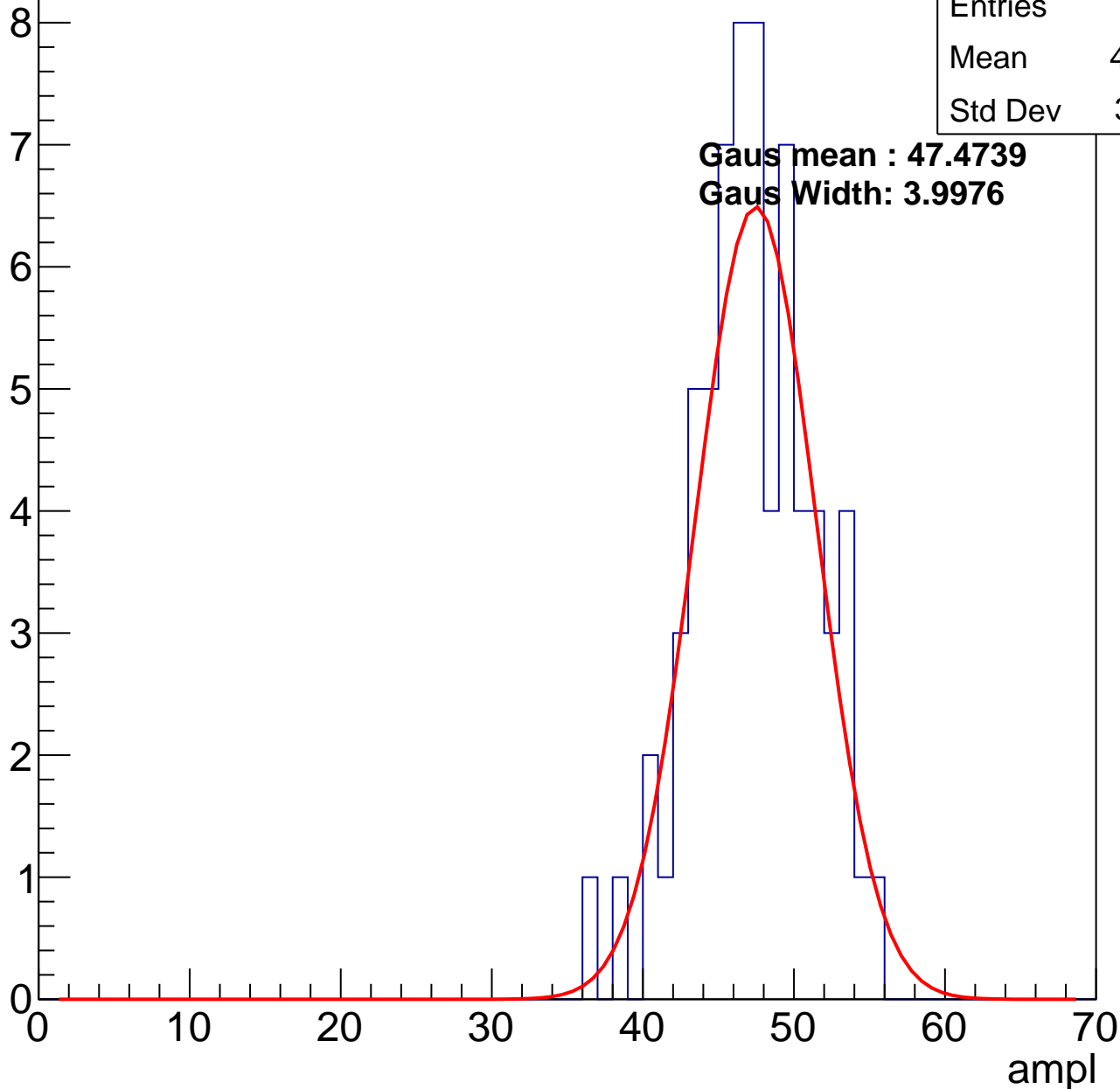
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	46.83
Std Dev	3.871

Gaus mean : 47.4739

Gaus Width: 3.9976

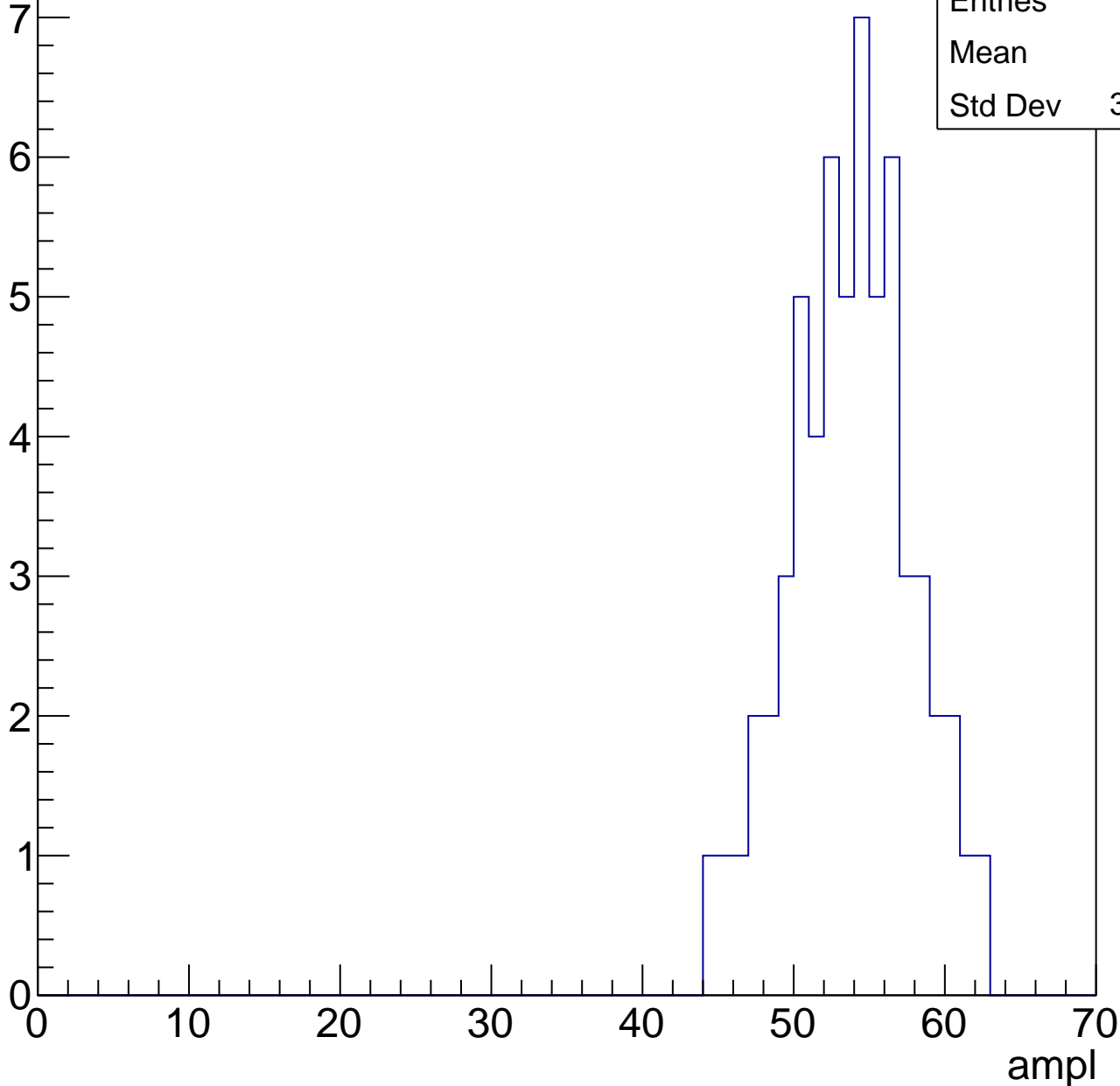


B0L001S, U22-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	53.3
Std Dev	3.968

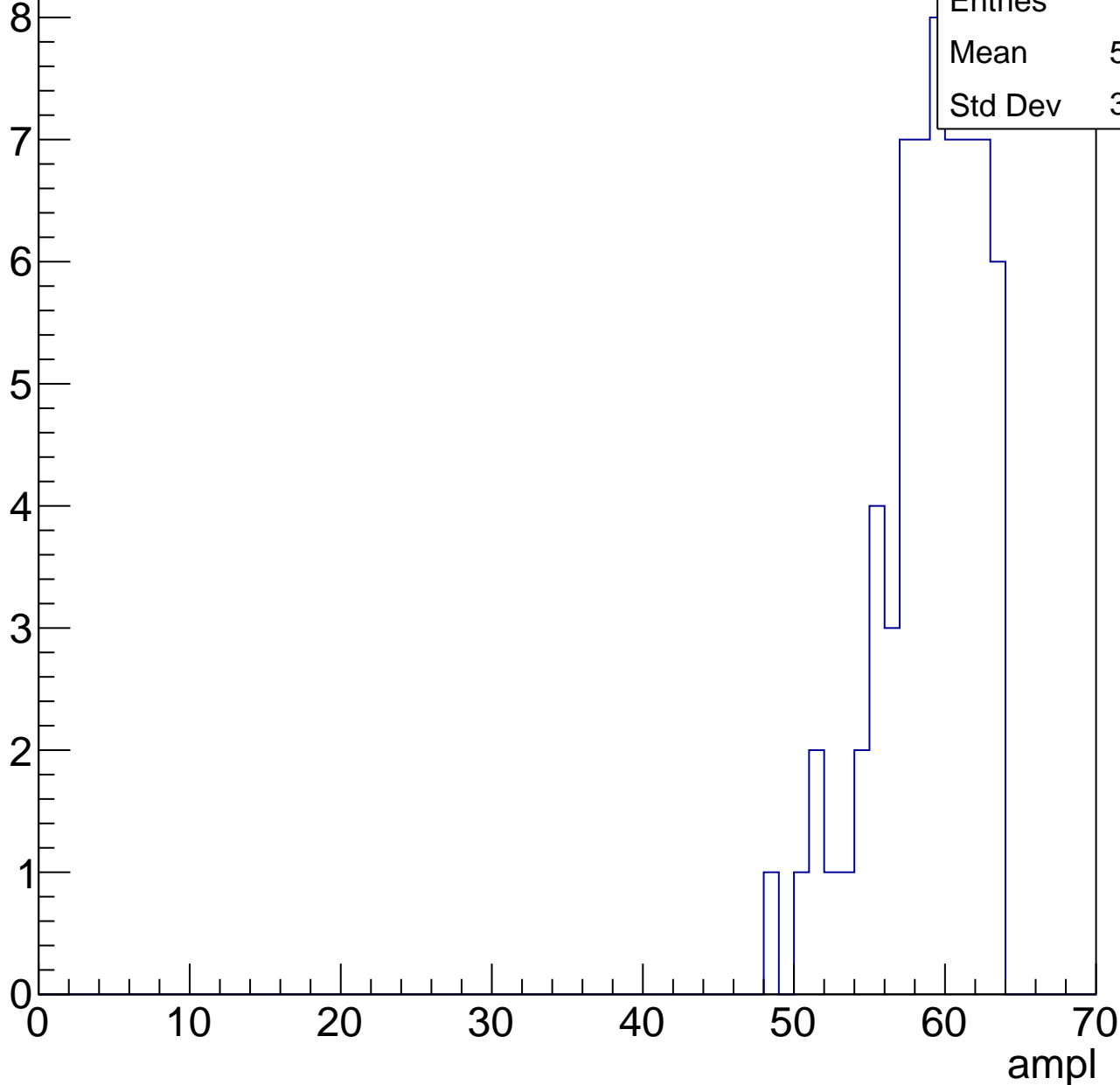


B0L001S, U22-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

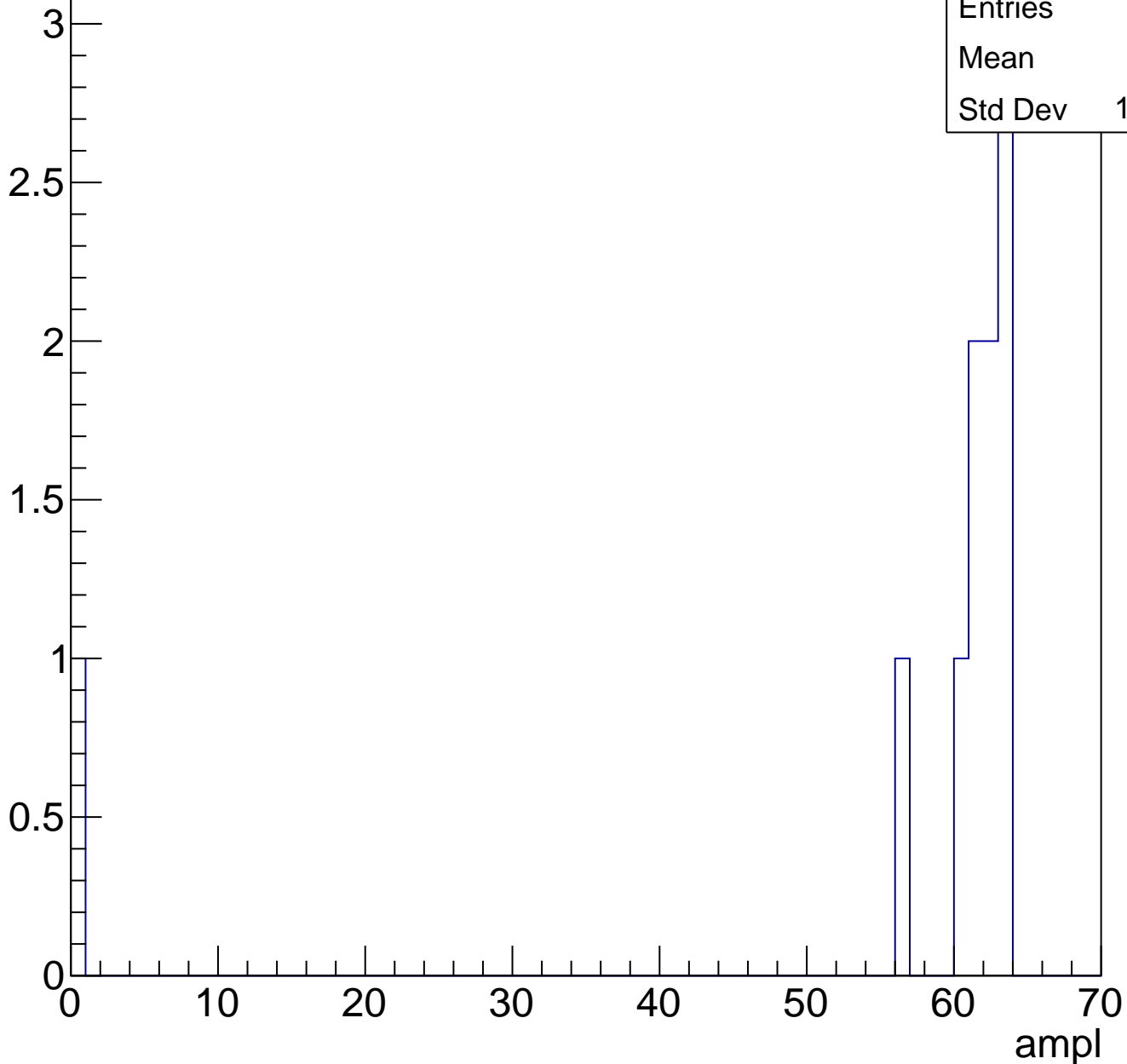
Entries	64
Mean	58.39
Std Dev	3.449



B0L001S, U22-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U22-ch122, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	29.01
Std Dev	6.71

Gaus mean : 30.8661

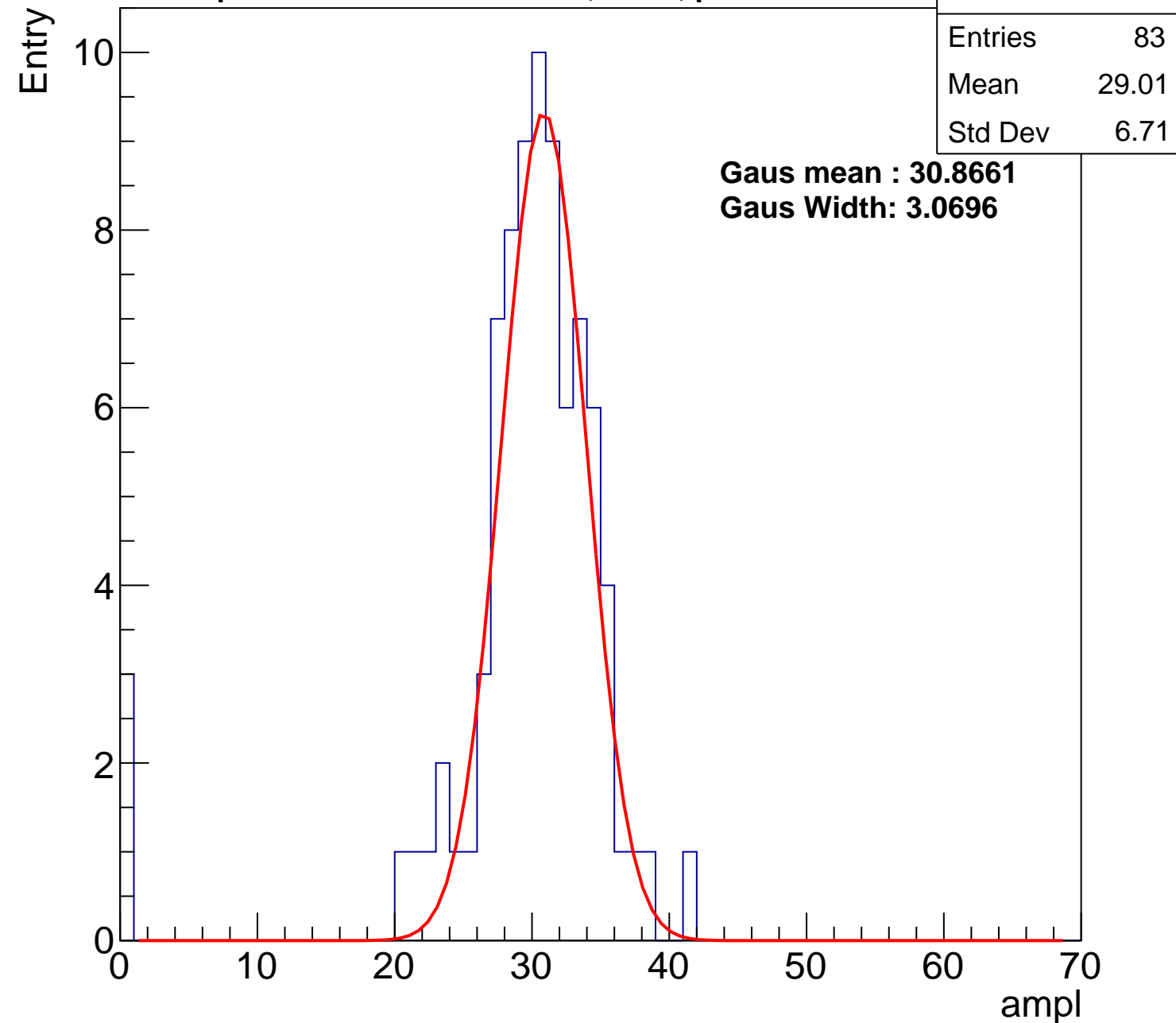
Gaus Width: 3.0696

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch122, adc1

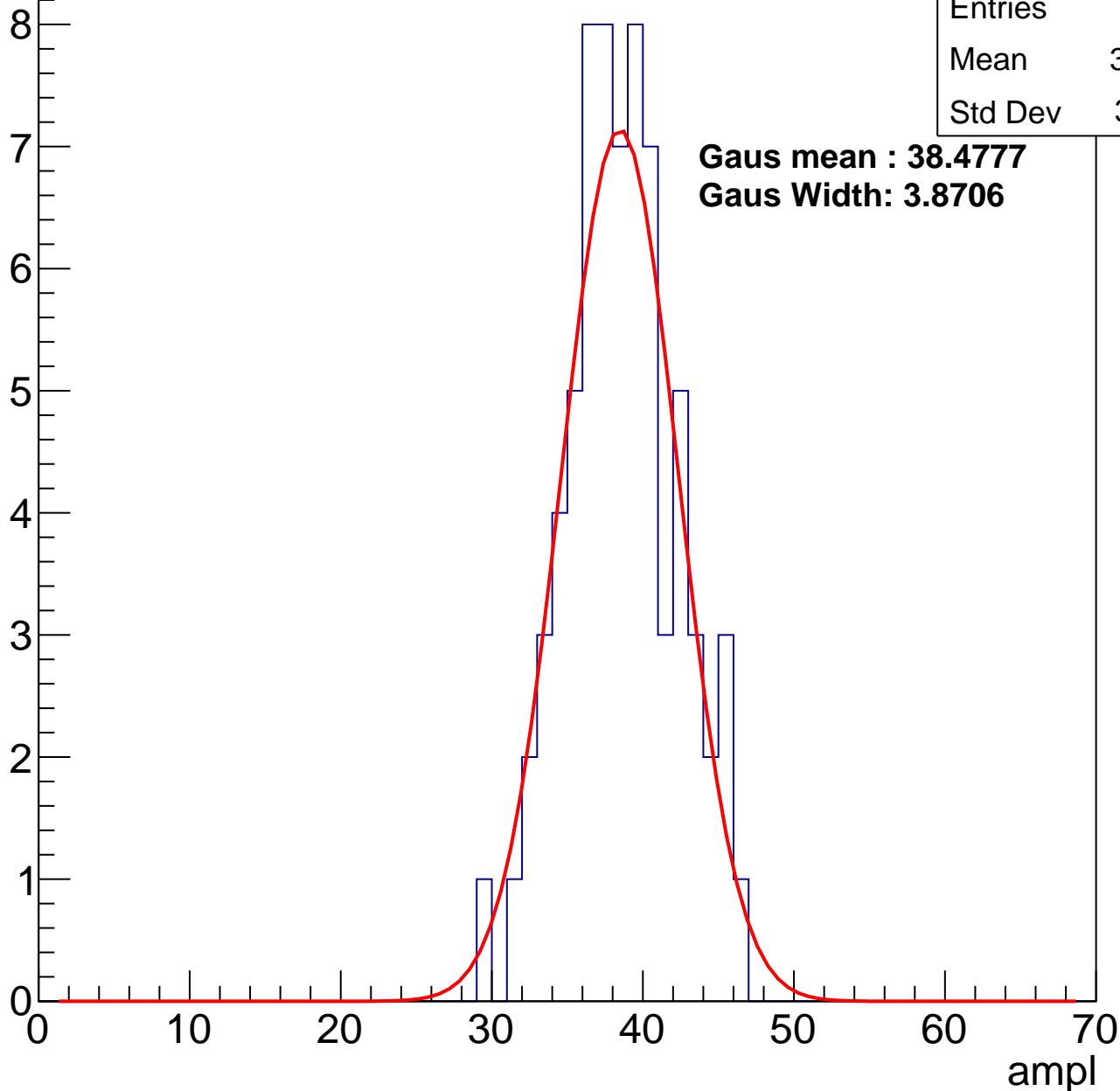
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.13
Std Dev	3.611

Gaus mean : 38.4777

Gaus Width: 3.8706



B0L001S, U22-ch122, adc2

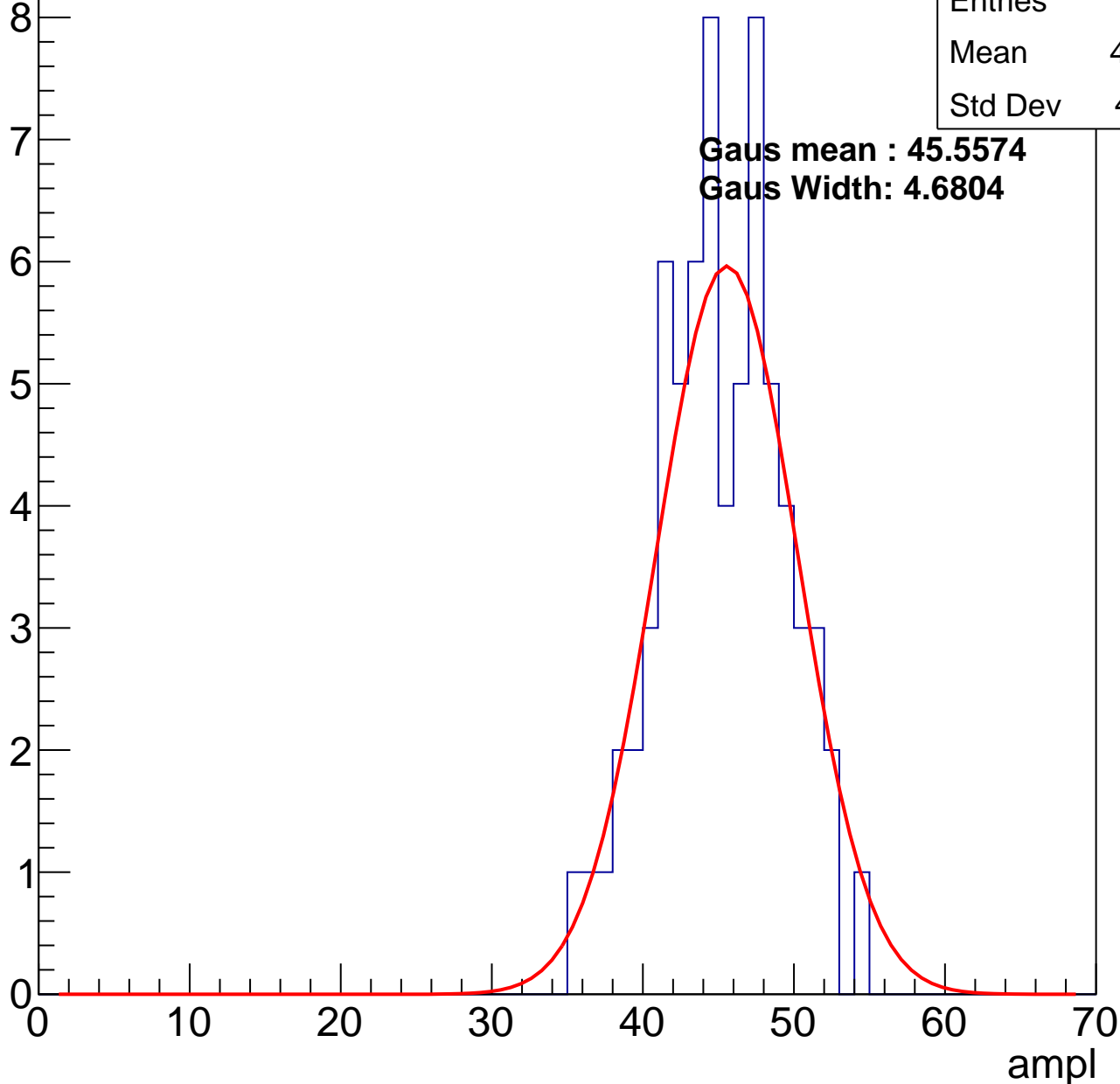
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.73
Std Dev	4.081

Gaus mean : 45.5574

Gaus Width: 4.6804

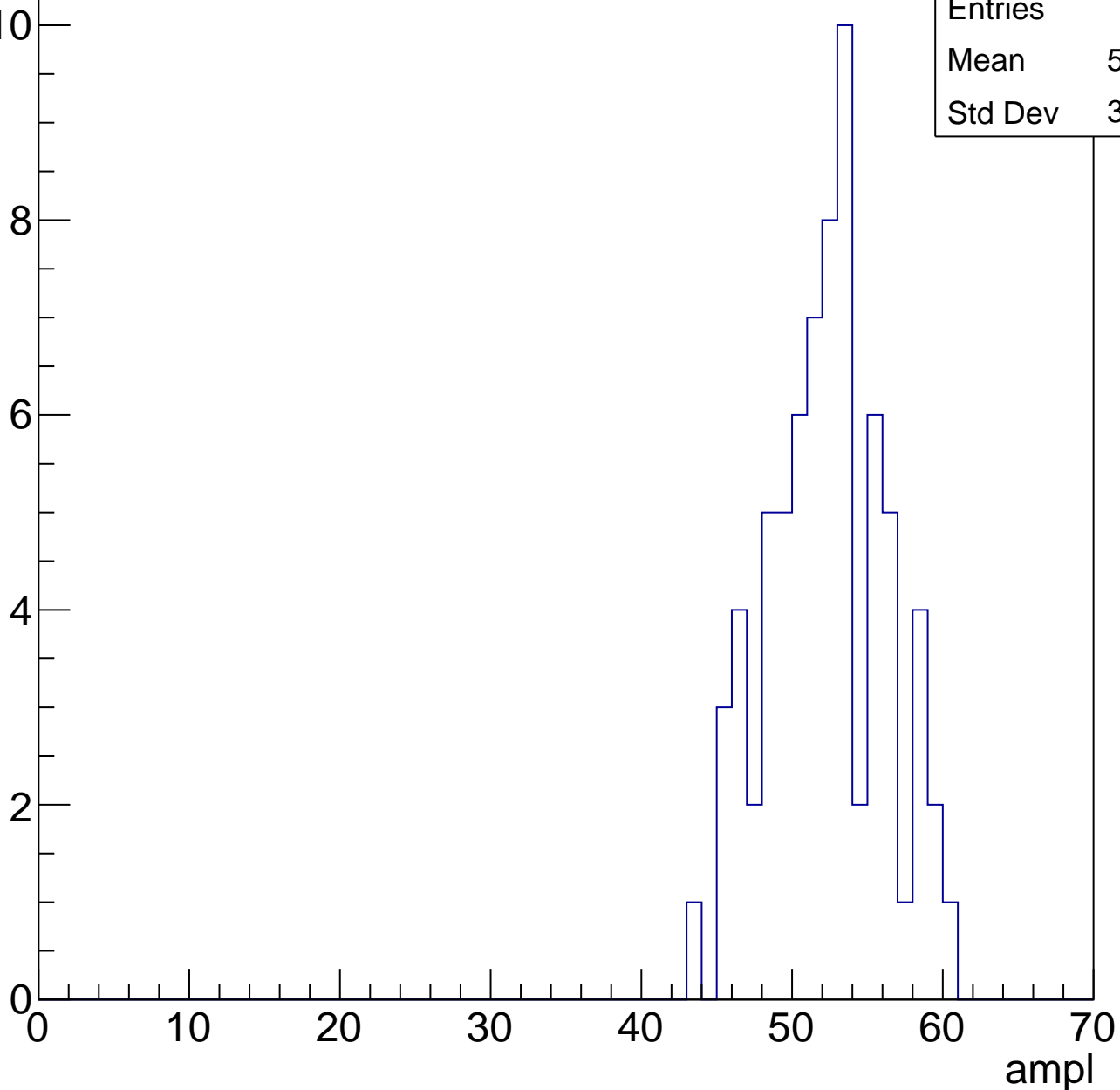


B0L001S, U22-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	51.79
Std Dev	3.833

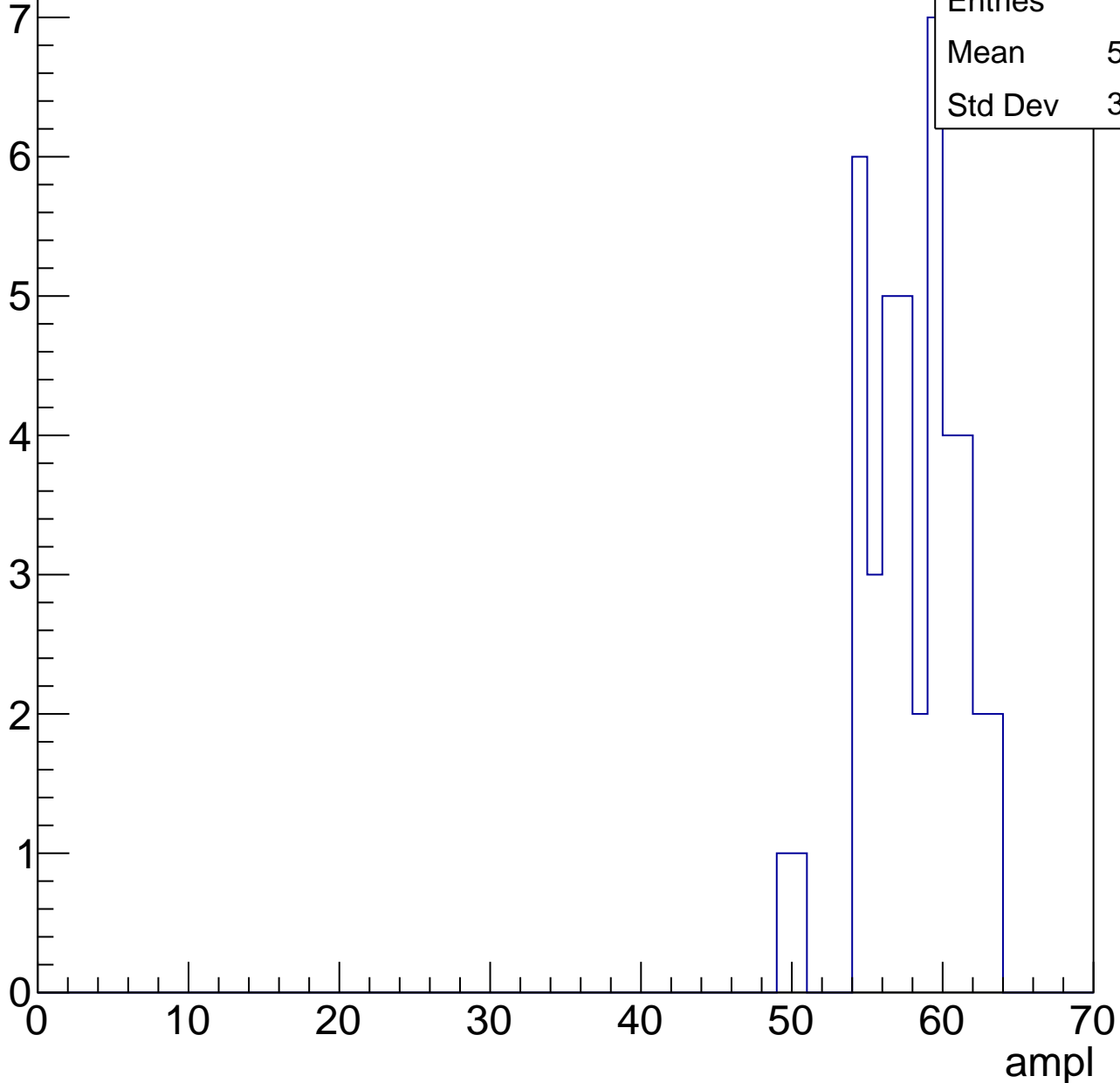


B0L001S, U22-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	57.52
Std Dev	3.179

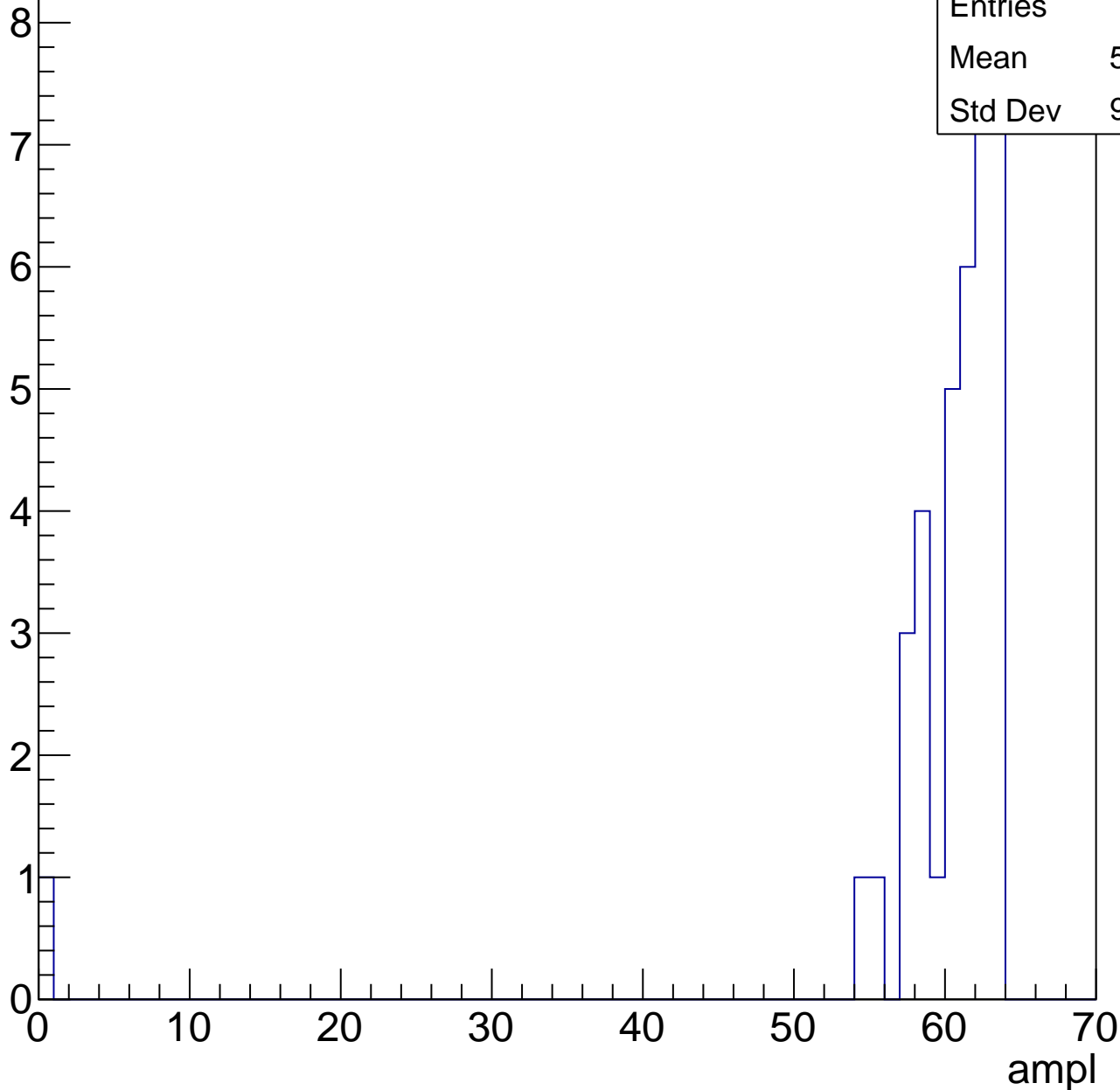


B0L001S, U22-ch122, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

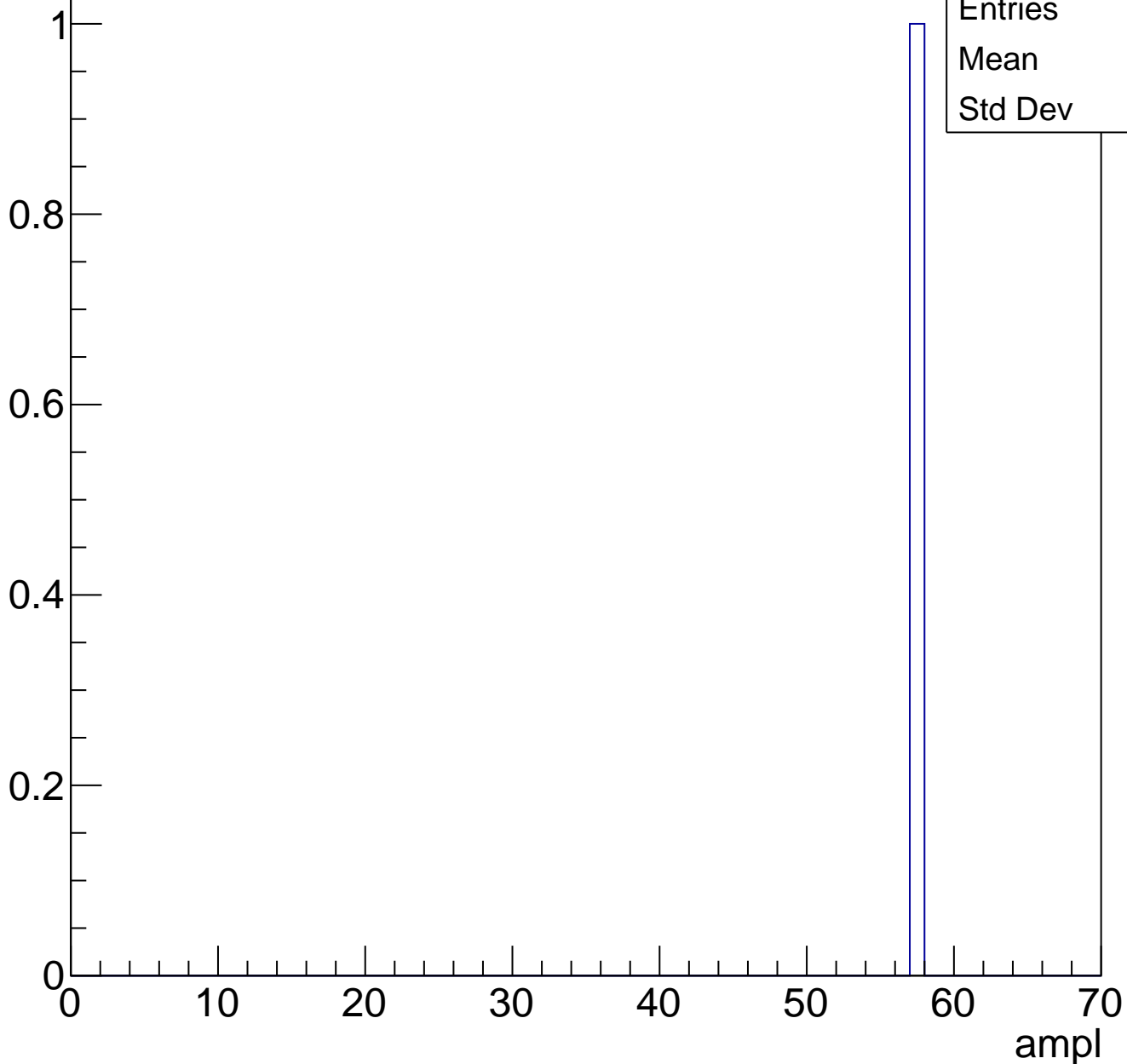
Entries	38
Mean	58.87
Std Dev	9.956



B0L001S, U22-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	57
Std Dev	0

B0L001S, U22-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch123, adc0

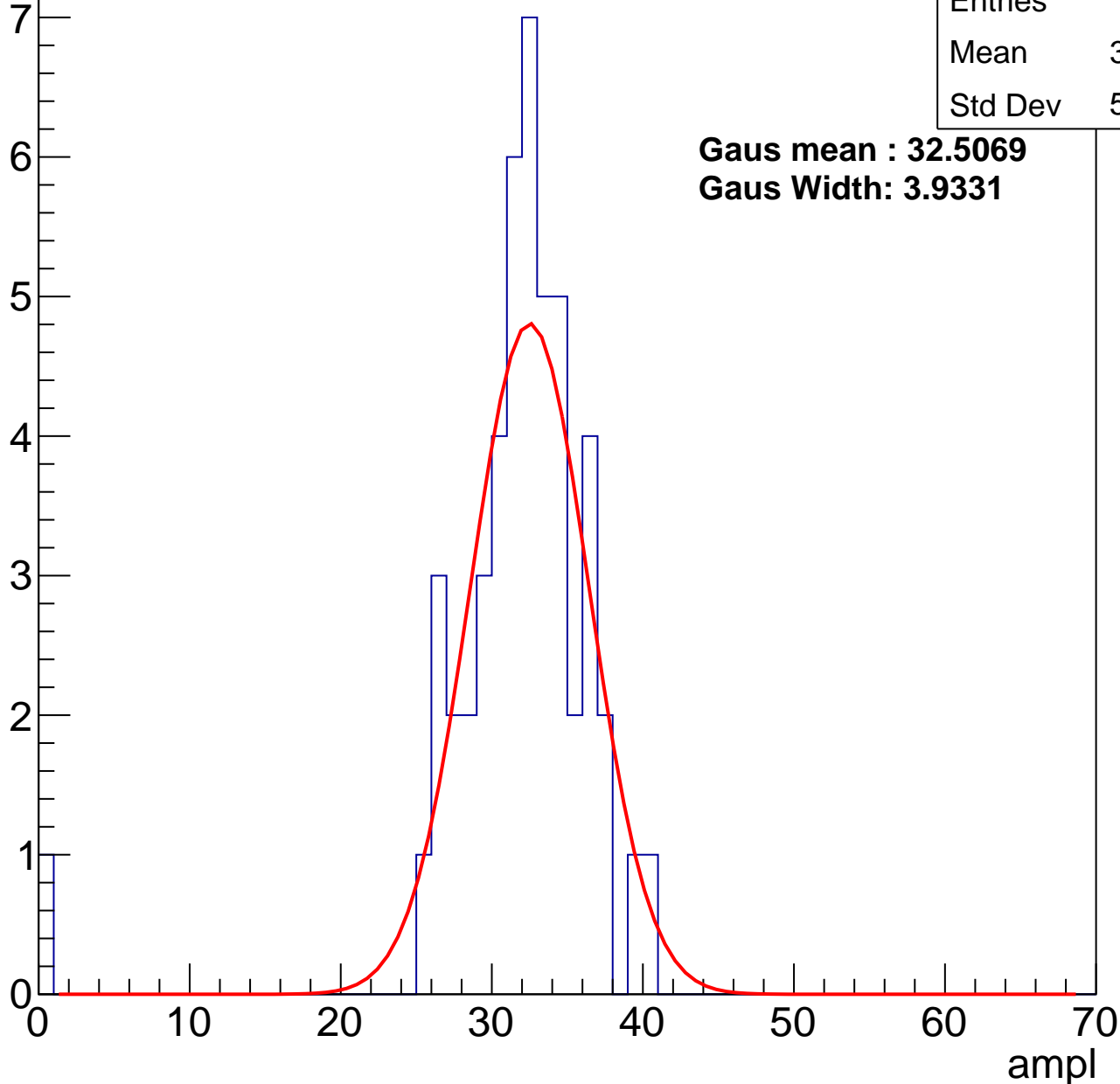
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	31.27
Std Dev	5.634

Gaus mean : 32.5069

Gaus Width: 3.9331



B0L001S, U22-ch123, adc1

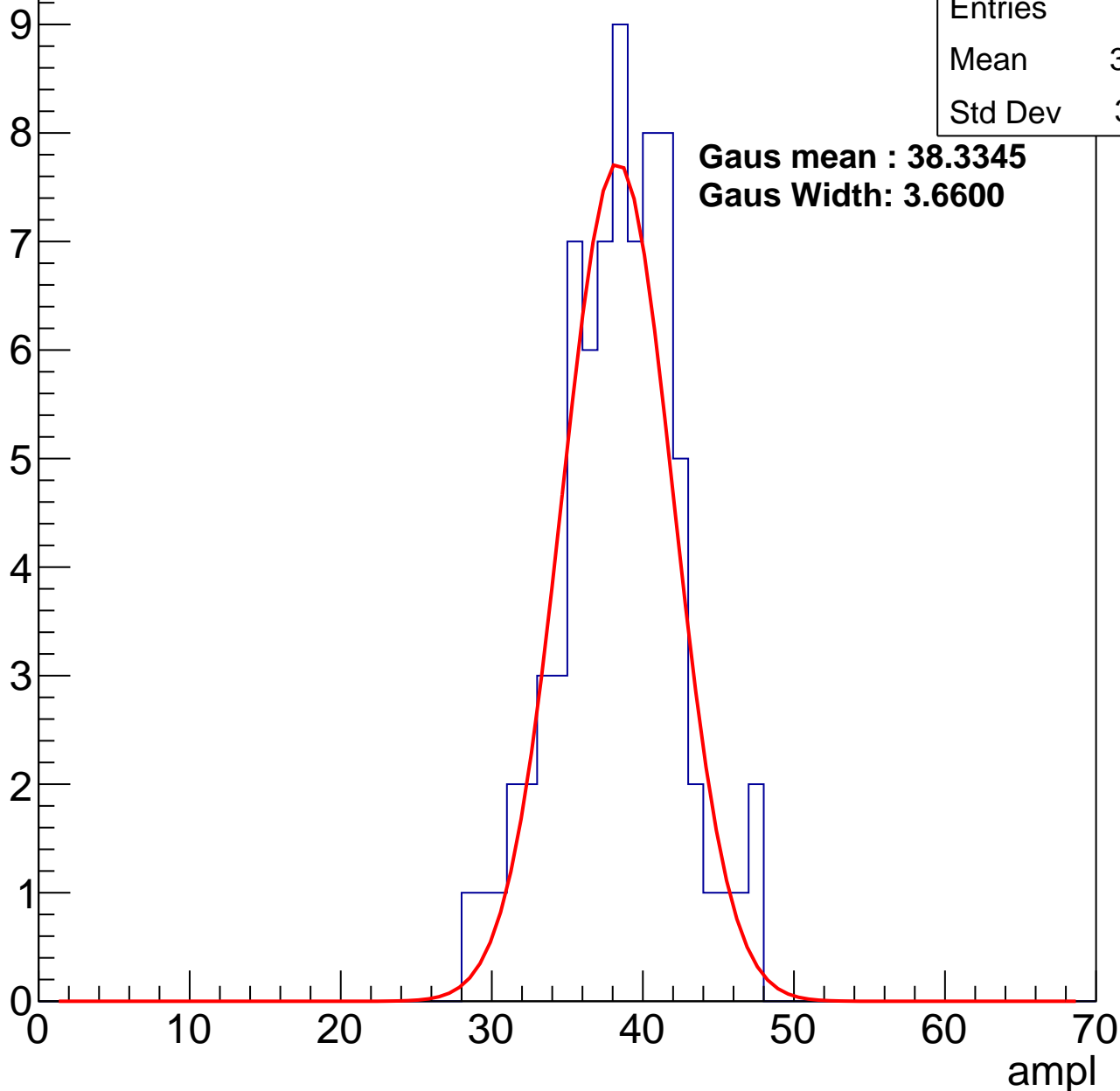
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	37.95
Std Dev	3.921

Gaus mean : 38.3345

Gaus Width: 3.6600



B0L001S, U22-ch123, adc2

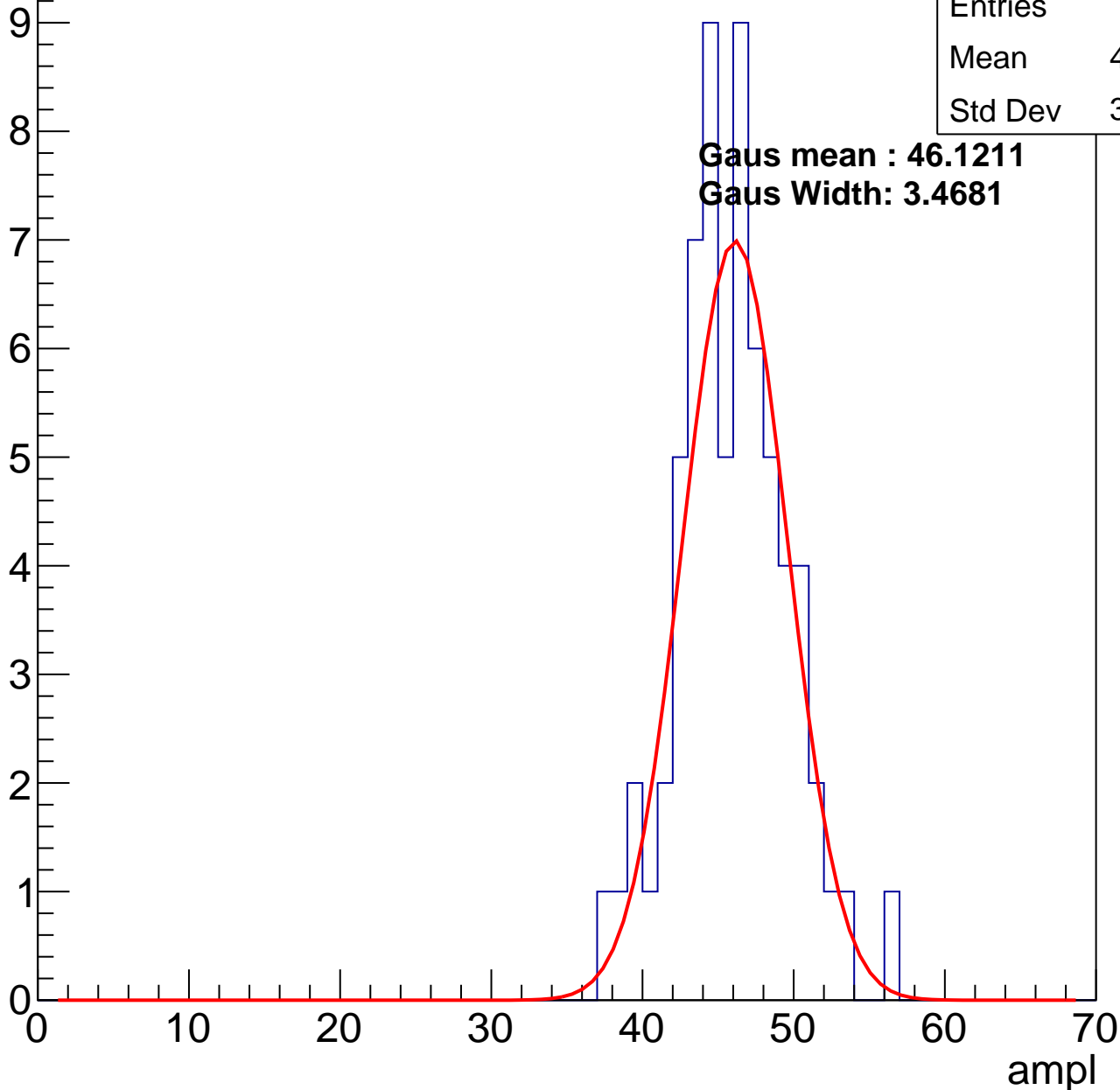
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	45.48
Std Dev	3.602

Gaus mean : 46.1211

Gaus Width: 3.4681

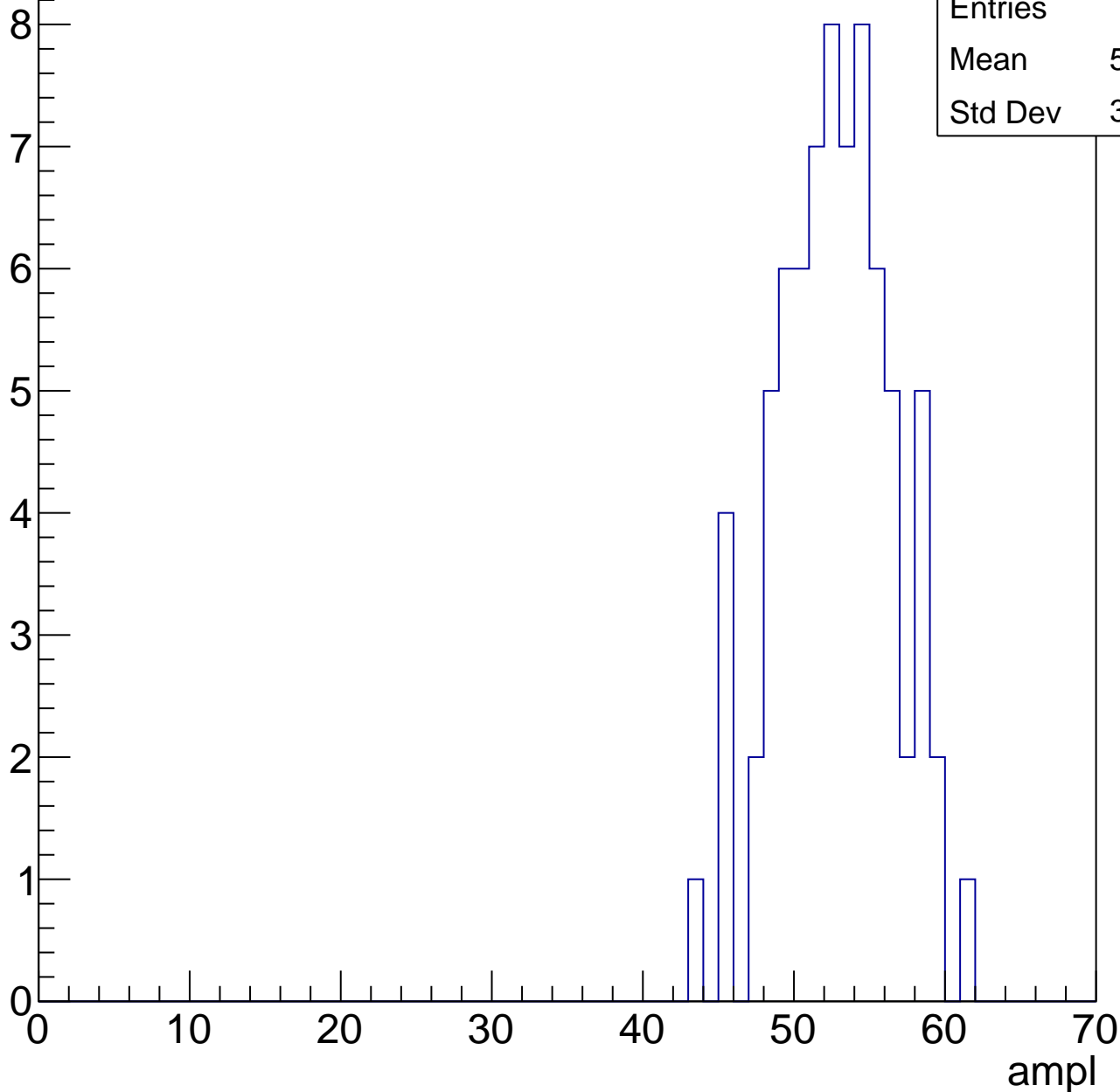


B0L001S, U22-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	52.27
Std Dev	3.775

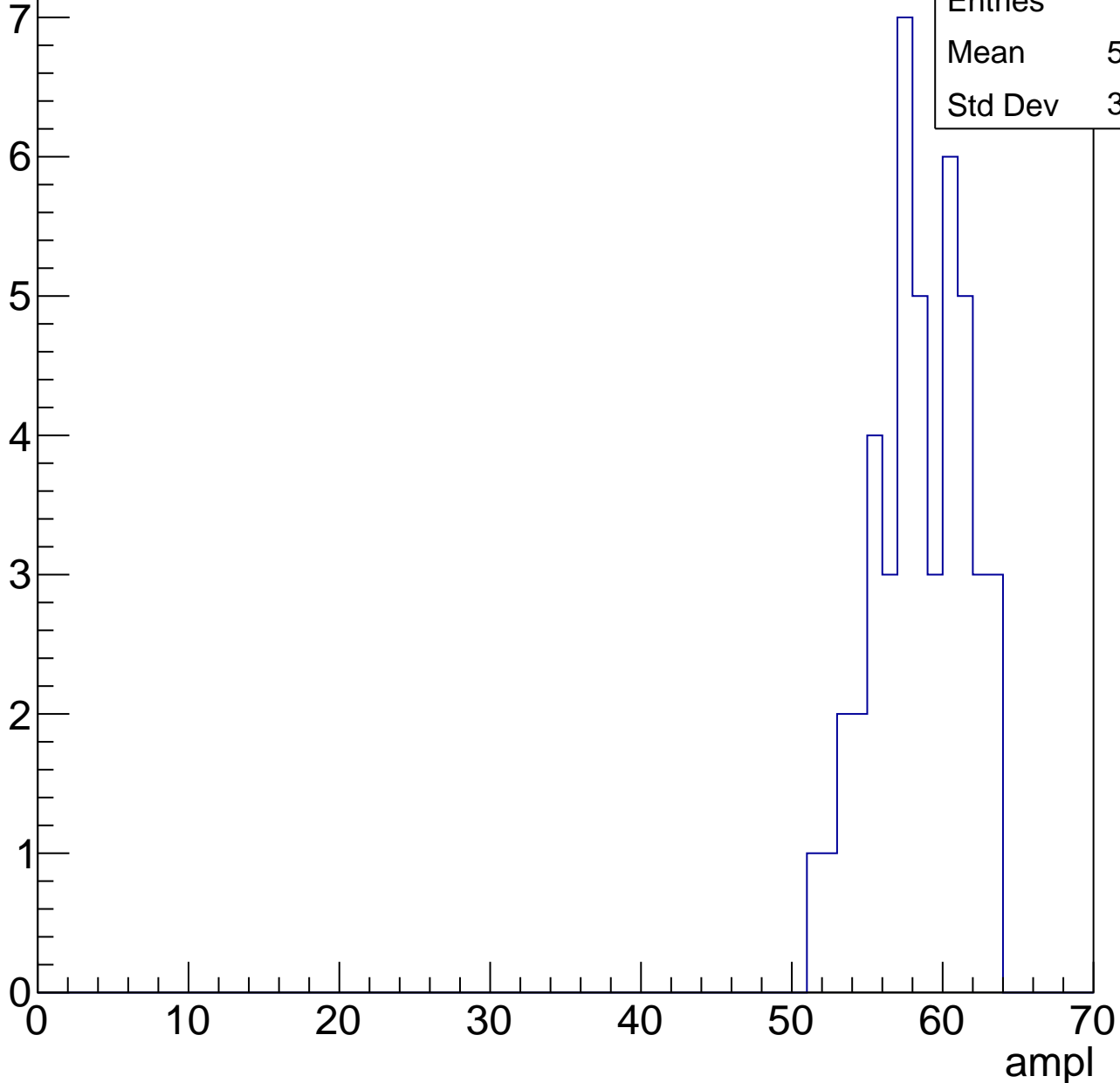


B0L001S, U22-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.02
Std Dev	3.044

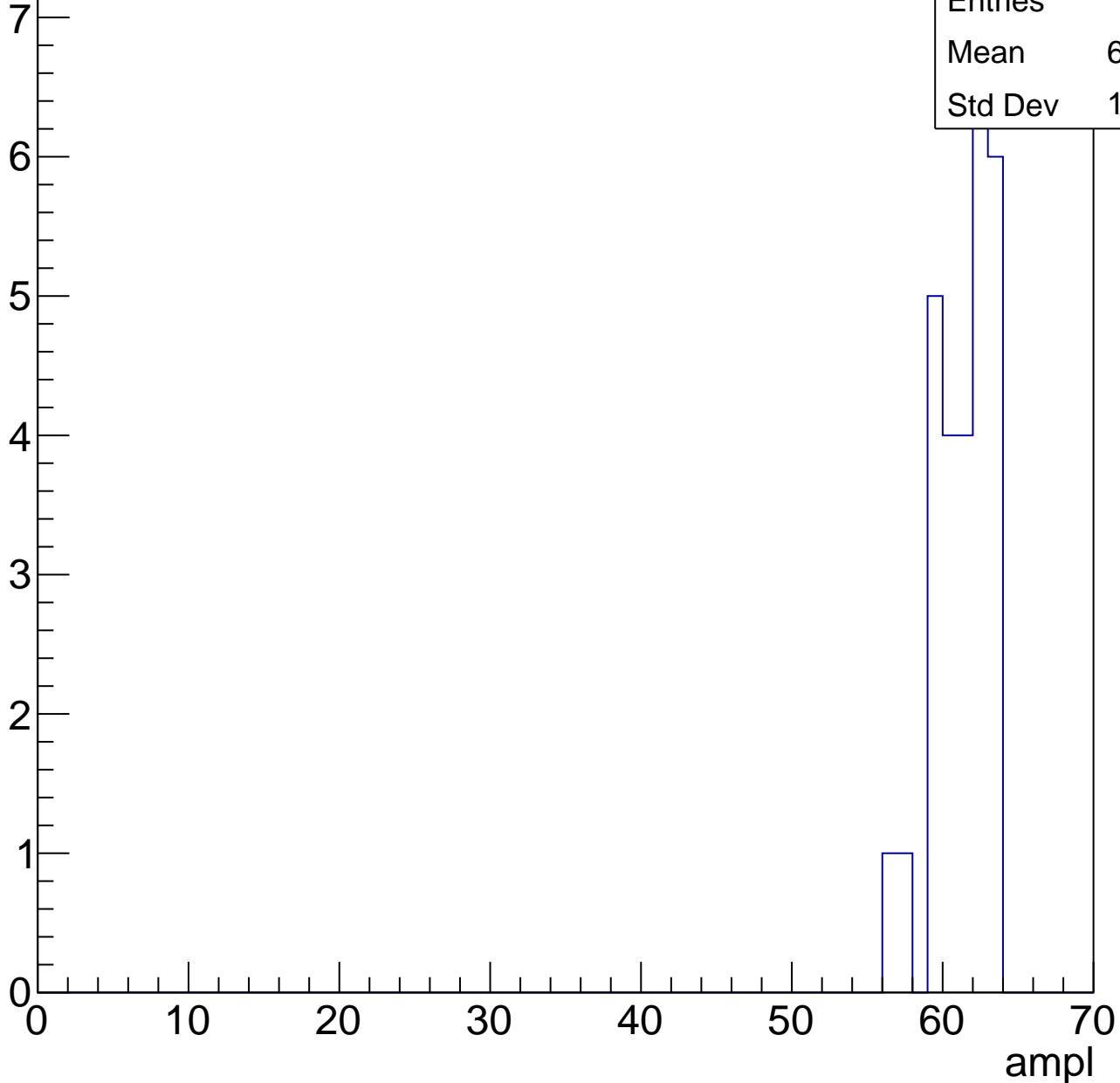


B0L001S, U22-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	28
Mean	60.86
Std Dev	1.846



B0L001S, U22-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch124, adc0

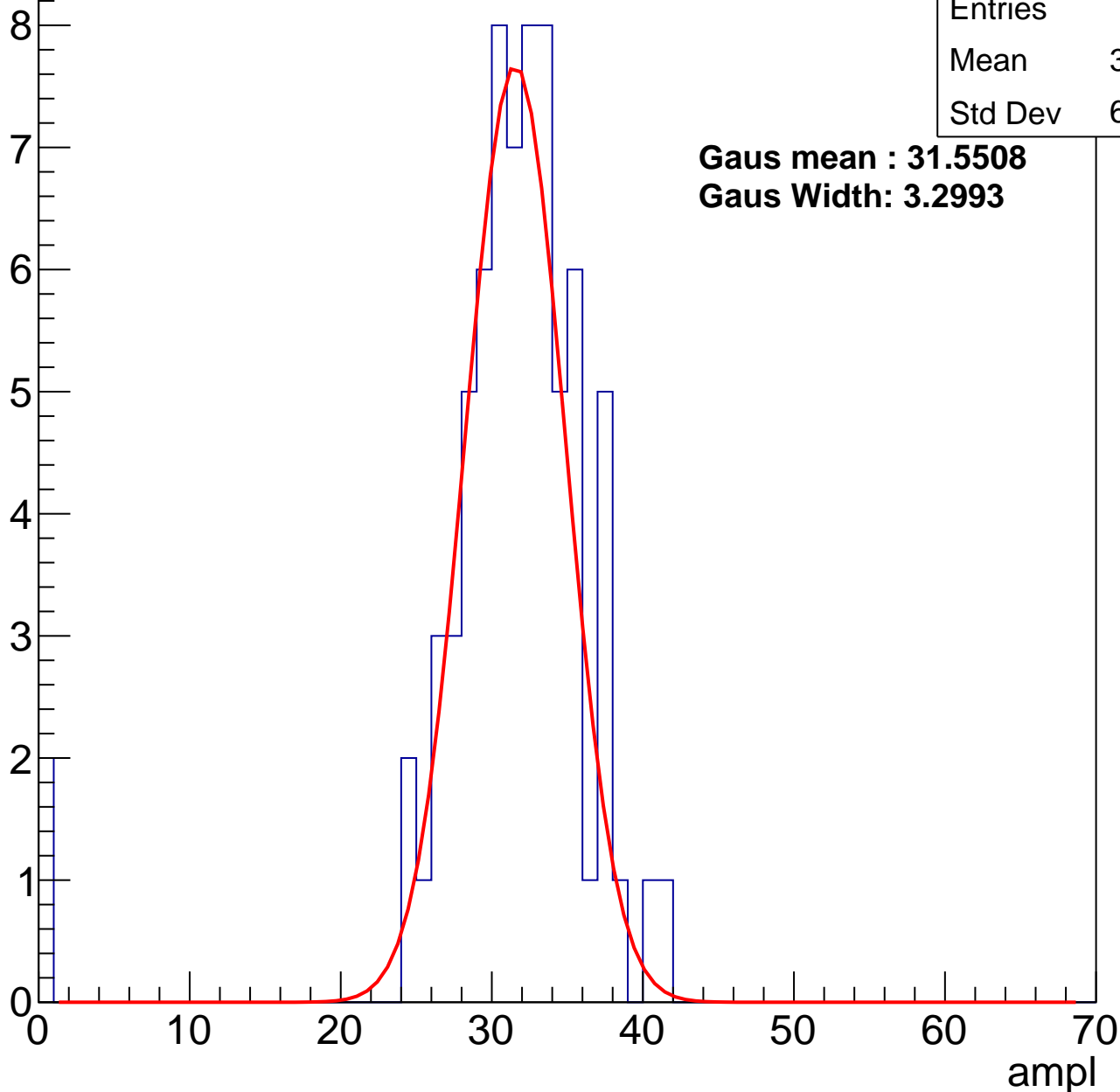
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	30.73
Std Dev	6.266

Gaus mean : 31.5508

Gaus Width: 3.2993



B0L001S, U22-ch124, adc1

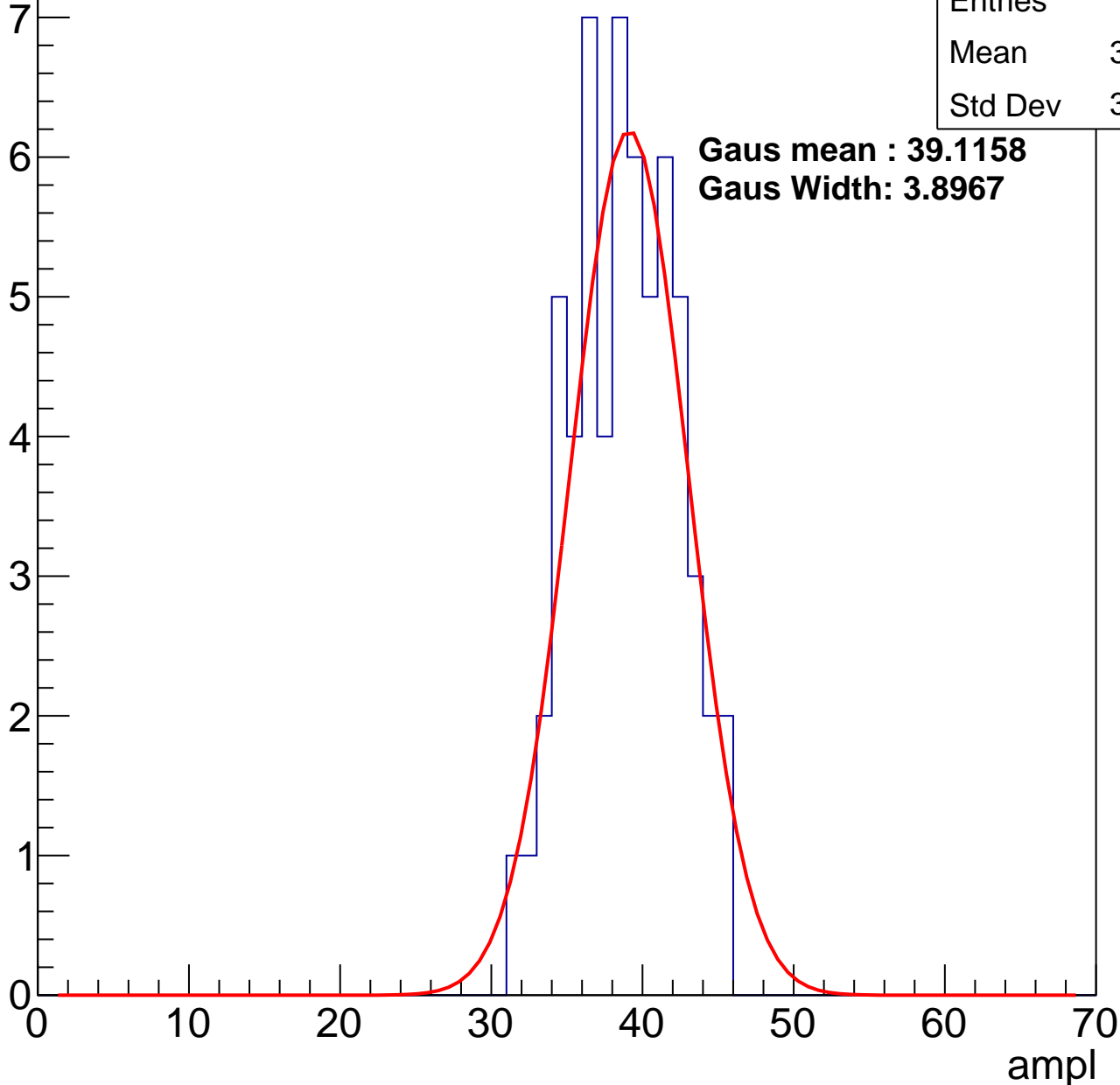
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	38.37
Std Dev	3.366

Gaus mean : 39.1158

Gaus Width: 3.8967



B0L001S, U22-ch124, adc2

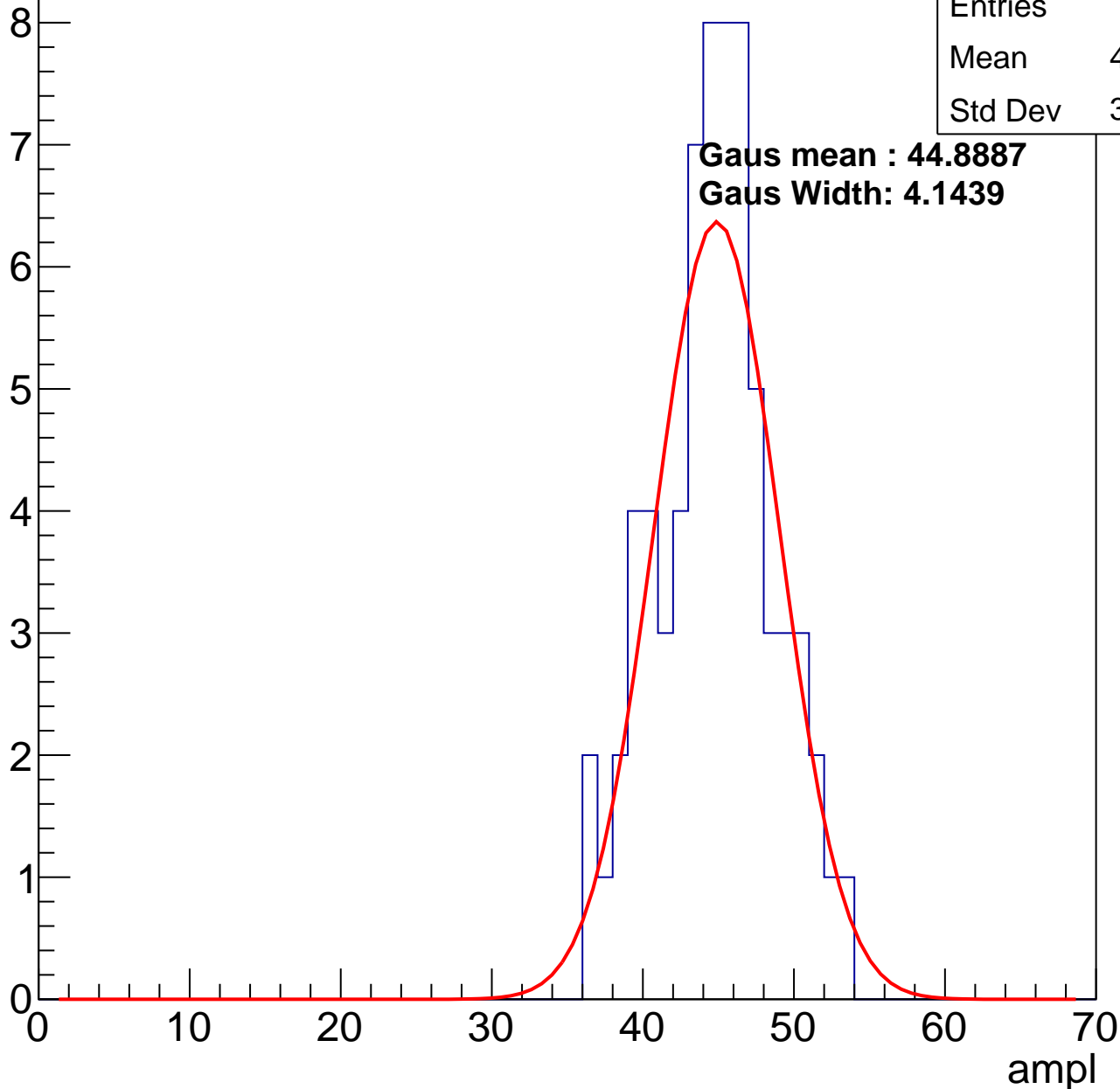
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	44.29
Std Dev	3.838

Gaus mean : 44.8887

Gaus Width: 4.1439

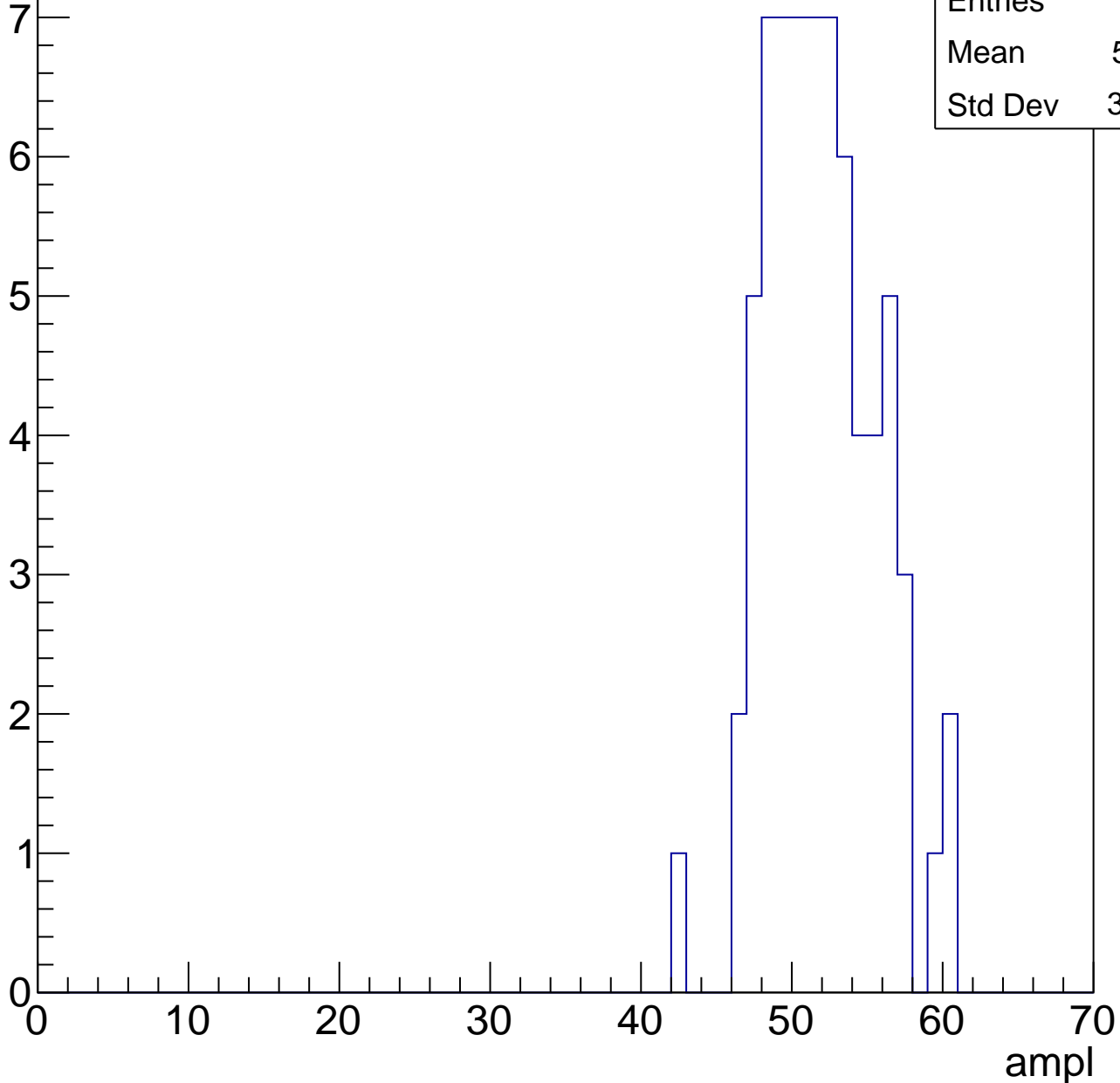


B0L001S, U22-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	51.51
Std Dev	3.599

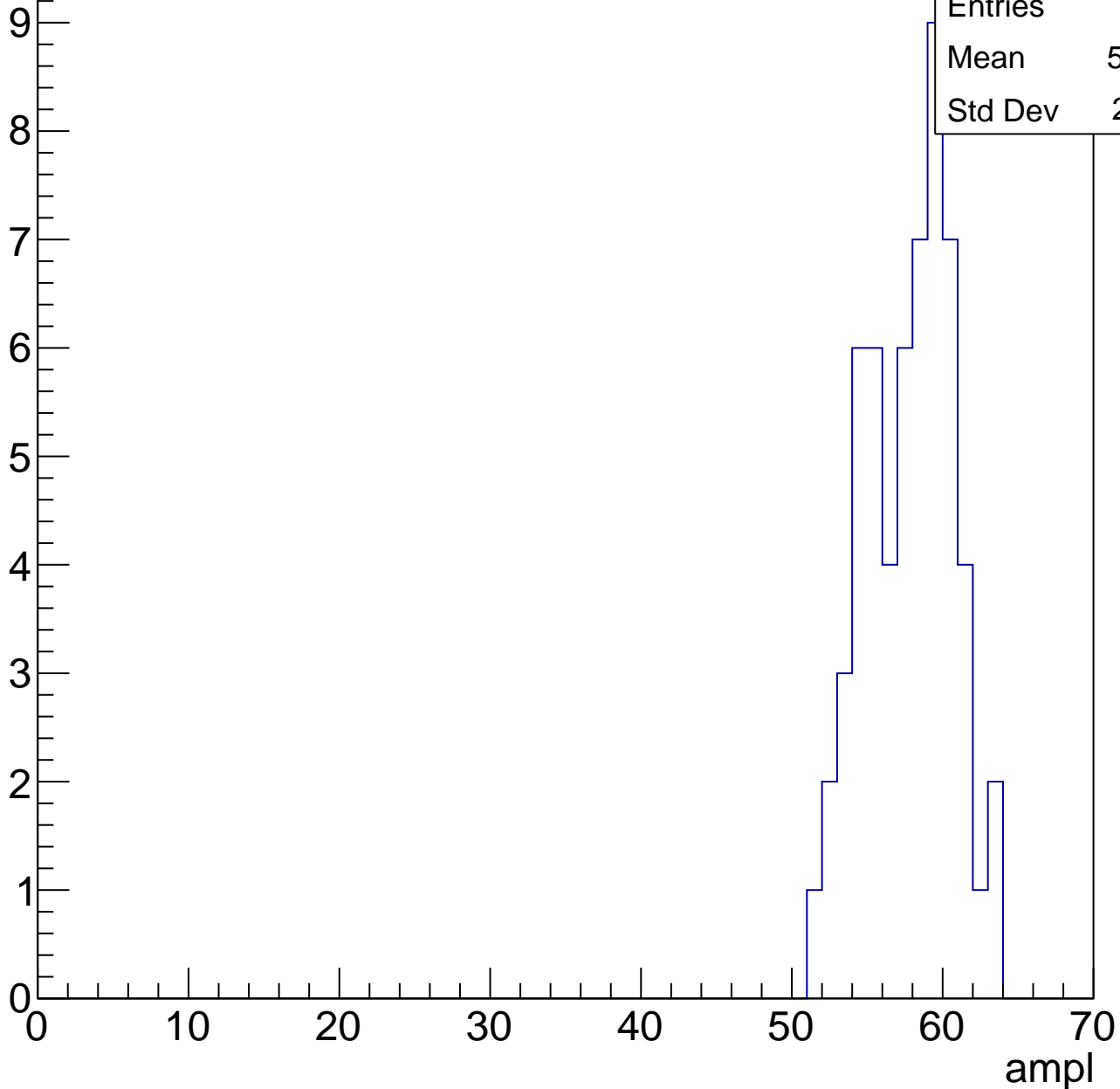


B0L001S, U22-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	57.29
Std Dev	2.871

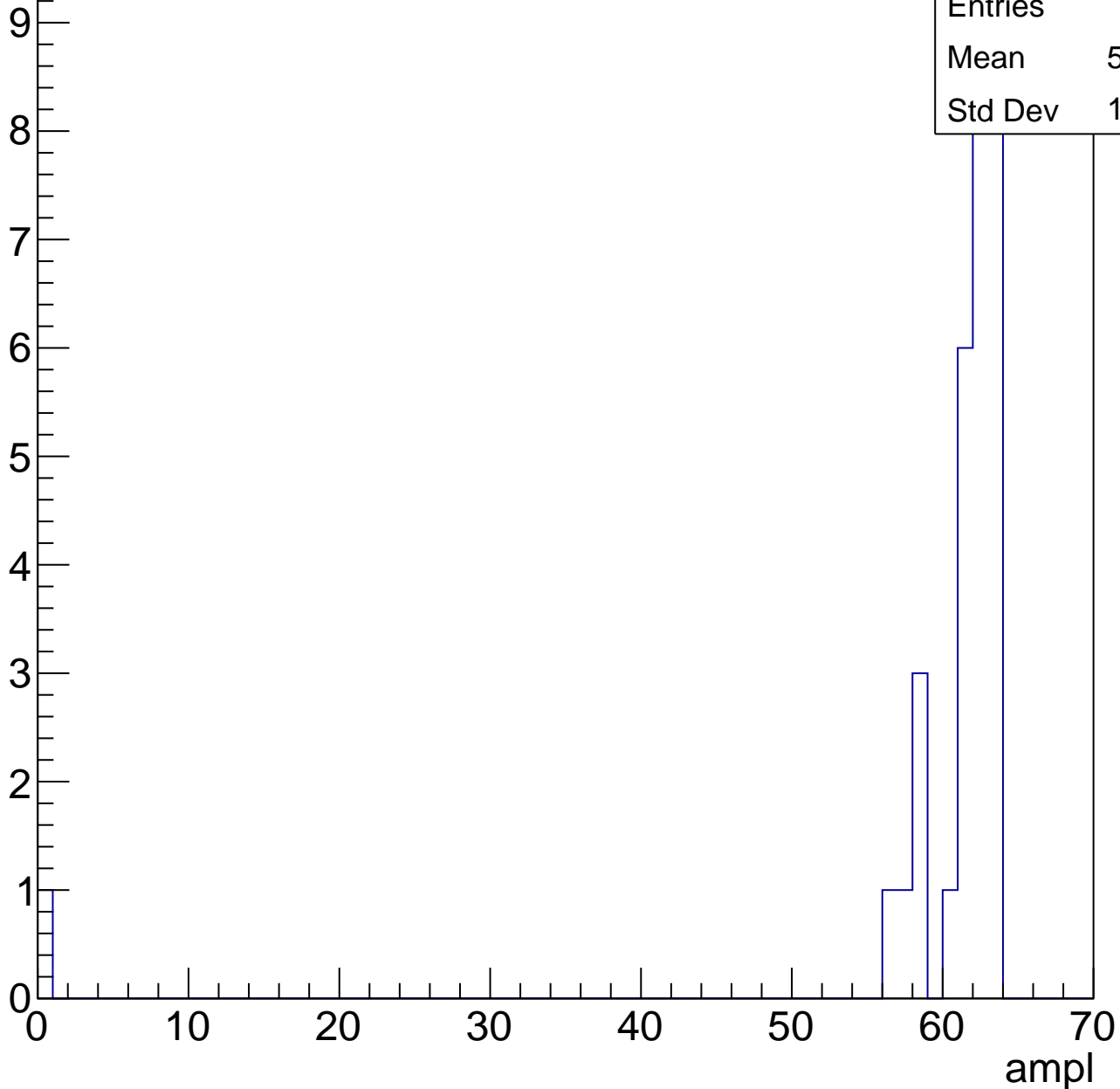


B0L001S, U22-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.17
Std Dev	11.15



B0L001S, U22-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch125, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	31.59
Std Dev	6.378

Gaus mean : 32.8165

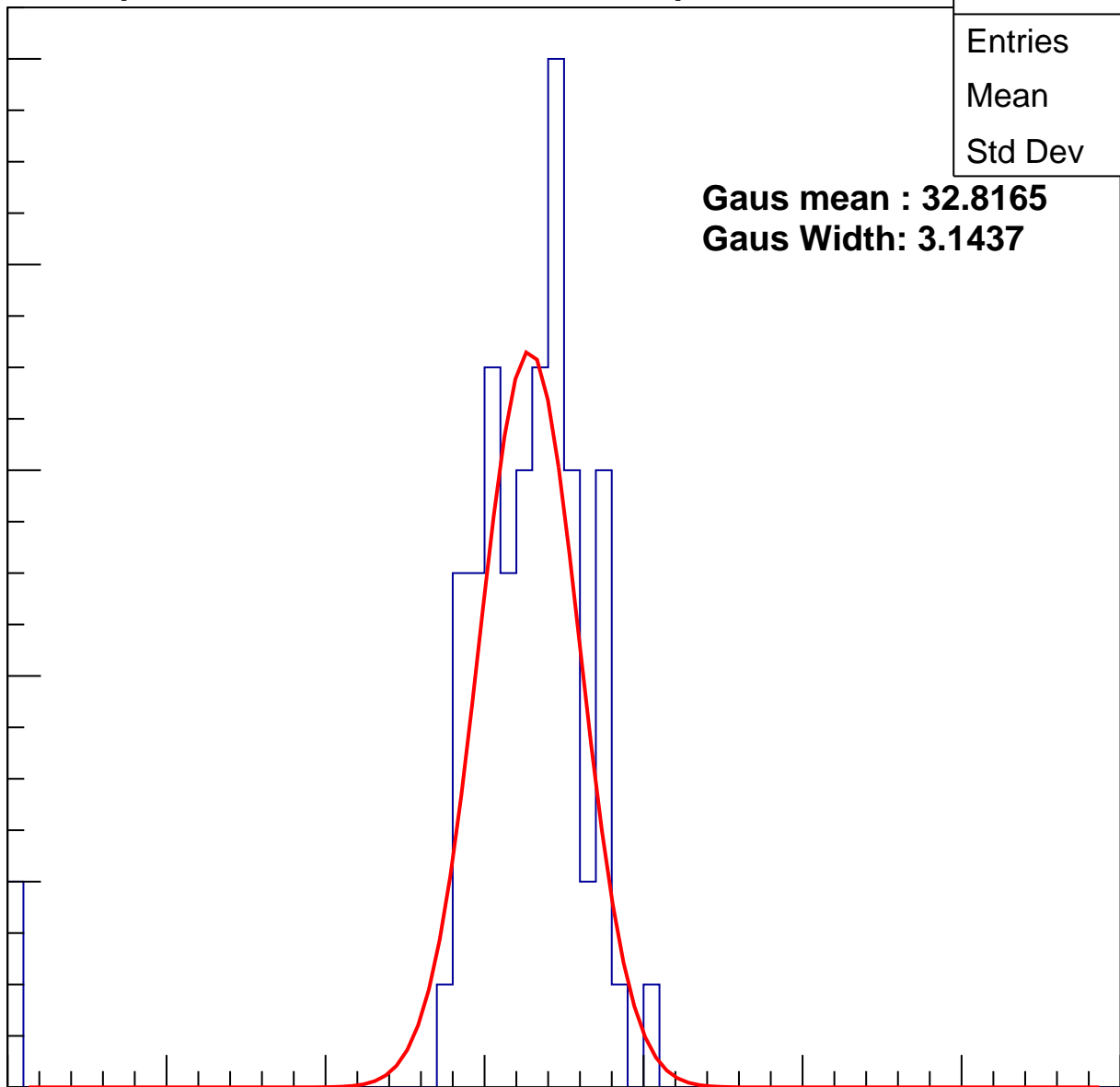
Gaus Width: 3.1437

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U22-ch125, adc1

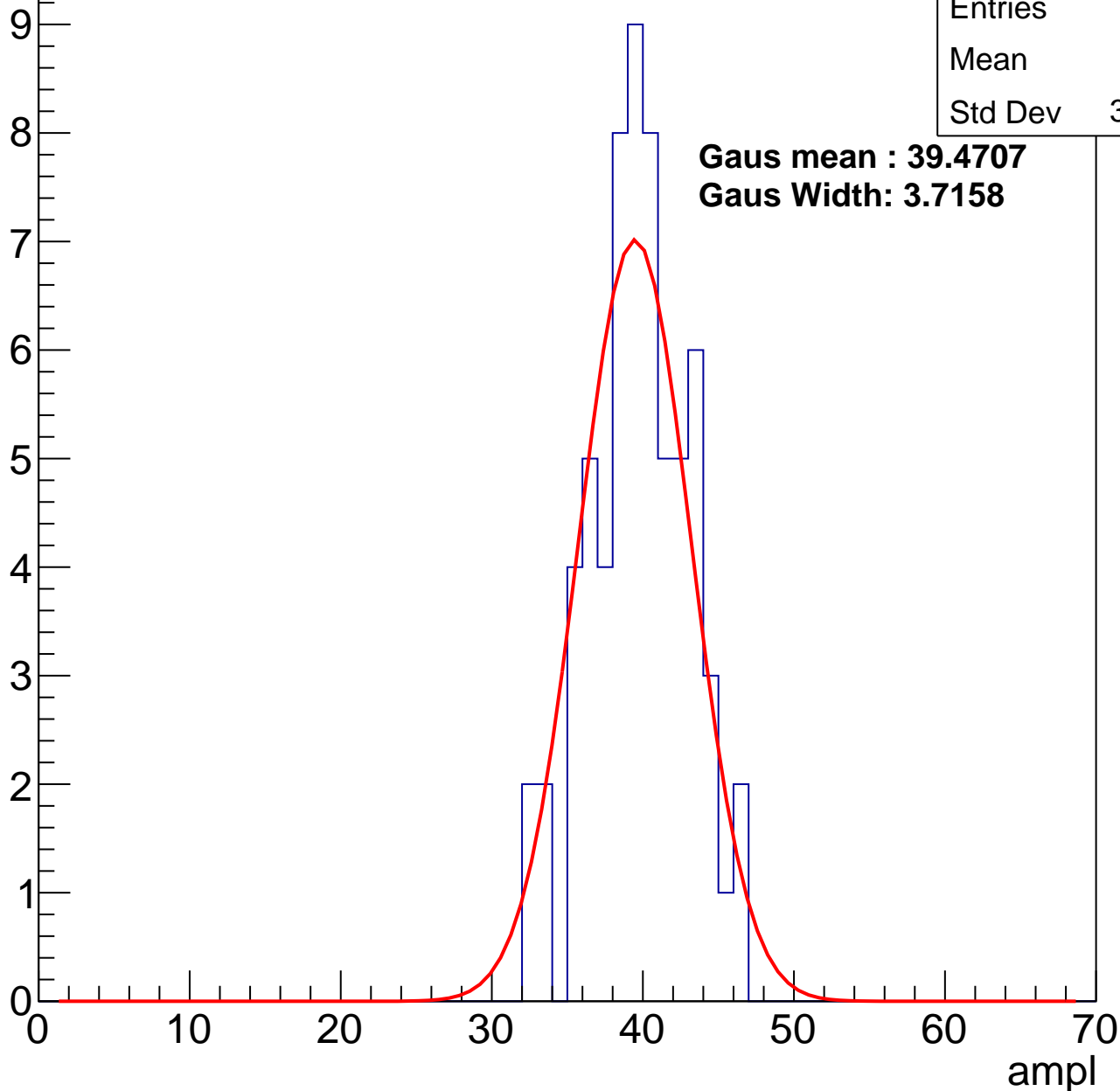
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	39.3
Std Dev	3.248

Gaus mean : 39.4707

Gaus Width: 3.7158



B0L001S, U22-ch125, adc2

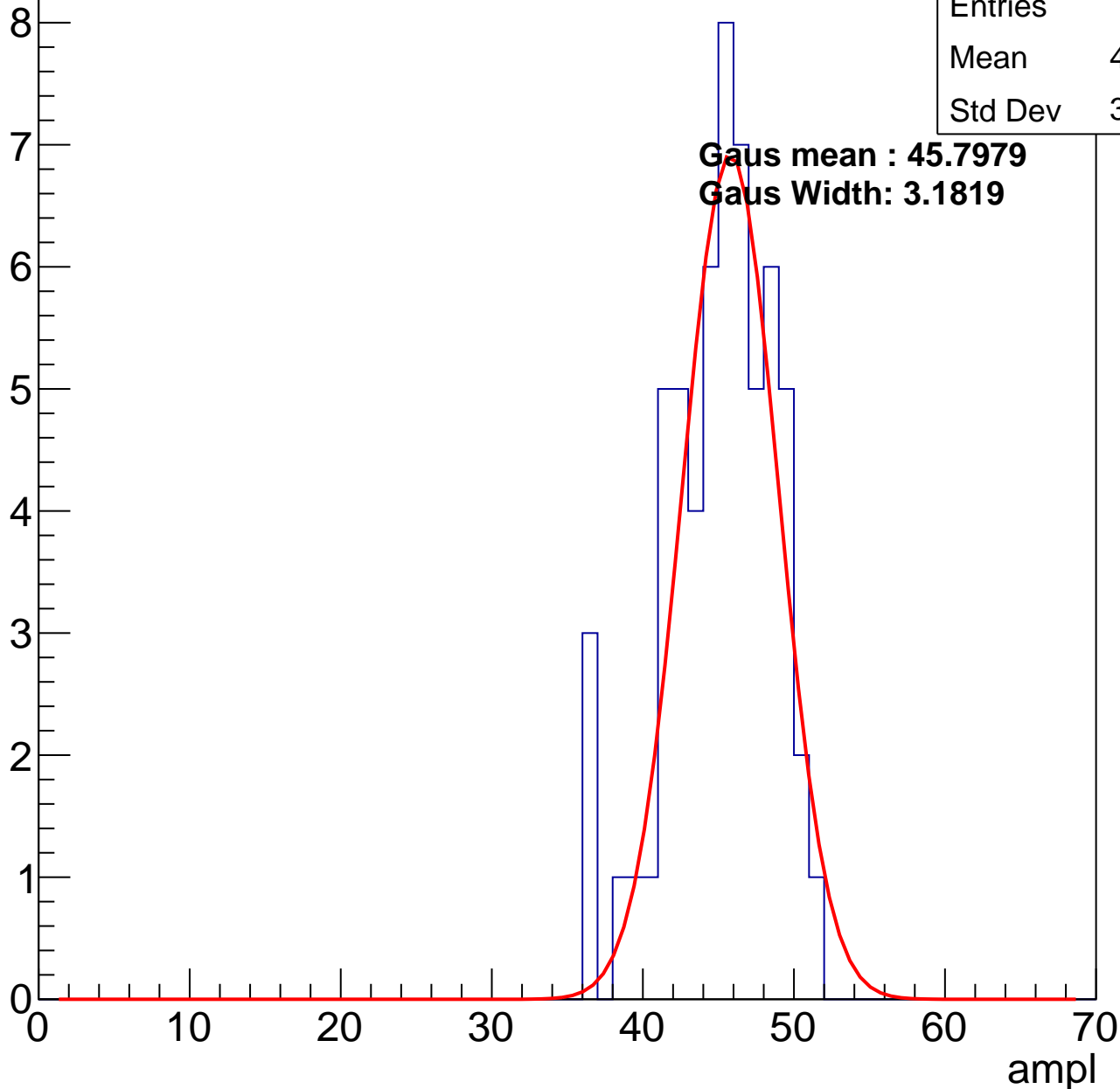
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.62
Std Dev	3.508

Gaus mean : 45.7979

Gaus Width: 3.1819

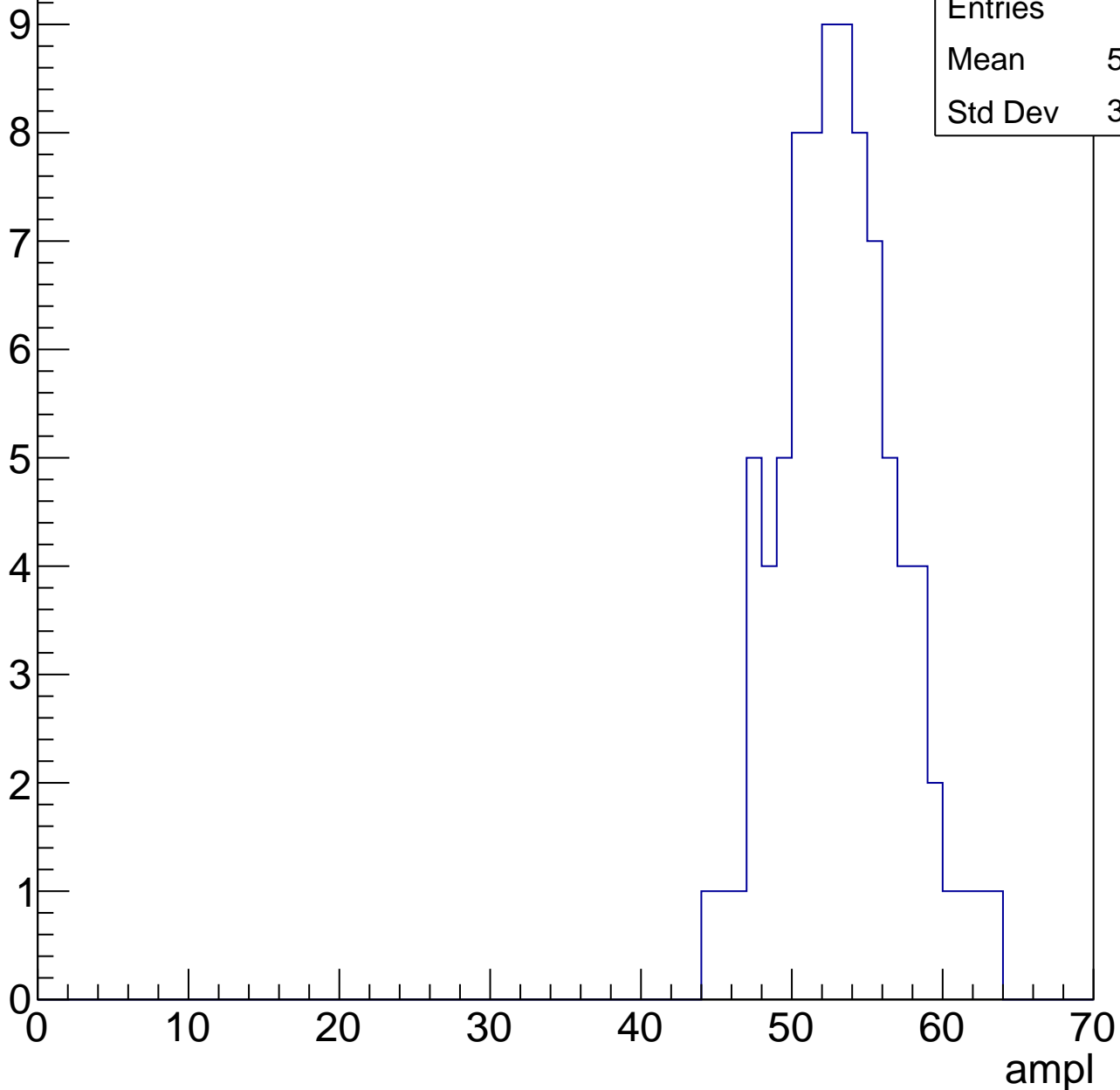


B0L001S, U22-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	52.72
Std Dev	3.867

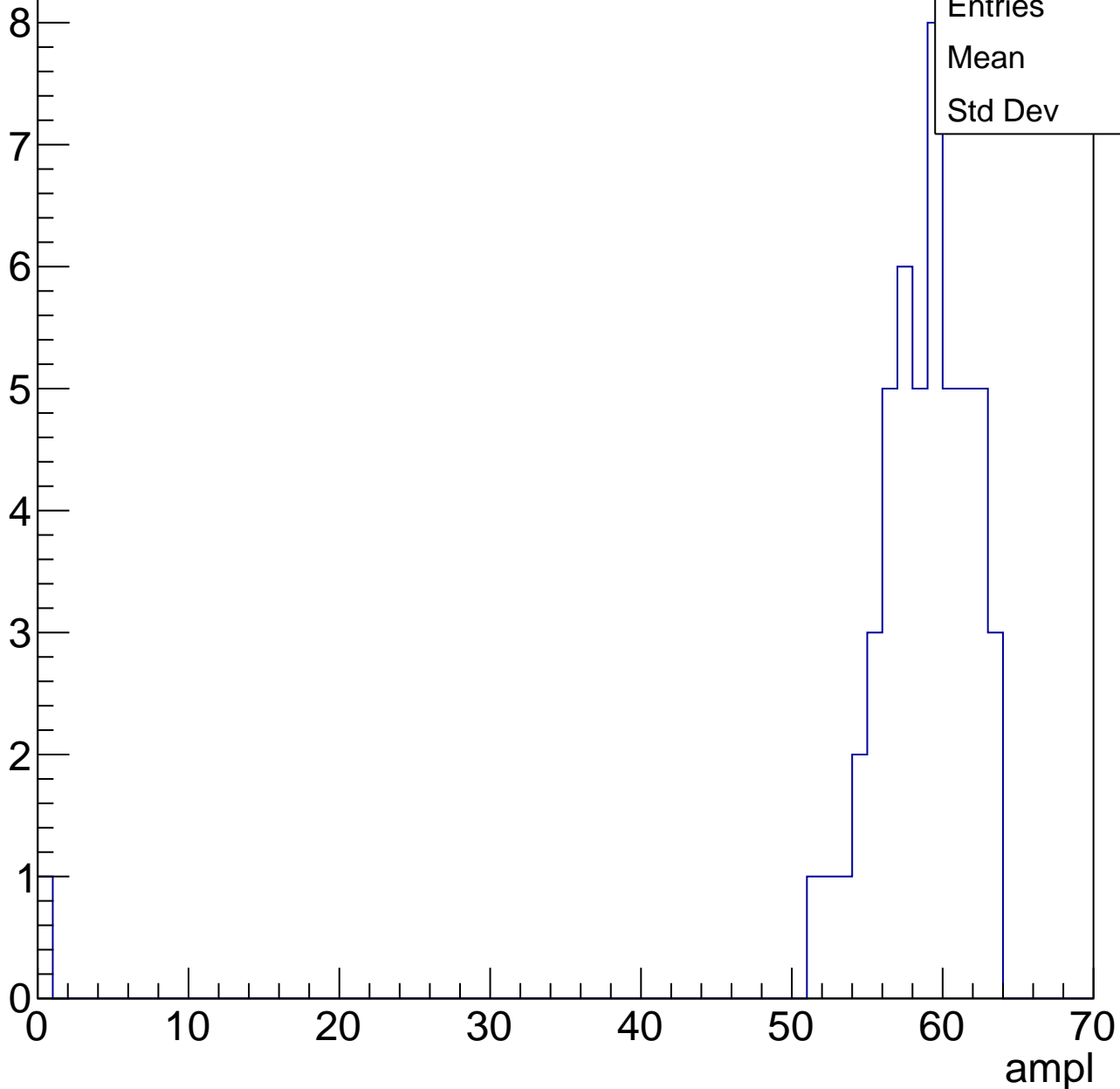


B0L001S, U22-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.2
Std Dev	8.58

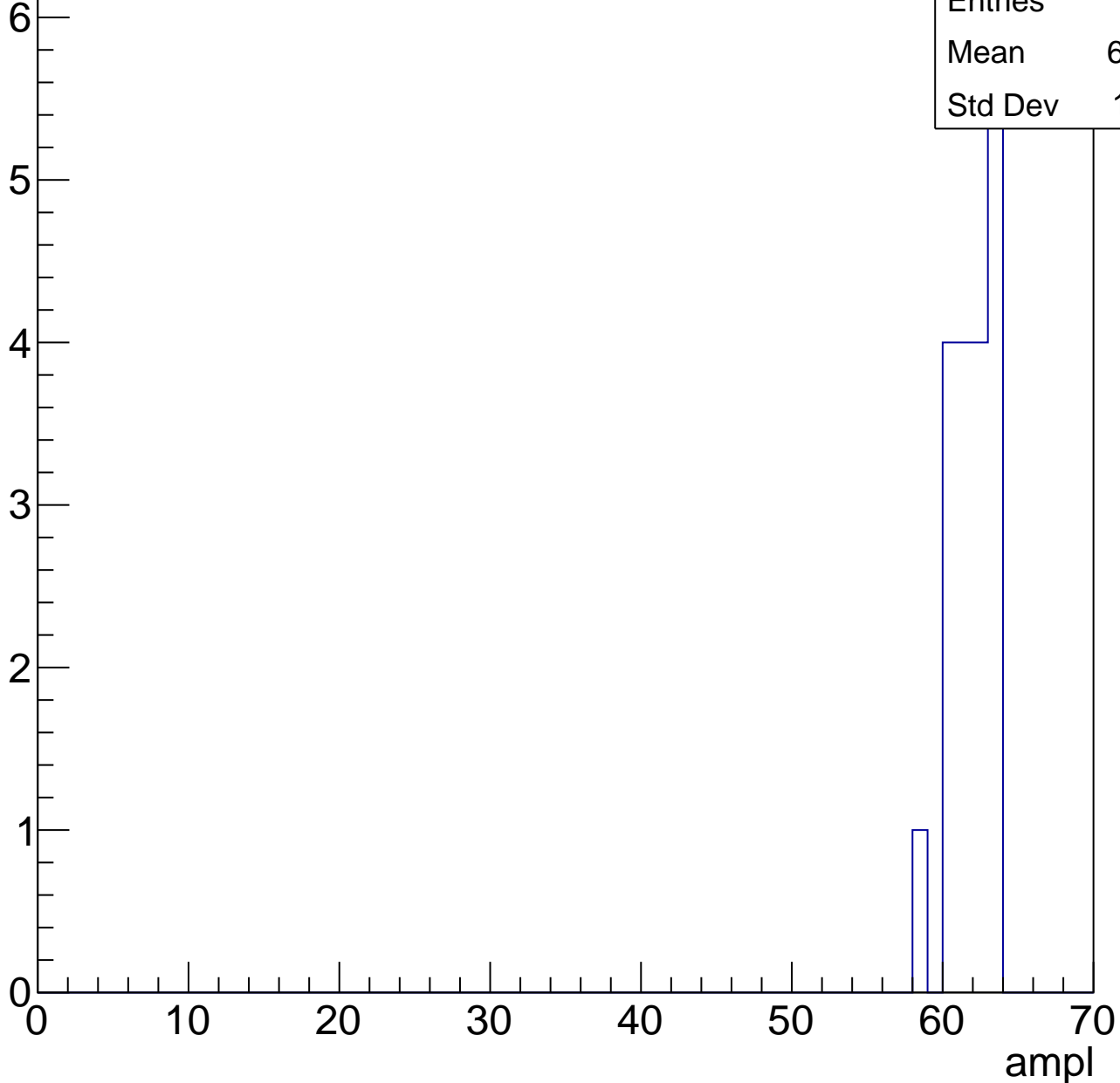


B0L001S, U22-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	19
Mean	61.47
Std Dev	1.391



B0L001S, U22-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

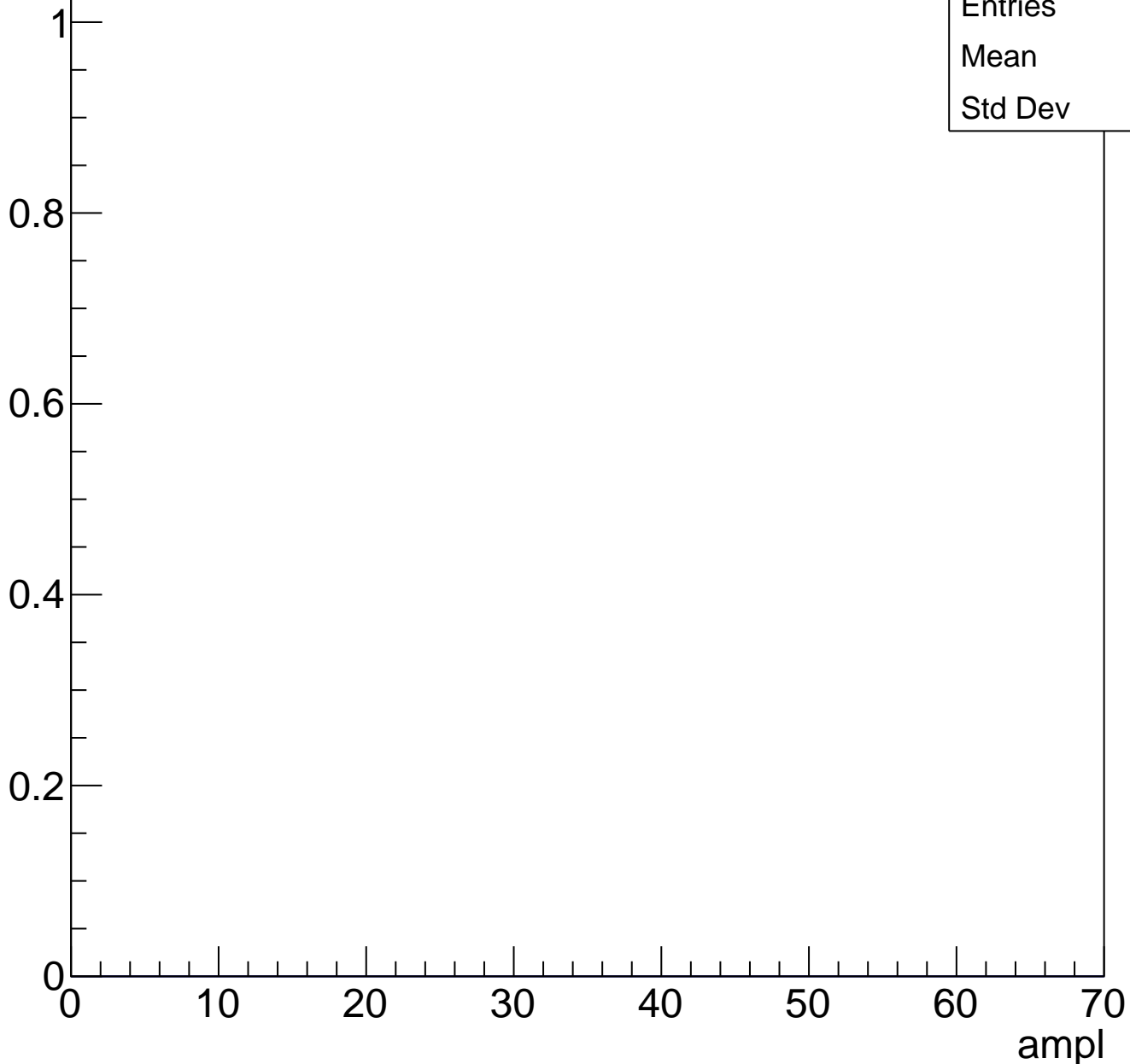


Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U22-ch126, adc0

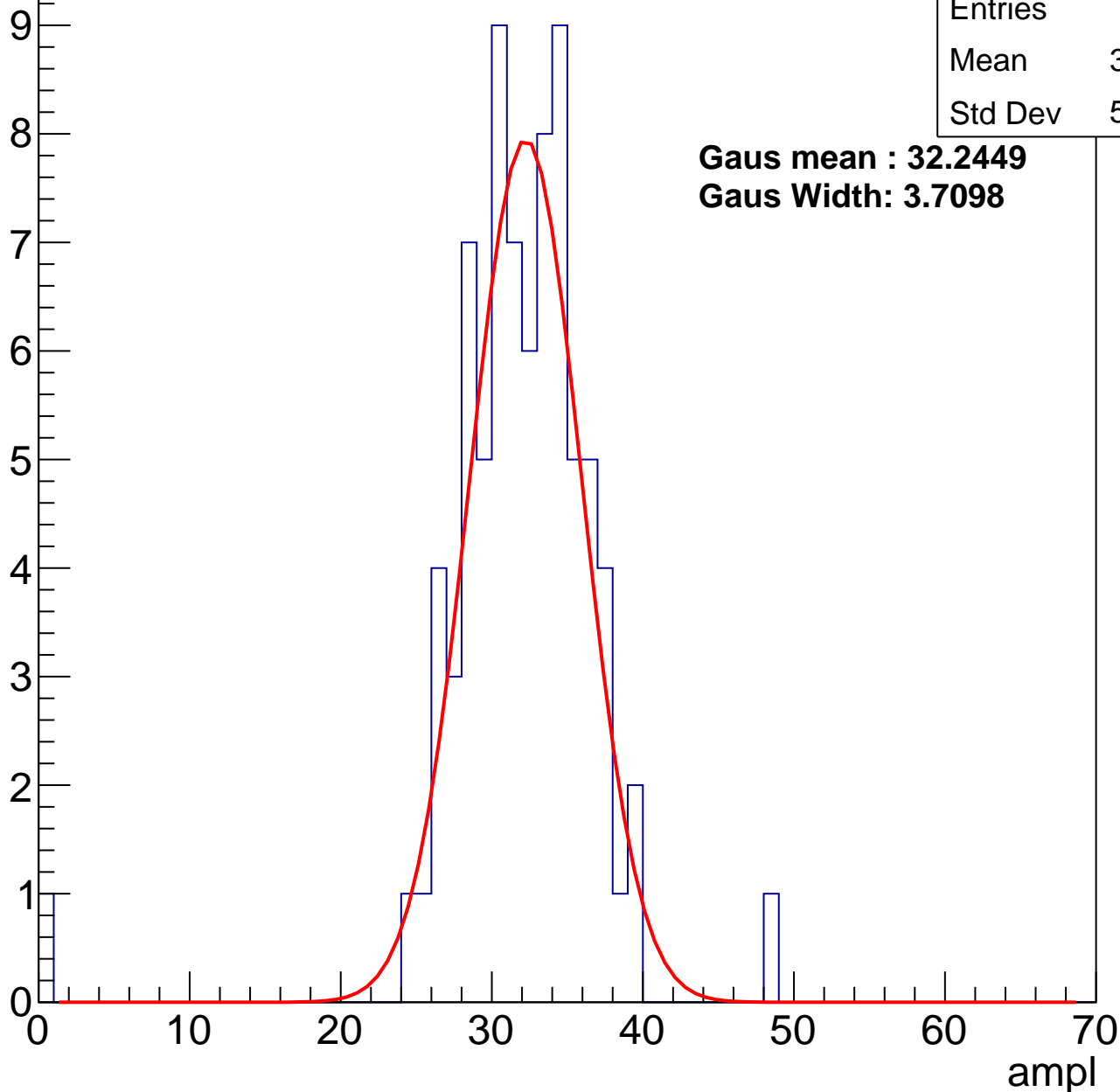
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	31.53
Std Dev	5.274

Gaus mean : 32.2449

Gaus Width: 3.7098



B0L001S, U22-ch126, adc1

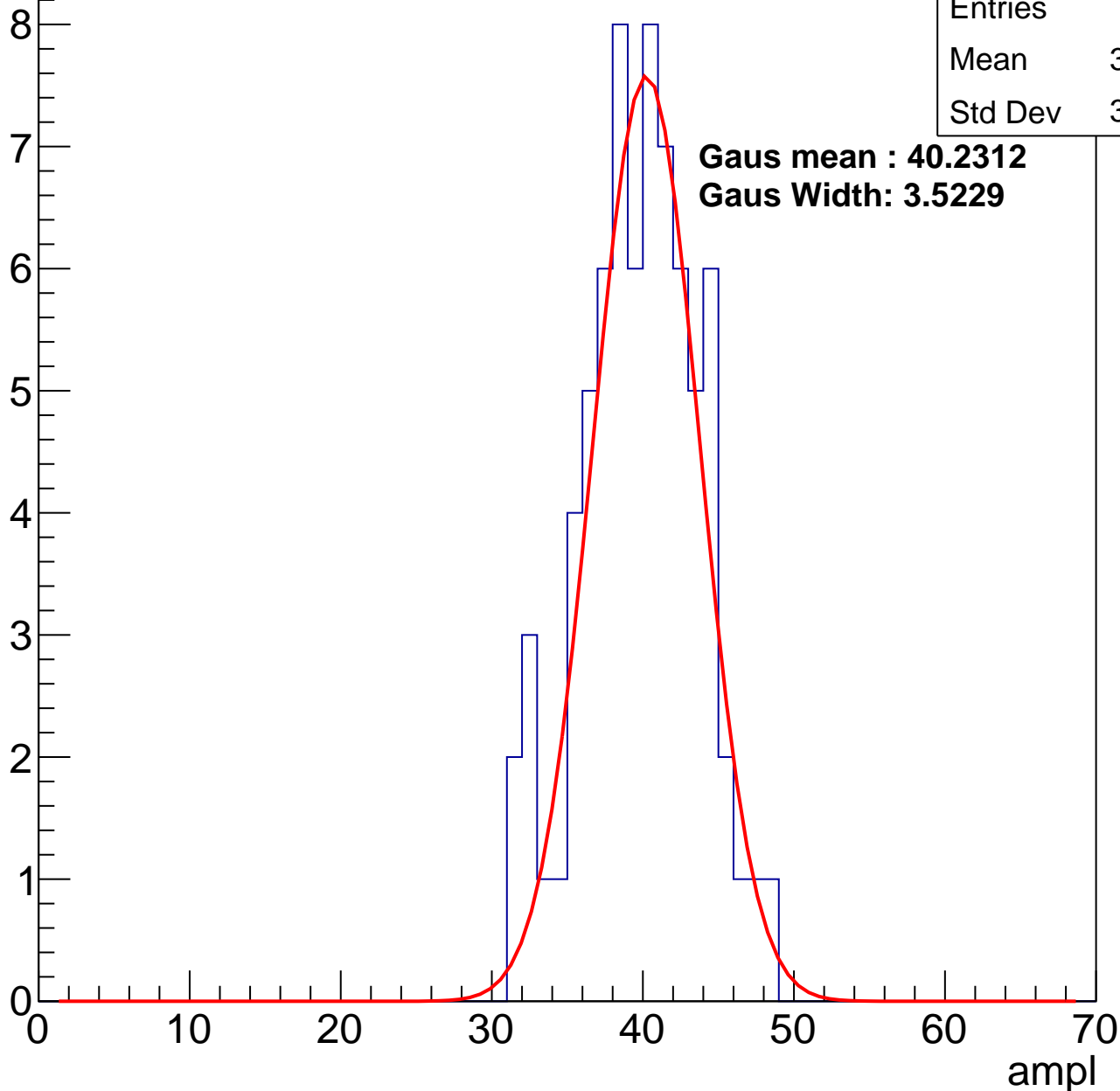
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	39.37
Std Dev	3.798

Gaus mean : 40.2312

Gaus Width: 3.5229



B0L001S, U22-ch126, adc2

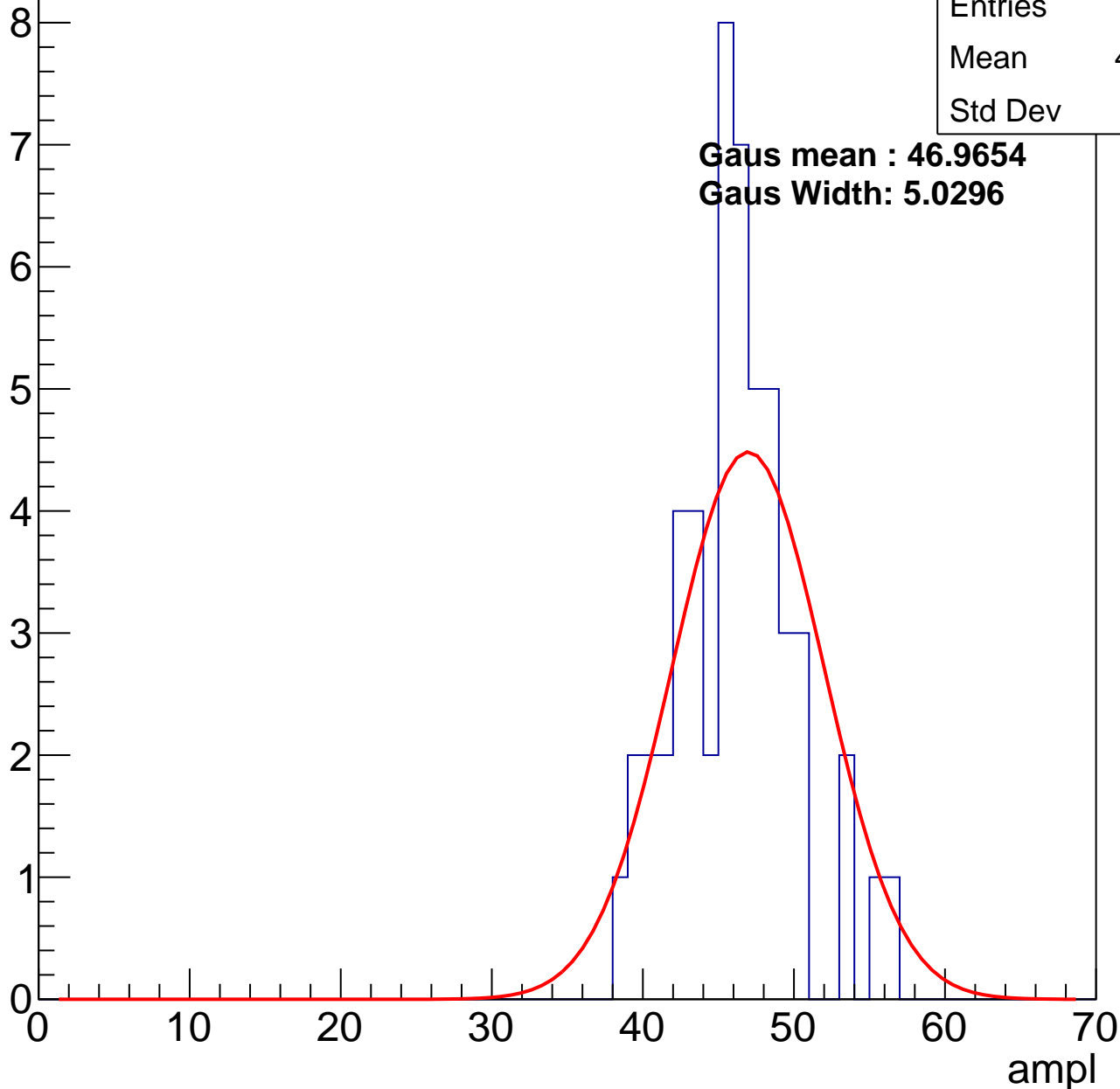
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	45.71
Std Dev	3.86

Gaus mean : 46.9654

Gaus Width: 5.0296

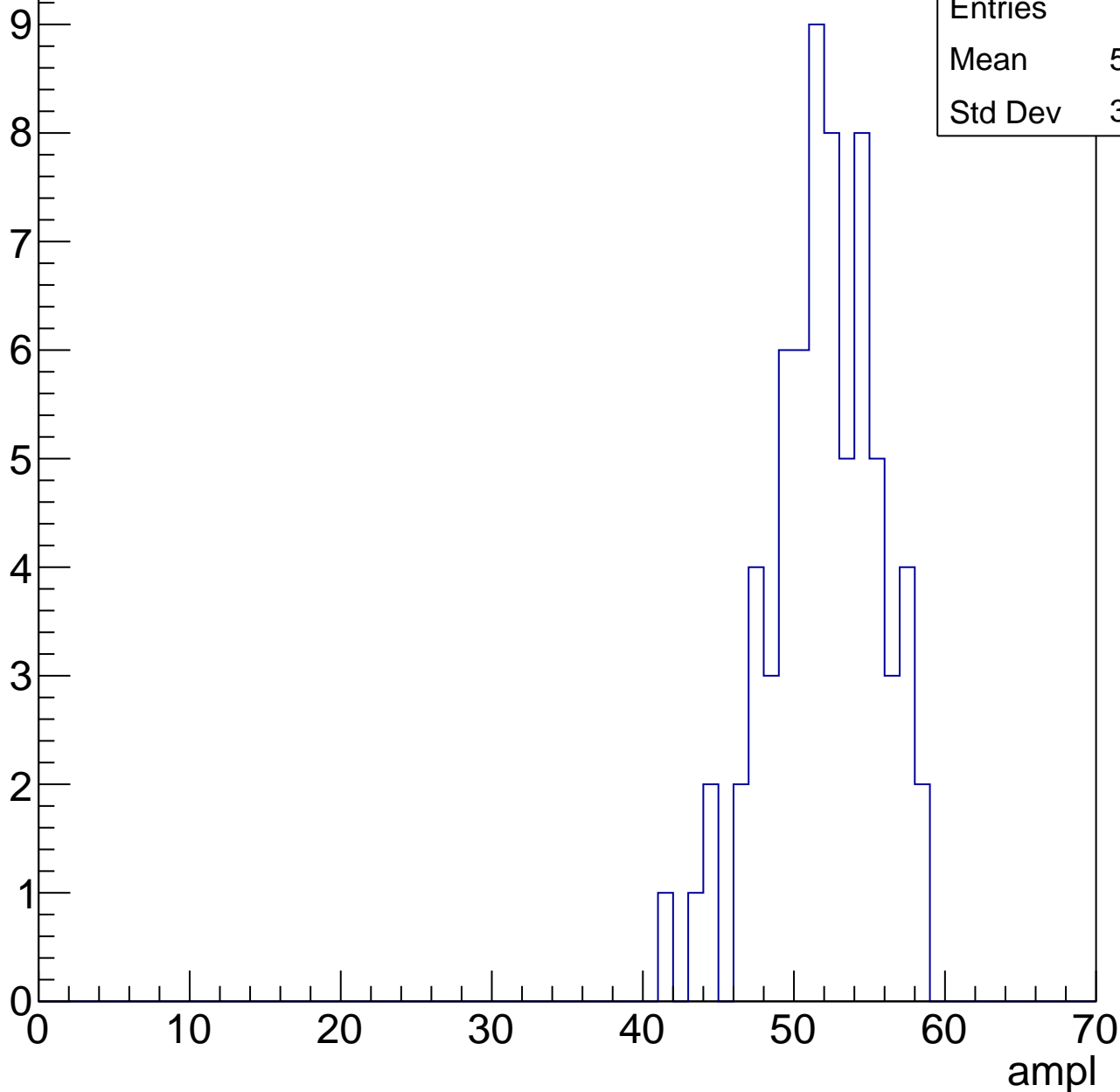


B0L001S, U22-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	51.43
Std Dev	3.645

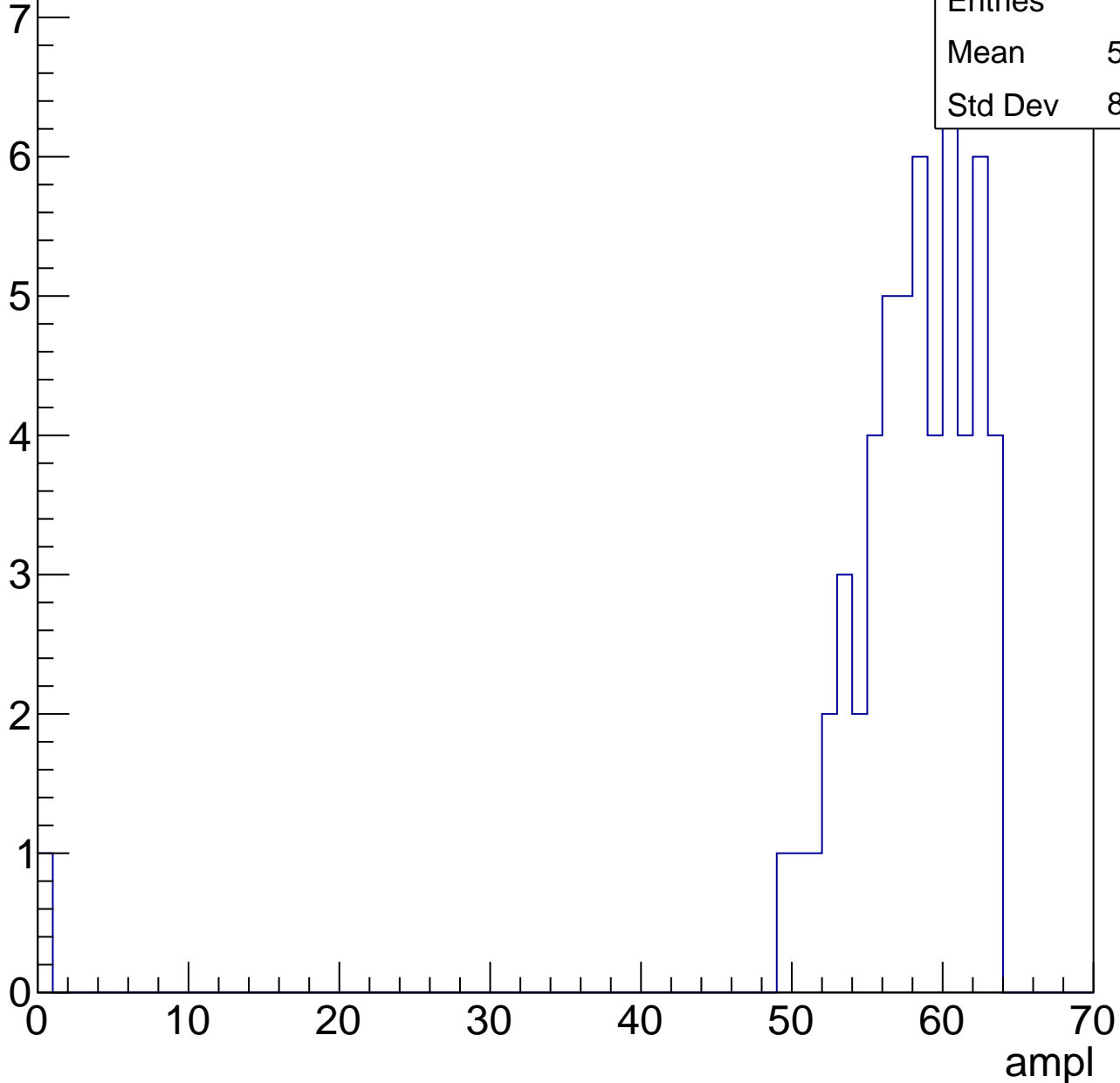


B0L001S, U22-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

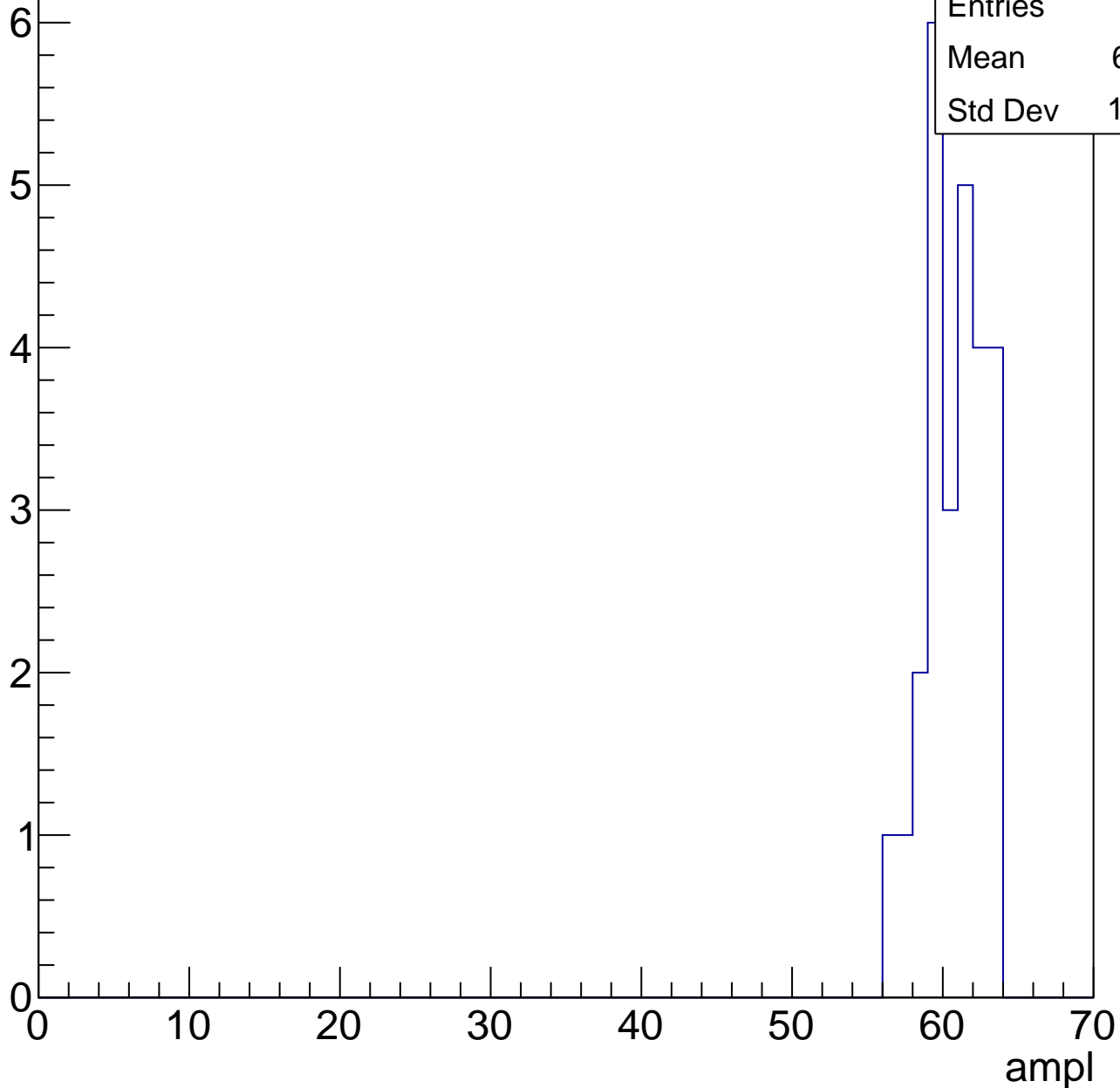
Entries	56
Mean	56.75
Std Dev	8.425



B0L001S, U22-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	26
Mean	60.31
Std Dev	1.897

B0L001S, U22-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch127, adc0

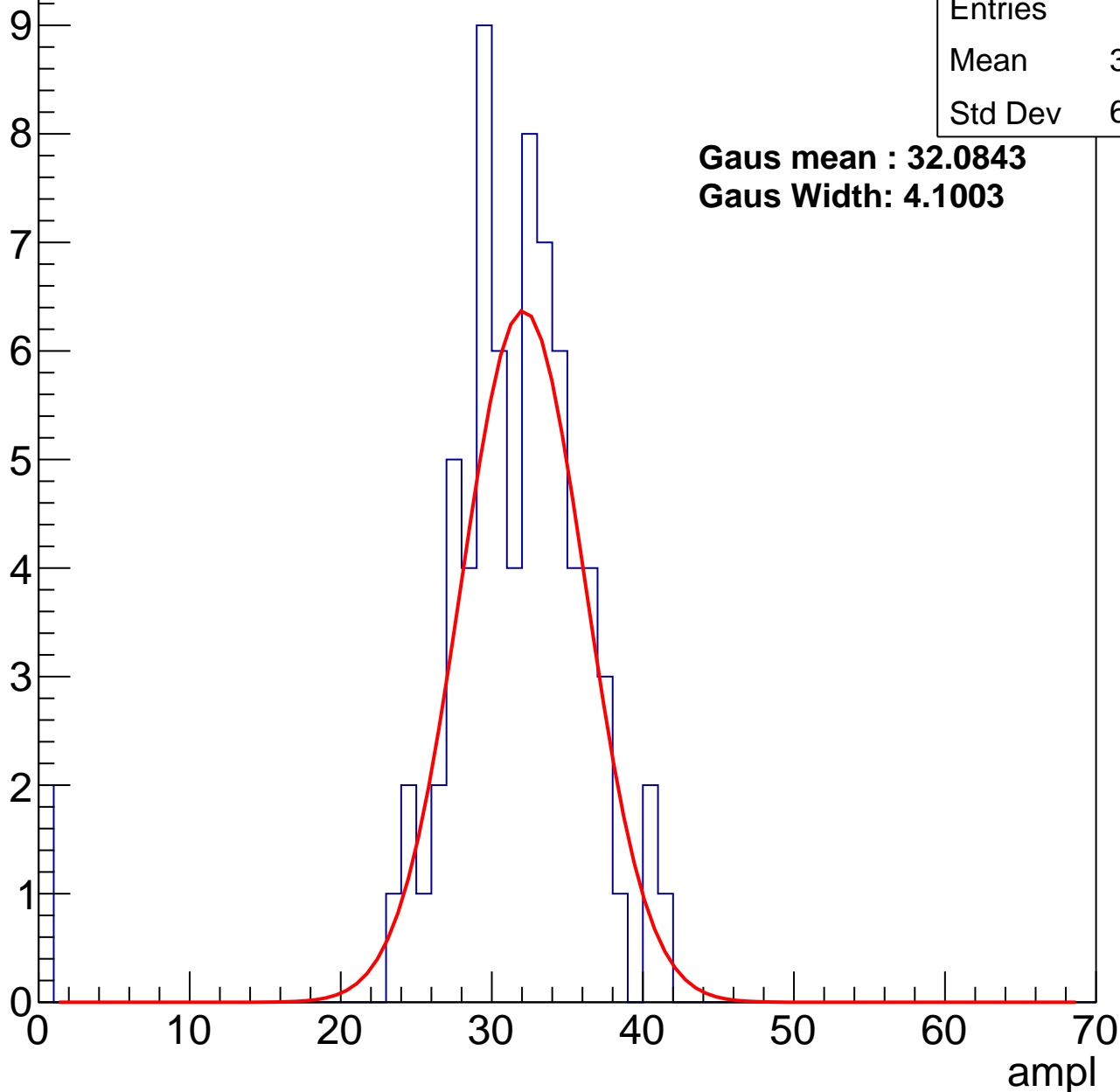
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	30.62
Std Dev	6.445

Gaus mean : 32.0843

Gaus Width: 4.1003



B0L001S, U22-ch127, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	38.04
Std Dev	3.514

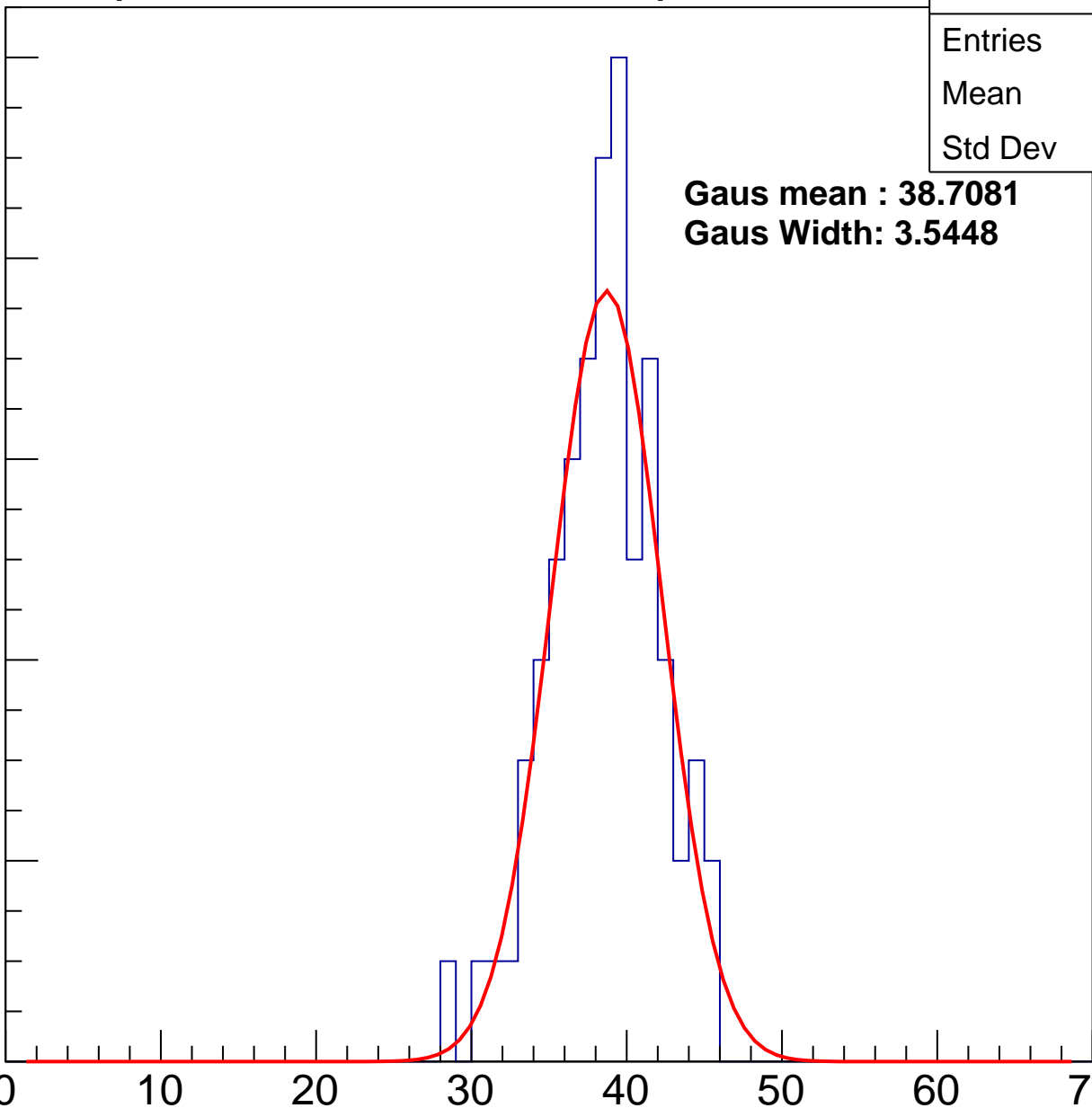
Gaus mean : 38.7081

Gaus Width: 3.5448

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U22-ch127, adc2

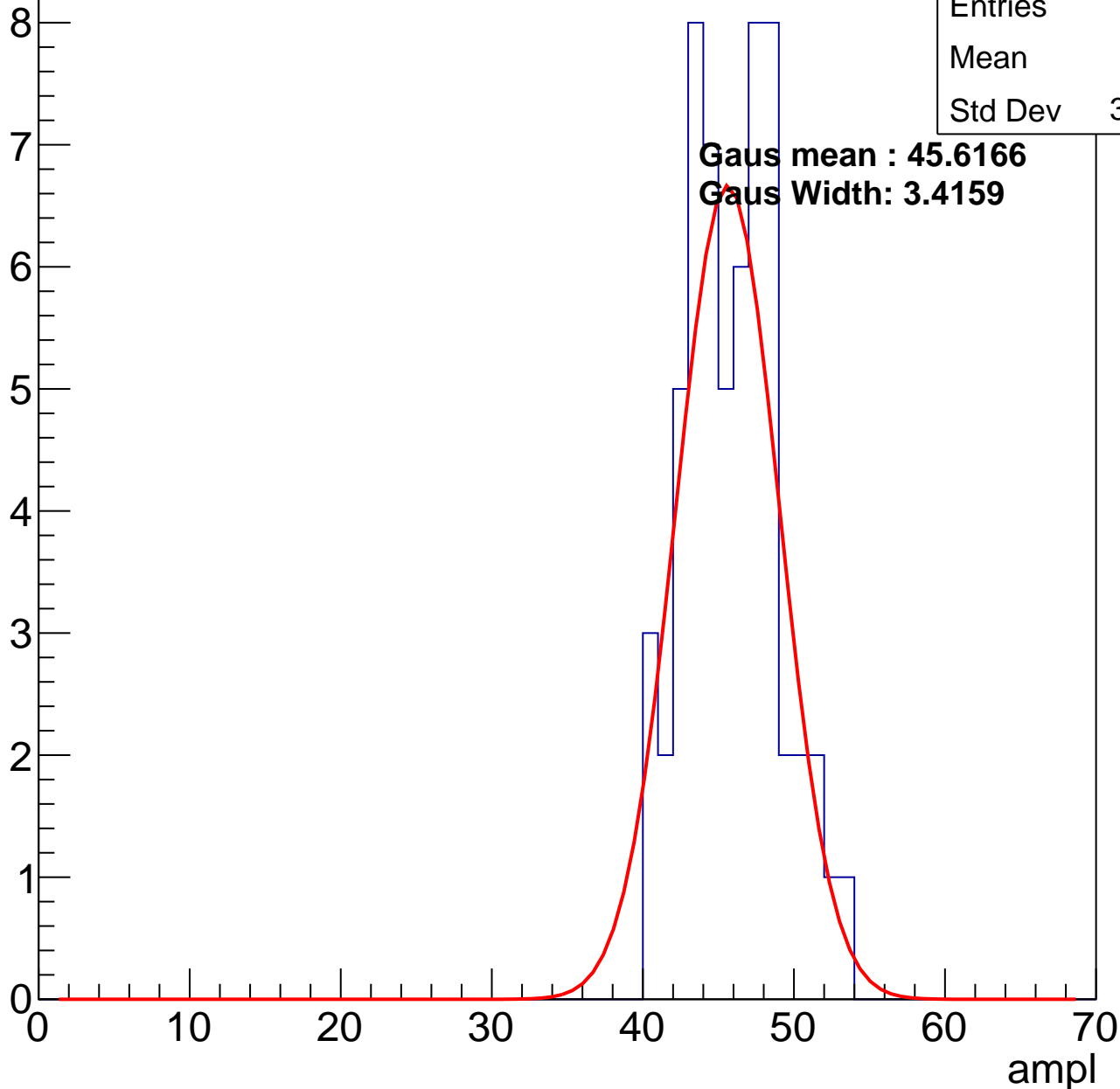
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	45.5
Std Dev	3.036

Gaus mean : 45.6166

Gaus Width: 3.4159

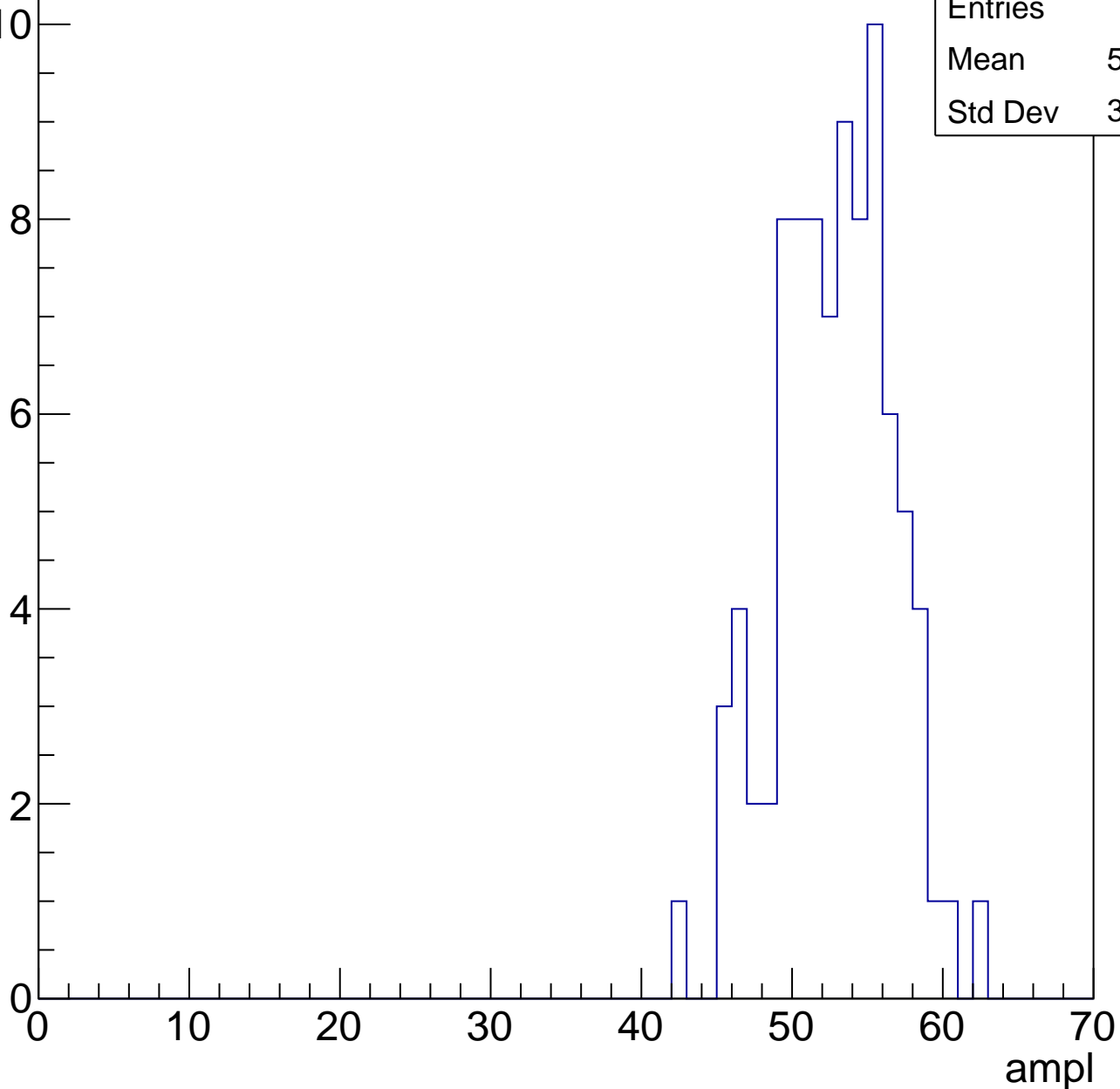


B0L001S, U22-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	88
Mean	52.36
Std Dev	3.817

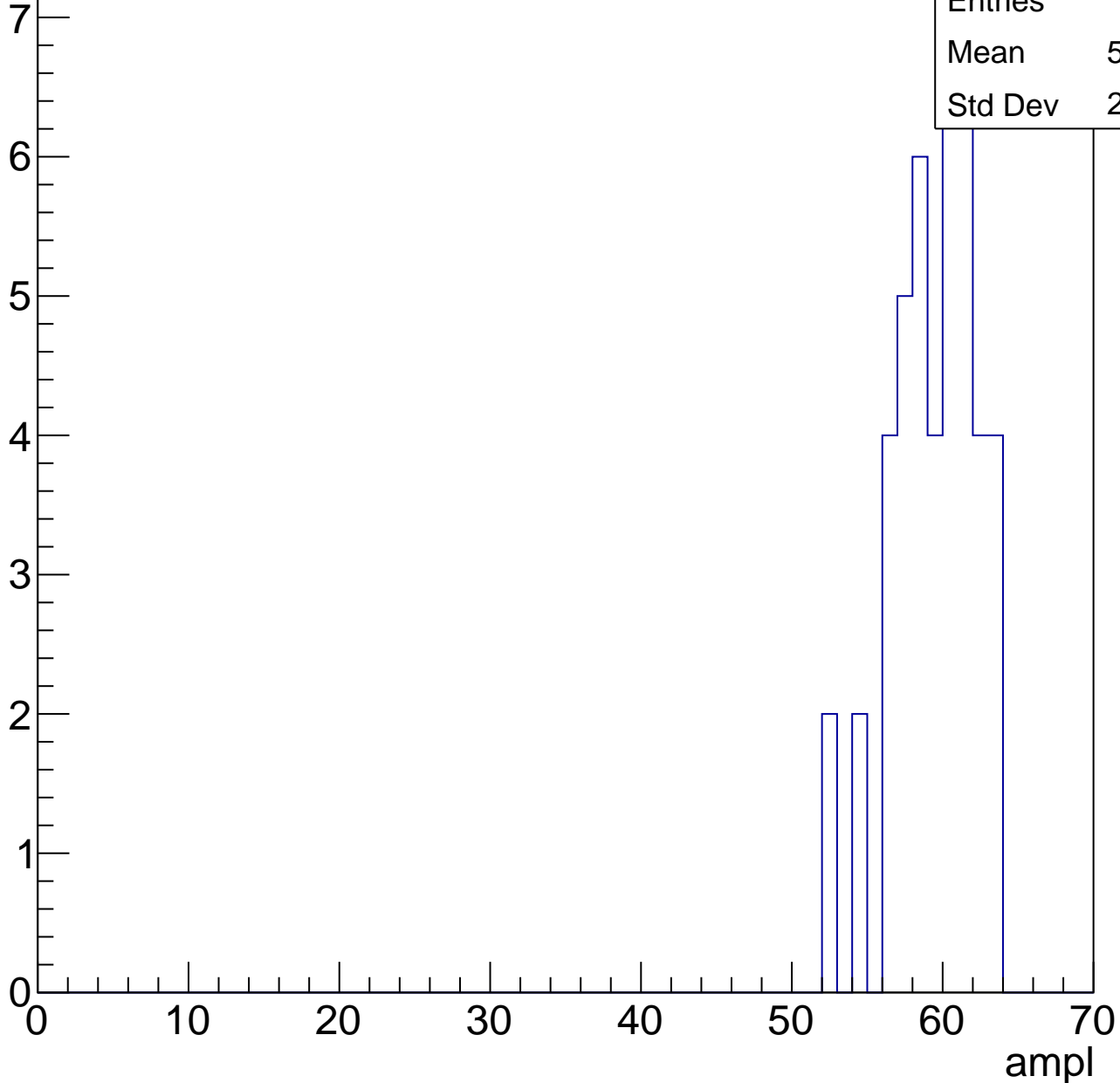


B0L001S, U22-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

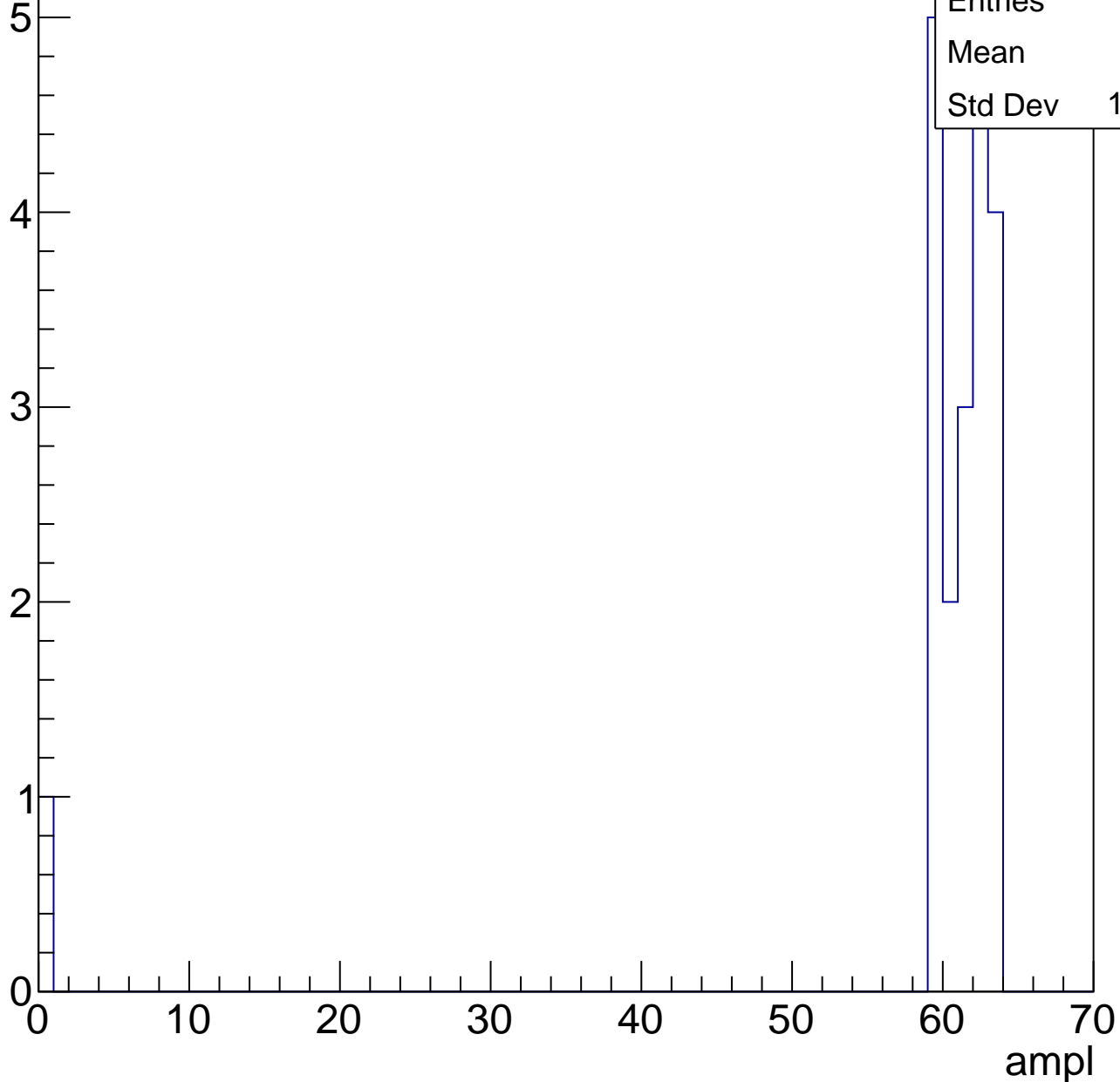
Entries	45
Mean	58.93
Std Dev	2.768



B0L001S, U22-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L001S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U22-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0