

B1L002S, U14-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.03

Turn on : 25.0226

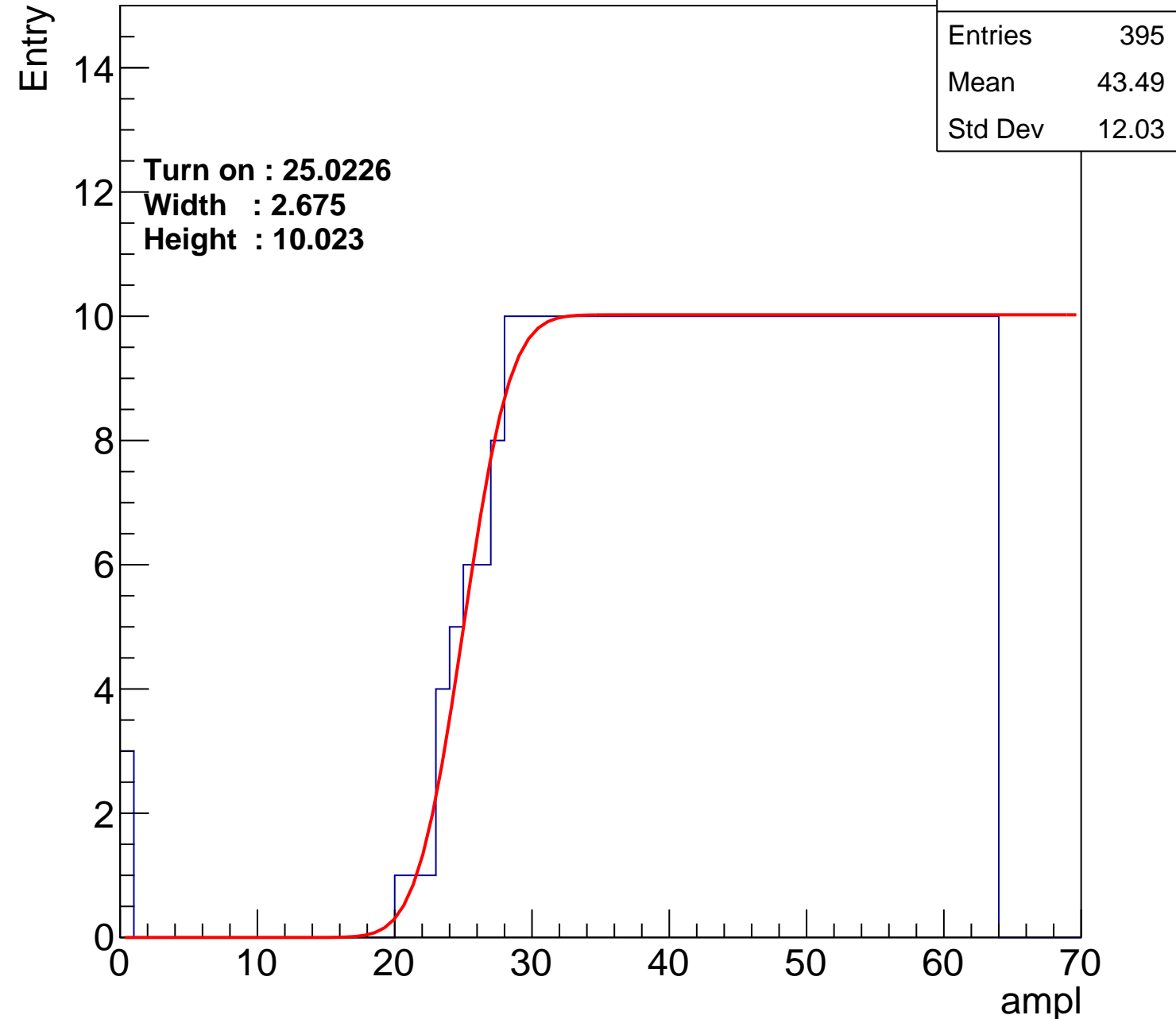
Width : 2.675

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.46
Std Dev	11.91

Turn on : 24.9453

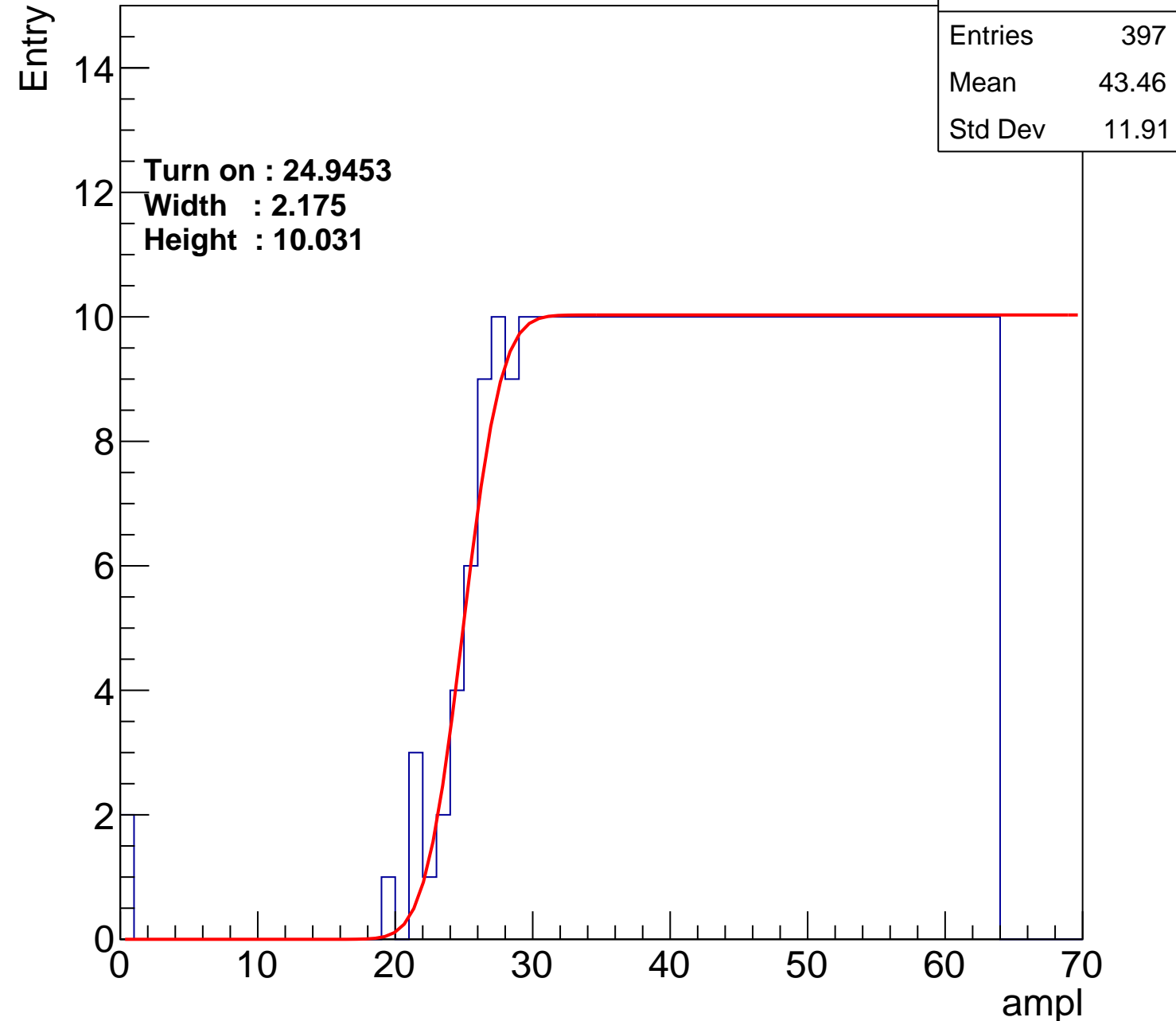
Width : 2.175

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.87
Std Dev	12.11

Turn on : 23.4978

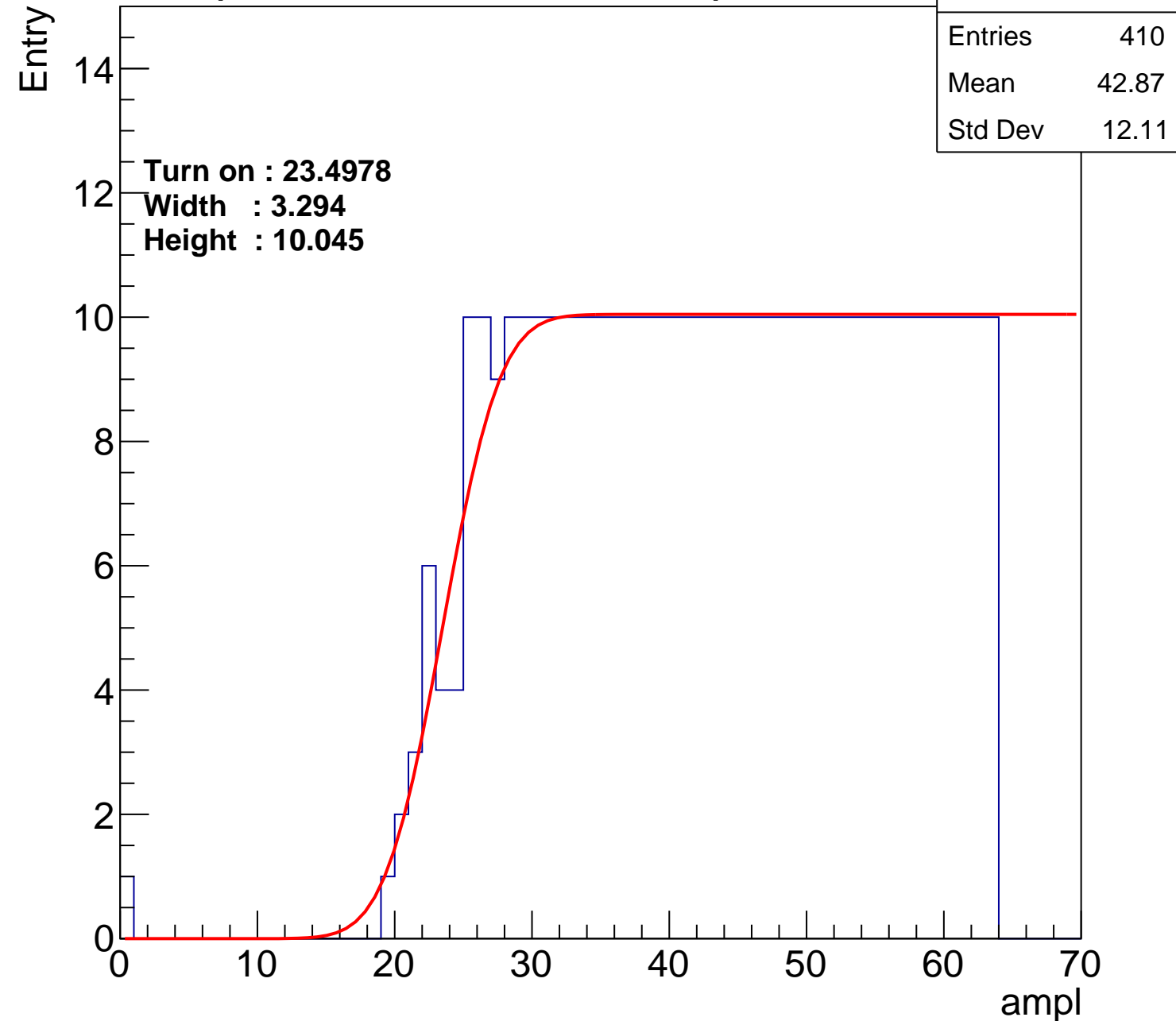
Width : 3.294

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.4252

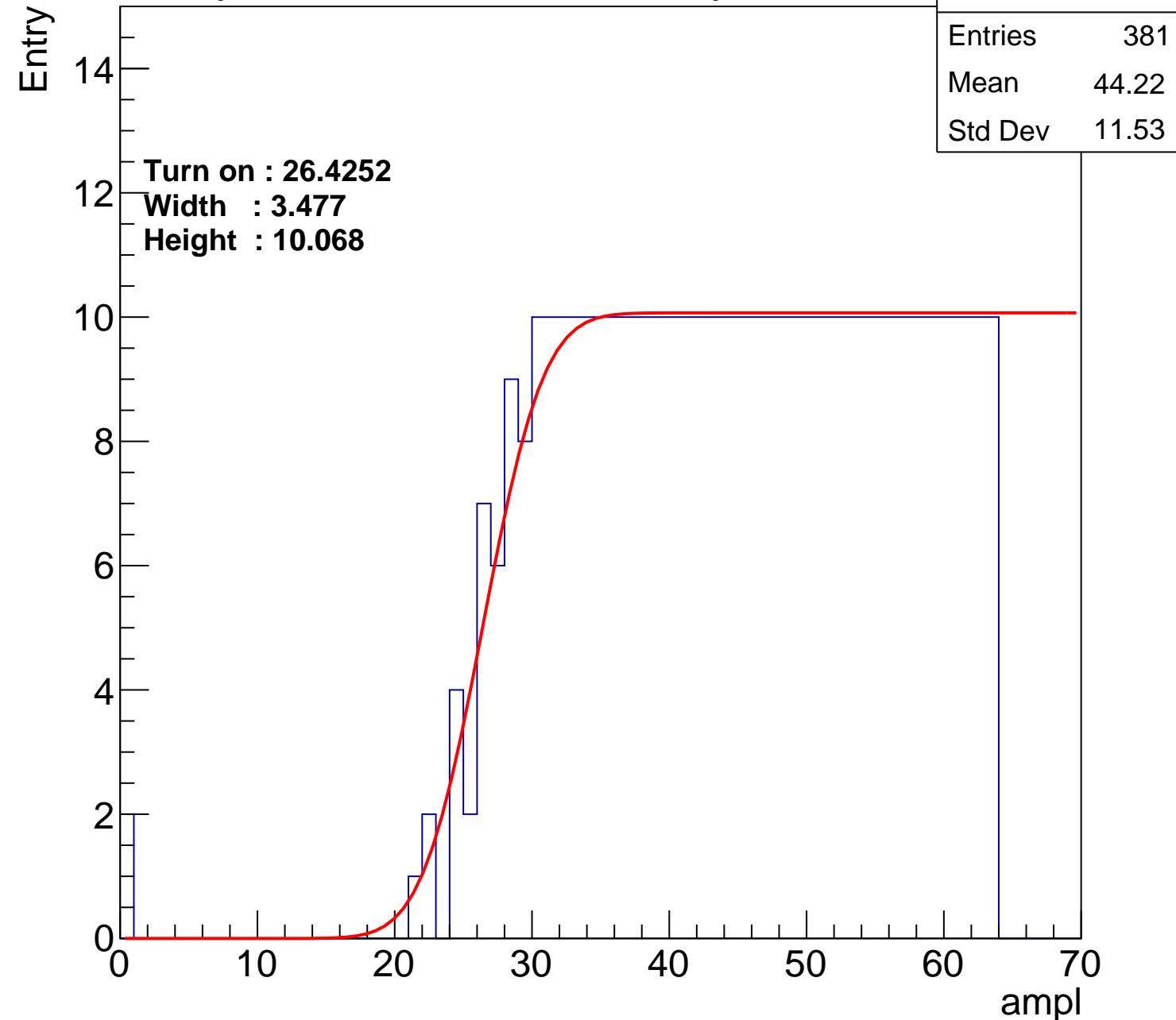
Width : 3.477

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.94
Std Dev	12.15

Turn on : 23.3939

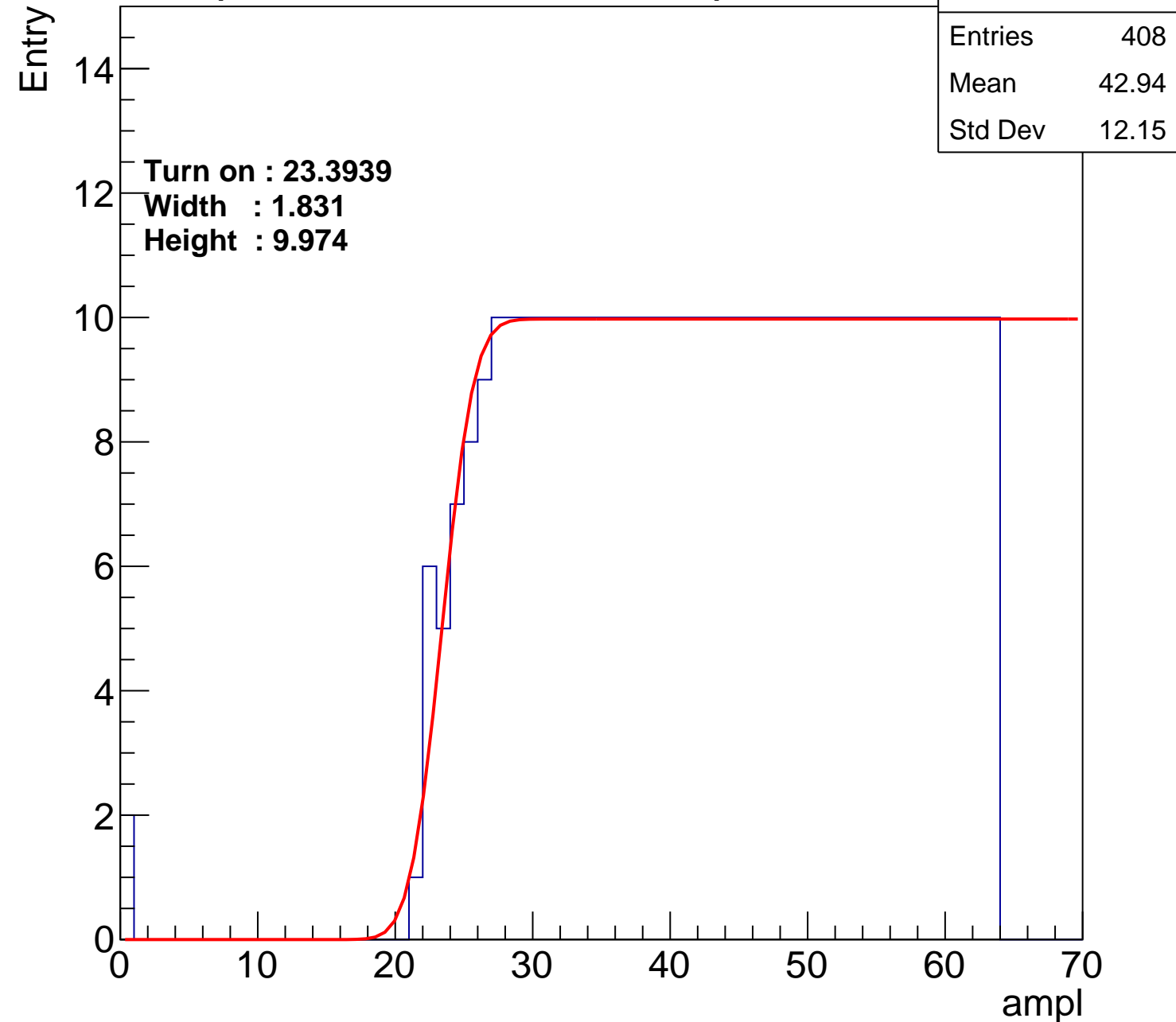
Width : 1.831

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.94
Std Dev	12.31

Turn on : 23.6720

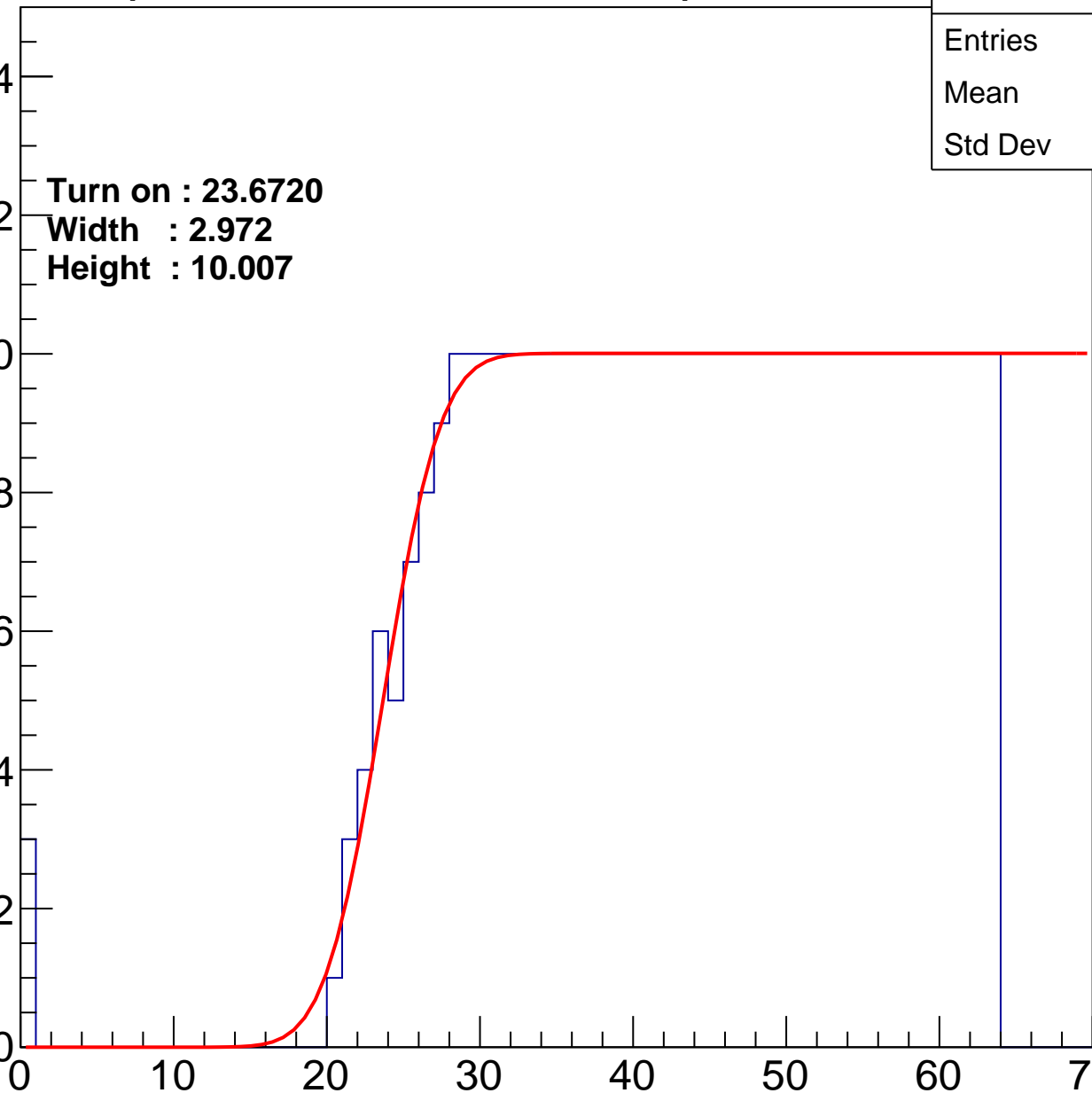
Width : 2.972

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch6

calib_packv5_042523_0143.root, FC#10, port B3

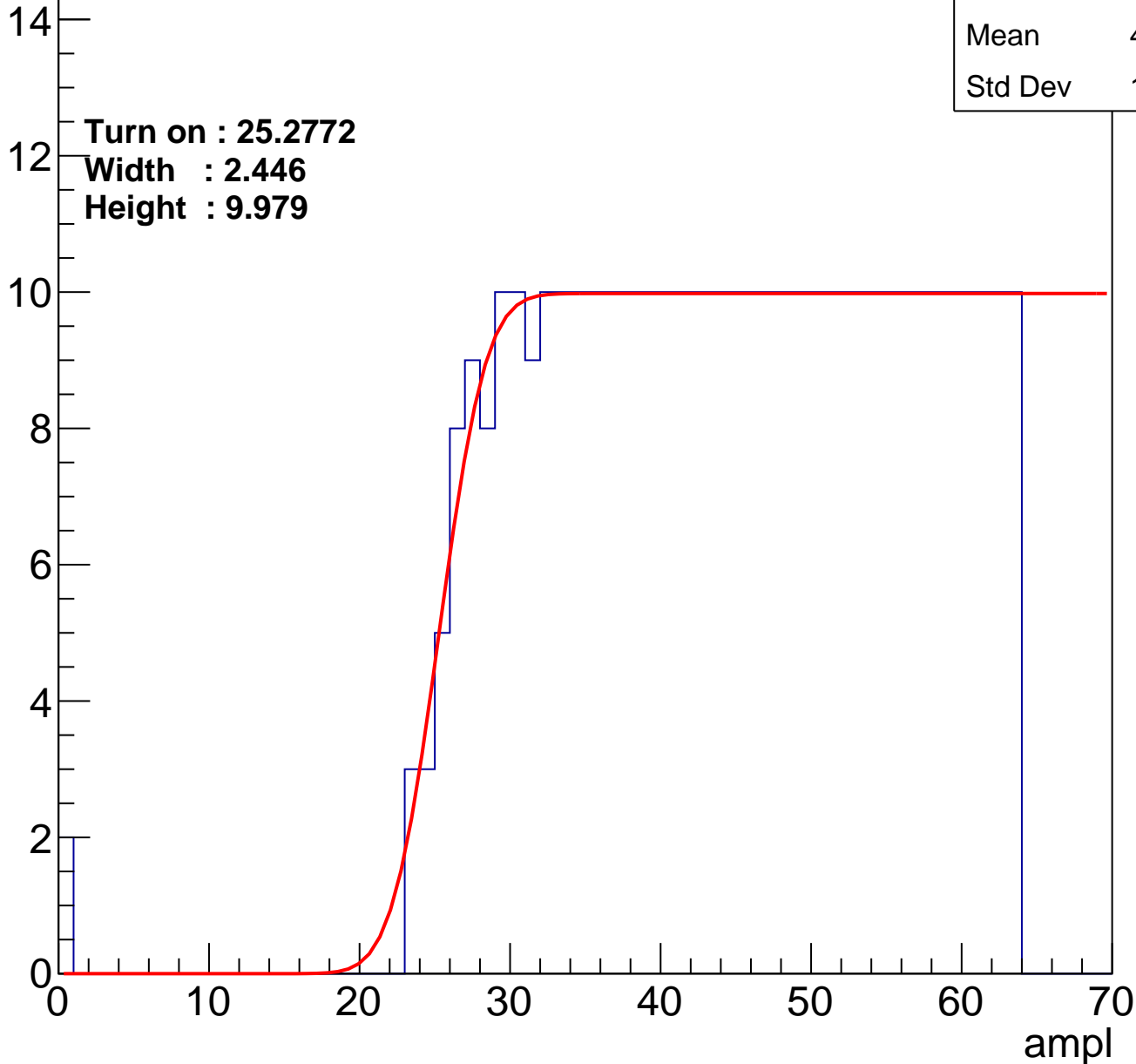
Entry

Entries	387
Mean	43.95
Std Dev	11.63

Turn on : 25.2772

Width : 2.446

Height : 9.979



B1L002S, U14-ch7

calib_packv5_042523_0143.root, FC#10, port B3

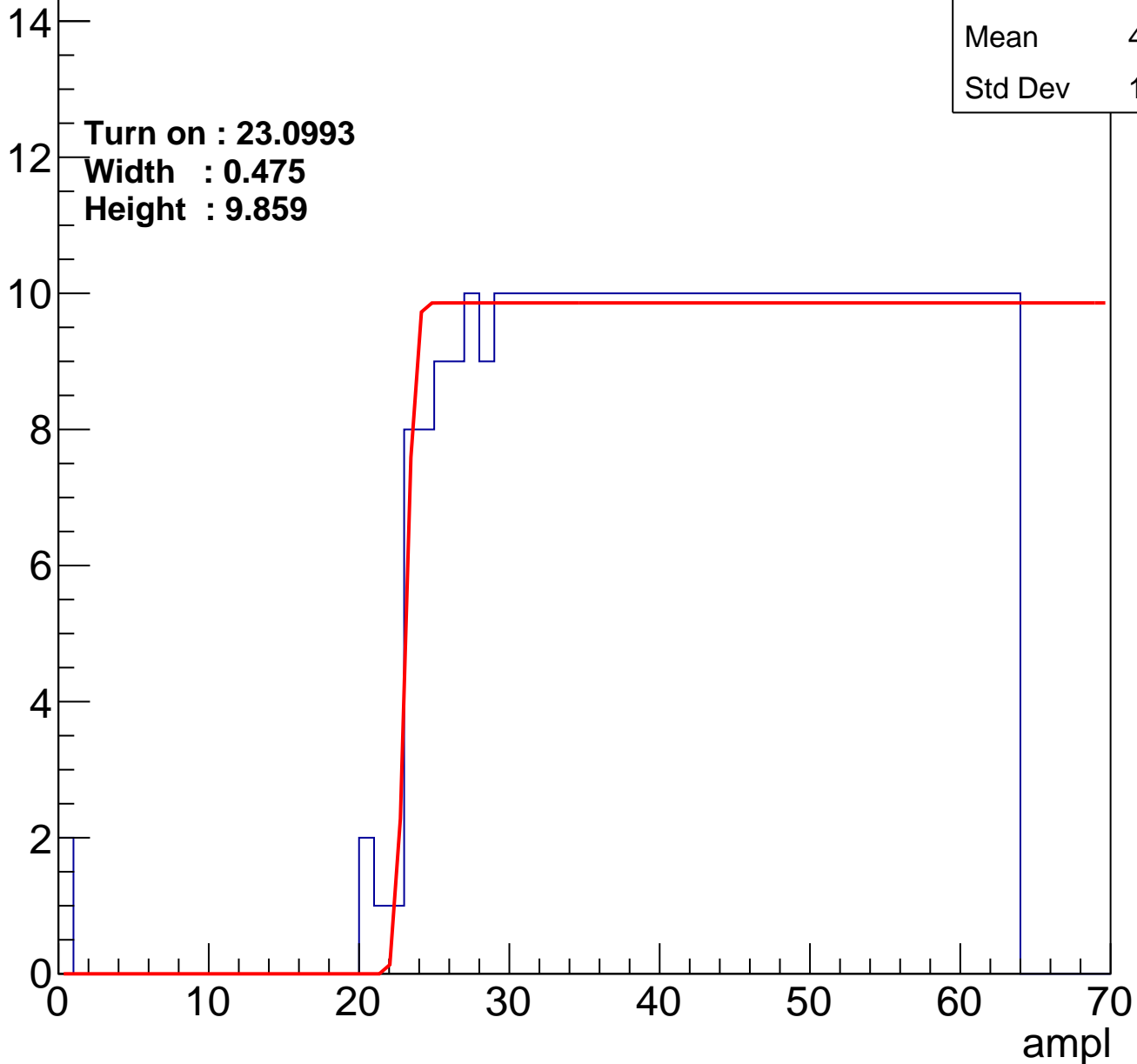
Entries	409
Mean	42.89
Std Dev	12.18

Turn on : 23.0993

Width : 0.475

Height : 9.859

Entry



B1L002S, U14-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.61
Std Dev	11.52

Turn on : 28.0905

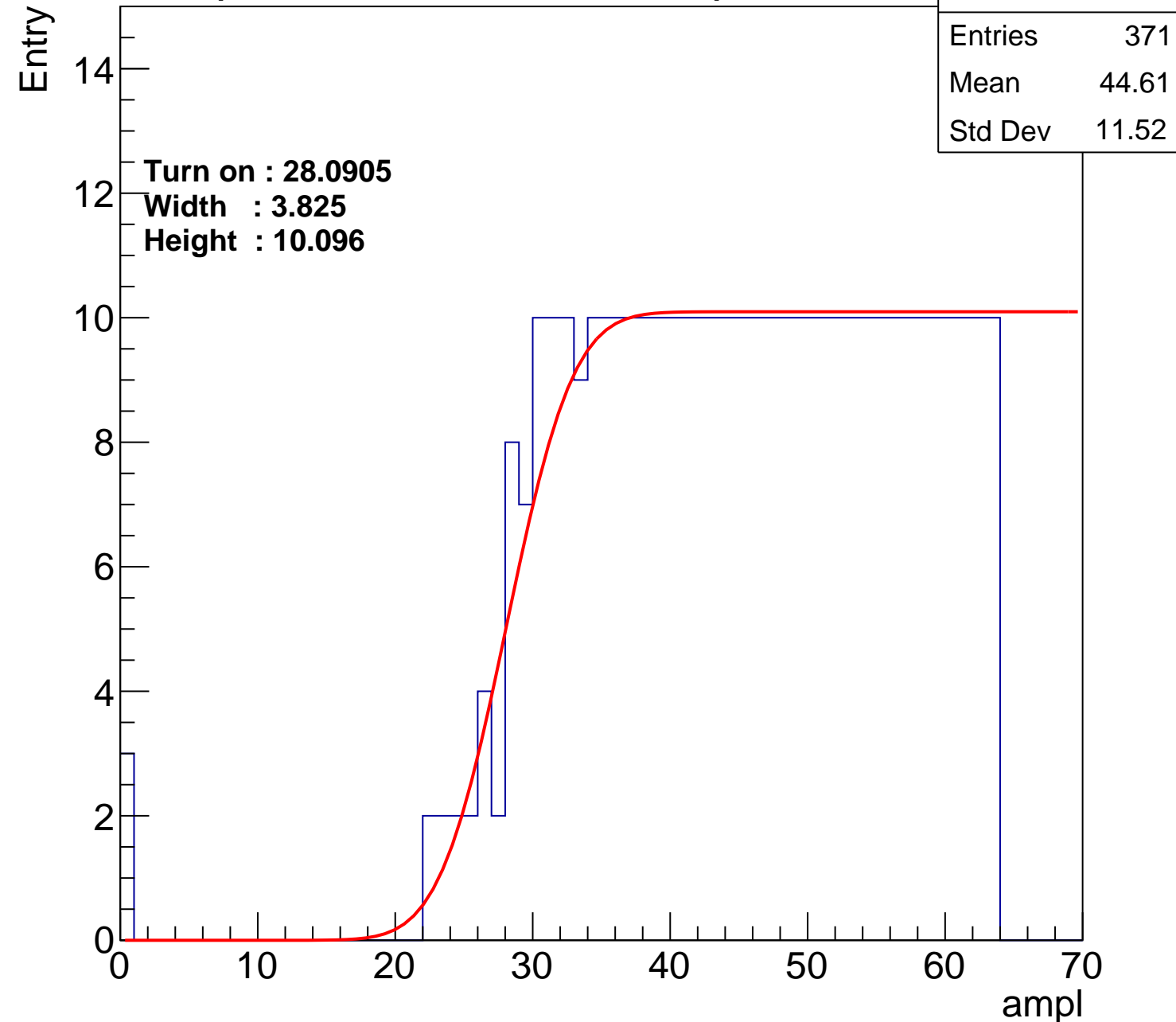
Width : 3.825

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.23
Std Dev	11.65

Turn on : 26.4158

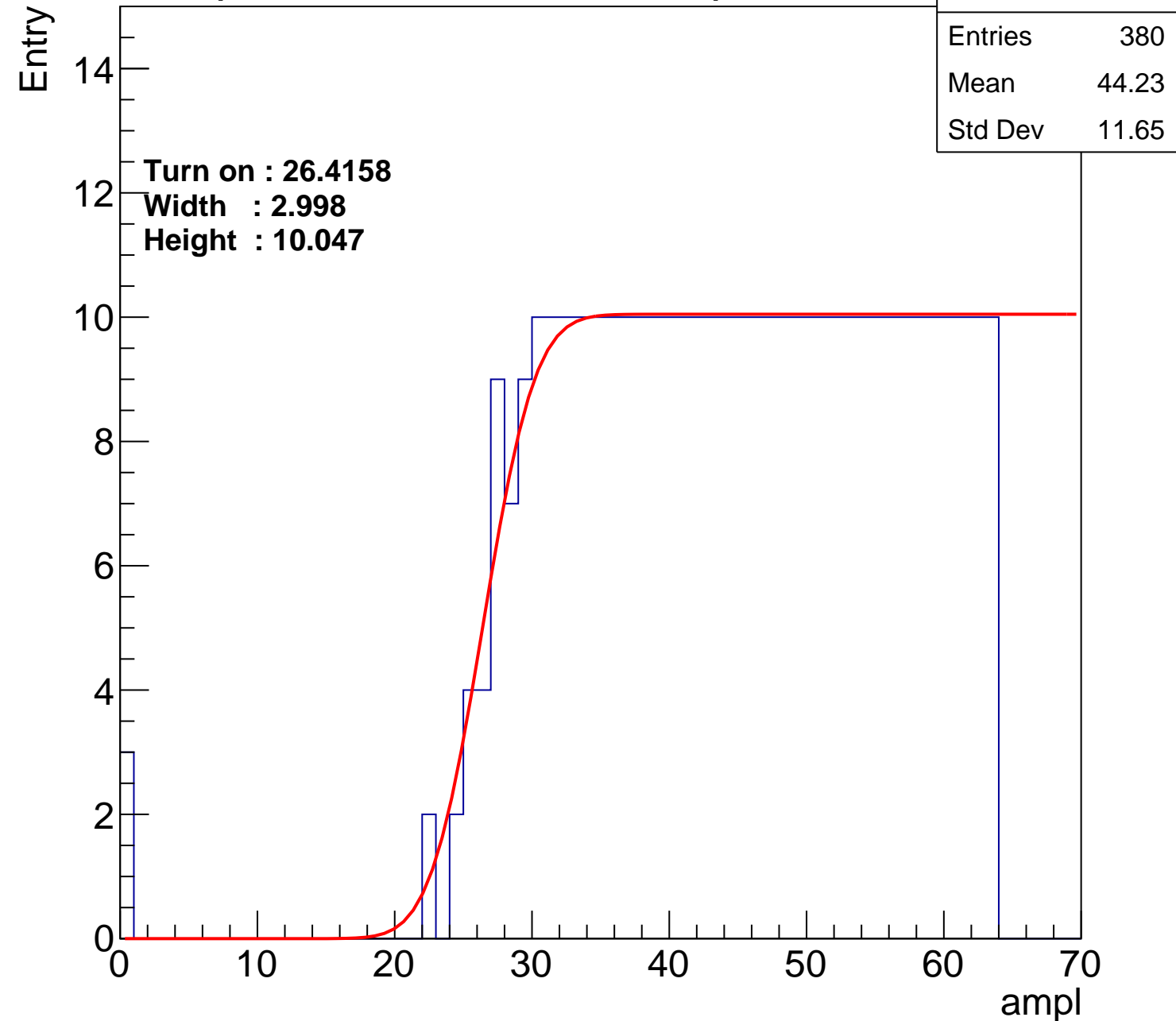
Width : 2.998

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.48
Std Dev	11.74

Turn on : 24.2344

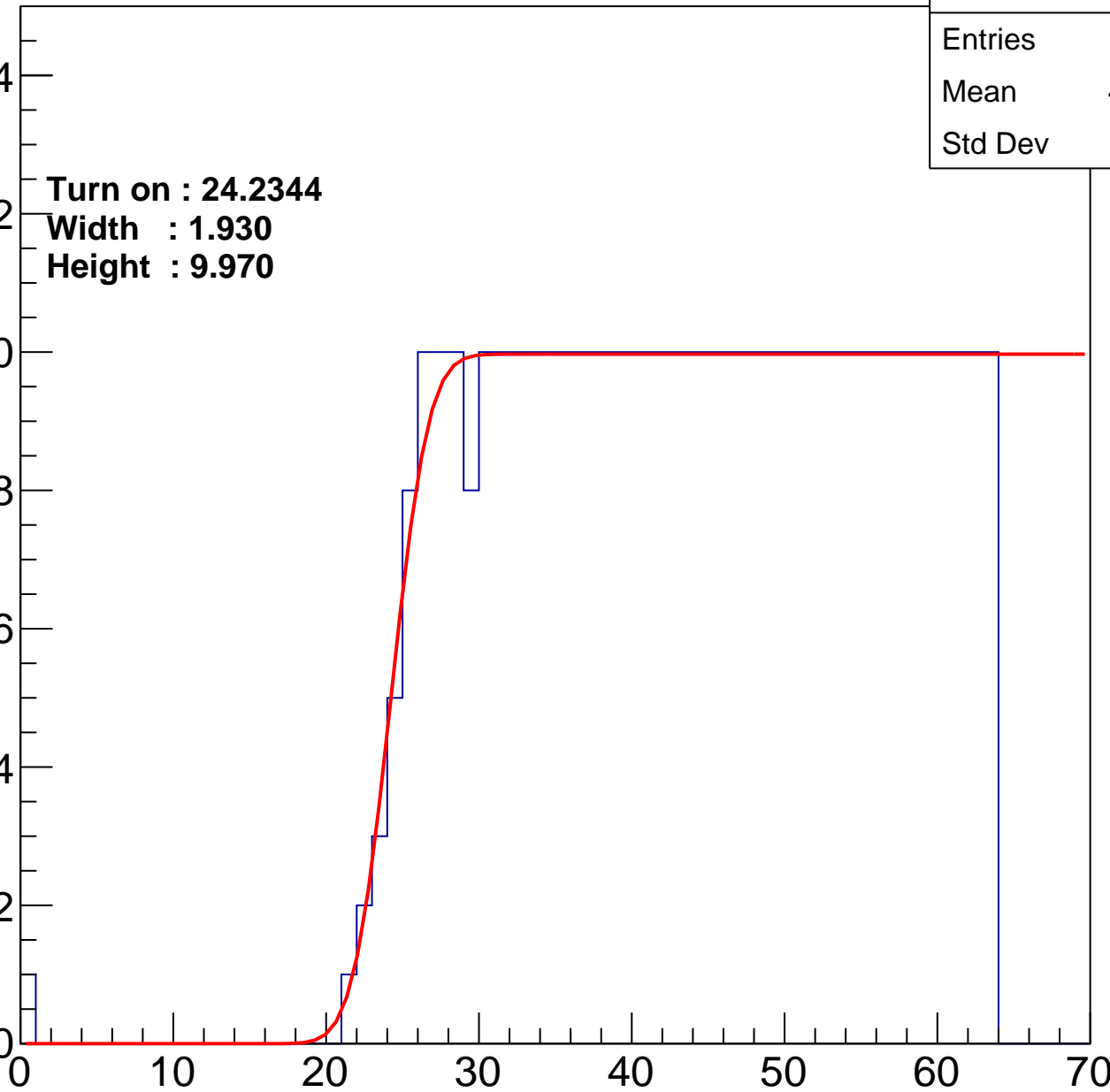
Width : 1.930

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch11

calib_packv5_042523_0143.root, FC#10, port B3

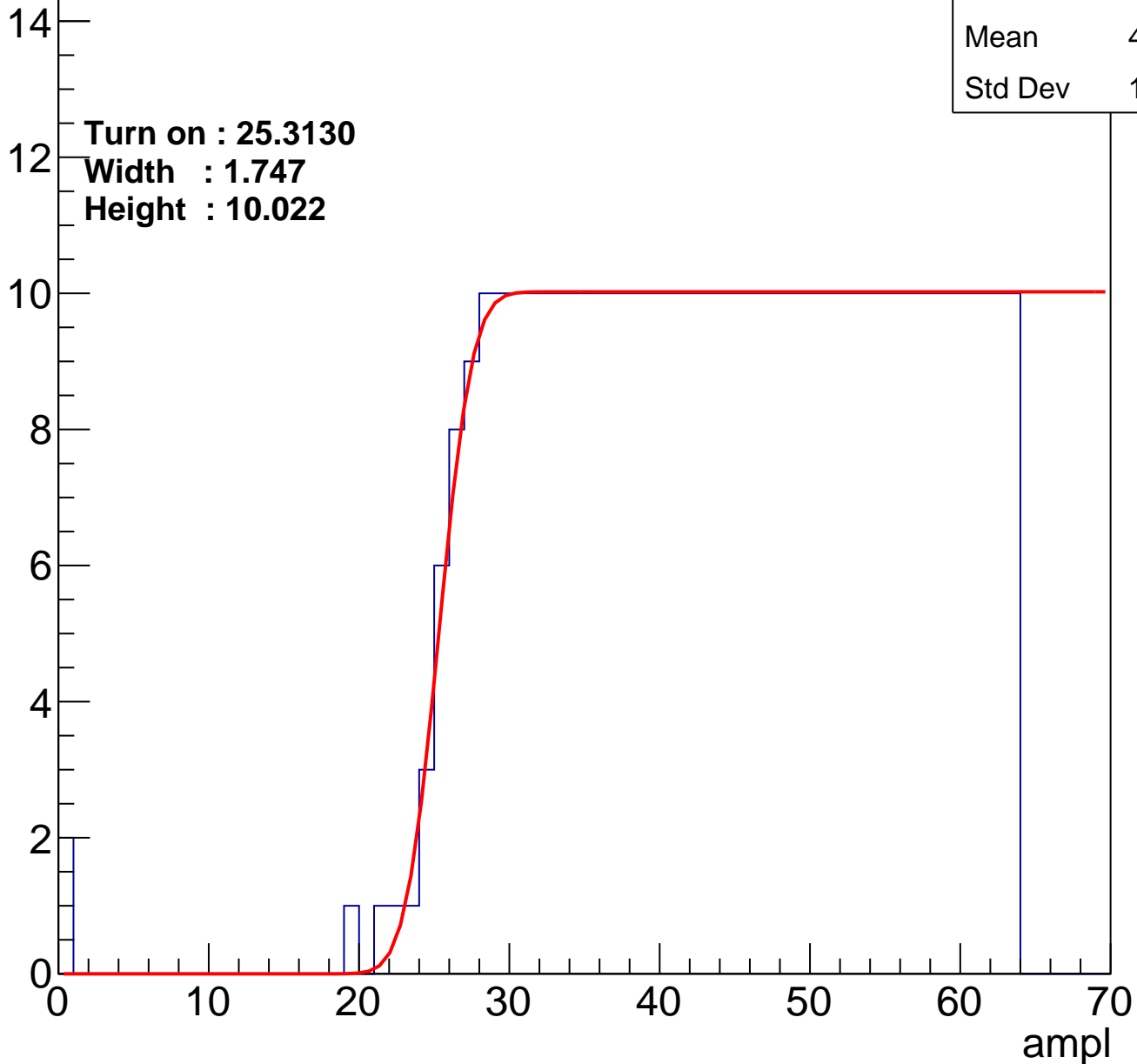
Entries	392
Mean	43.72
Std Dev	11.75

Turn on : 25.3130

Width : 1.747

Height : 10.022

Entry



B1L002S, U14-ch12

calib_packv5_042523_0143.root, FC#10, port B3

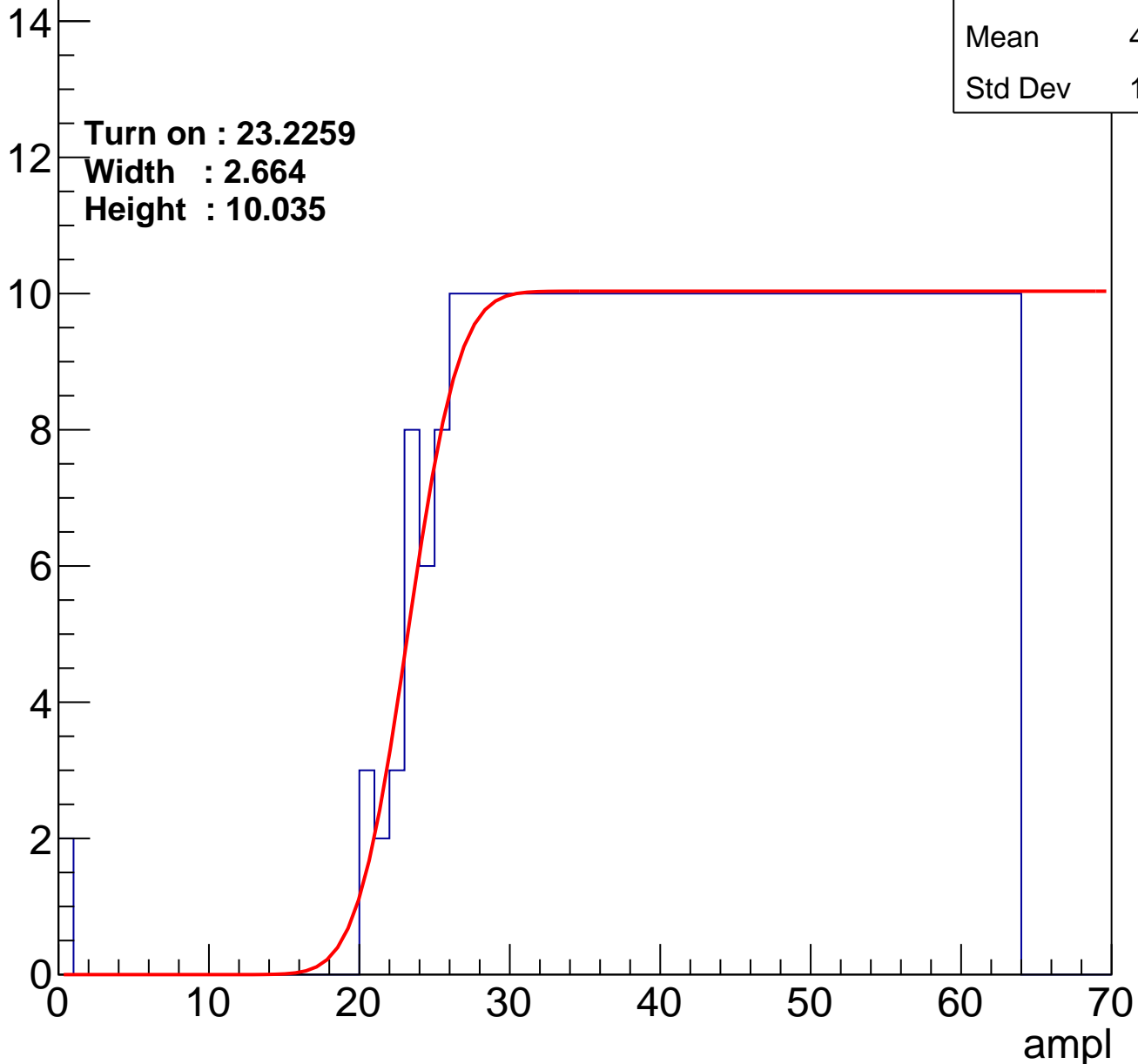
Entries	412
Mean	42.73
Std Dev	12.27

Turn on : 23.2259

Width : 2.664

Height : 10.035

Entry



B1L002S, U14-ch13

calib_packv5_042523_0143.root, FC#10, port B3

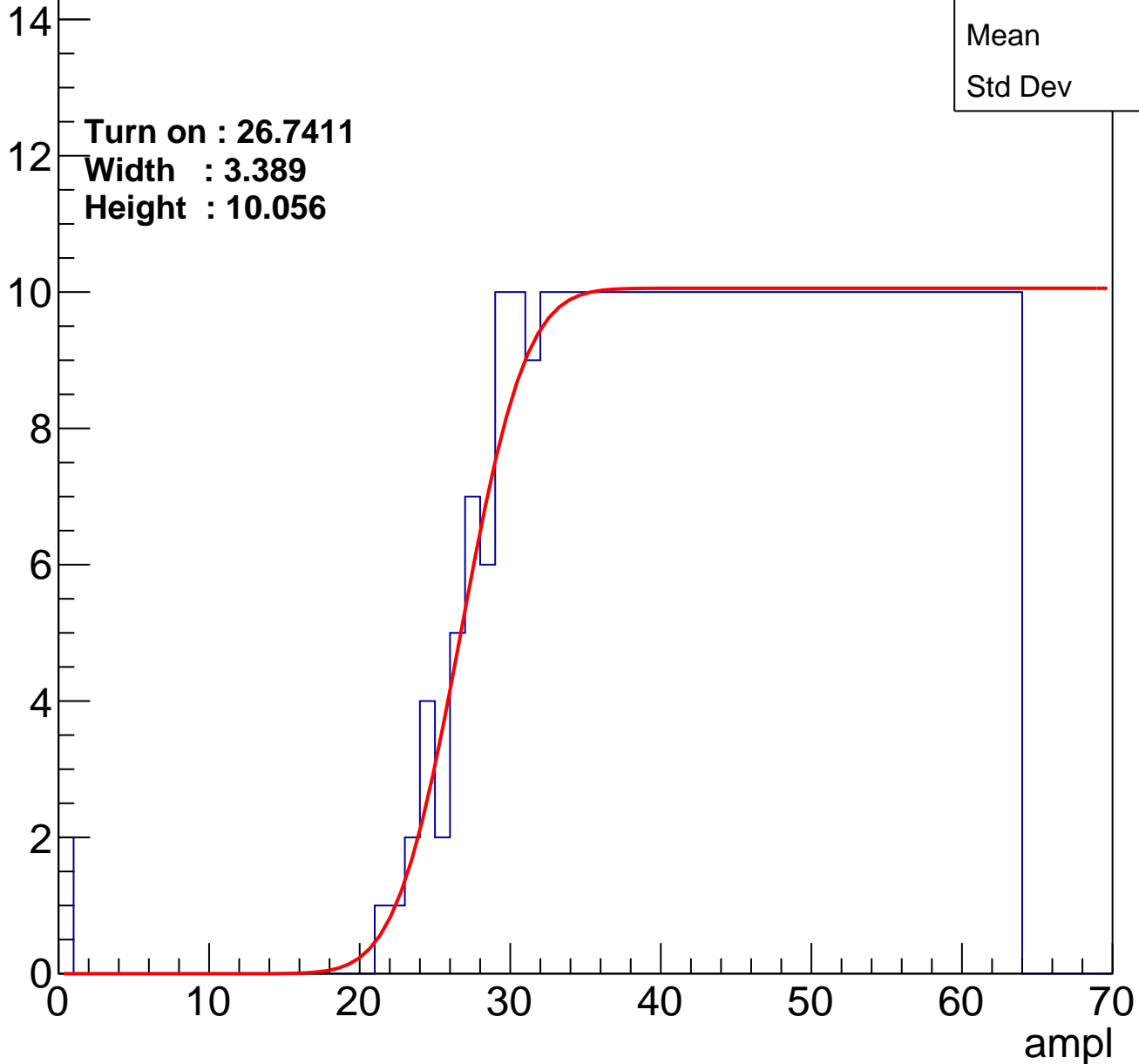
Entries	379
Mean	44.3
Std Dev	11.5

Turn on : 26.7411

Width : 3.389

Height : 10.056

Entry



B1L002S, U14-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.51
Std Dev	11.99

Turn on : 24.5543

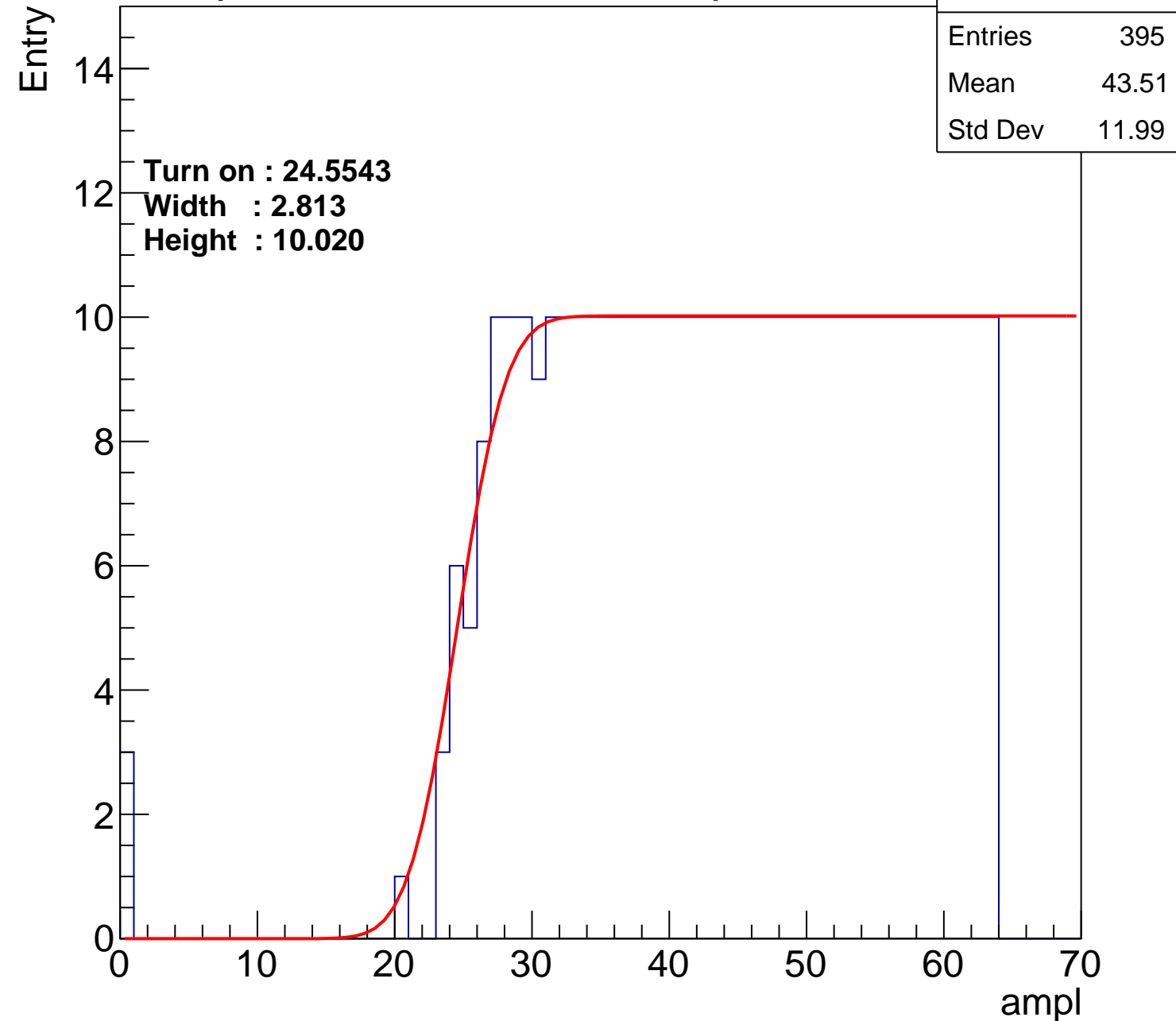
Width : 2.813

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.52
Std Dev	11.85

Turn on : 24.0951

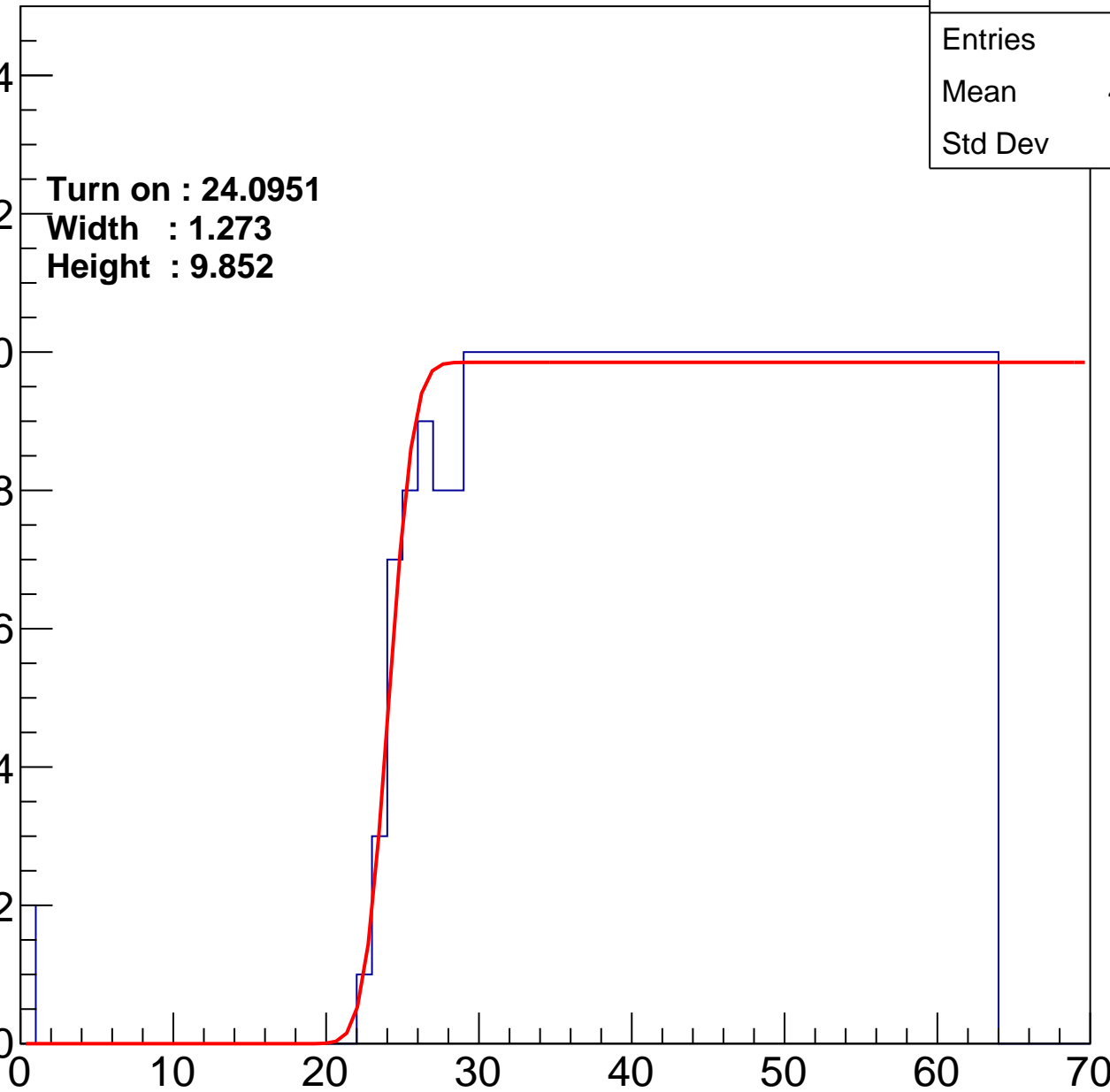
Width : 1.273

Height : 9.852

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch16

calib_packv5_042523_0143.root, FC#10, port B3

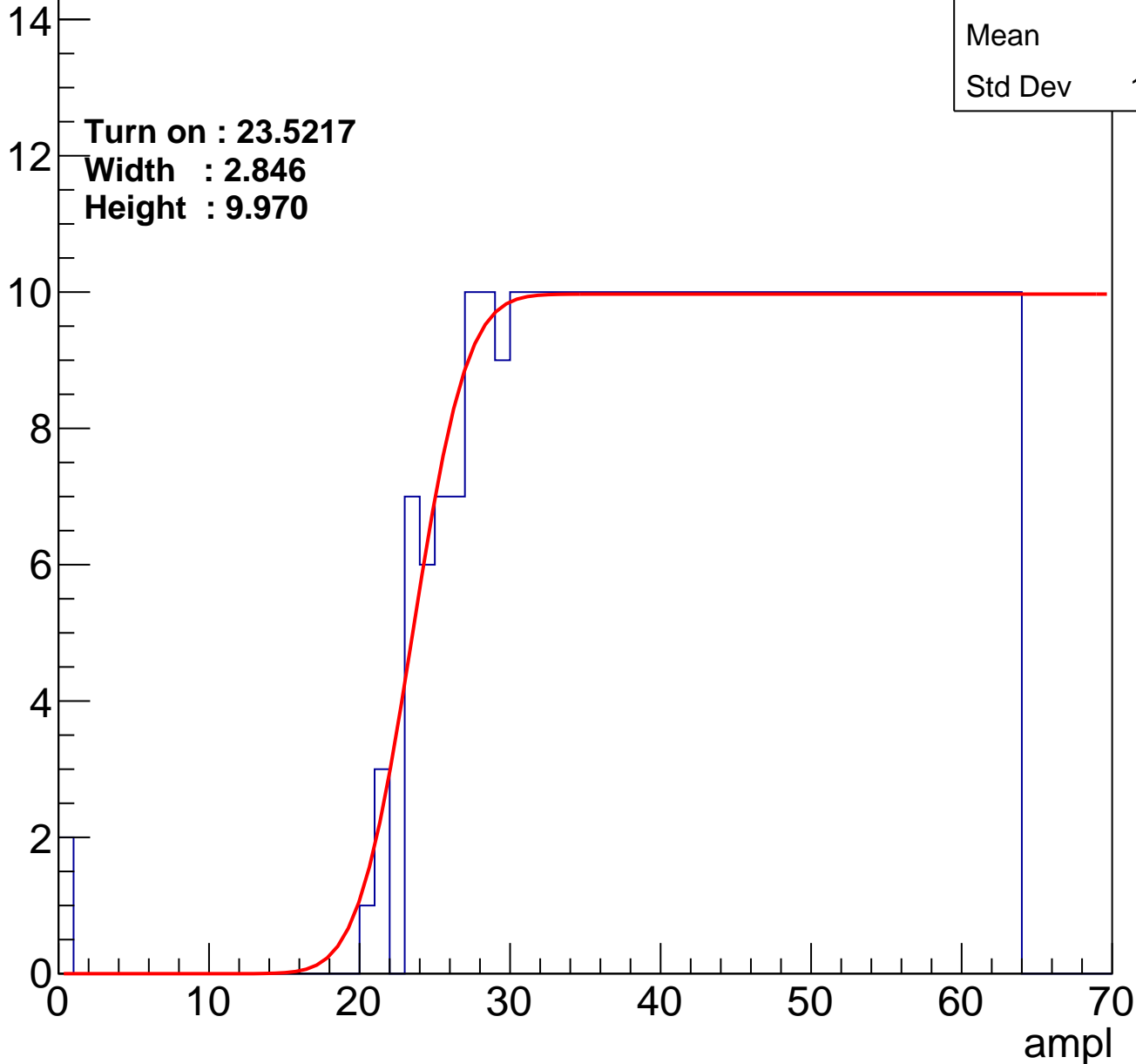
Entries	402
Mean	43.2
Std Dev	12.05

Turn on : 23.5217

Width : 2.846

Height : 9.970

Entry



B1L002S, U14-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.61

Turn on : 25.5158

Width : 2.317

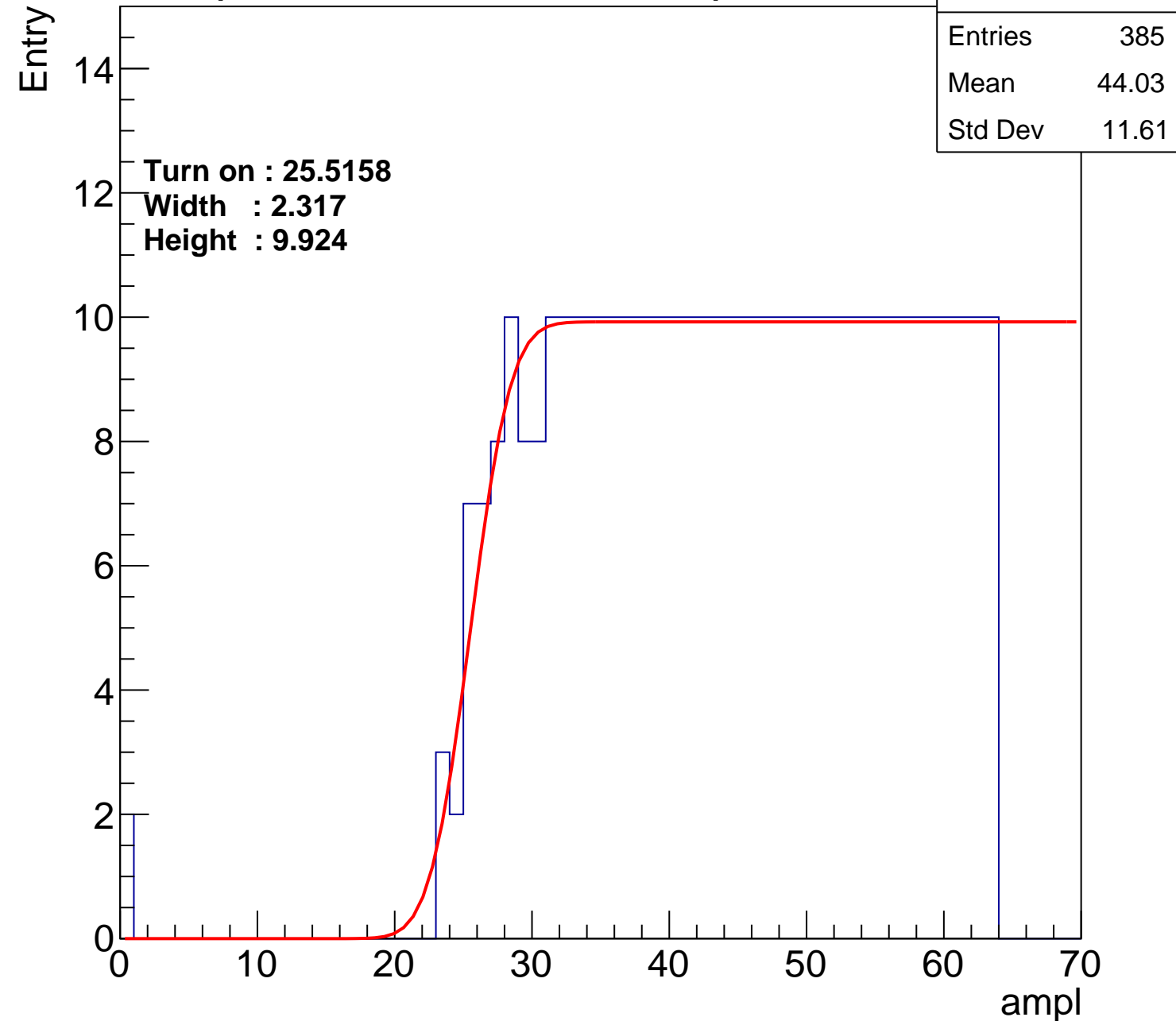
Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U14-ch18

calib_packv5_042523_0143.root, FC#10, port B3

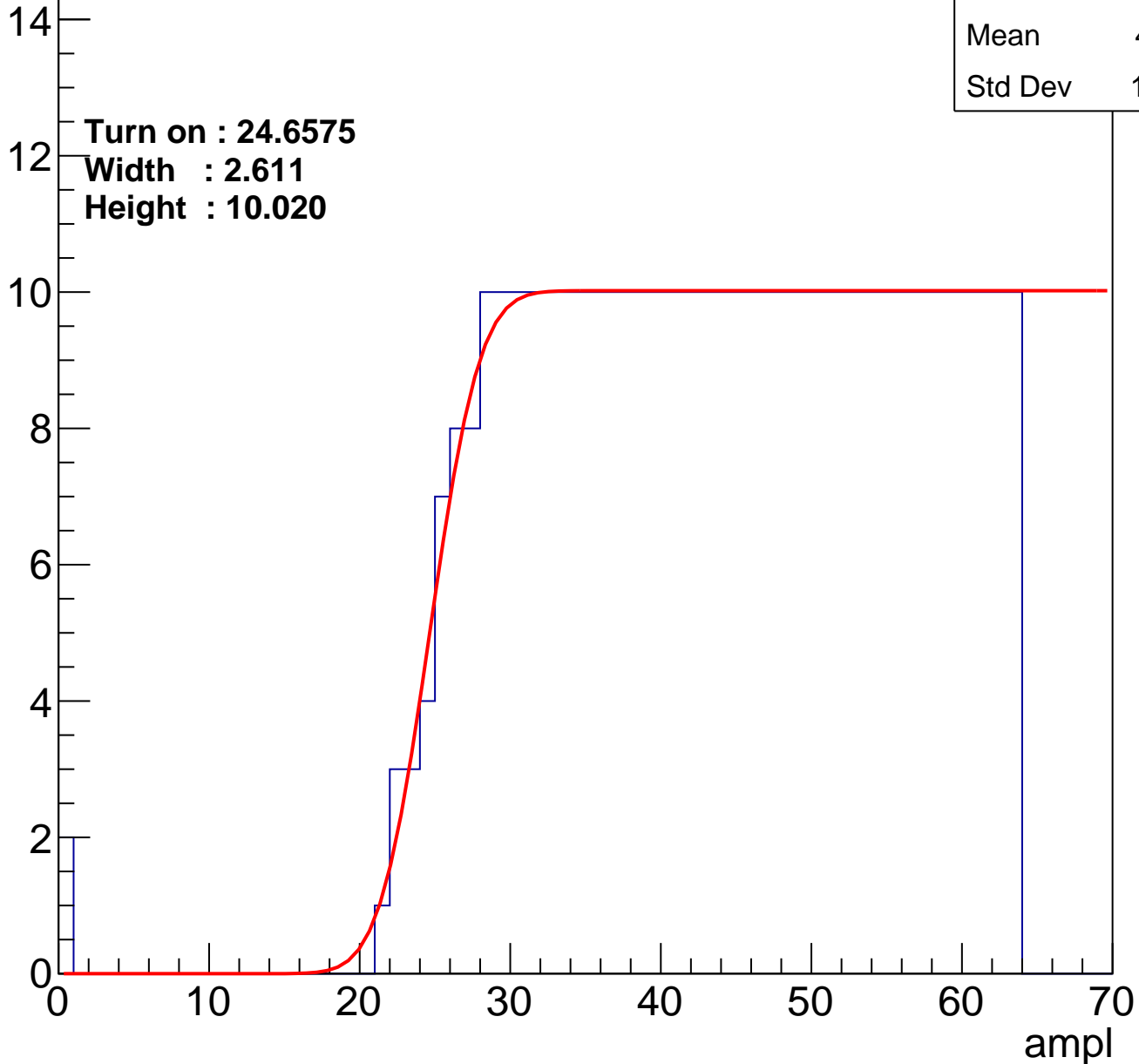
Entries	396
Mean	43.51
Std Dev	11.87

Turn on : 24.6575

Width : 2.611

Height : 10.020

Entry



B1L002S, U14-ch19

calib_packv5_042523_0143.root, FC#10, port B3

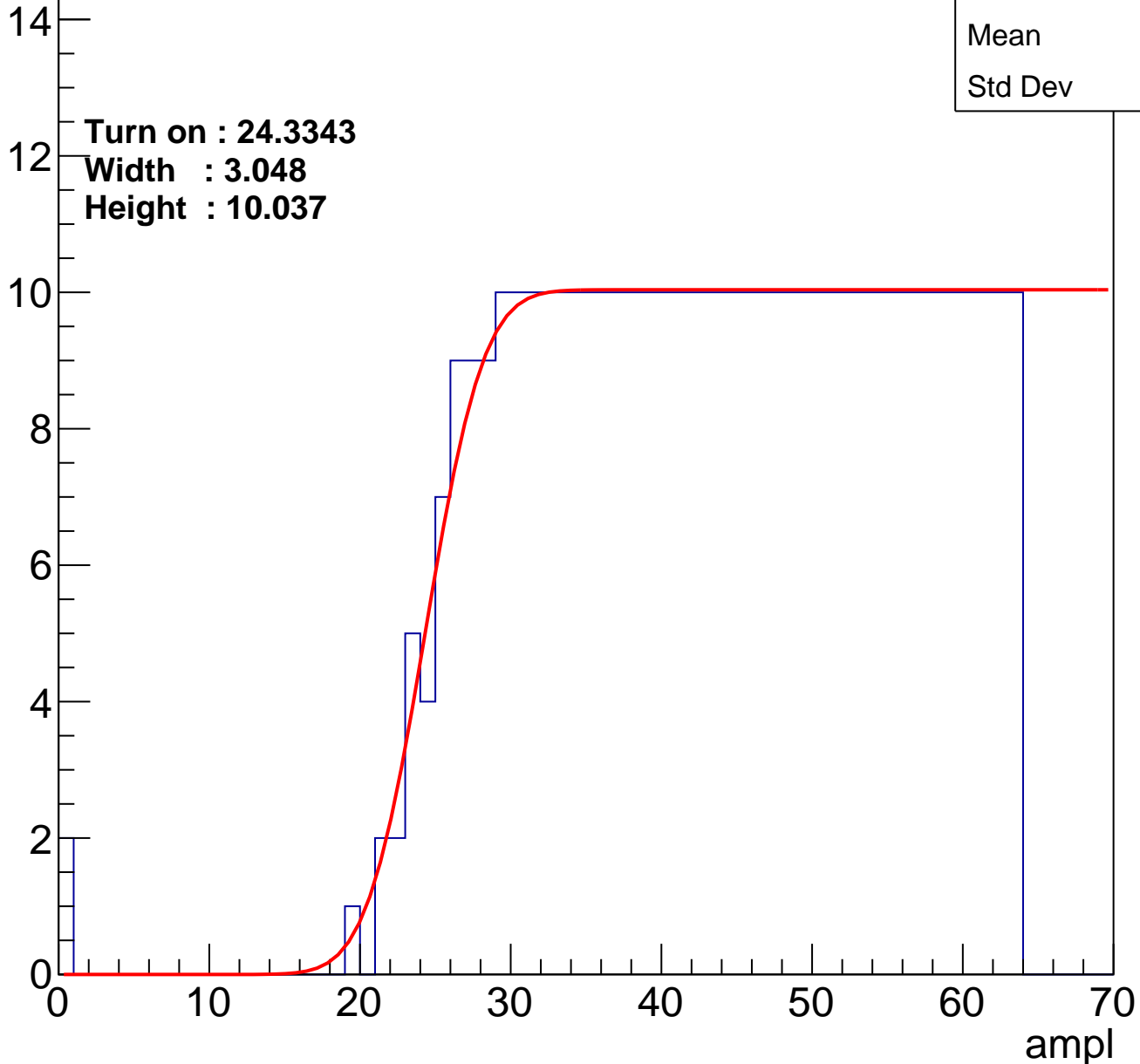
Entries	400
Mean	43.3
Std Dev	12

Turn on : 24.3343

Width : 3.048

Height : 10.037

Entry



B1L002S, U14-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.63
Std Dev	12.08

Turn on : 25.5232

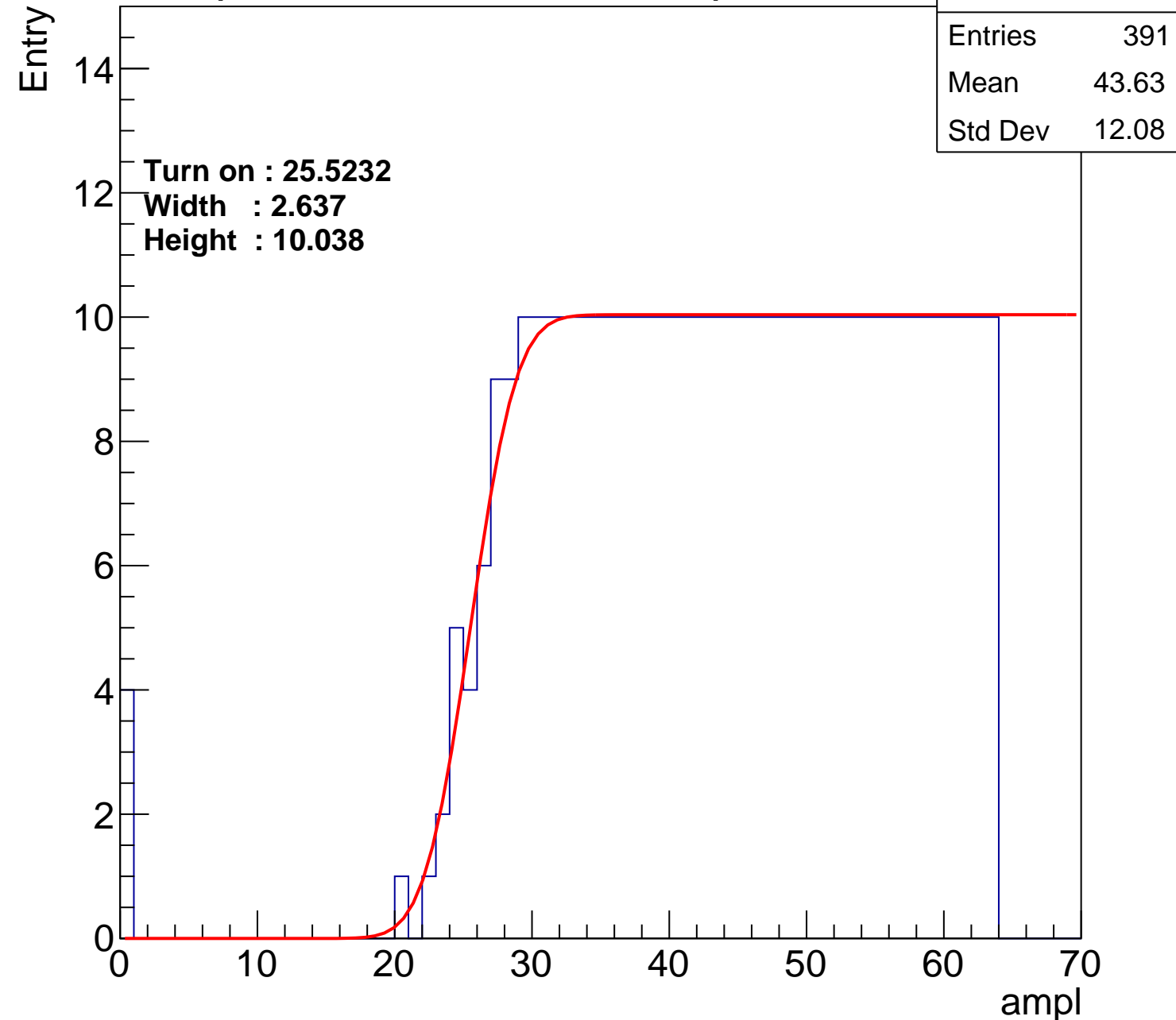
Width : 2.637

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.12
Std Dev	11.76

Turn on : 27.0095

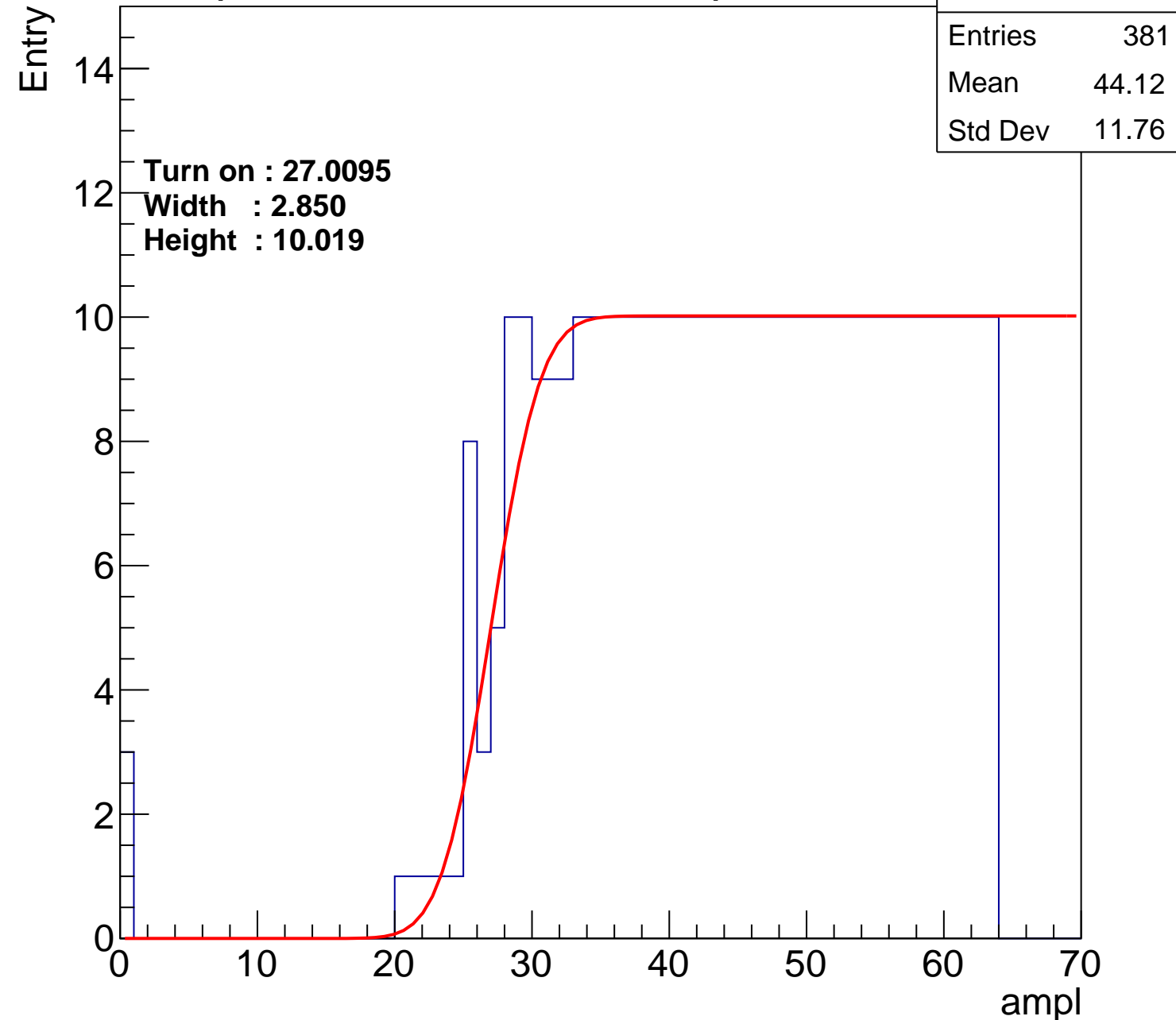
Width : 2.850

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch22

calib_packv5_042523_0143.root, FC#10, port B3

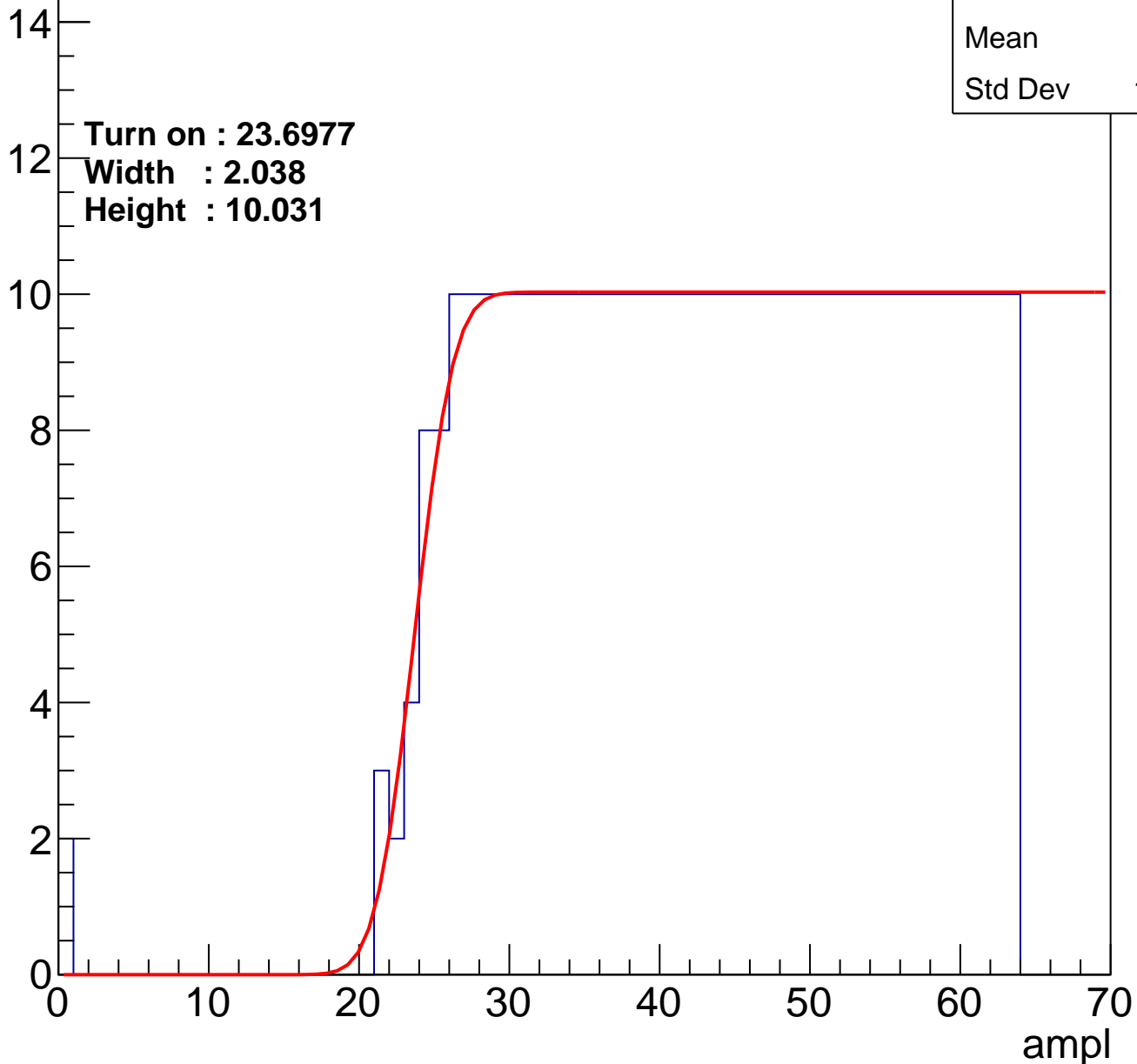
Entries	407
Mean	43
Std Dev	12.11

Turn on : 23.6977

Width : 2.038

Height : 10.031

Entry



B1L002S, U14-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.6
Std Dev	11.72

Turn on : 24.9396

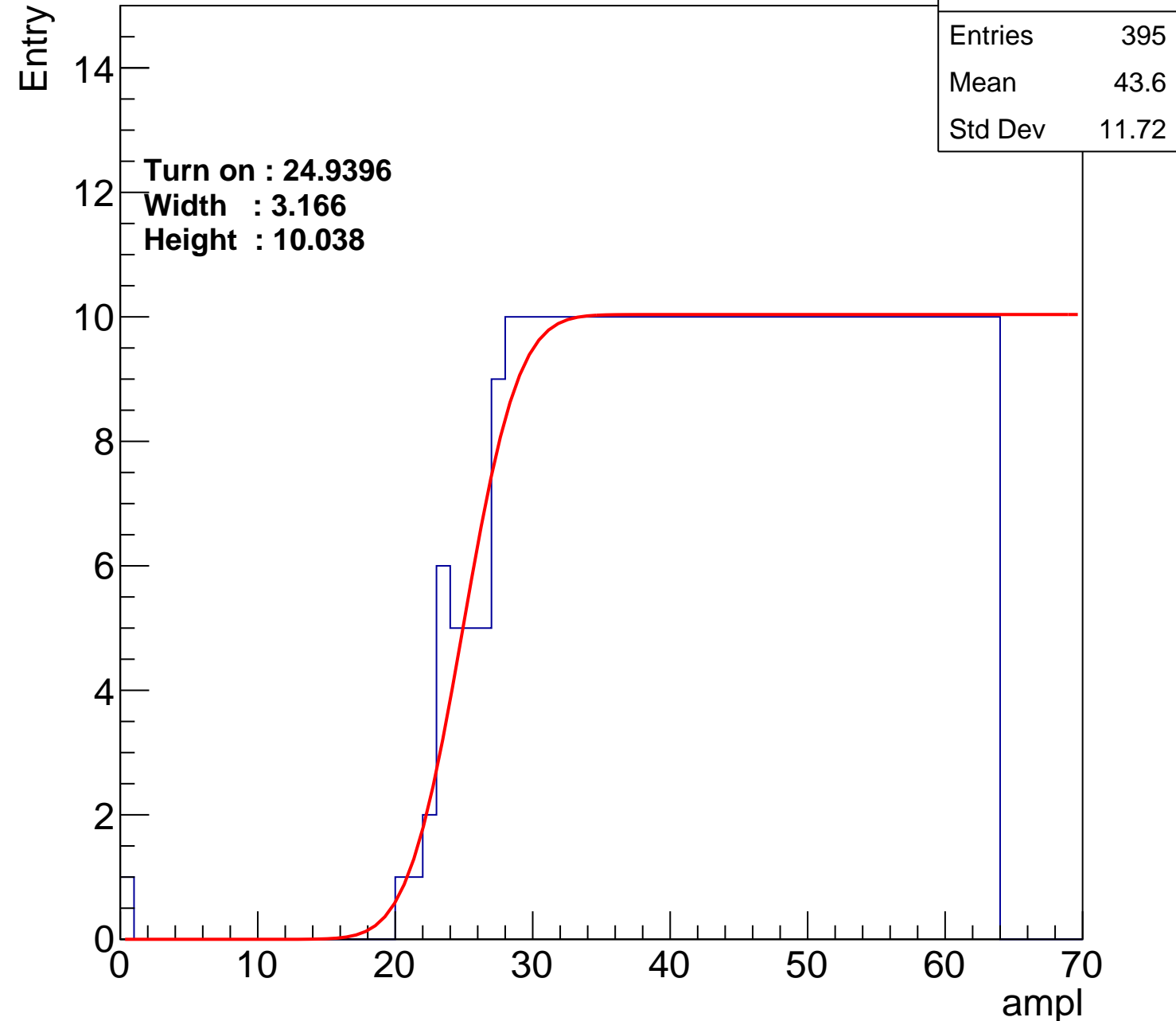
Width : 3.166

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.8652

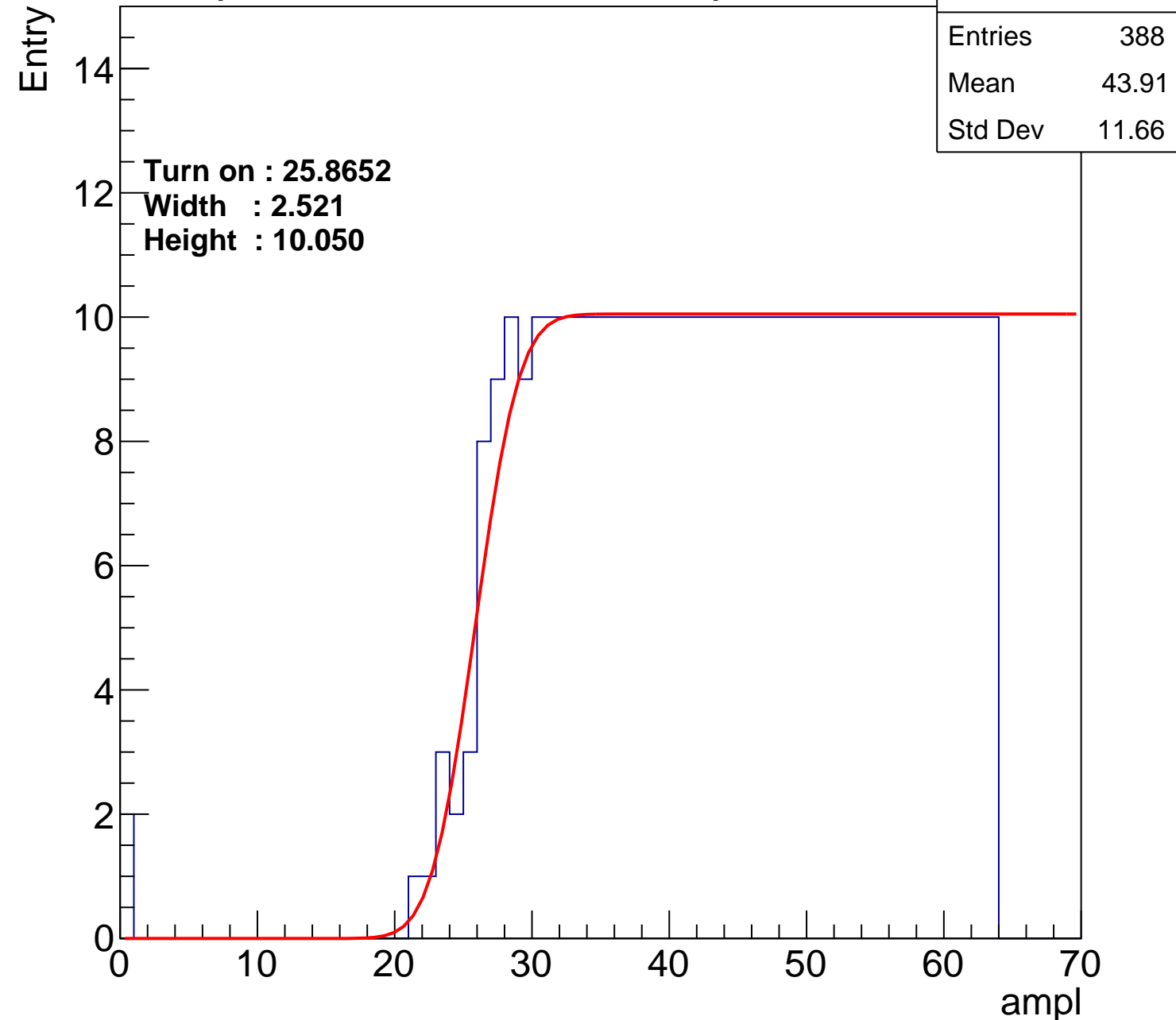
Width : 2.521

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch25

calib_packv5_042523_0143.root, FC#10, port B3

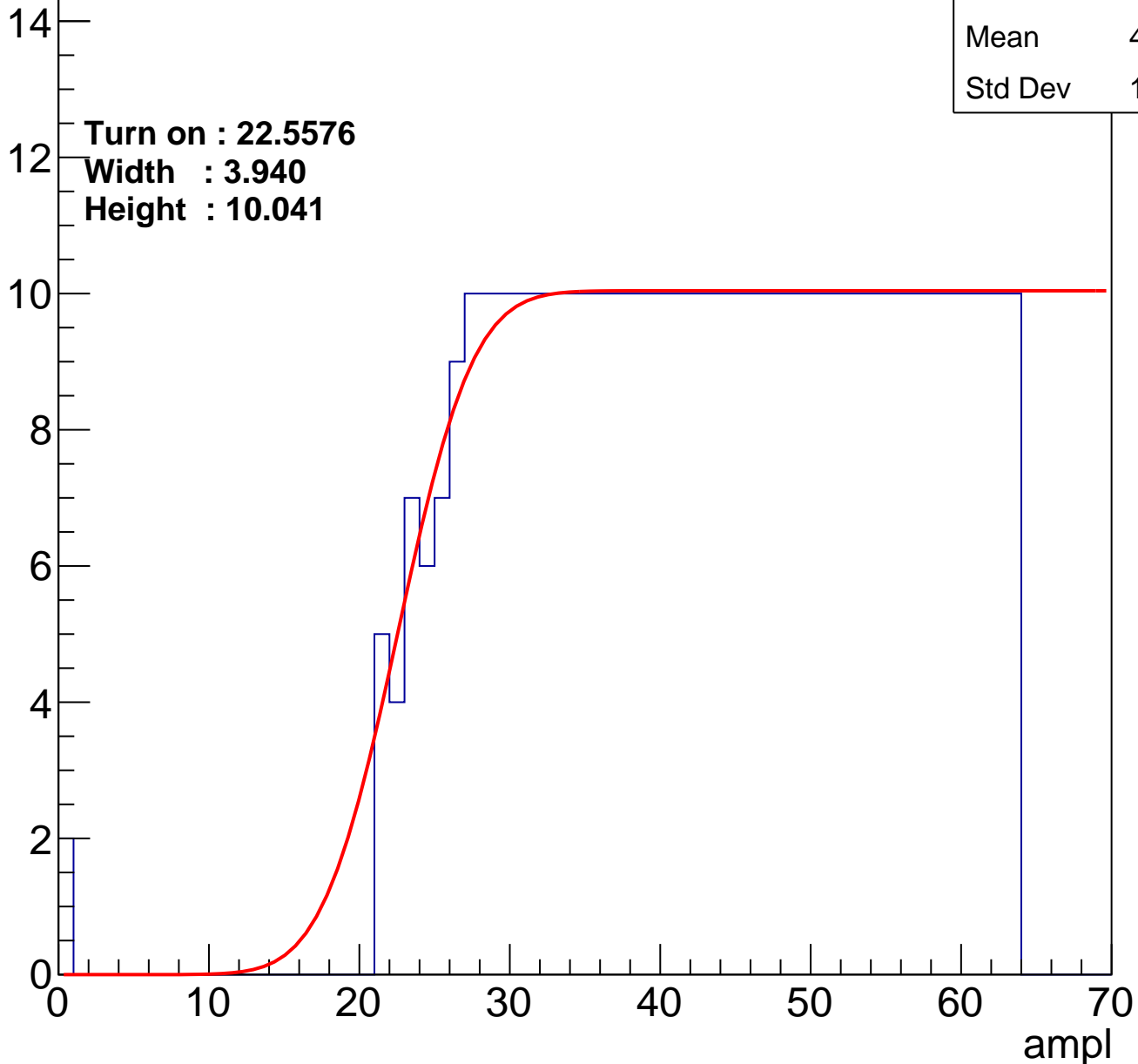
Entries	410
Mean	42.82
Std Dev	12.23

Turn on : 22.5576

Width : 3.940

Height : 10.041

Entry



B1L002S, U14-ch26

calib_packv5_042523_0143.root, FC#10, port B3

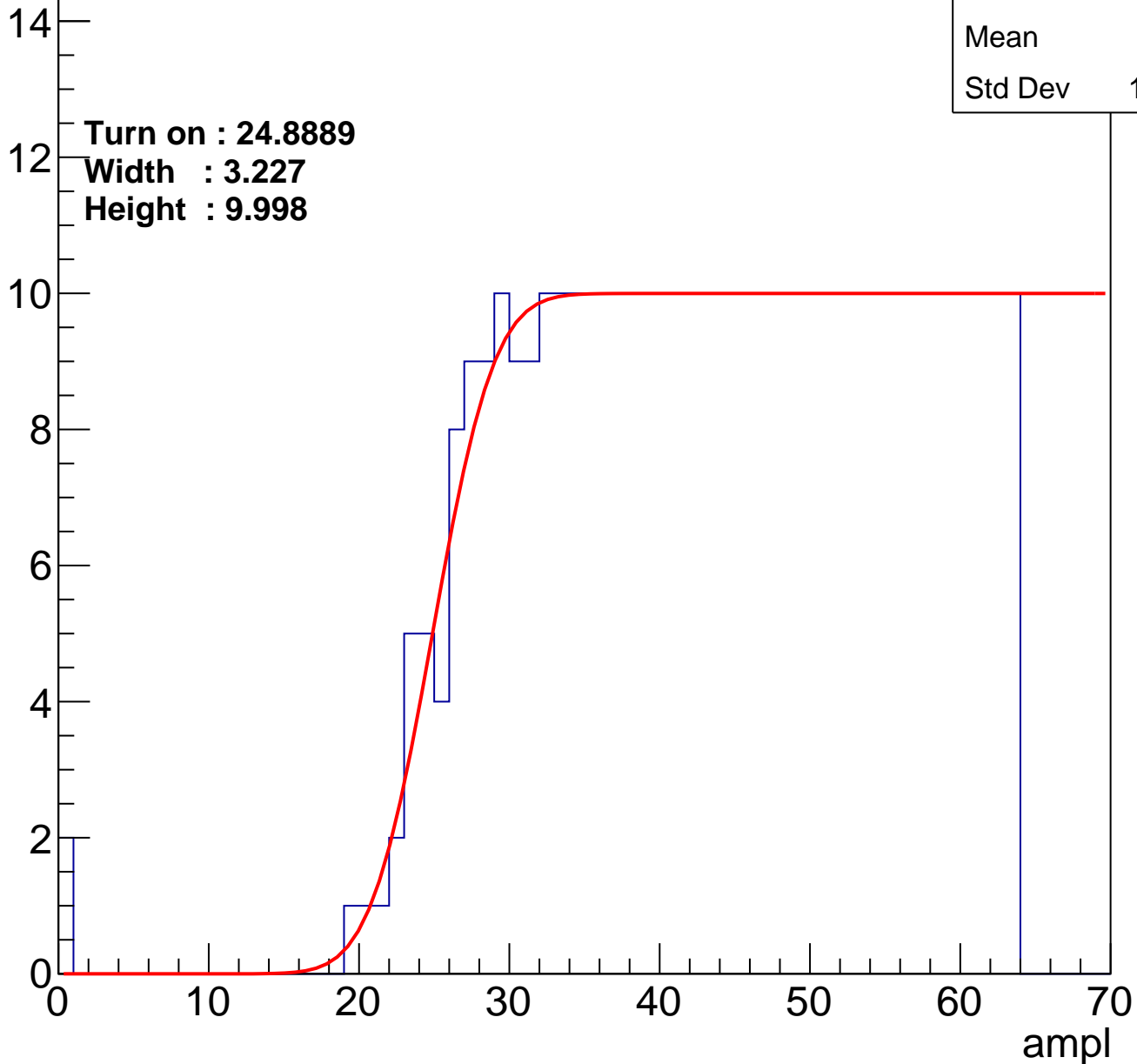
Entries	395
Mean	43.5
Std Dev	11.94

Turn on : 24.8889

Width : 3.227

Height : 9.998

Entry



B1L002S, U14-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.6506

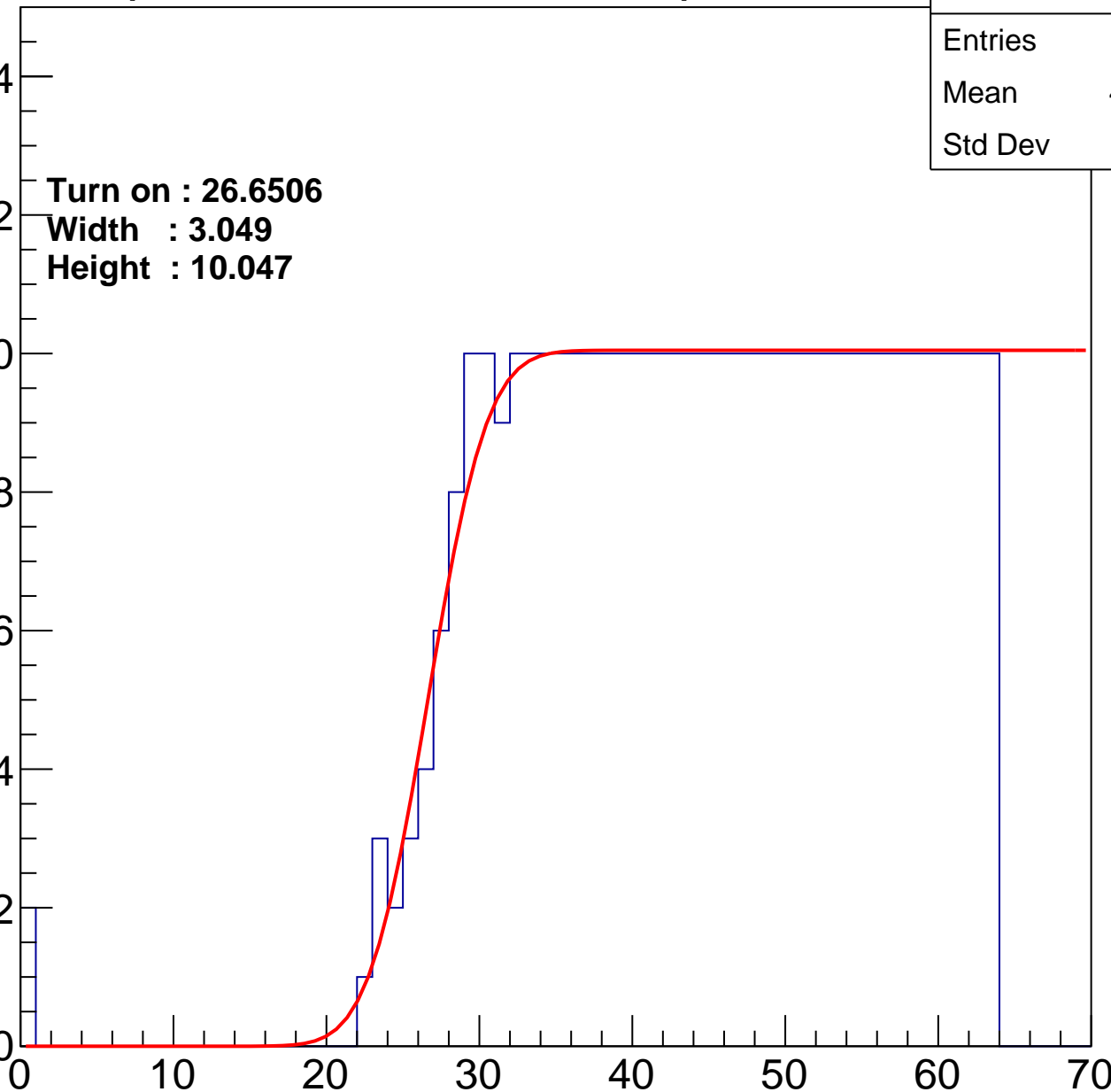
Width : 3.049

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.52
Std Dev	11.69

Turn on : 24.6185

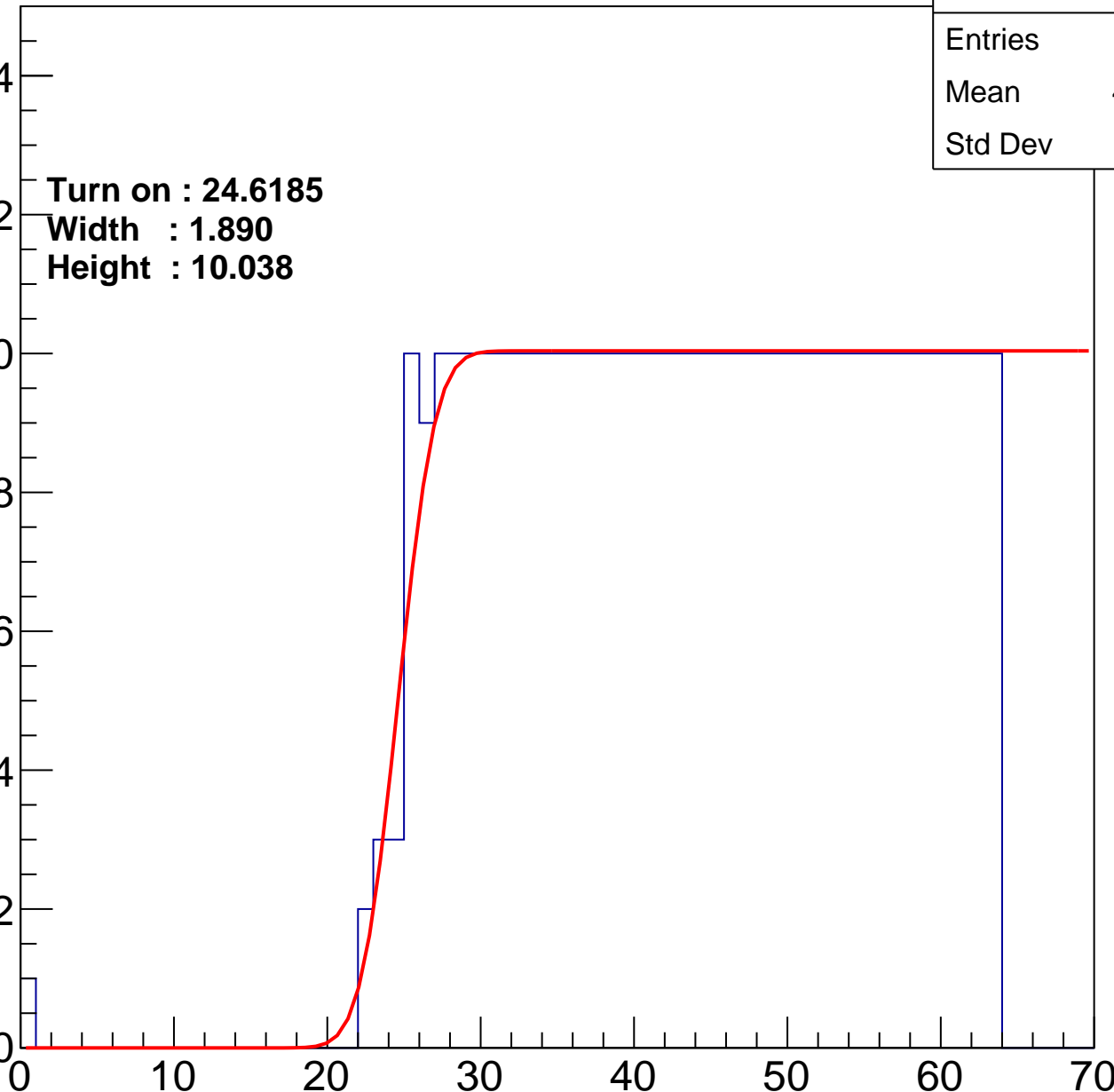
Width : 1.890

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.5
Std Dev	12.01

Turn on : 25.0613

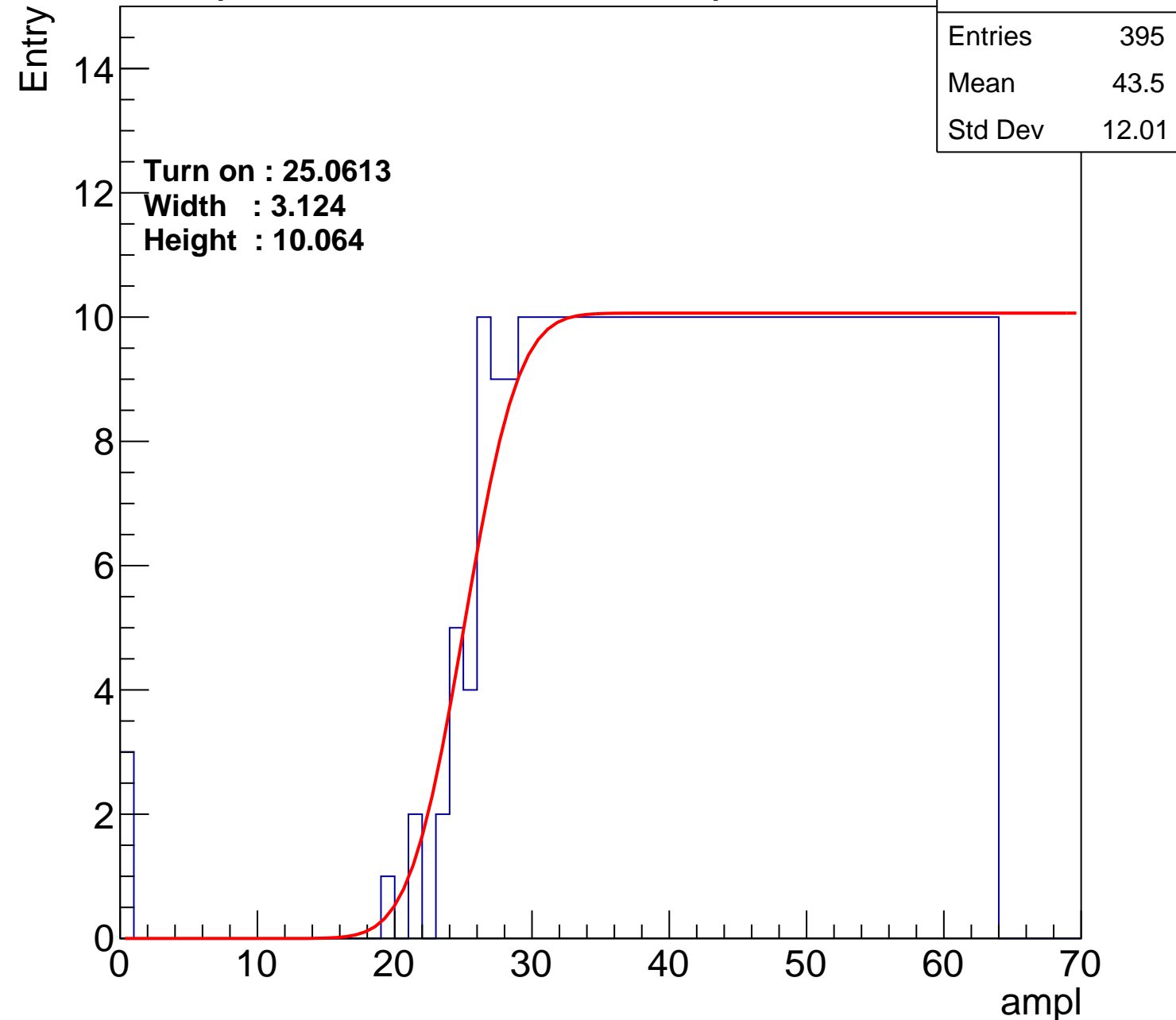
Width : 3.124

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.89
Std Dev	11.8

Turn on : 26.0788

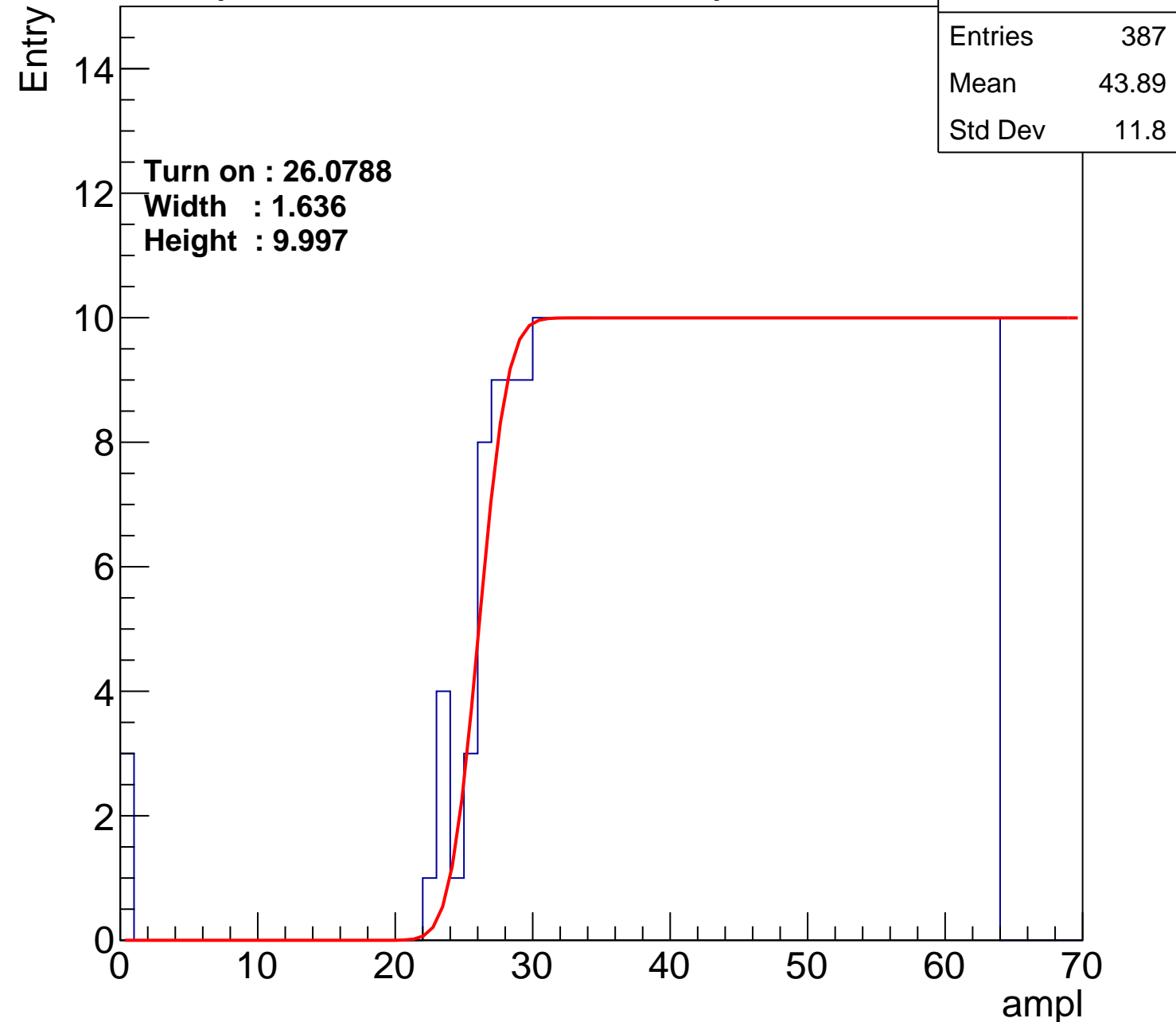
Width : 1.636

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.79
Std Dev	12.15

Turn on : 25.9564

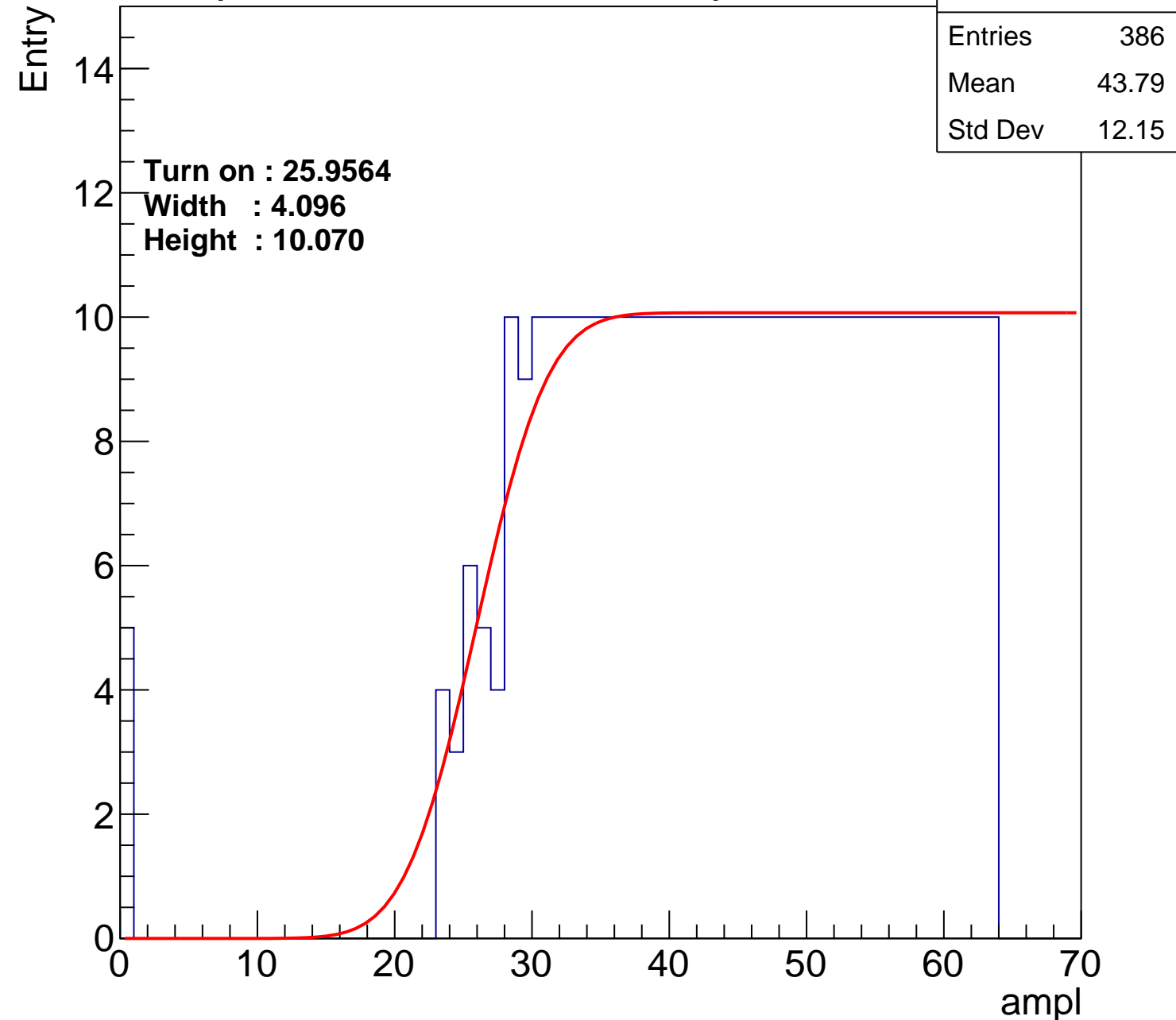
Width : 4.096

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch32

calib_packv5_042523_0143.root, FC#10, port B3

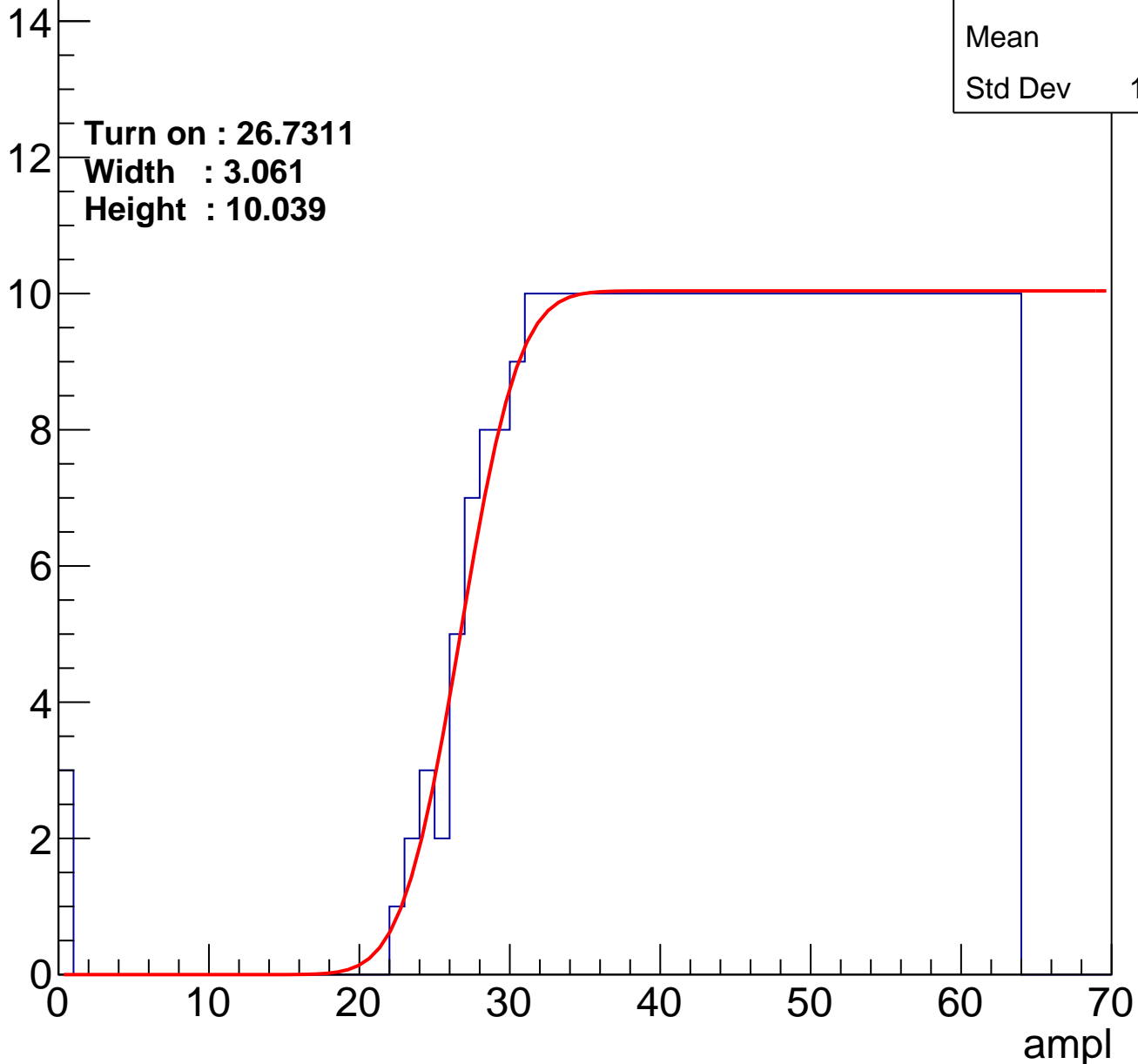
Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.7311

Width : 3.061

Height : 10.039

Entry



B1L002S, U14-ch33

calib_packv5_042523_0143.root, FC#10, port B3

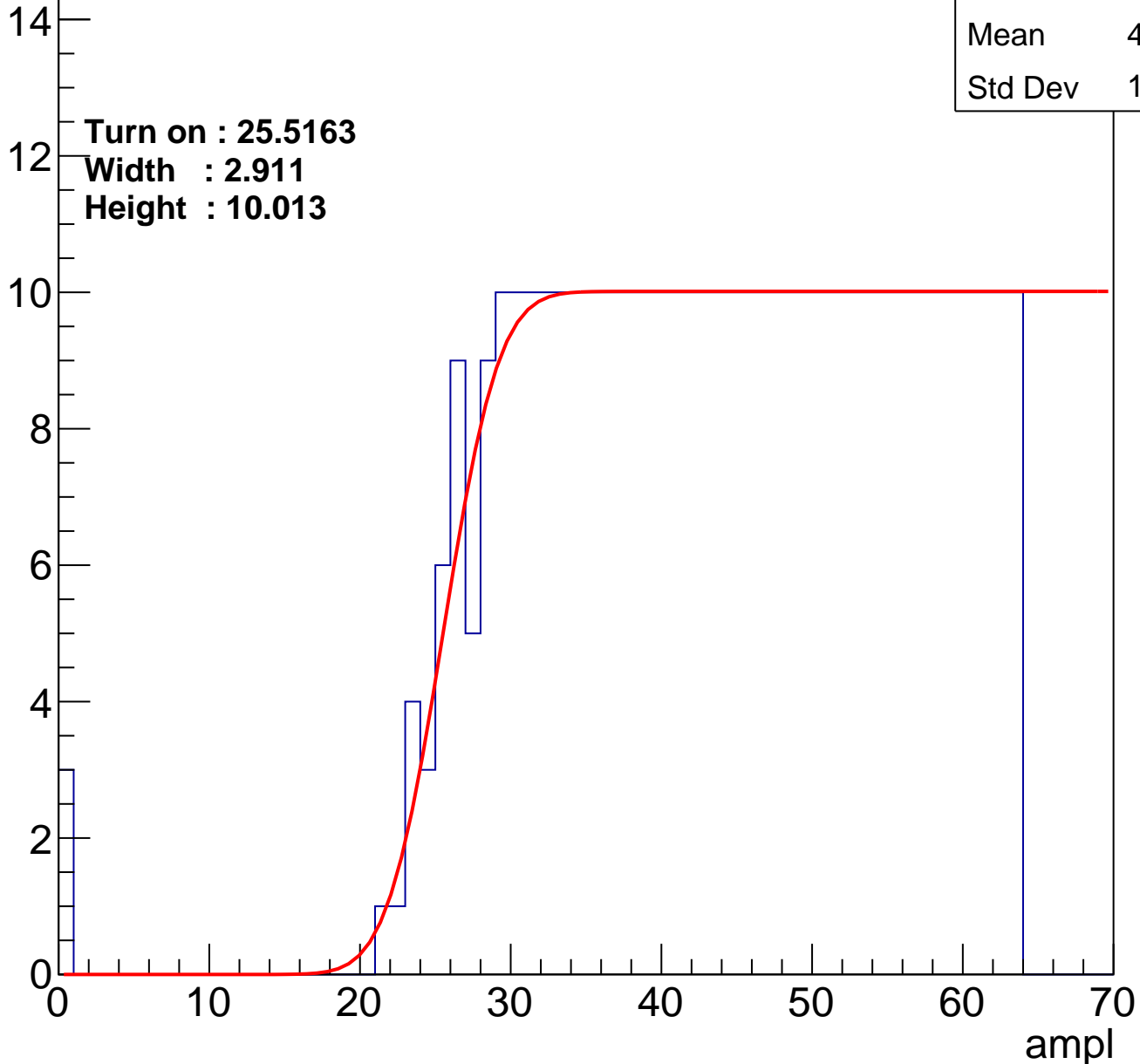
Entries	391
Mean	43.68
Std Dev	11.93

Turn on : 25.5163

Width : 2.911

Height : 10.013

Entry



B1L002S, U14-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.38
Std Dev	11.97

Turn on : 24.5385

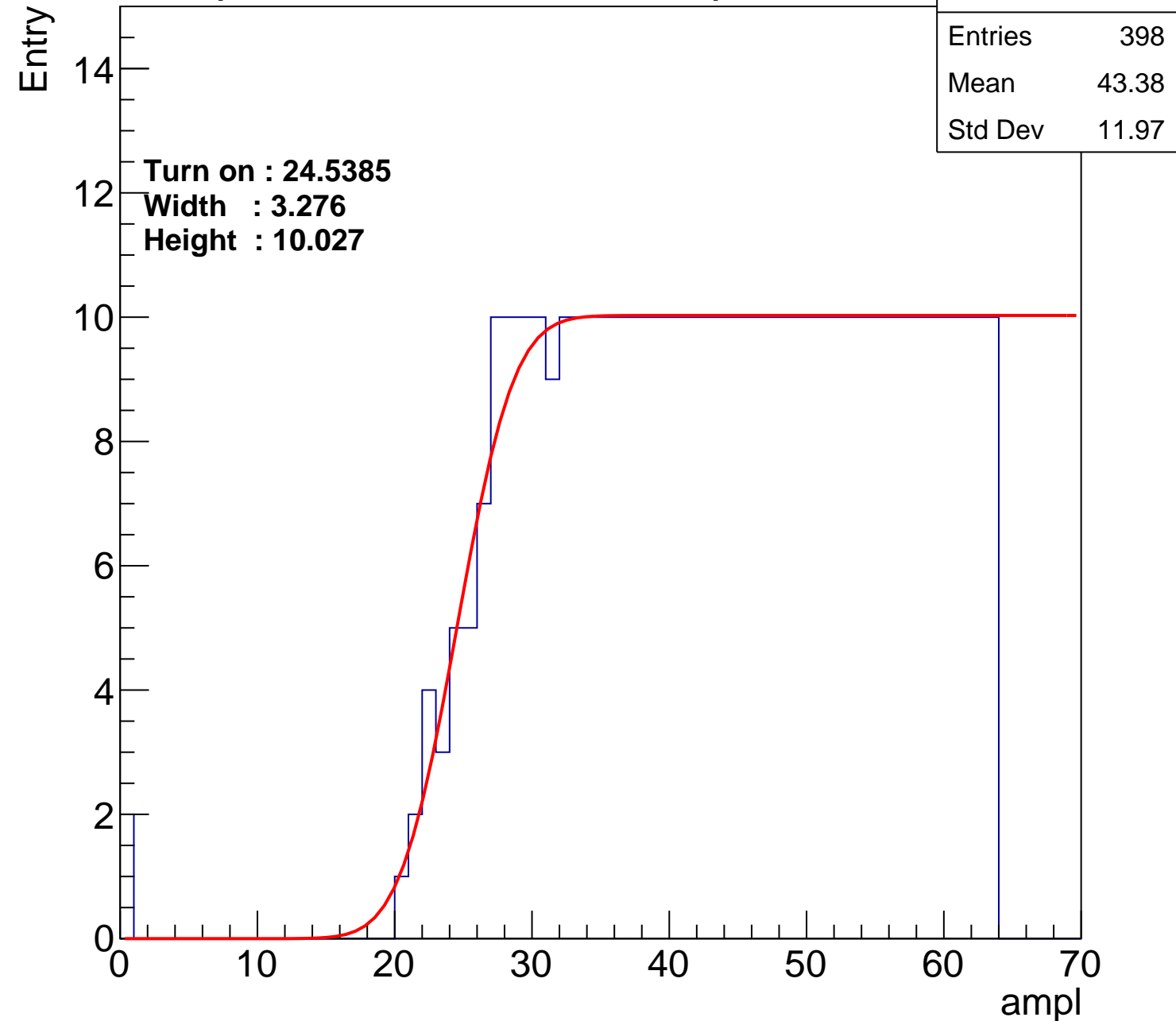
Width : 3.276

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.6022

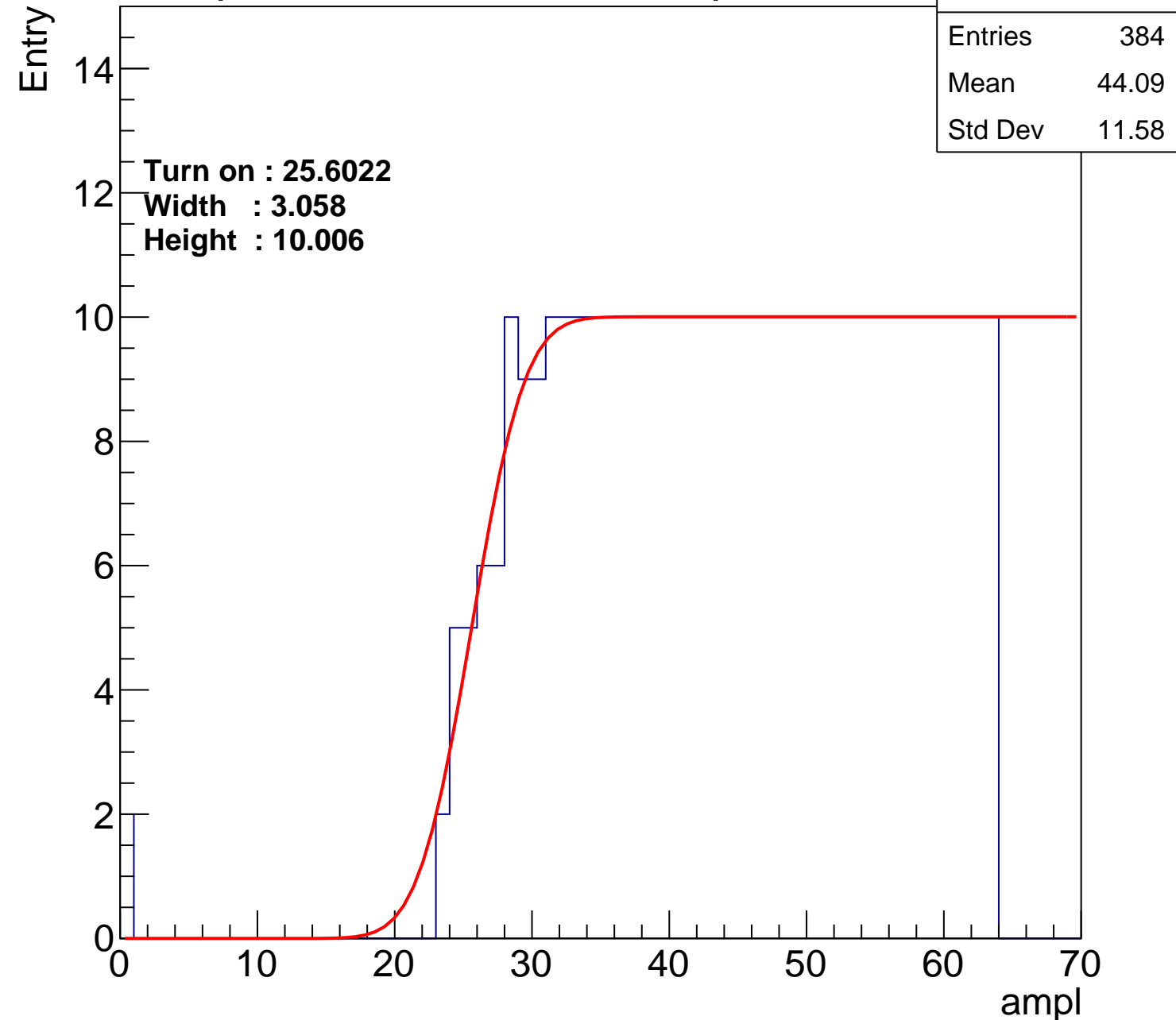
Width : 3.058

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.8

Turn on : 25.6129

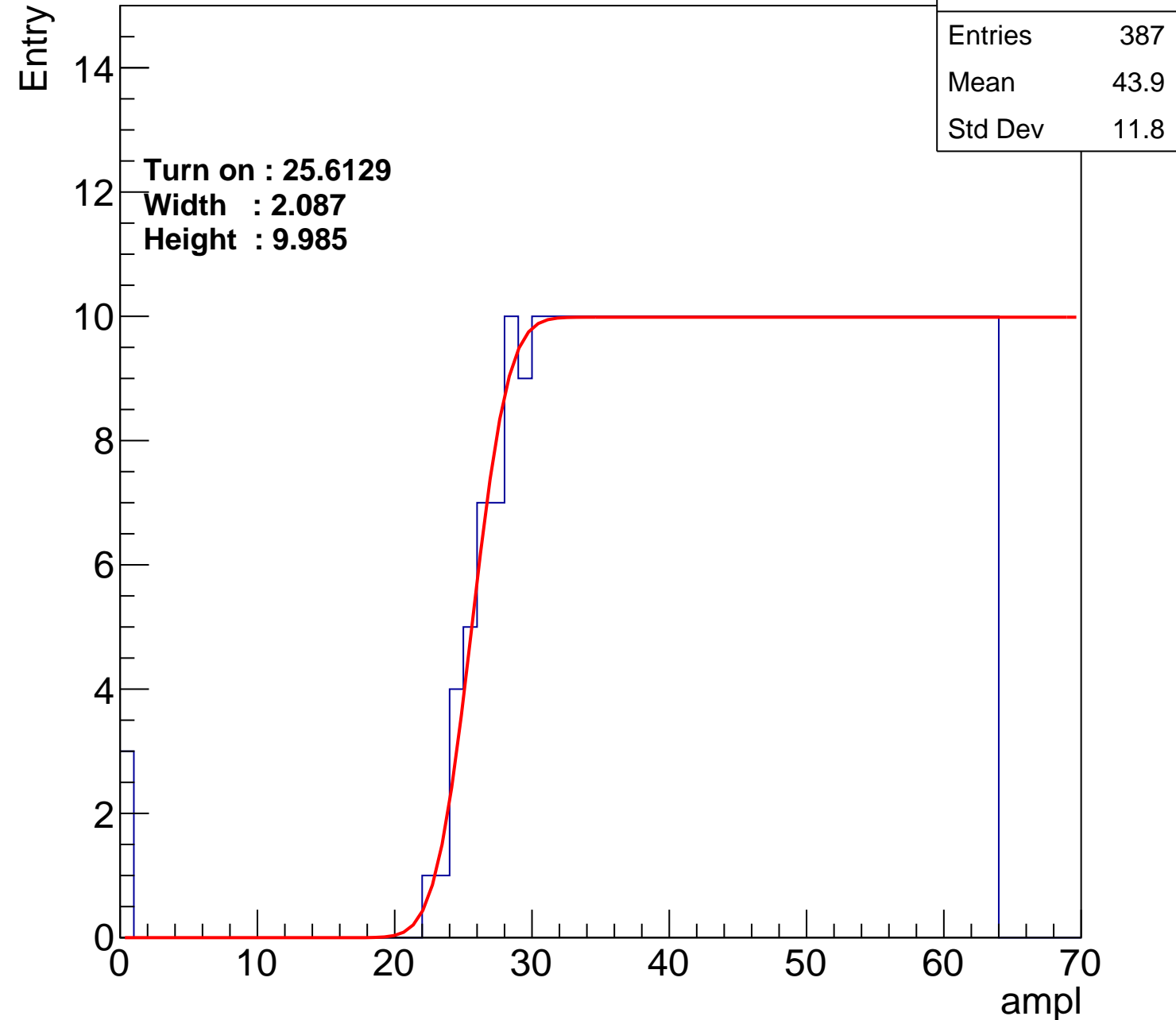
Width : 2.087

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch37

calib_packv5_042523_0143.root, FC#10, port B3

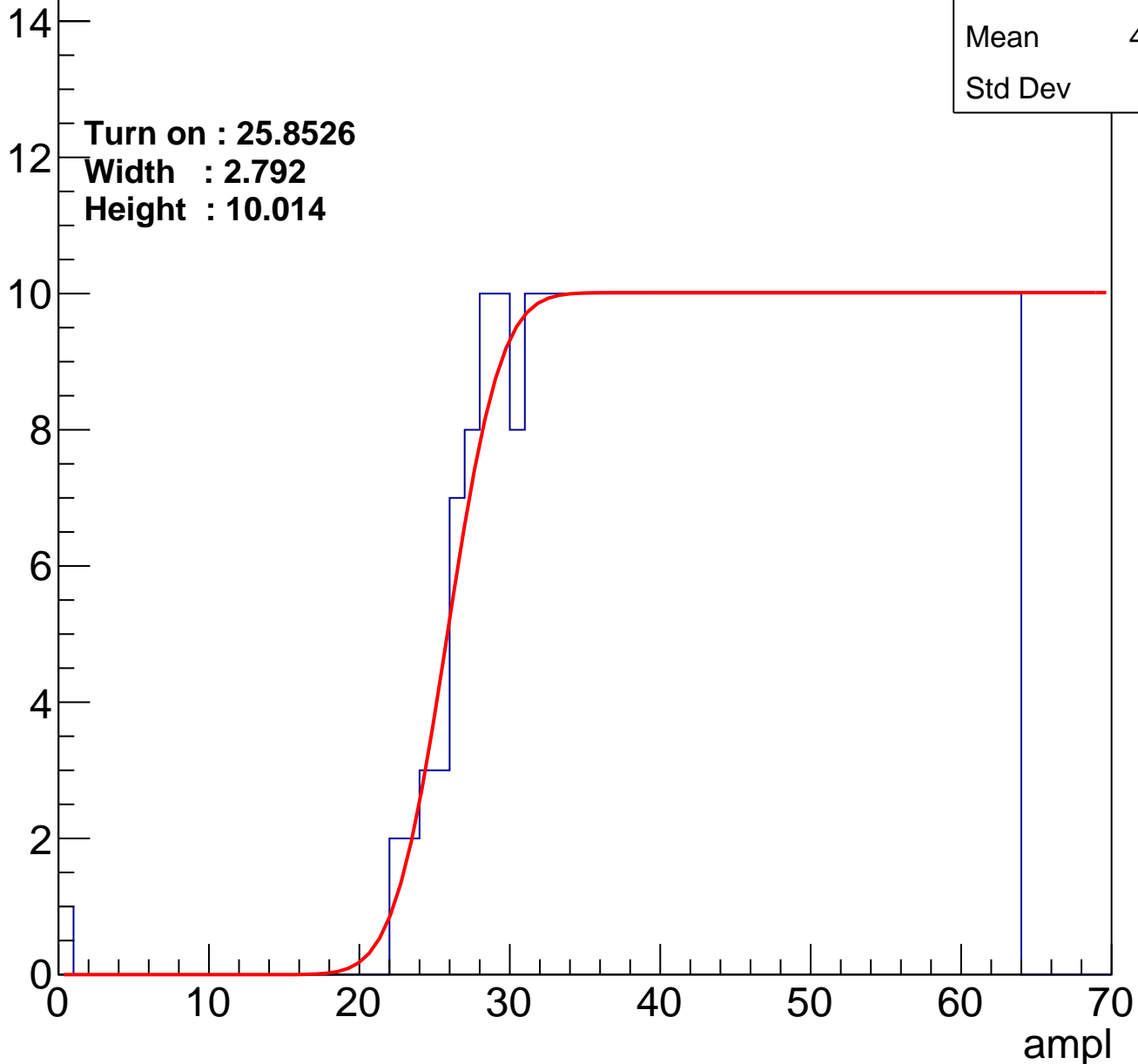
Entries	384
Mean	44.15
Std Dev	11.4

Turn on : 25.8526

Width : 2.792

Height : 10.014

Entry



B1L002S, U14-ch38

calib_packv5_042523_0143.root, FC#10, port B3

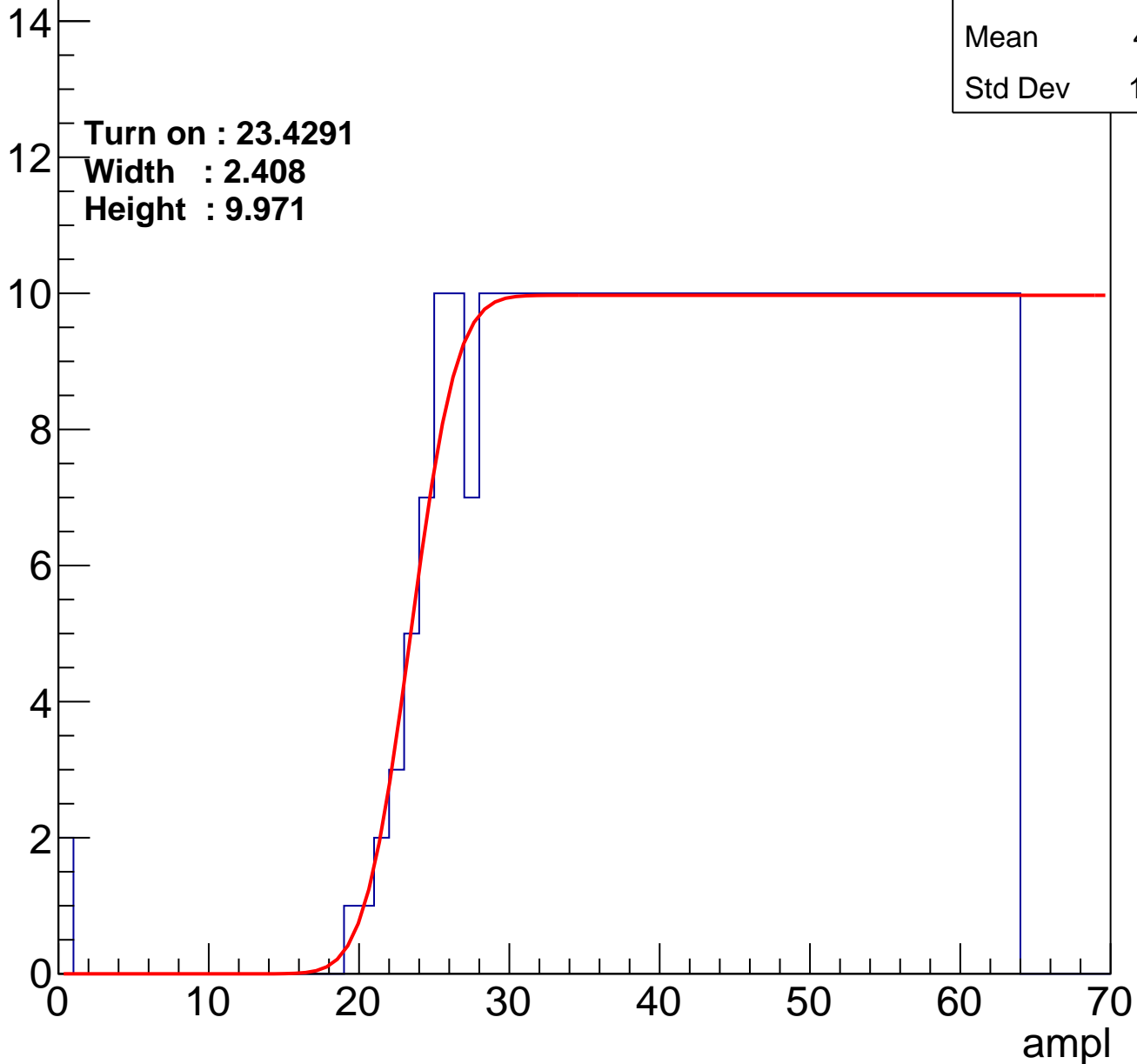
Entries	408
Mean	42.91
Std Dev	12.19

Turn on : 23.4291

Width : 2.408

Height : 9.971

Entry



B1L002S, U14-ch39

calib_packv5_042523_0143.root, FC#10, port B3

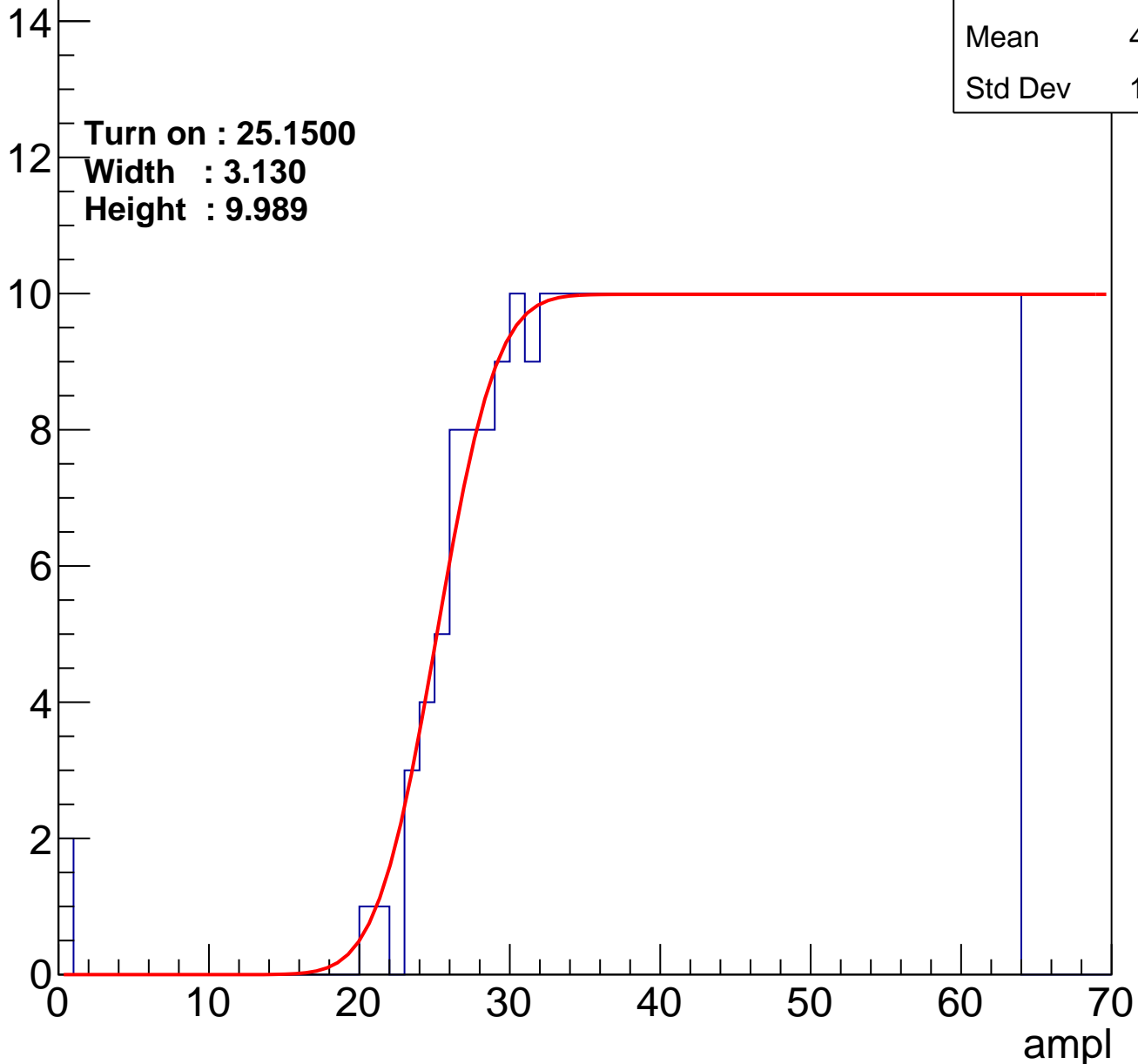
Entries	388
Mean	43.86
Std Dev	11.73

Turn on : 25.1500

Width : 3.130

Height : 9.989

Entry



B1L002S, U14-ch40

calib_packv5_042523_0143.root, FC#10, port B3

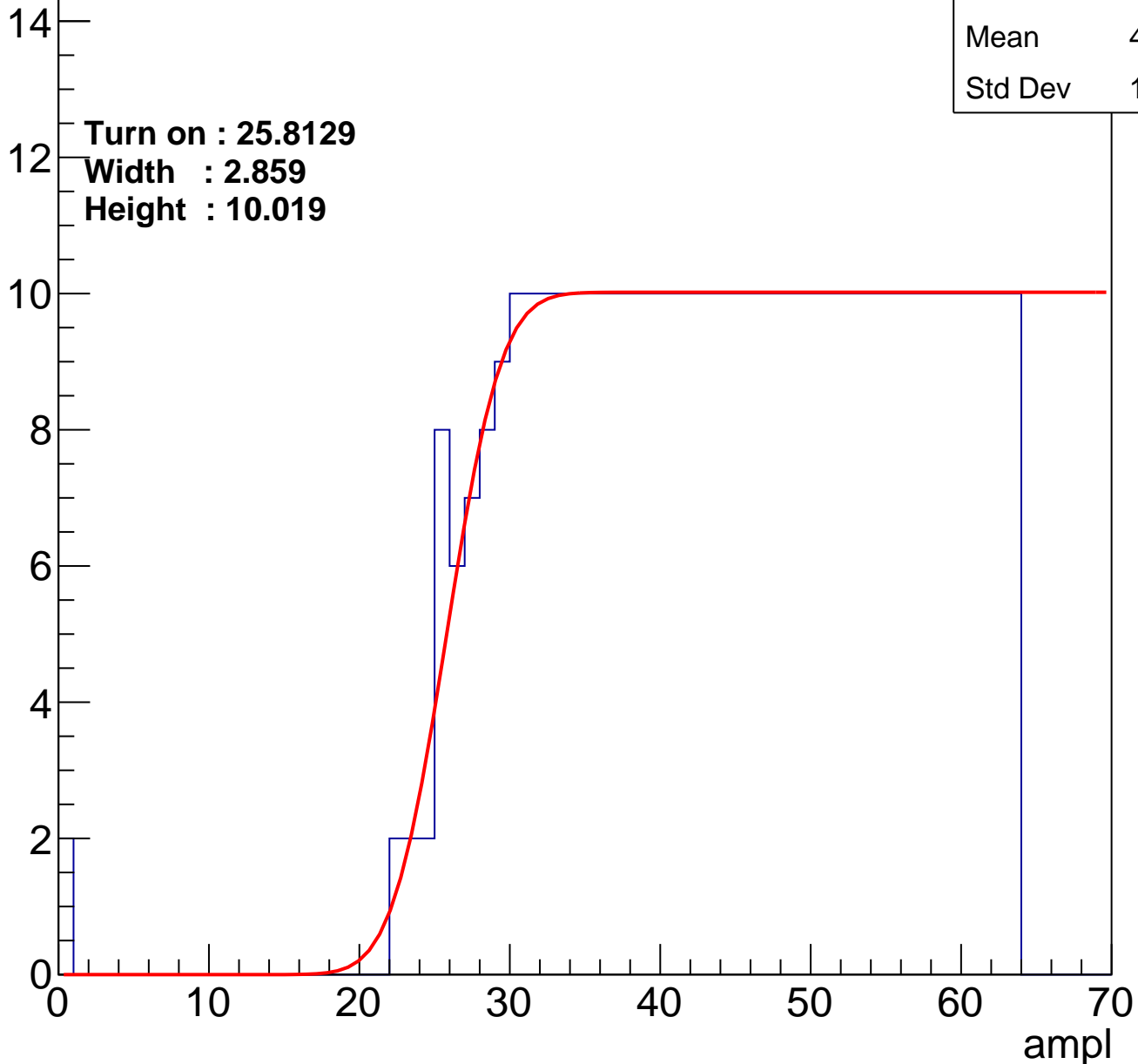
Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 25.8129

Width : 2.859

Height : 10.019

Entry



B1L002S, U14-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.45
Std Dev	11.8

Turn on : 24.3995

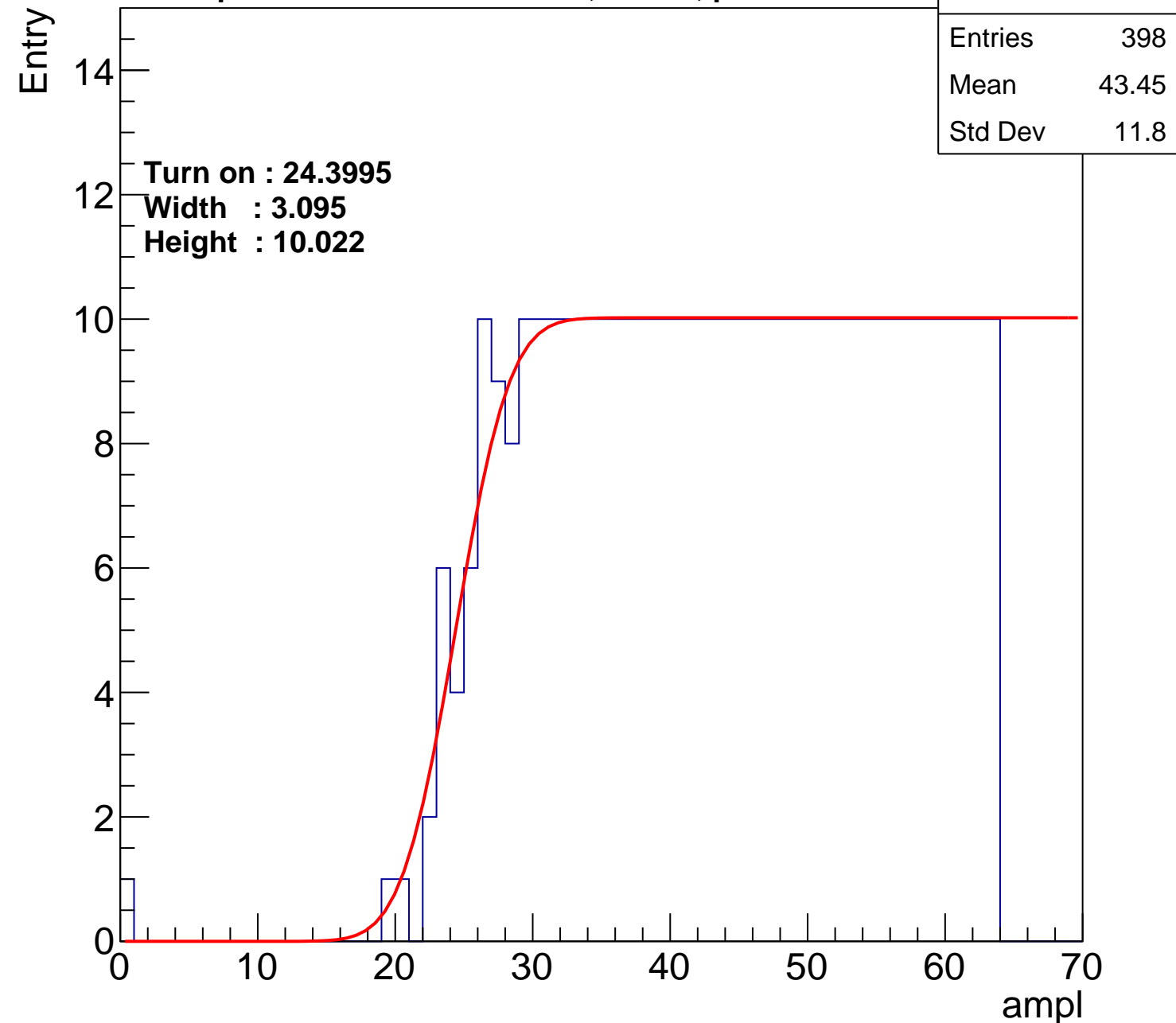
Width : 3.095

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.53
Std Dev	11.9

Turn on : 24.7356

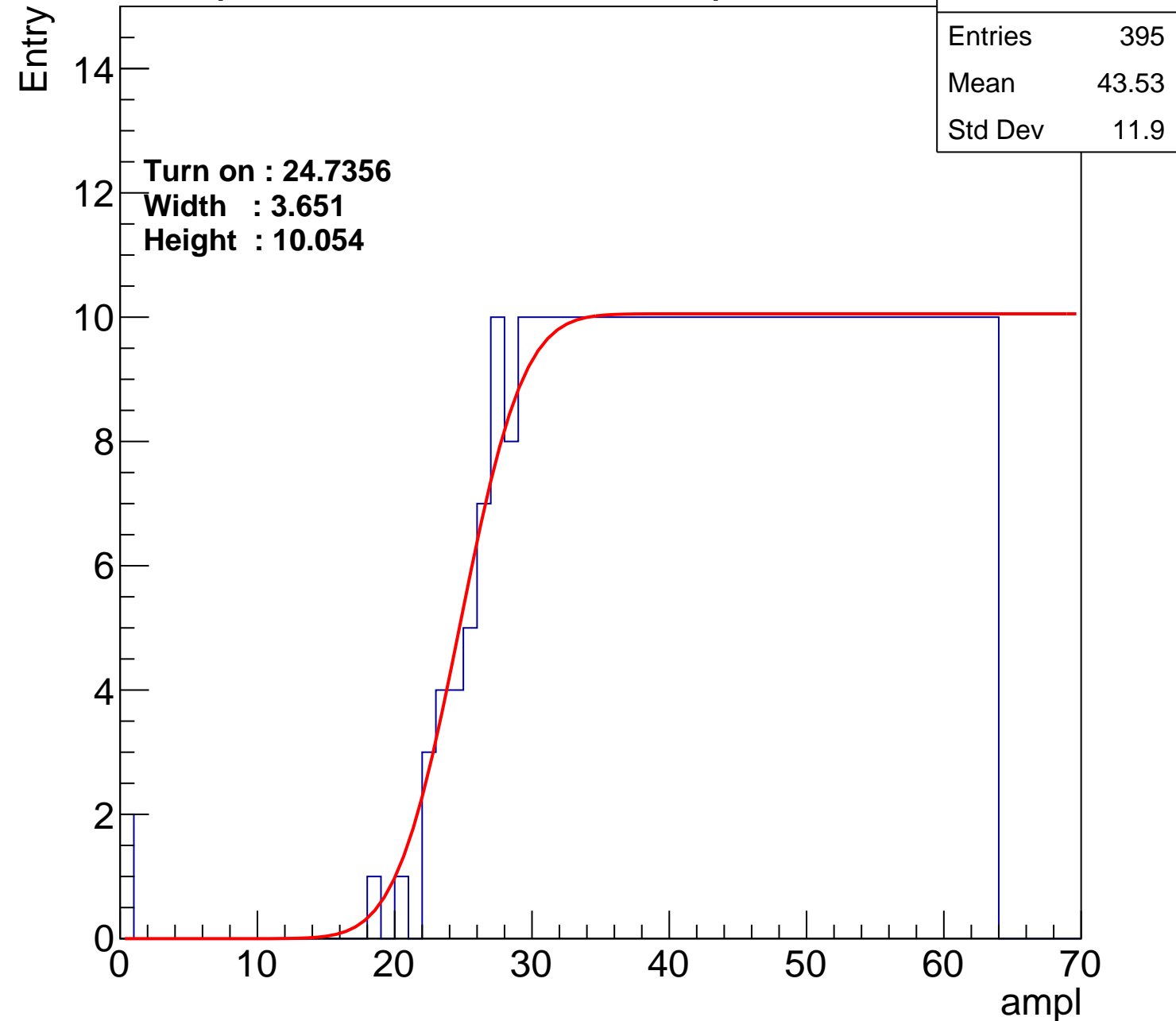
Width : 3.651

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch43

calib_packv5_042523_0143.root, FC#10, port B3

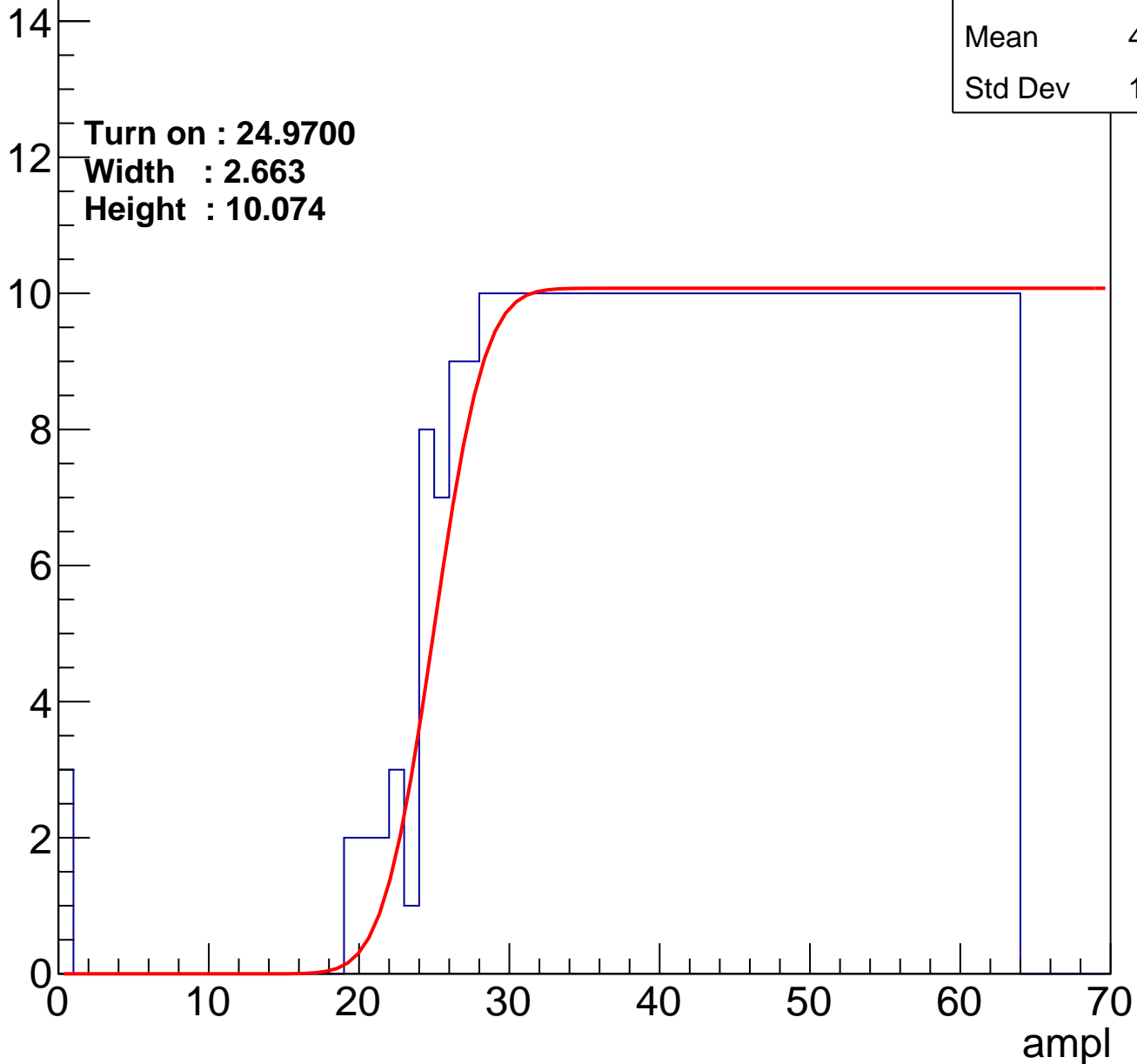
Entries	406
Mean	42.94
Std Dev	12.32

Turn on : 24.9700

Width : 2.663

Height : 10.074

Entry



B1L002S, U14-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.43
Std Dev	11.83

Turn on : 25.1474

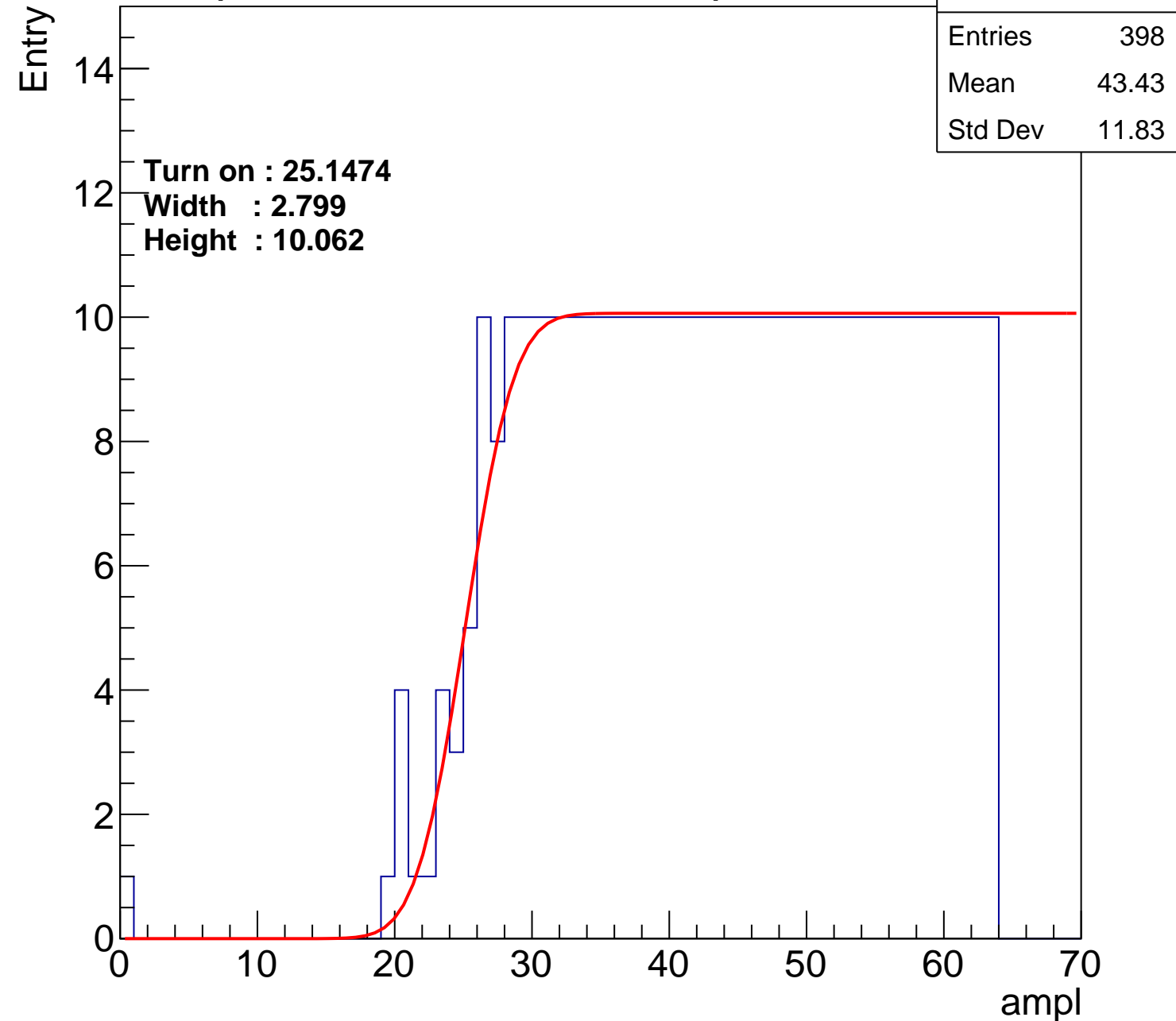
Width : 2.799

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch45

calib_packv5_042523_0143.root, FC#10, port B3

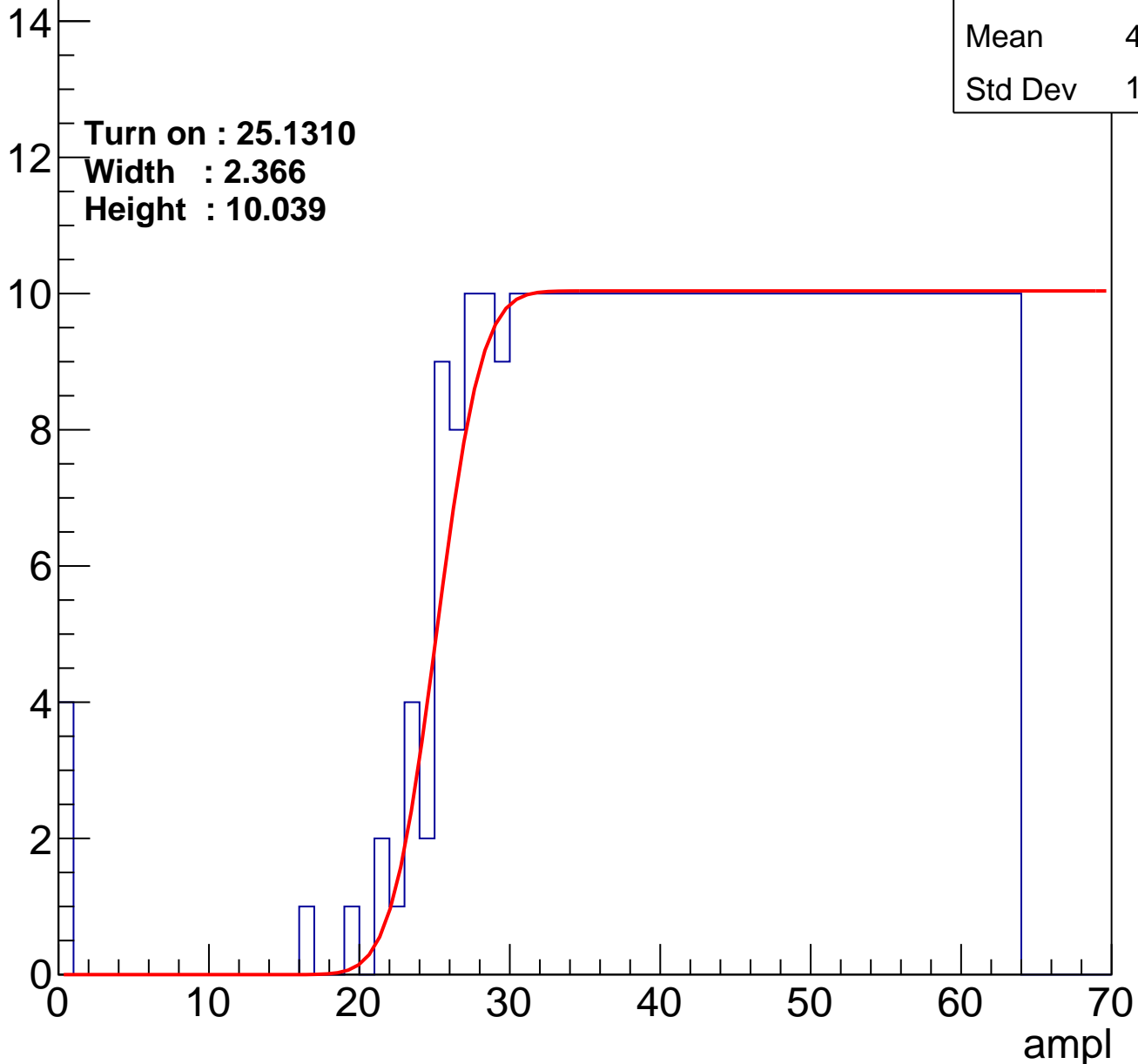
Entries	401
Mean	43.12
Std Dev	12.35

Turn on : 25.1310

Width : 2.366

Height : 10.039

Entry



B1L002S, U14-ch46

calib_packv5_042523_0143.root, FC#10, port B3

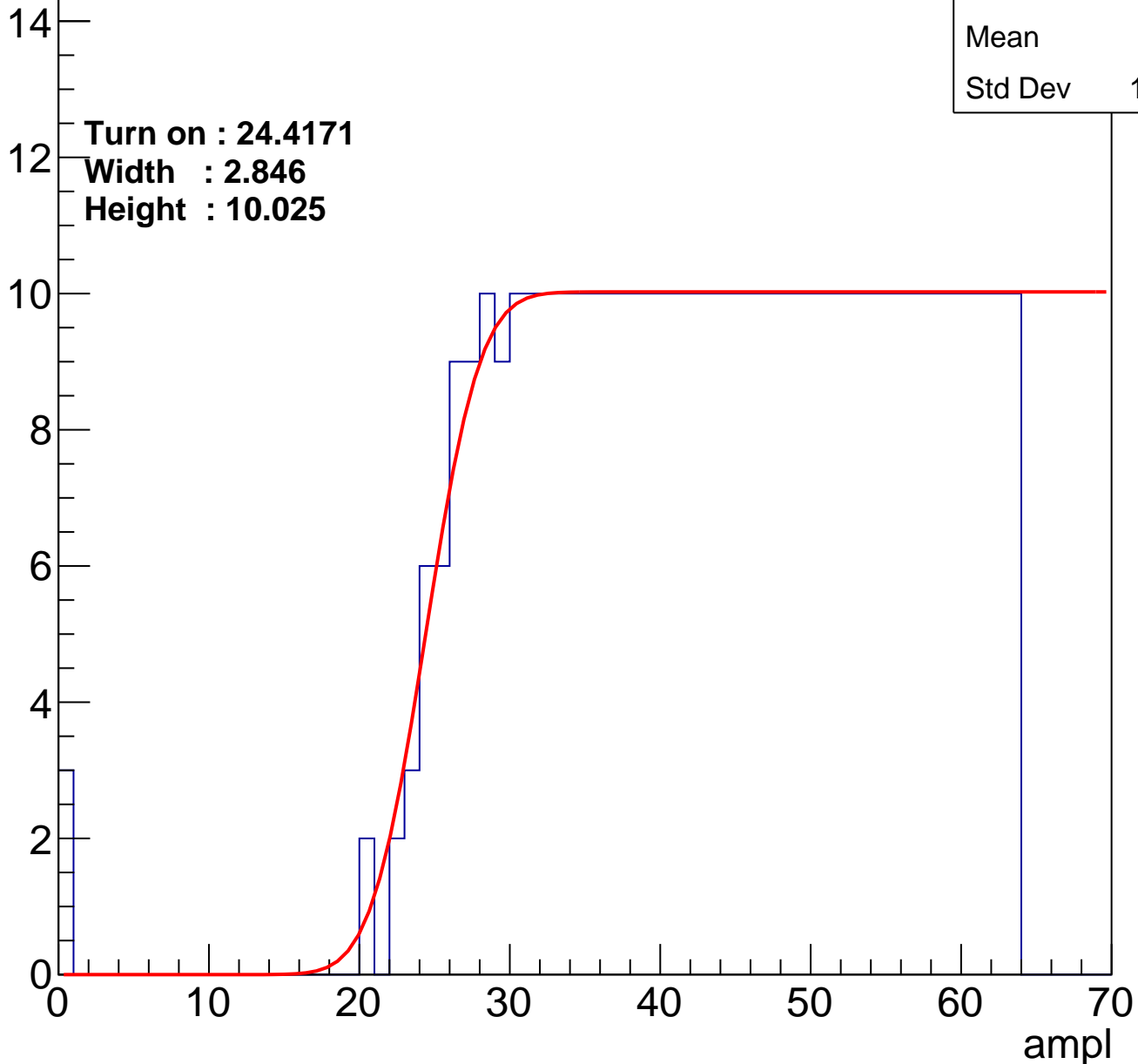
Entries	399
Mean	43.3
Std Dev	12.12

Turn on : 24.4171

Width : 2.846

Height : 10.025

Entry



B1L002S, U14-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.76
Std Dev	11.62

Turn on : 24.7918

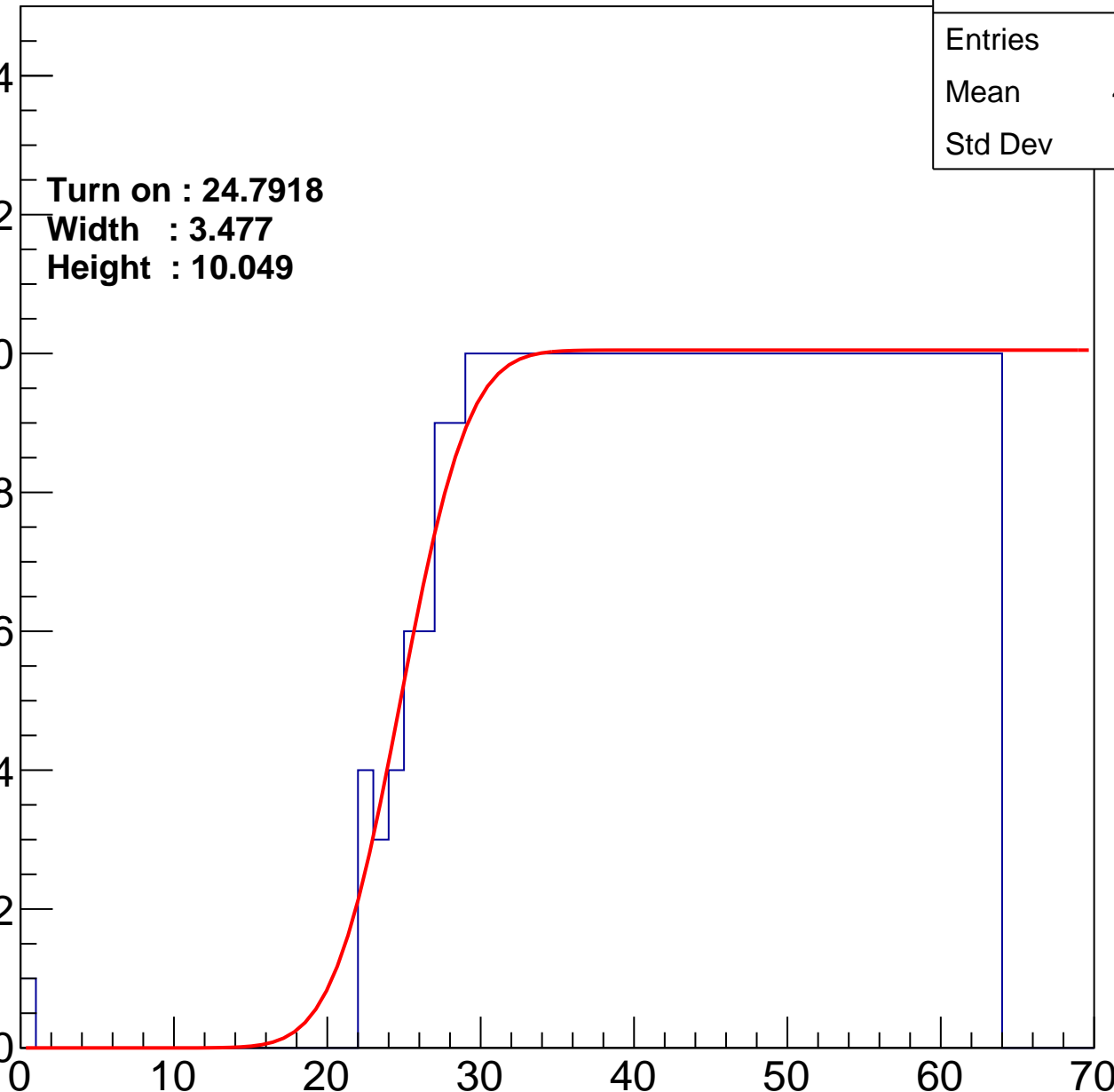
Width : 3.477

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.14
Std Dev	11.85

Turn on : 26.5933

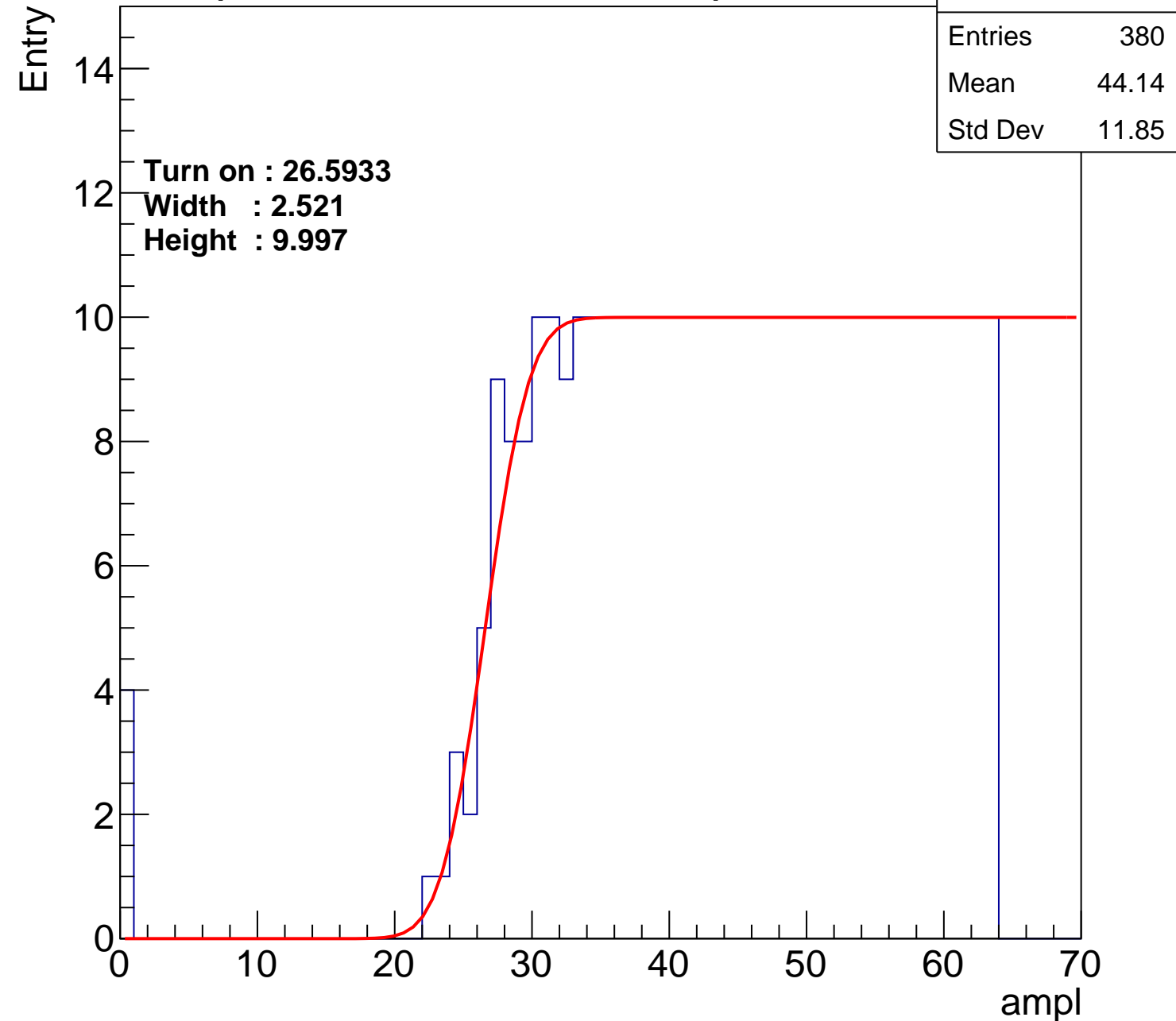
Width : 2.521

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch49

calib_packv5_042523_0143.root, FC#10, port B3

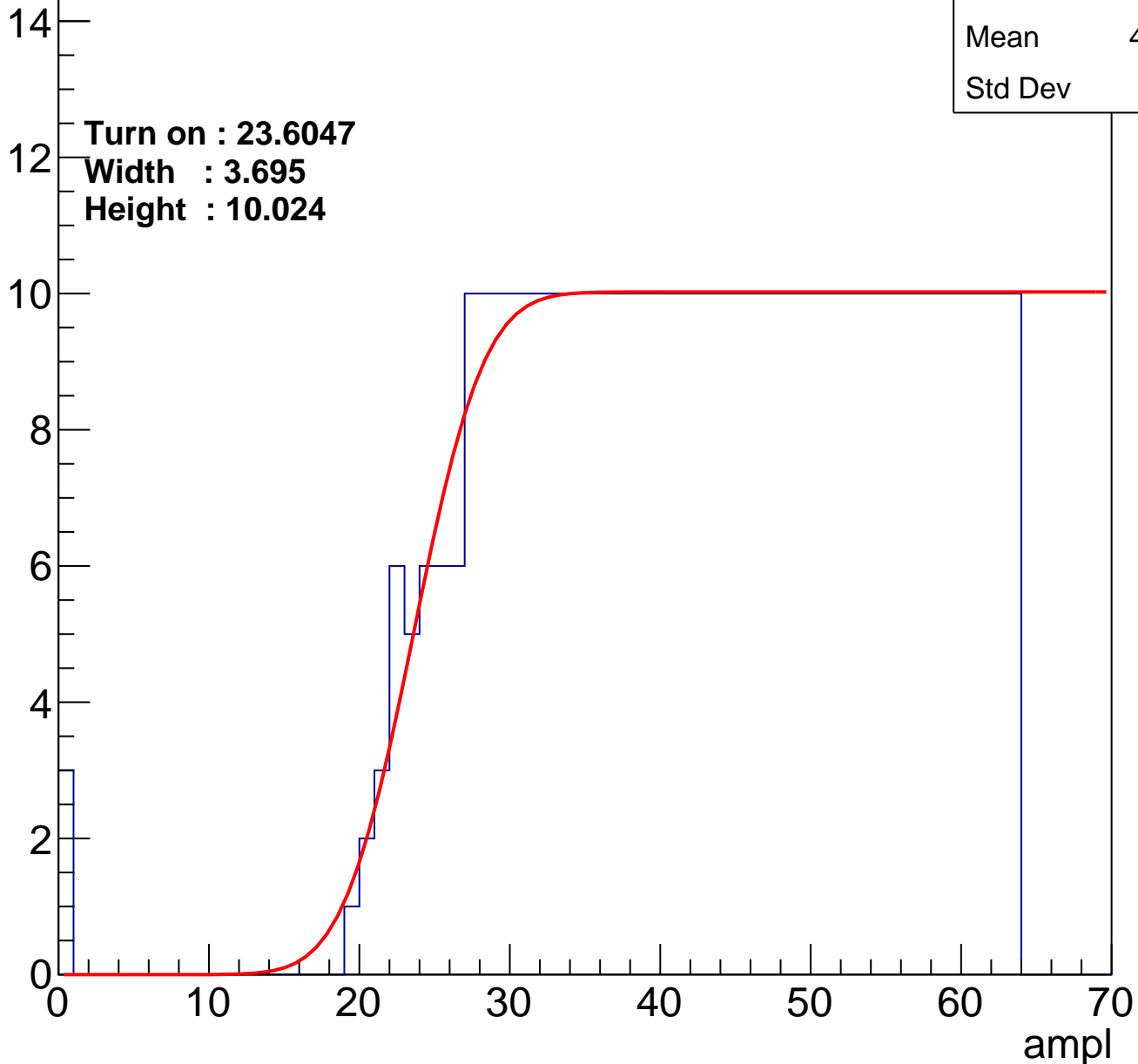
Entries	408
Mean	42.82
Std Dev	12.4

Turn on : 23.6047

Width : 3.695

Height : 10.024

Entry



B1L002S, U14-ch50

calib_packv5_042523_0143.root, FC#10, port B3

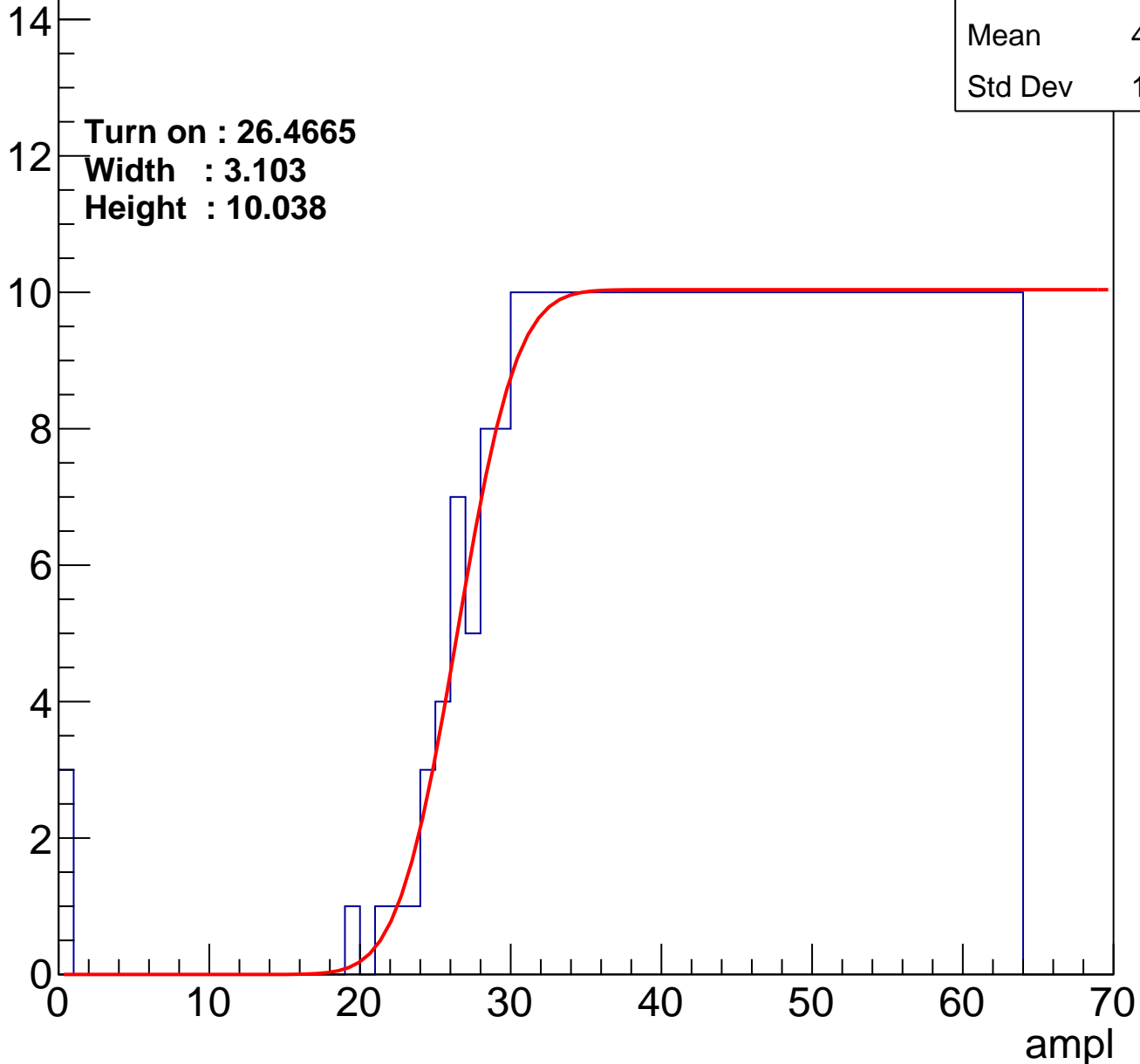
Entries	382
Mean	44.08
Std Dev	11.77

Turn on : 26.4665

Width : 3.103

Height : 10.038

Entry



B1L002S, U14-ch51

calib_packv5_042523_0143.root, FC#10, port B3

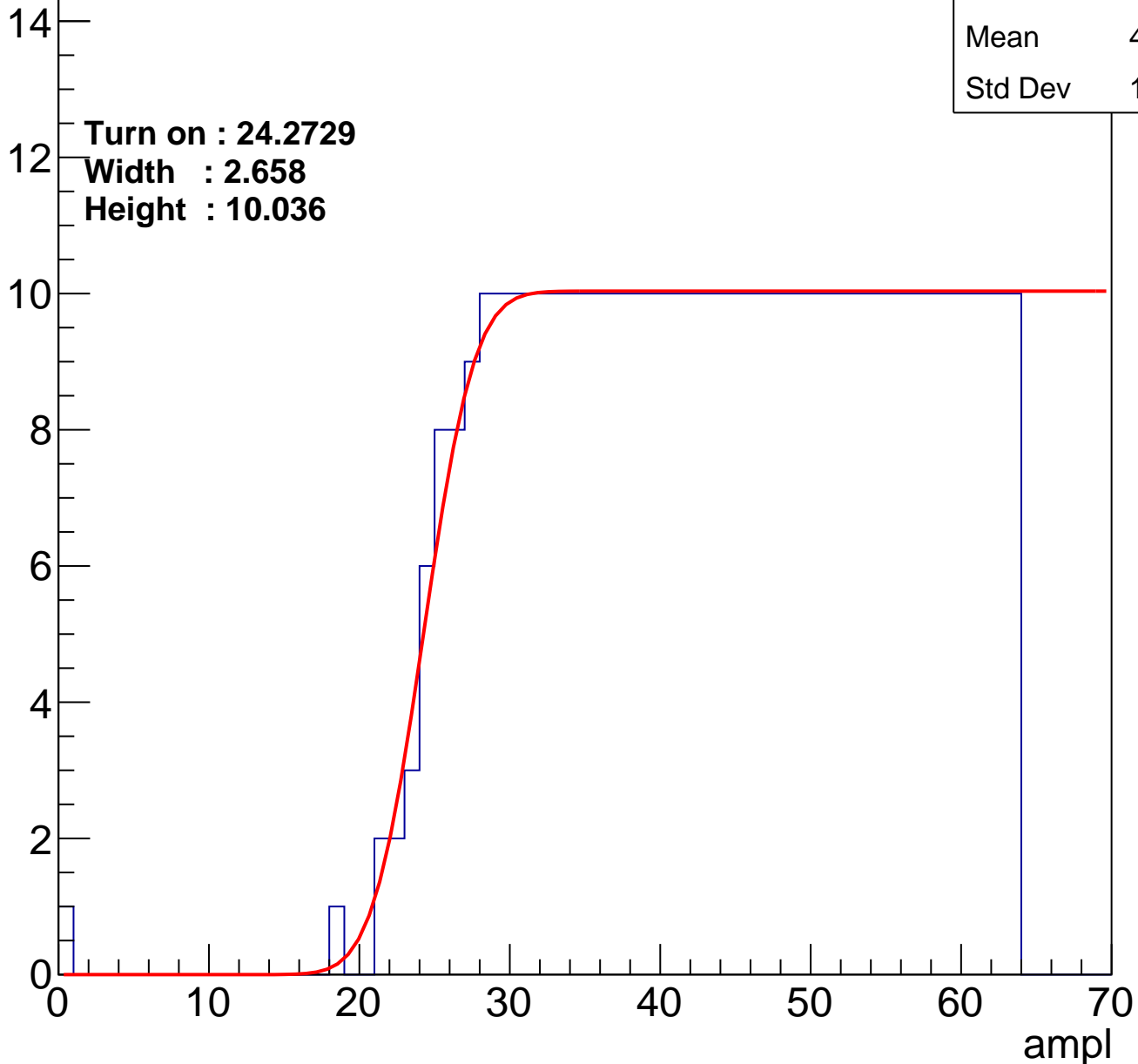
Entries	400
Mean	43.37
Std Dev	11.83

Turn on : 24.2729

Width : 2.658

Height : 10.036

Entry



B1L002S, U14-ch52

calib_packv5_042523_0143.root, FC#10, port B3

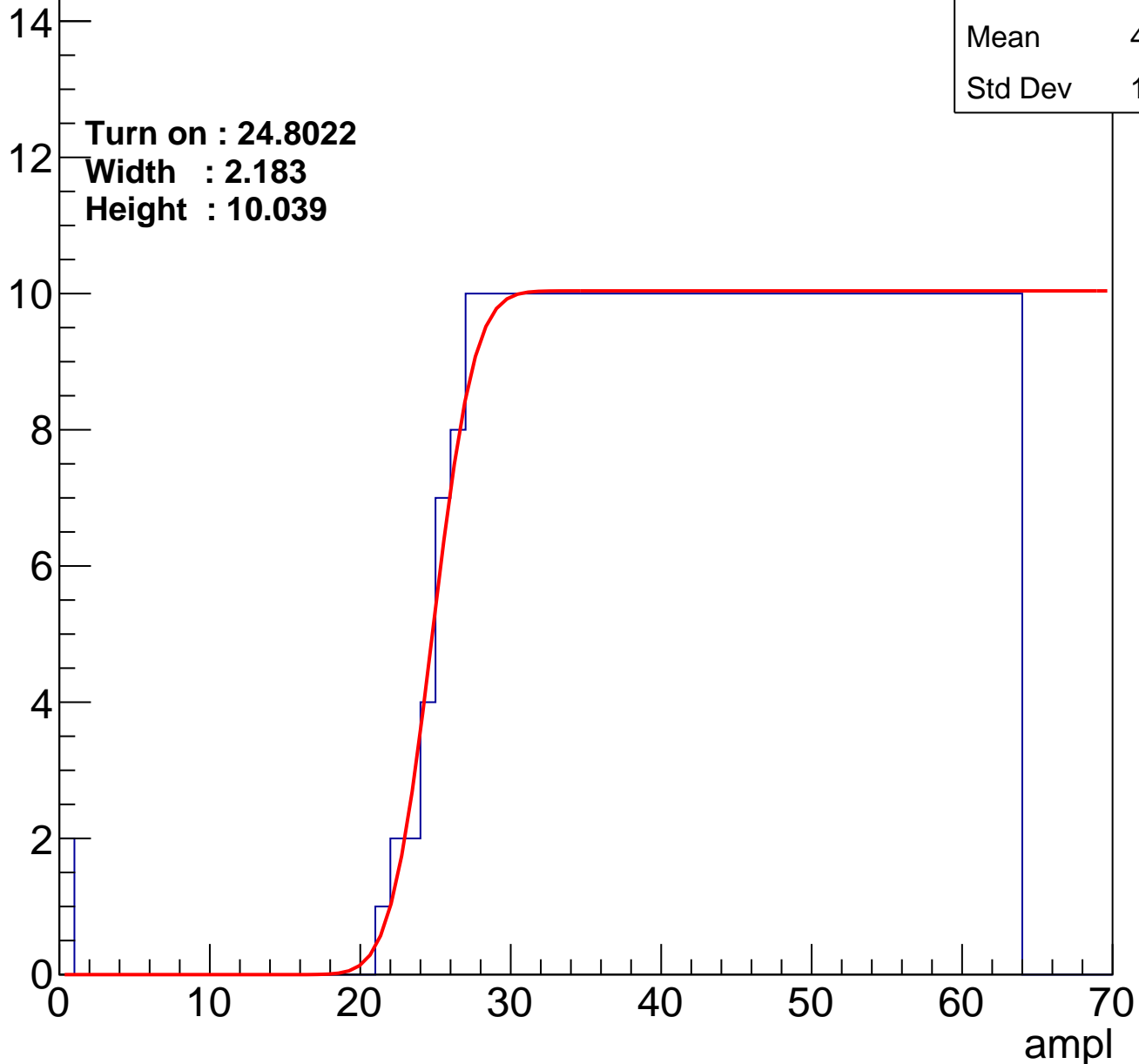
Entries	396
Mean	43.54
Std Dev	11.83

Turn on : 24.8022

Width : 2.183

Height : 10.039

Entry



B1L002S, U14-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.19
Std Dev	12.17

Turn on : 24.4423

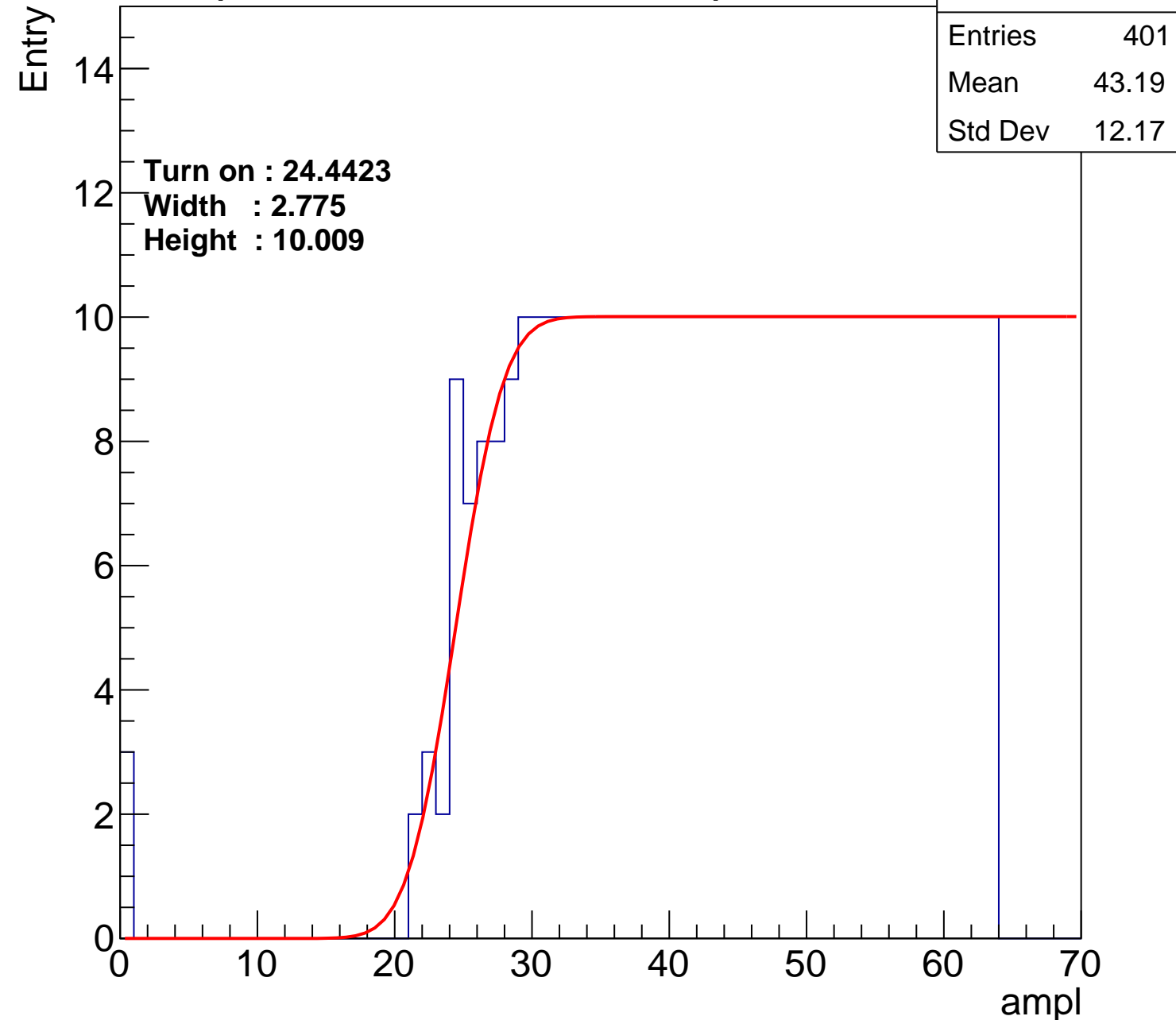
Width : 2.775

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch54

calib_packv5_042523_0143.root, FC#10, port B3

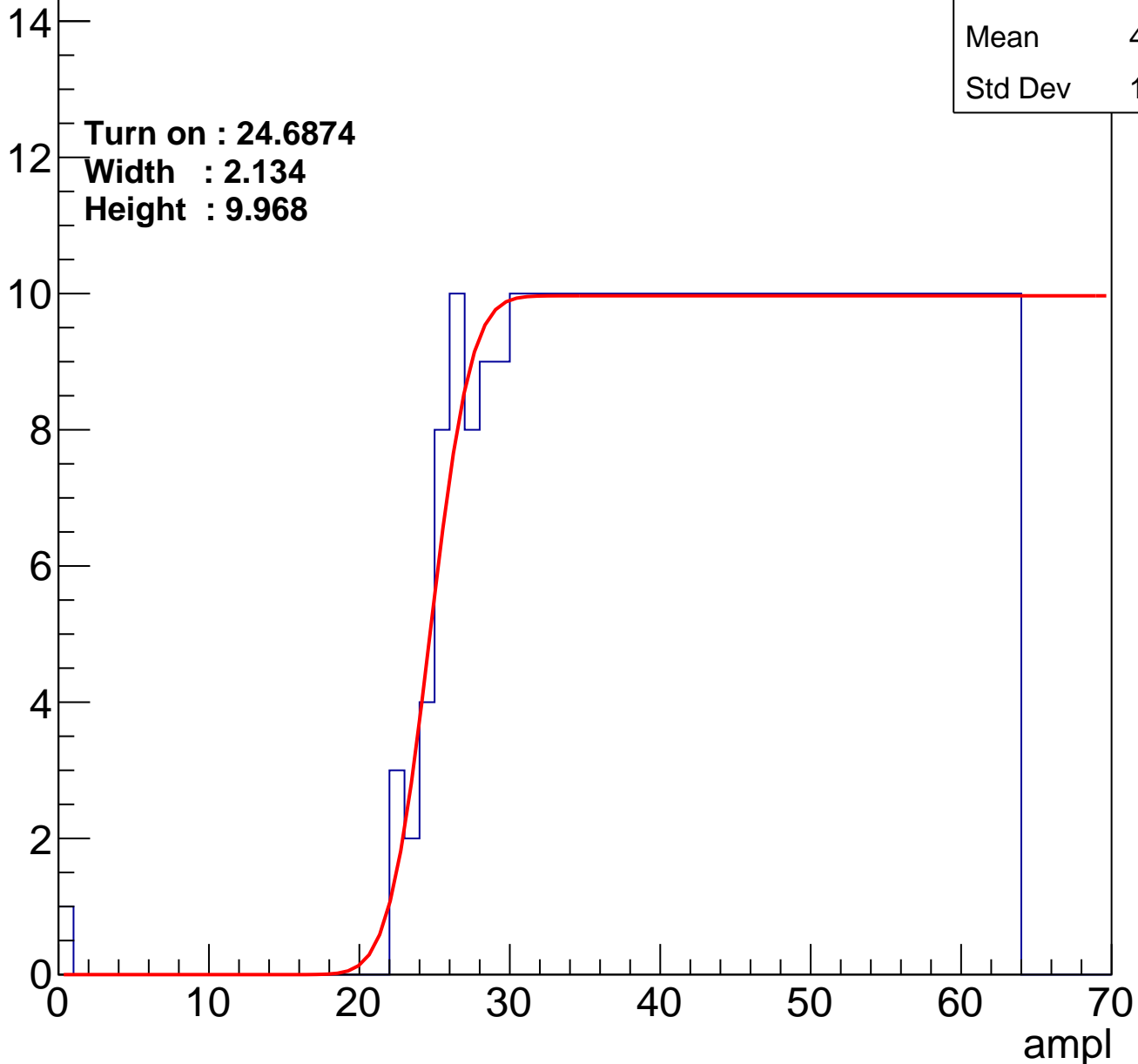
Entries	394
Mean	43.67
Std Dev	11.65

Turn on : 24.6874

Width : 2.134

Height : 9.968

Entry



B1L002S, U14-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.8
Std Dev	11.9

Turn on : 25.0437

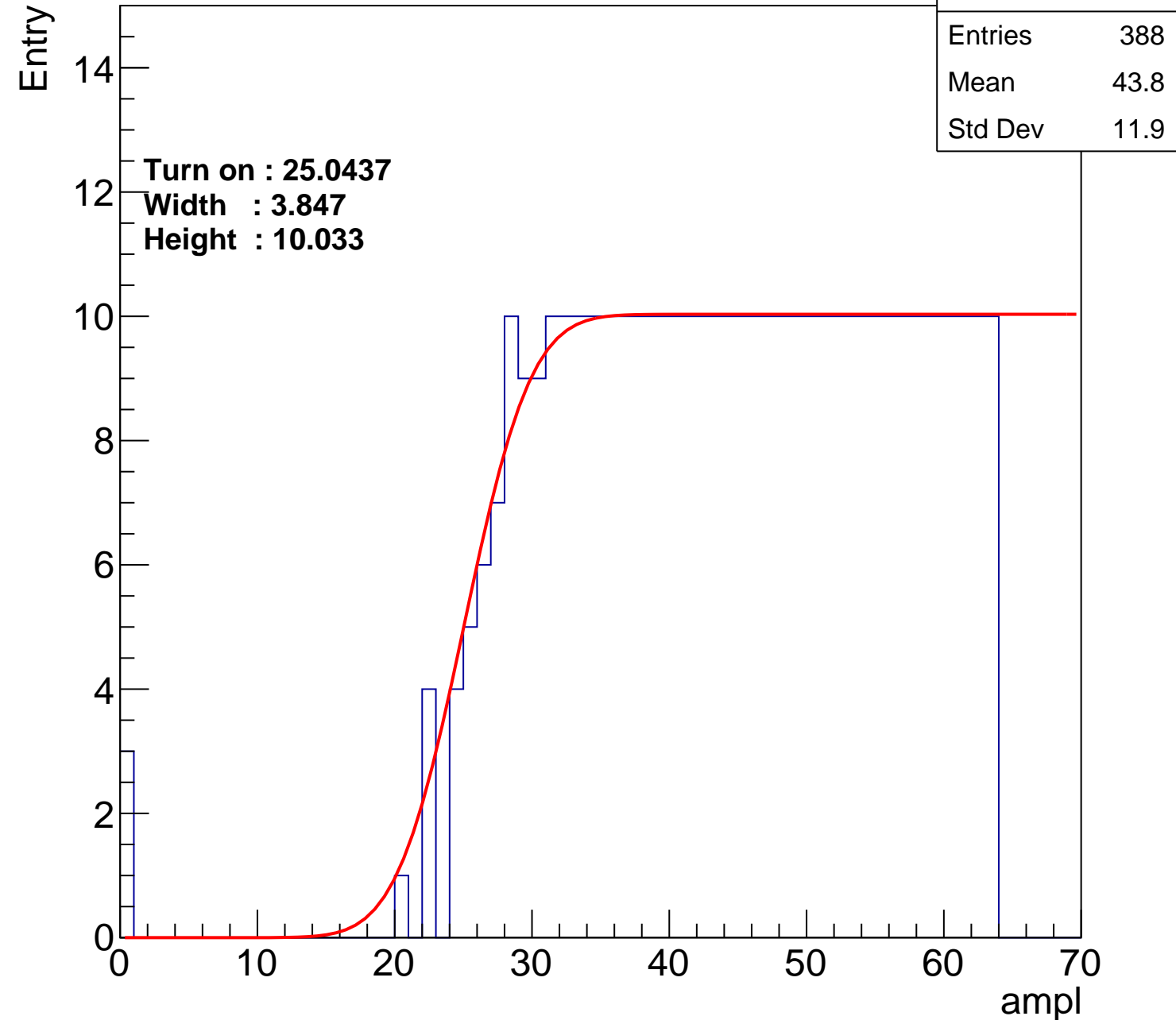
Width : 3.847

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.93
Std Dev	11.92

Turn on : 26.0476

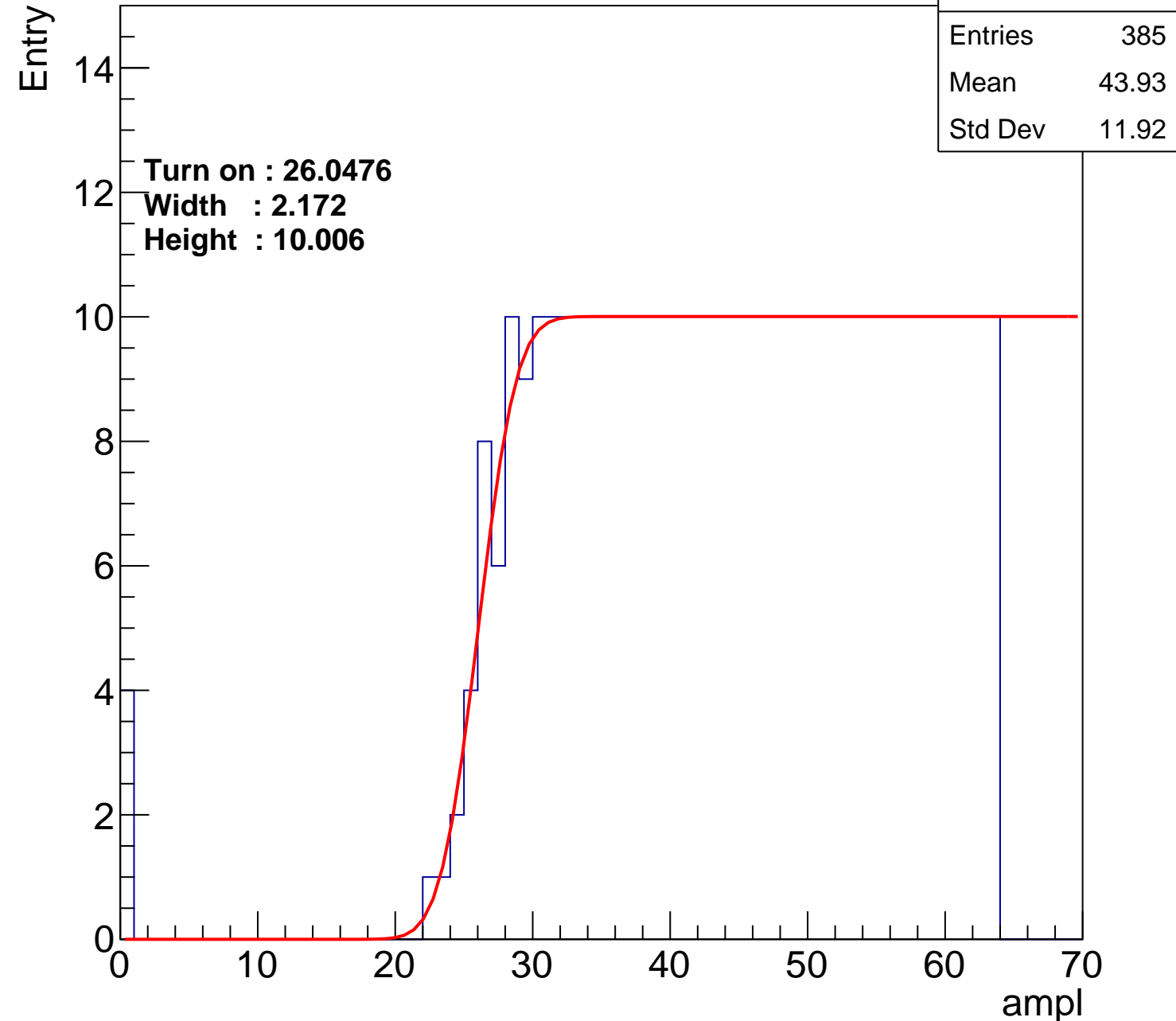
Width : 2.172

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.5479

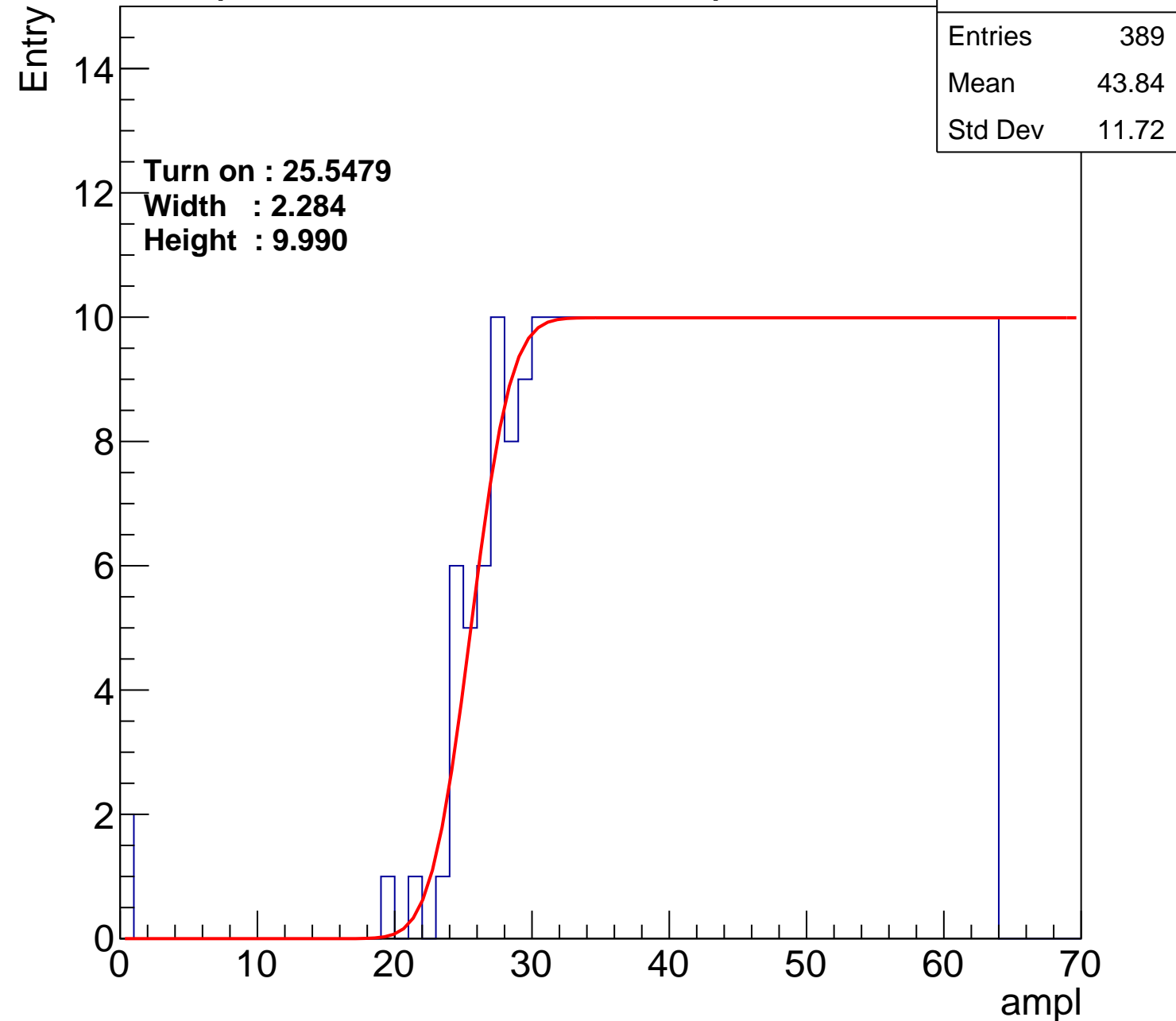
Width : 2.284

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.04
Std Dev	11.47

Turn on : 25.6339

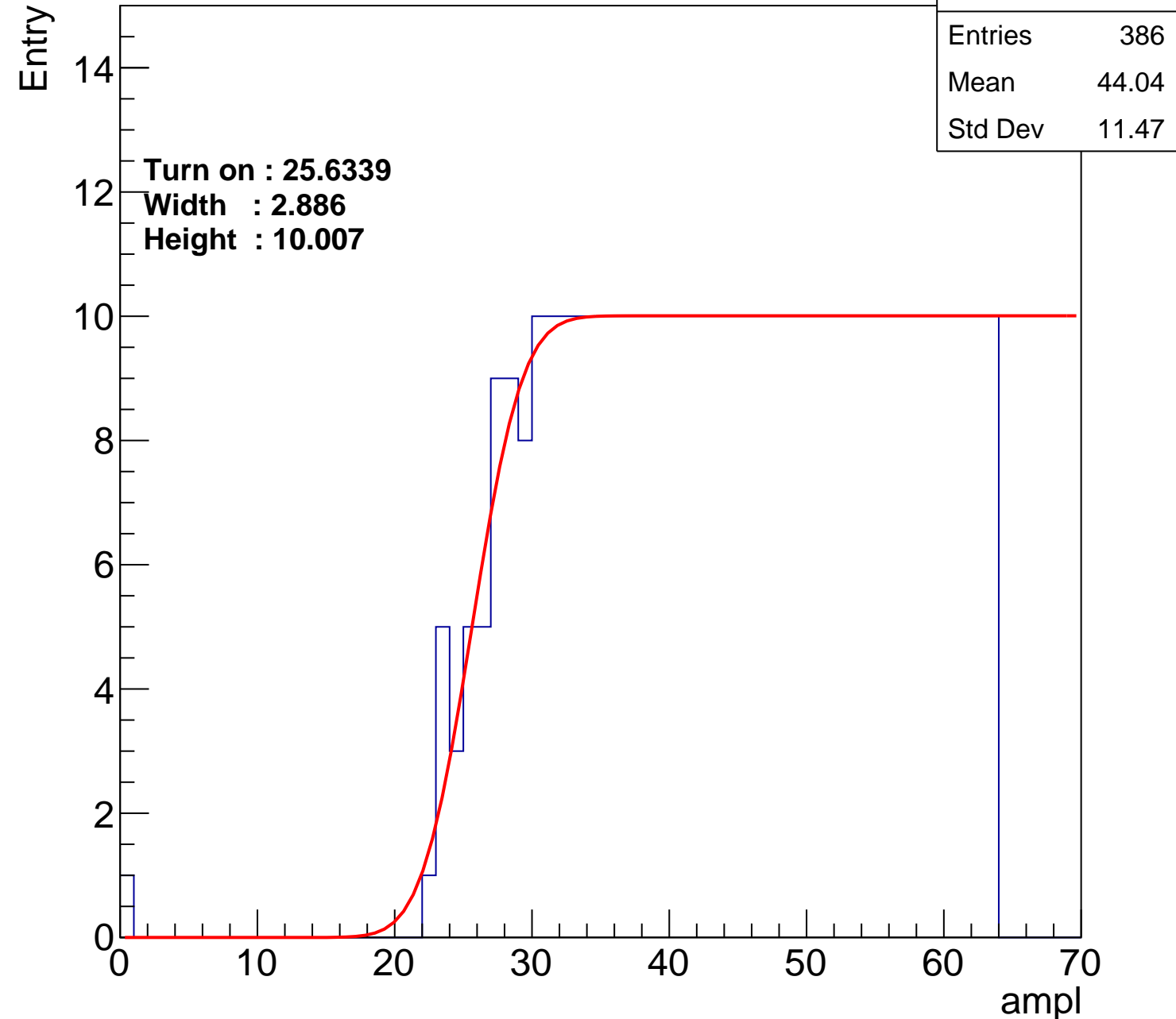
Width : 2.886

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch59

calib_packv5_042523_0143.root, FC#10, port B3

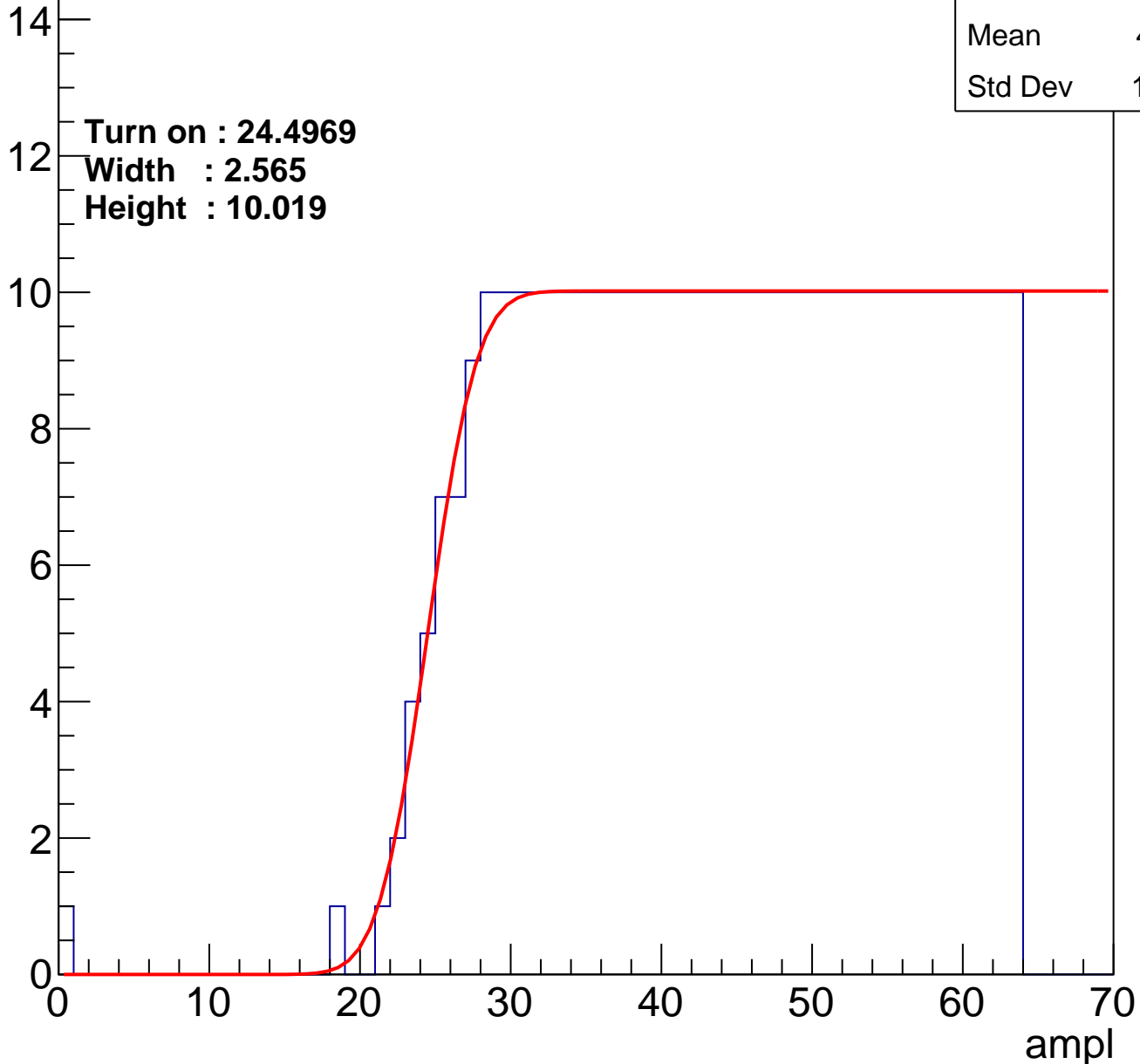
Entries	397
Mean	43.51
Std Dev	11.75

Turn on : 24.4969

Width : 2.565

Height : 10.019

Entry



B1L002S, U14-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.68
Std Dev	11.82

Turn on : 25.8155

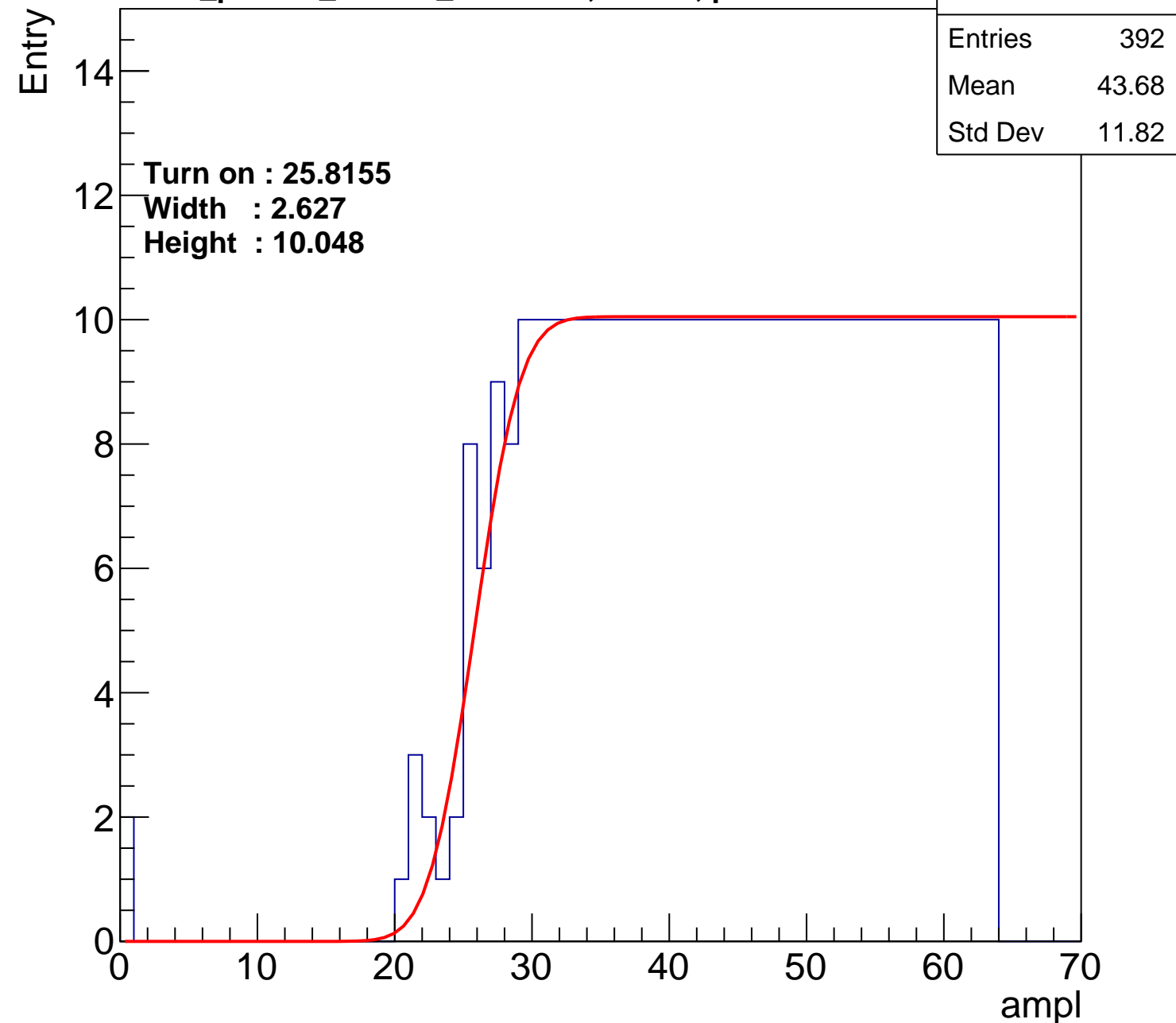
Width : 2.627

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 24.8985

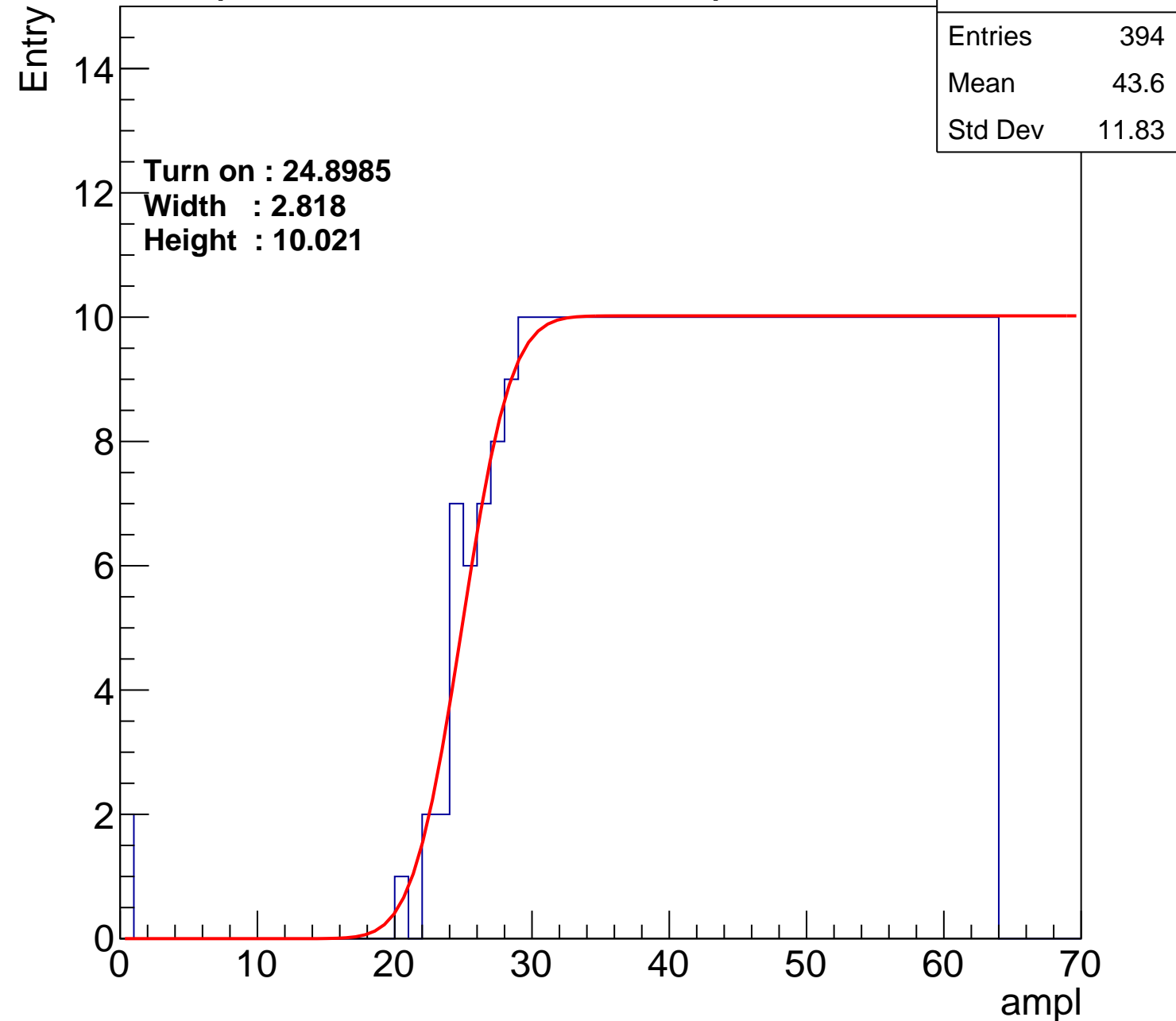
Width : 2.818

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 24.9706

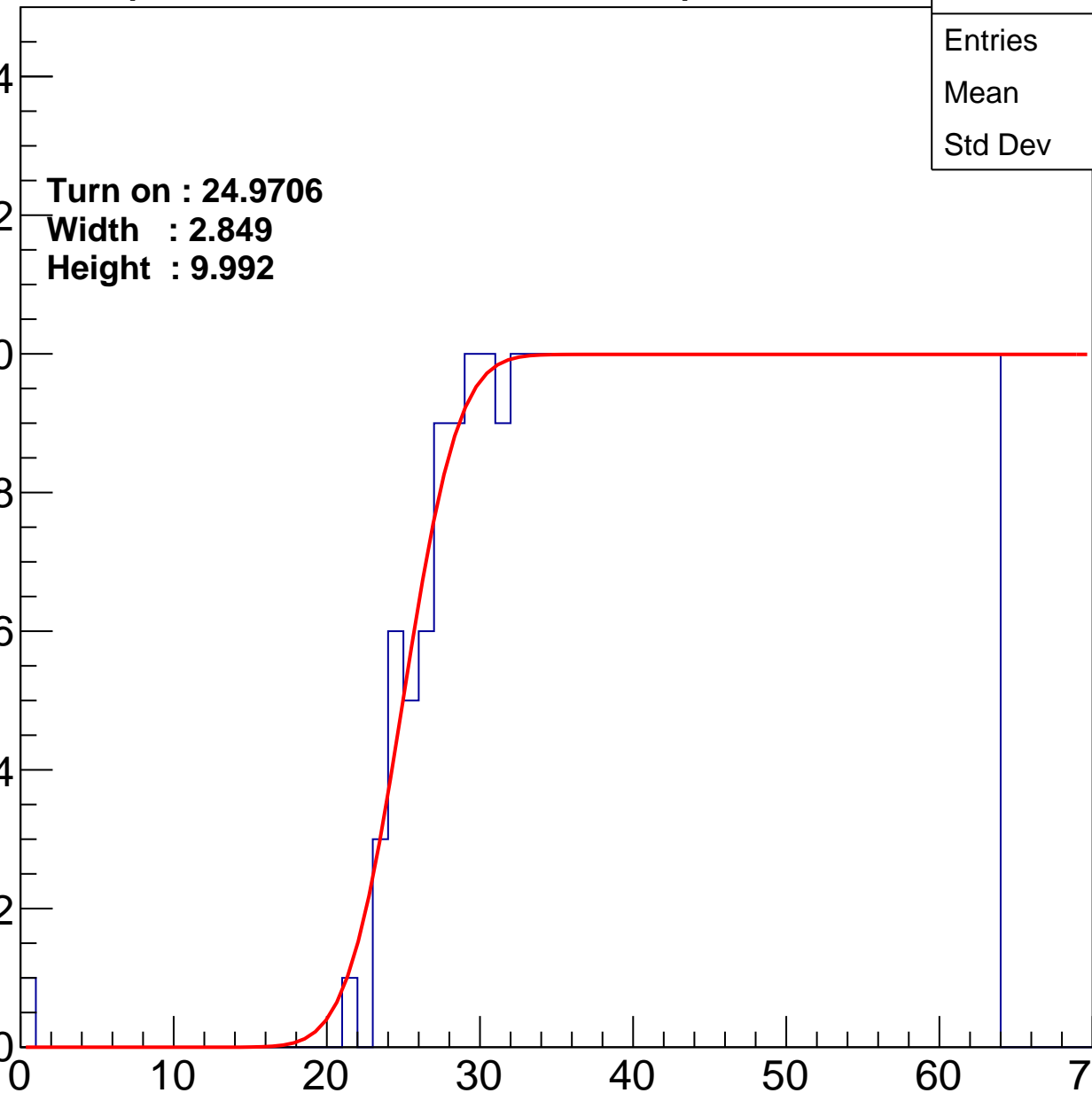
Width : 2.849

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch63

calib_packv5_042523_0143.root, FC#10, port B3

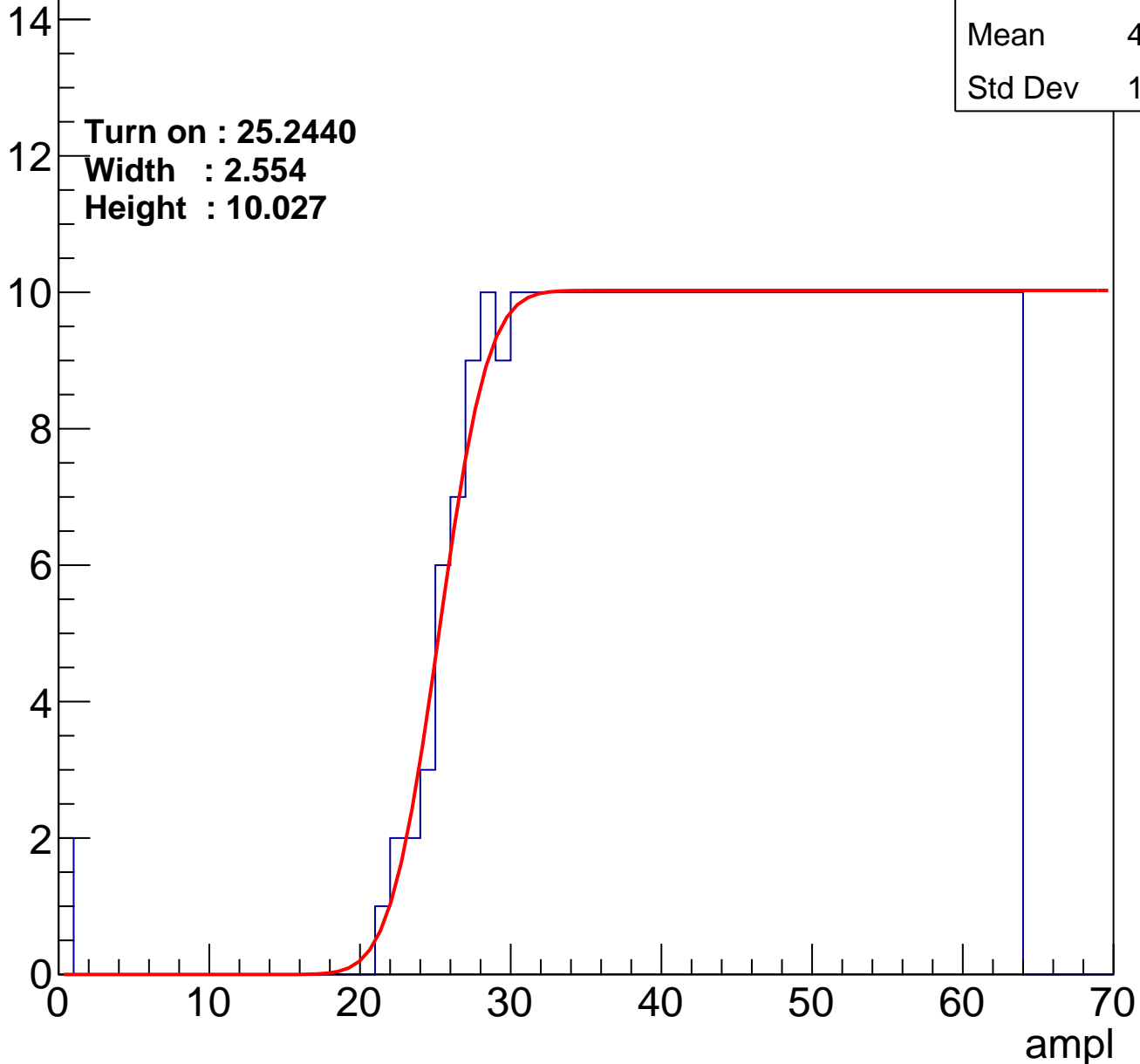
Entries	391
Mean	43.76
Std Dev	11.74

Turn on : 25.2440

Width : 2.554

Height : 10.027

Entry



B1L002S, U14-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.94
Std Dev	12.26

Turn on : 23.7855

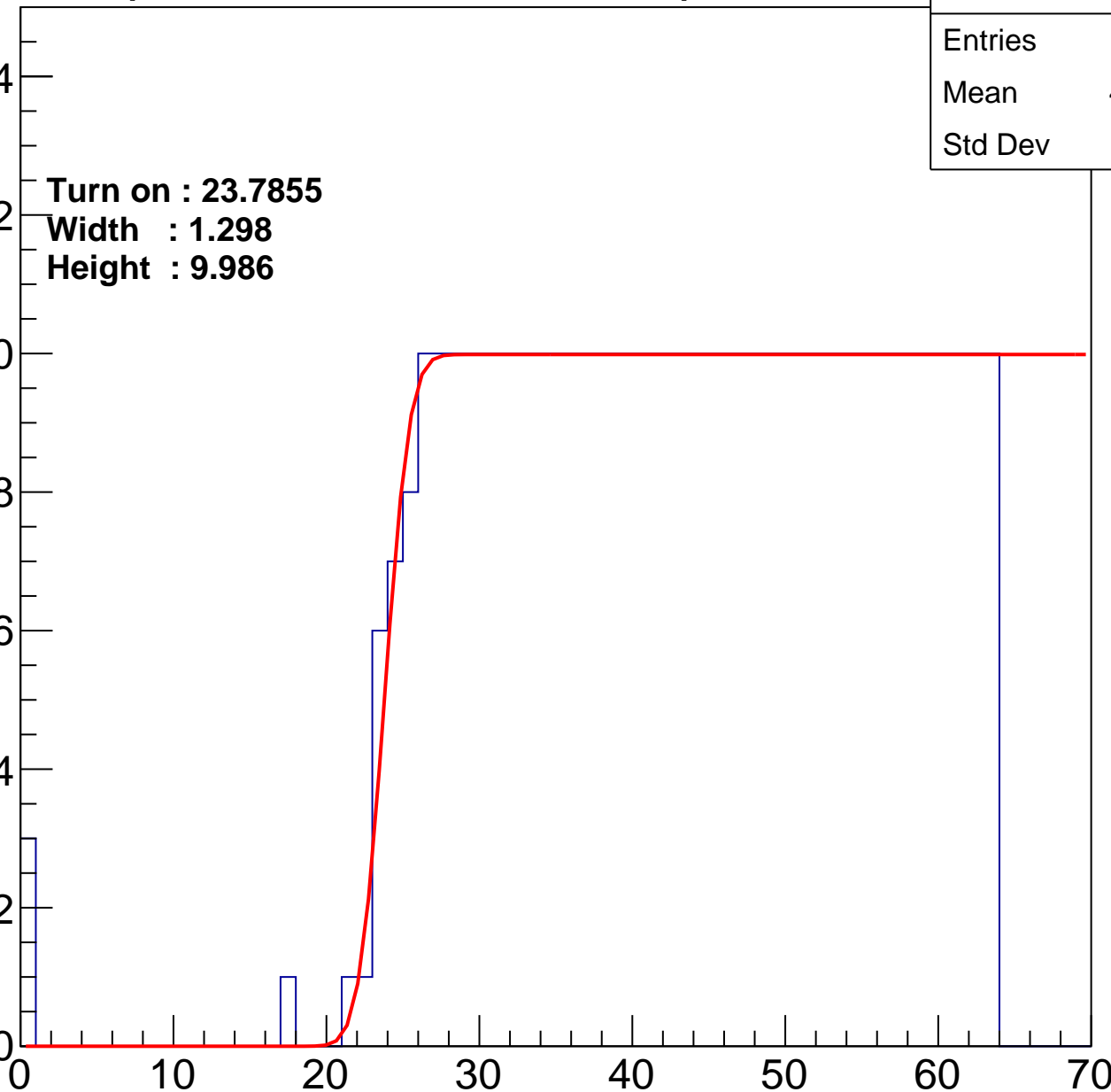
Width : 1.298

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch65

calib_packv5_042523_0143.root, FC#10, port B3

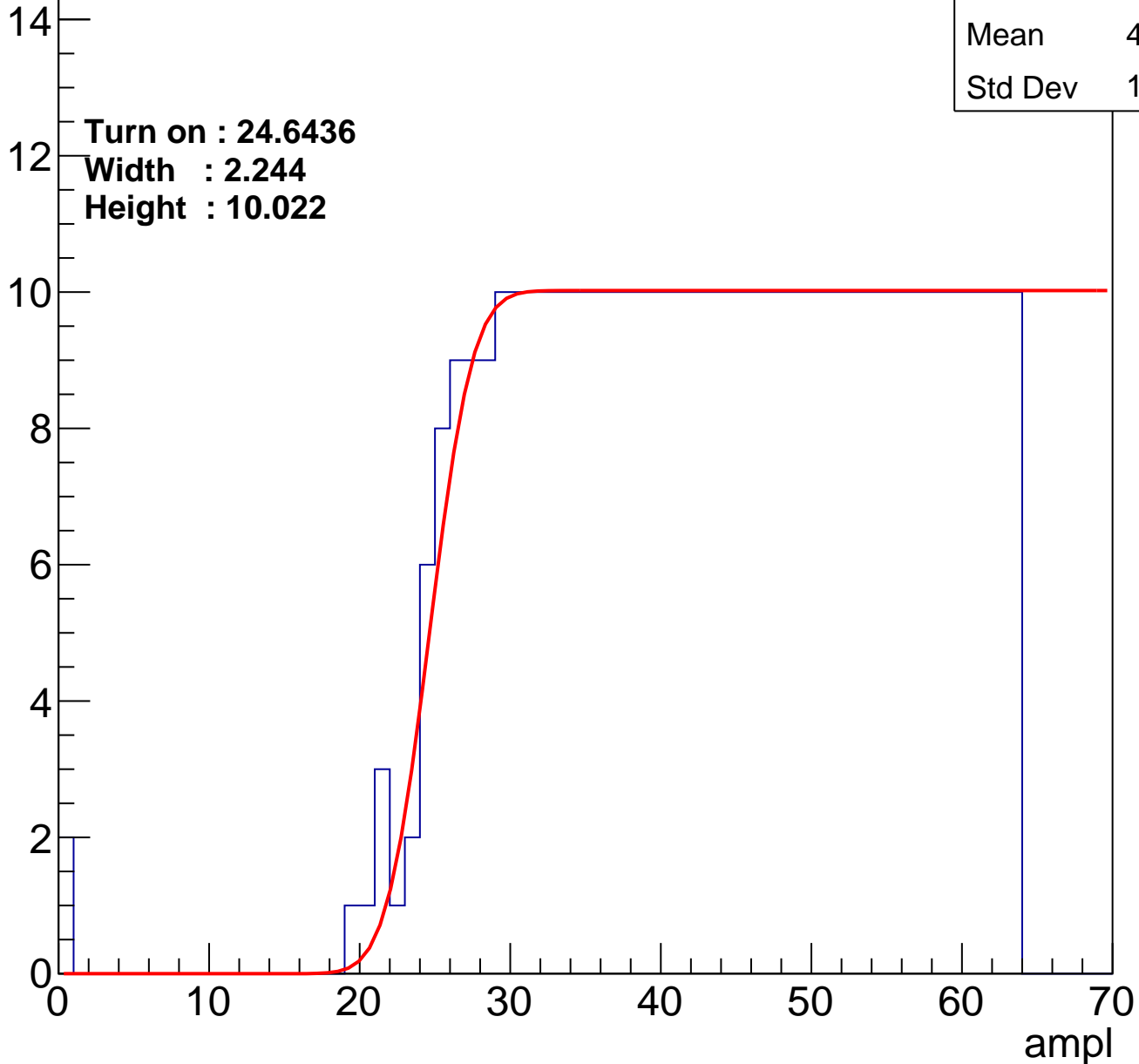
Entries	401
Mean	43.25
Std Dev	12.03

Turn on : 24.6436

Width : 2.244

Height : 10.022

Entry



B1L002S, U14-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.27
Std Dev	12.27

Turn on : 25.1797

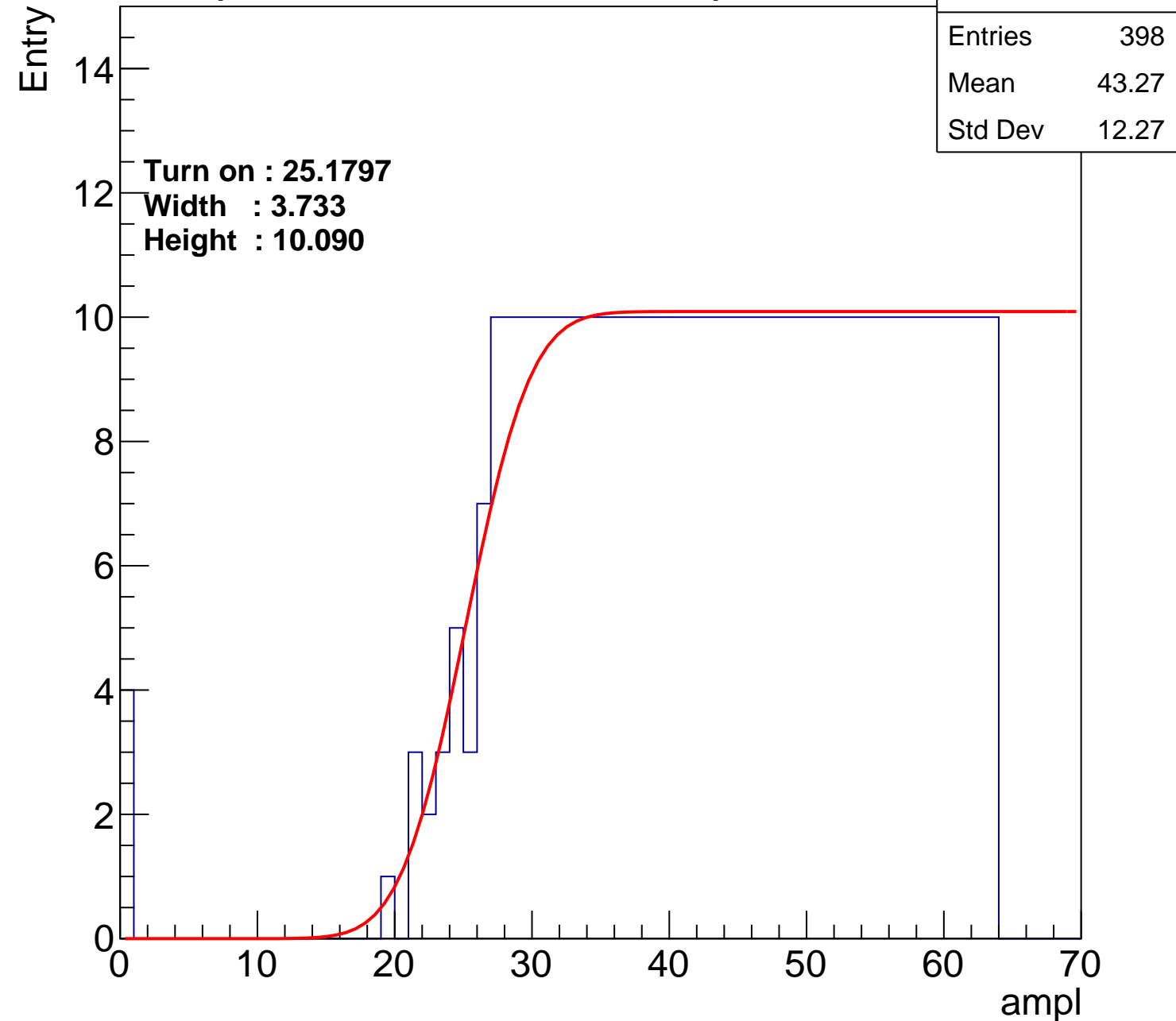
Width : 3.733

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch67

calib_packv5_042523_0143.root, FC#10, port B3

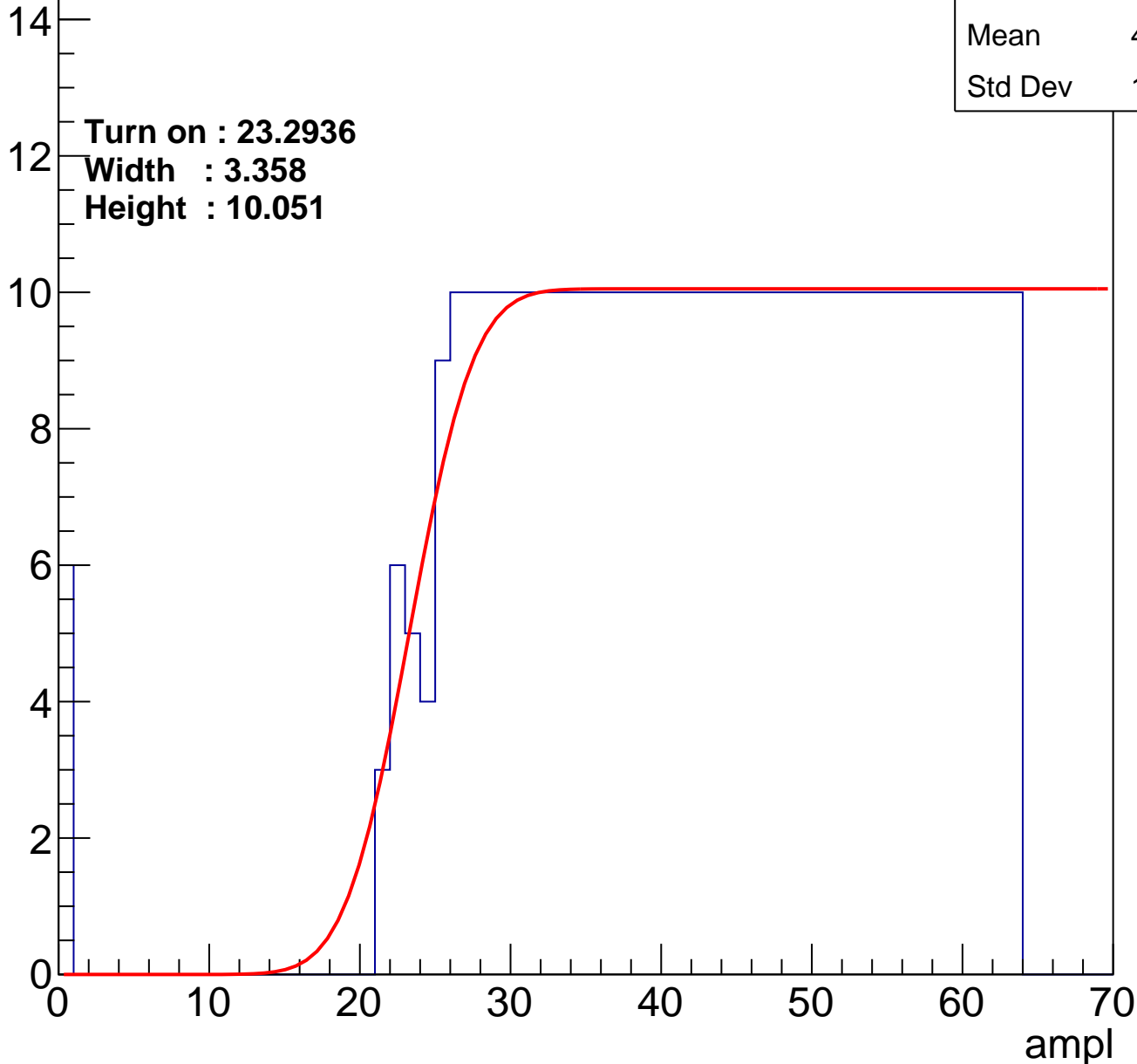
Entries	413
Mean	42.47
Std Dev	12.83

Turn on : 23.2936

Width : 3.358

Height : 10.051

Entry



B1L002S, U14-ch68

calib_packv5_042523_0143.root, FC#10, port B3

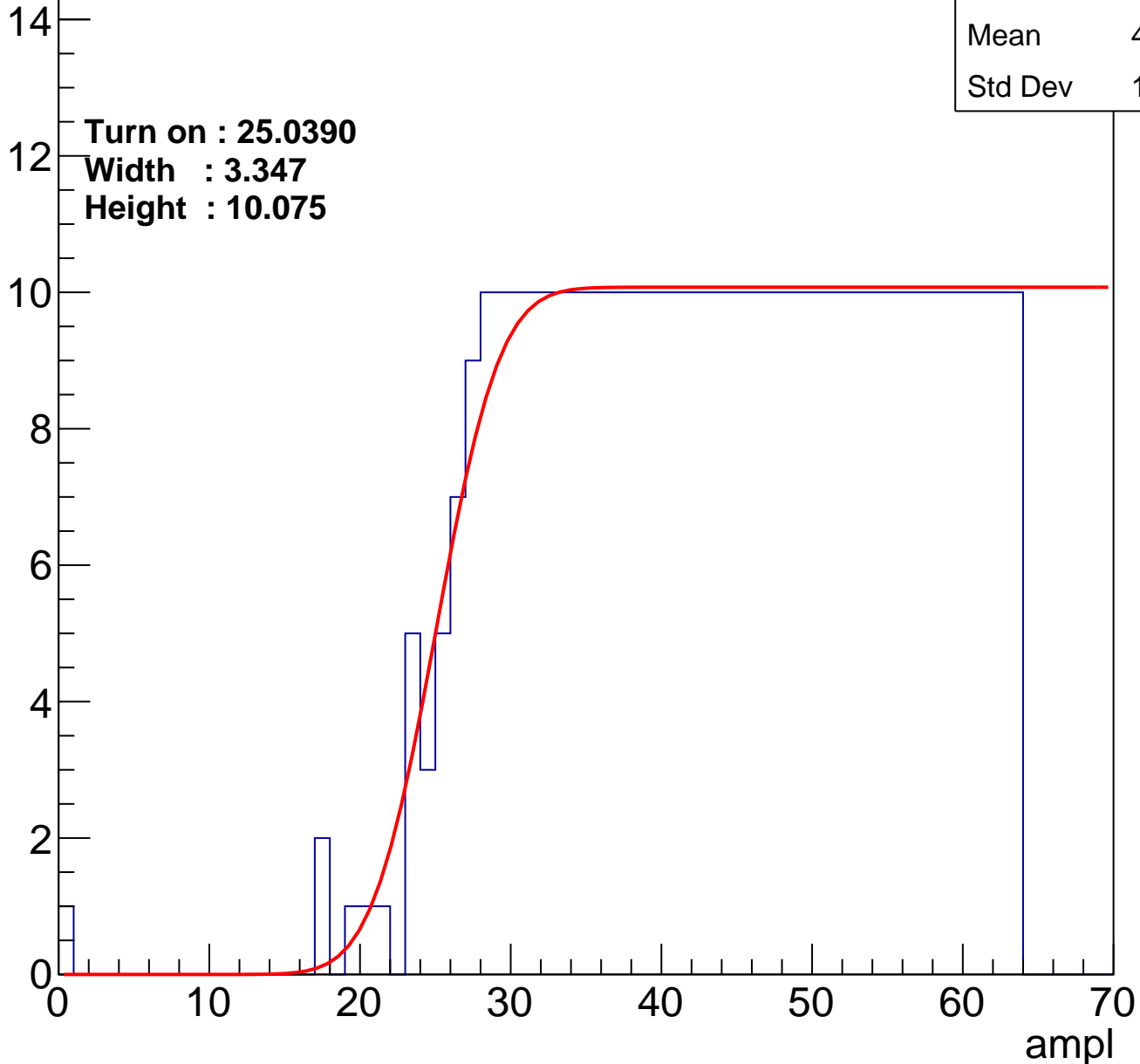
Entries	395
Mean	43.57
Std Dev	11.78

Turn on : 25.0390

Width : 3.347

Height : 10.075

Entry



B1L002S, U14-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 24.9610

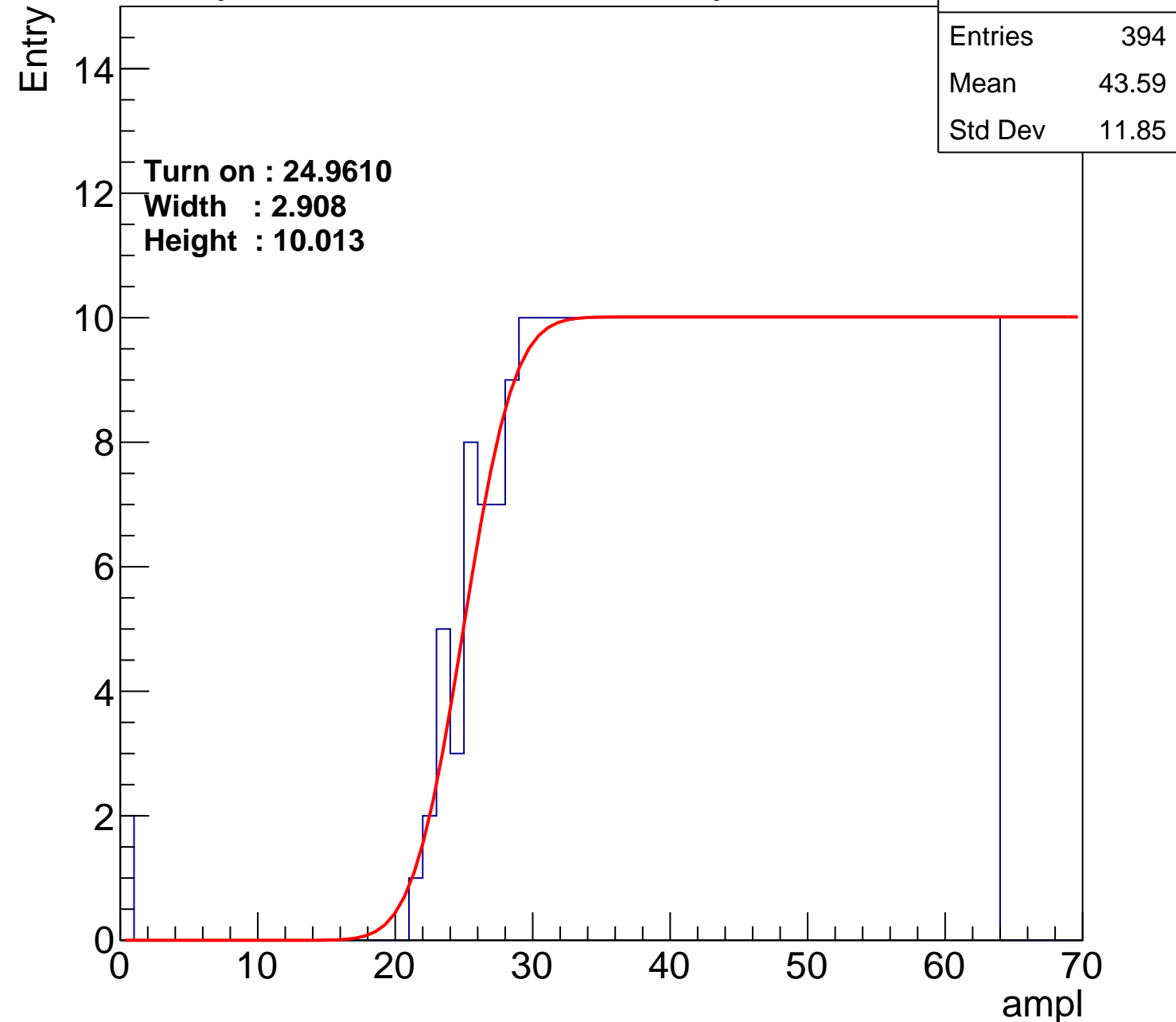
Width : 2.908

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch70

calib_packv5_042523_0143.root, FC#10, port B3

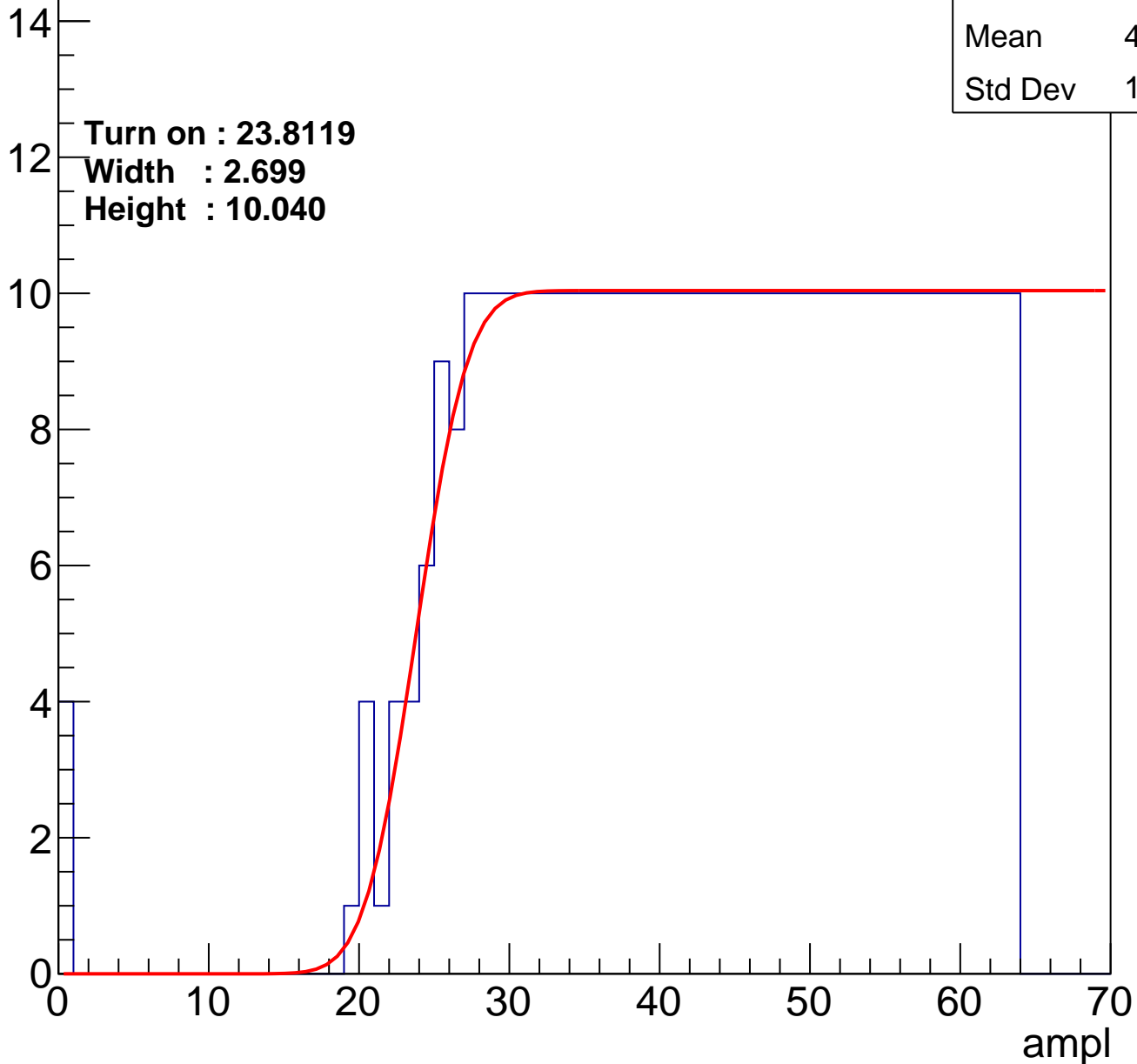
Entries	411
Mean	42.64
Std Dev	12.57

Turn on : 23.8119

Width : 2.699

Height : 10.040

Entry



B1L002S, U14-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.24
Std Dev	11.92

Turn on : 23.8664

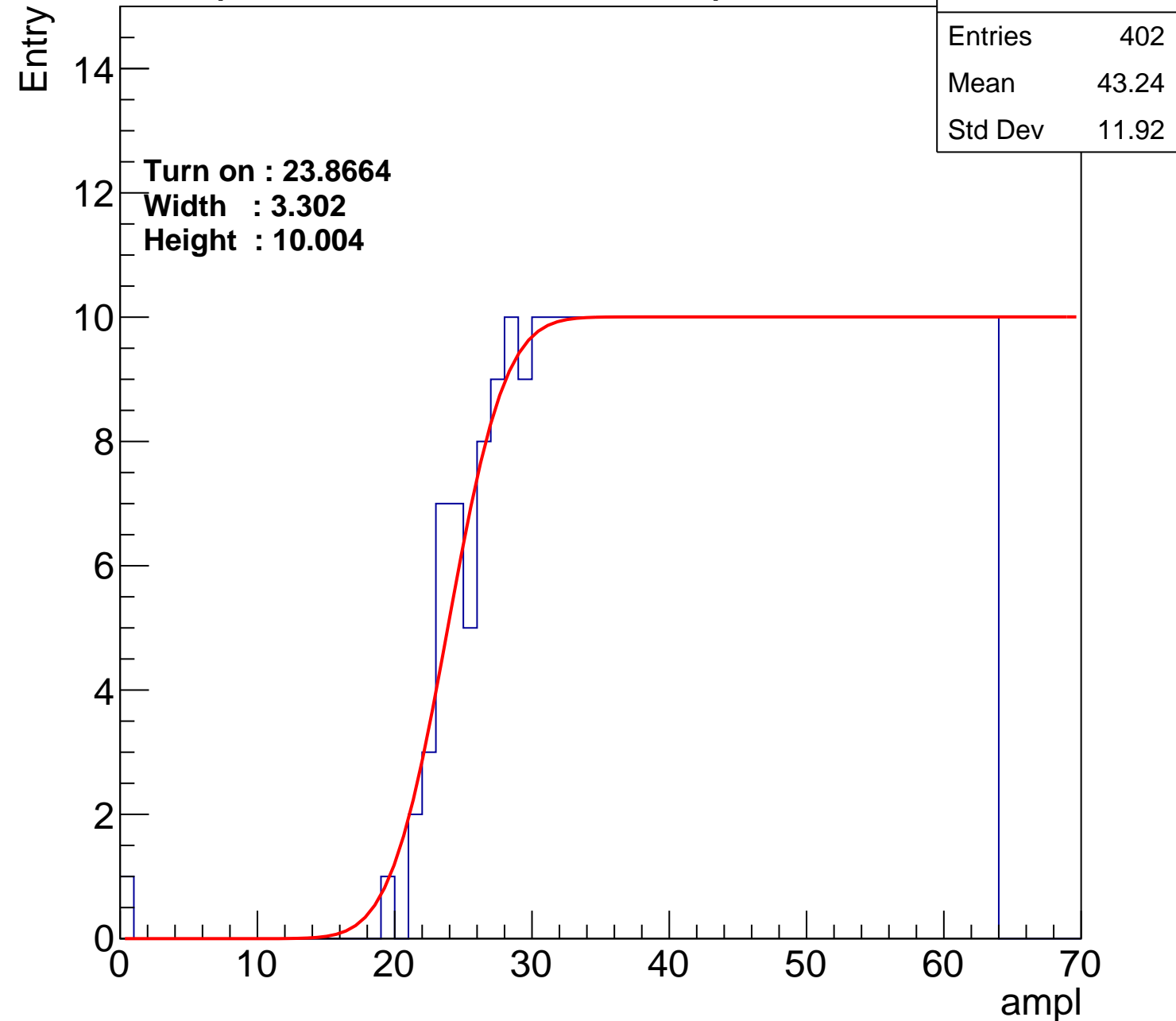
Width : 3.302

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch72

calib_packv5_042523_0143.root, FC#10, port B3

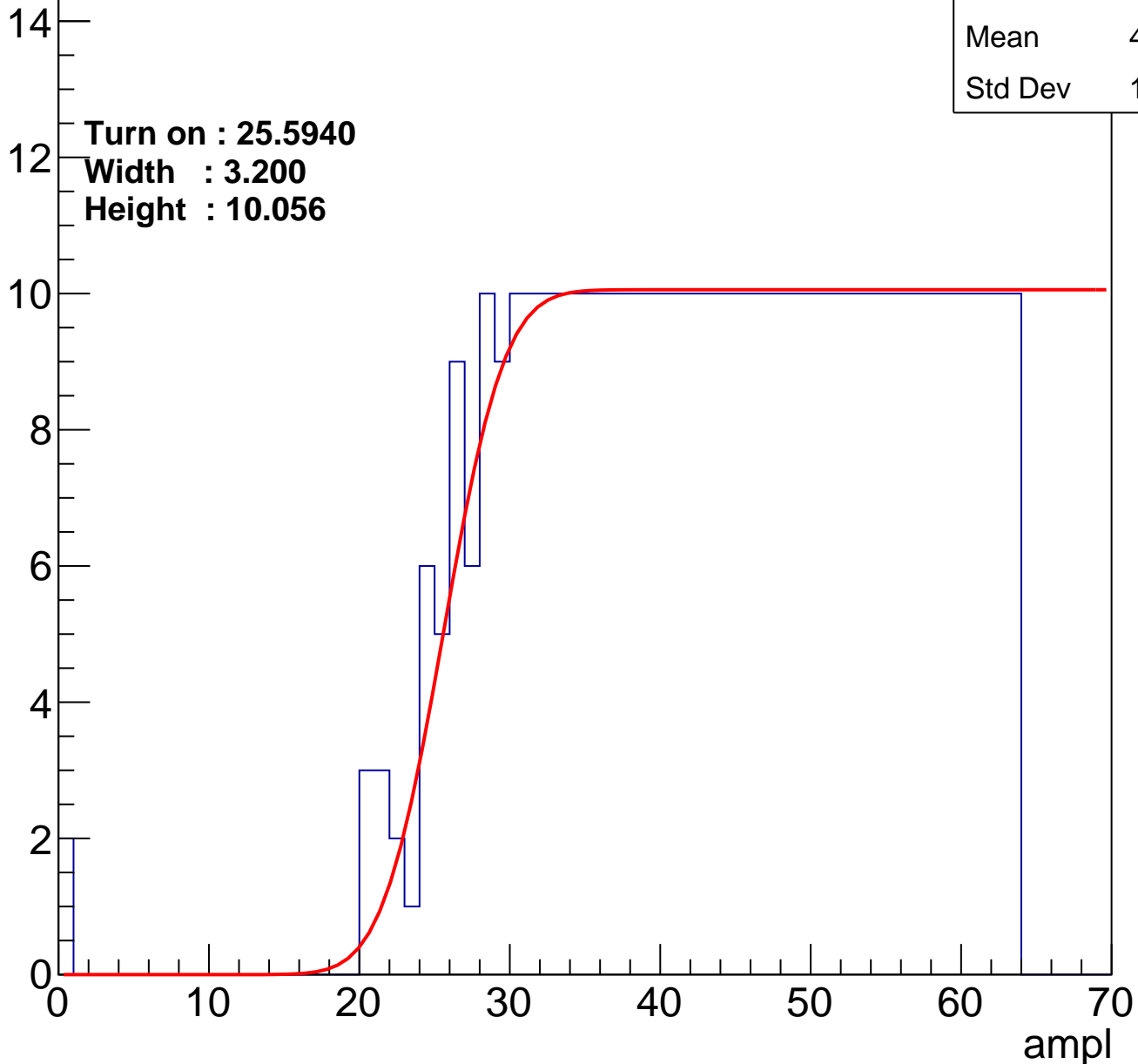
Entries	396
Mean	43.45
Std Dev	11.97

Turn on : 25.5940

Width : 3.200

Height : 10.056

Entry



B1L002S, U14-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.51
Std Dev	12.04

Turn on : 24.8736

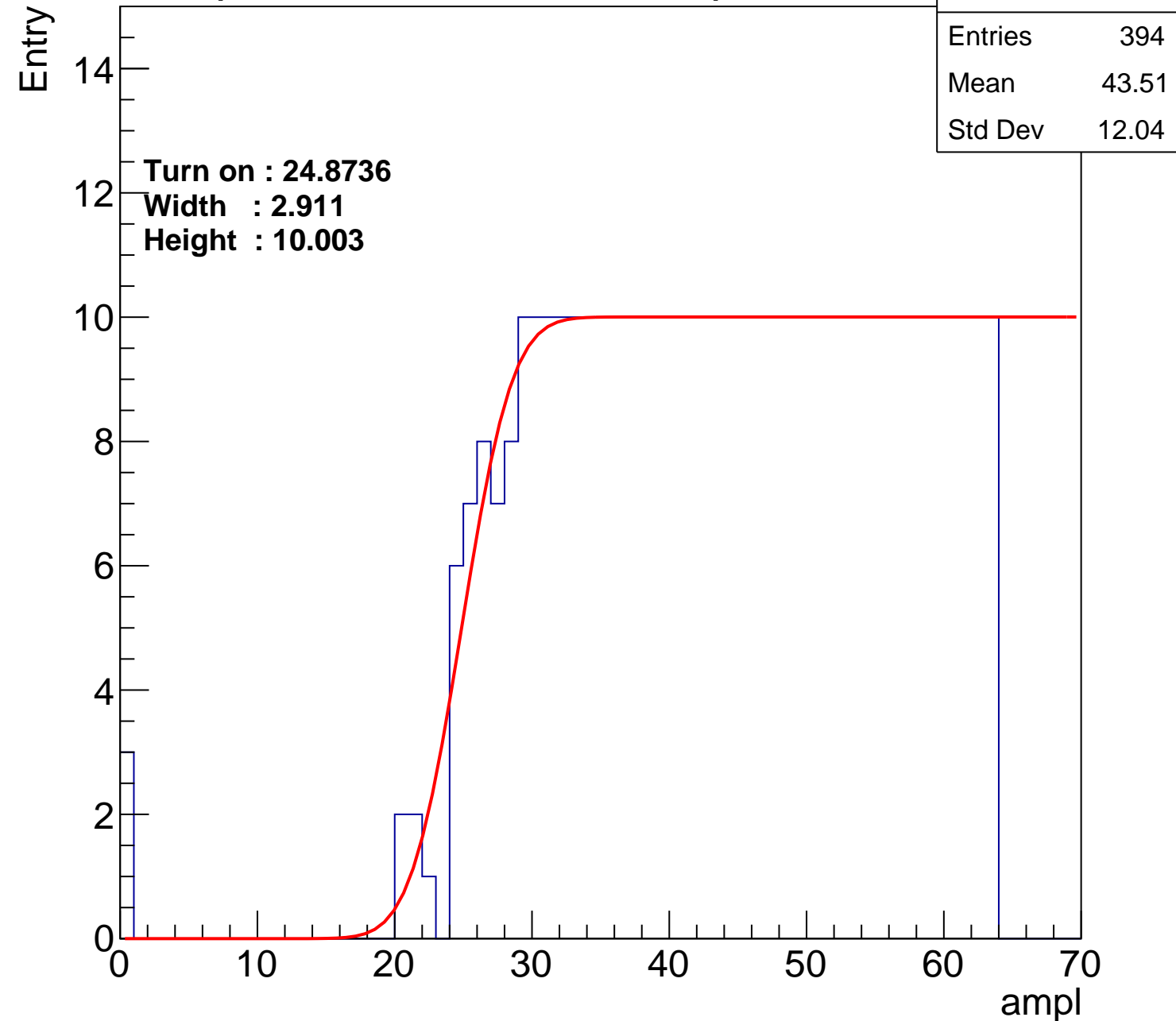
Width : 2.911

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.97
Std Dev	12.26

Turn on : 24.1862

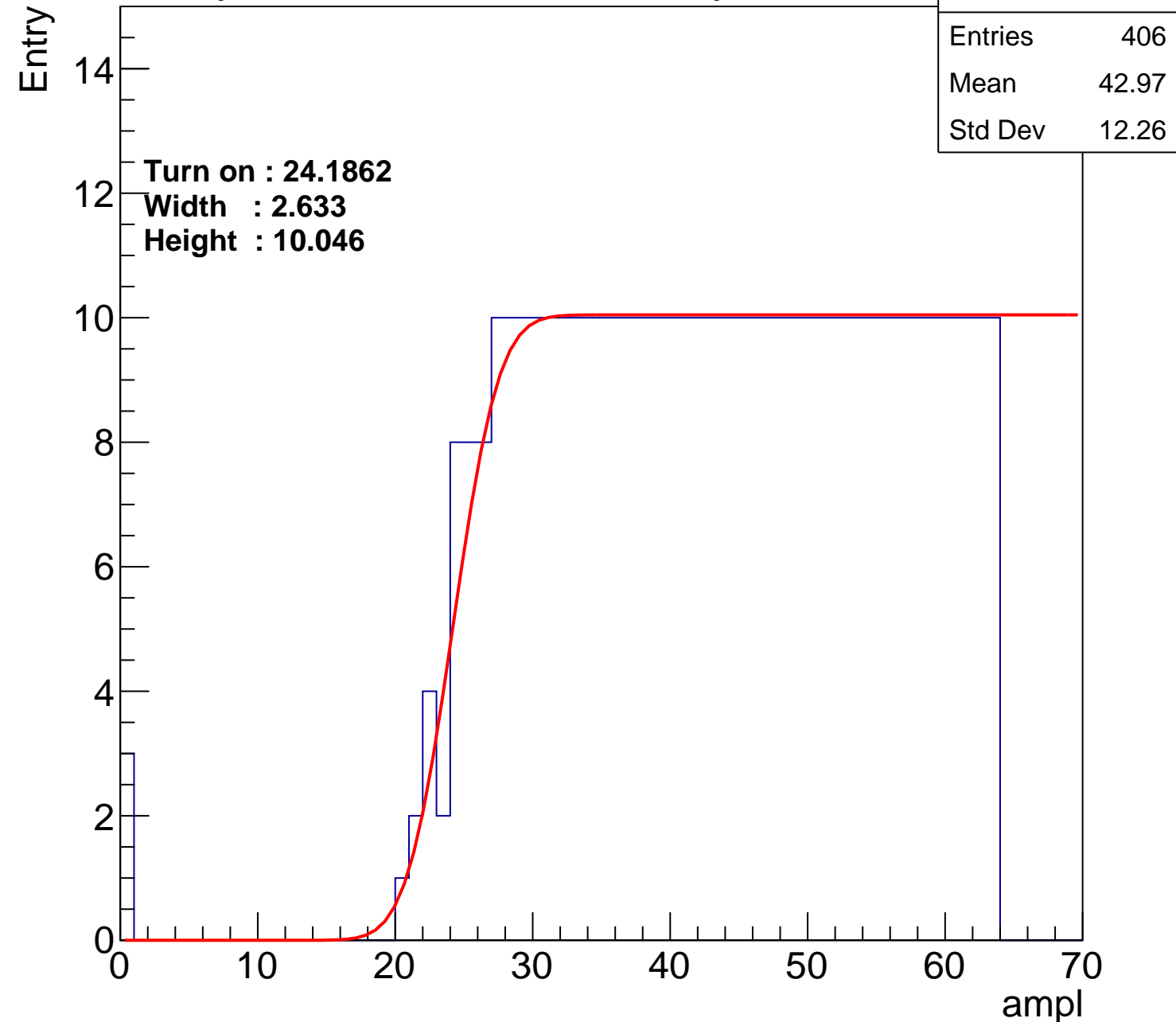
Width : 2.633

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch75

calib_packv5_042523_0143.root, FC#10, port B3

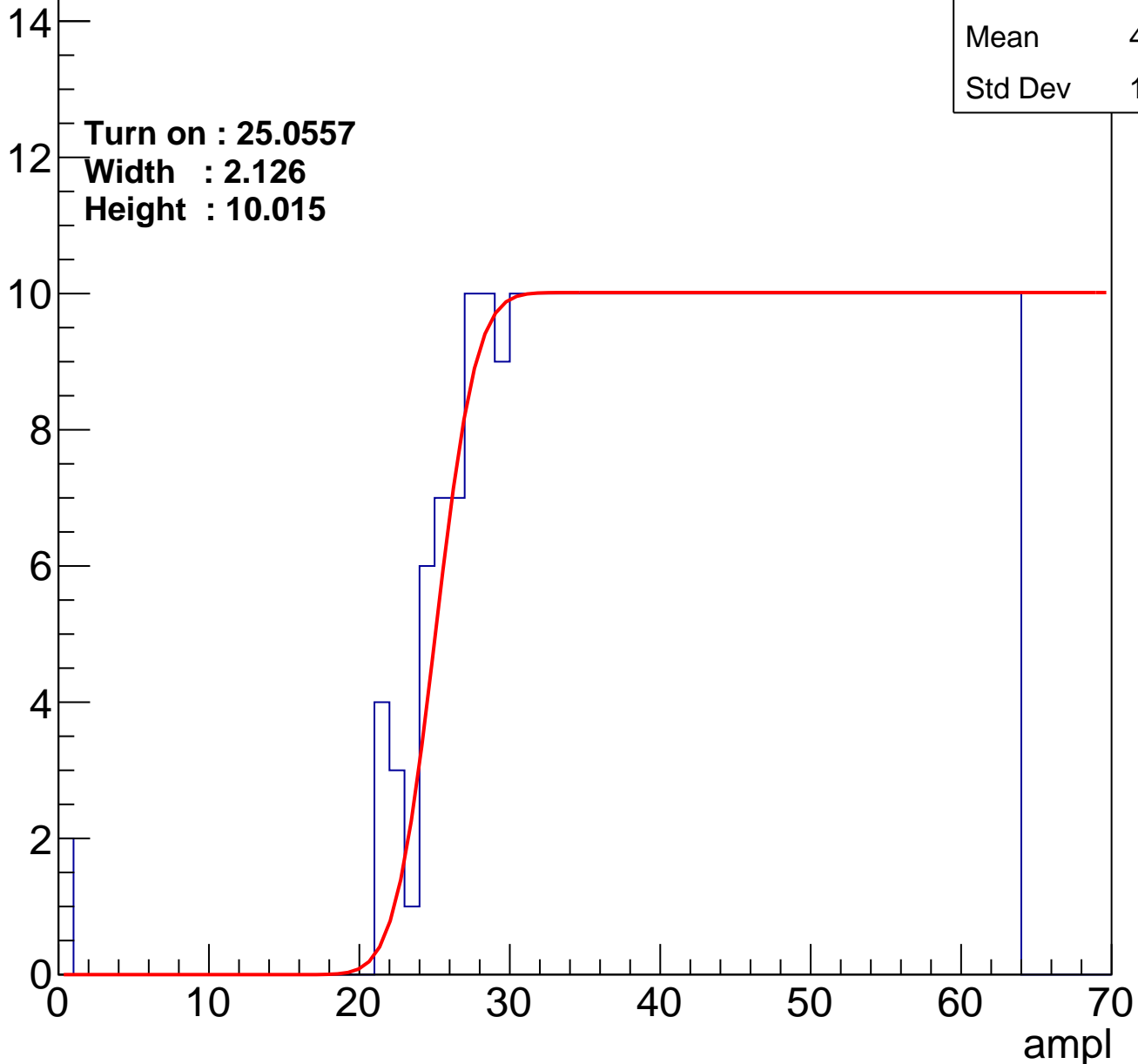
Entries	399
Mean	43.35
Std Dev	11.98

Turn on : 25.0557

Width : 2.126

Height : 10.015

Entry



B1L002S, U14-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.02
Std Dev	12.34

Turn on : 23.7247

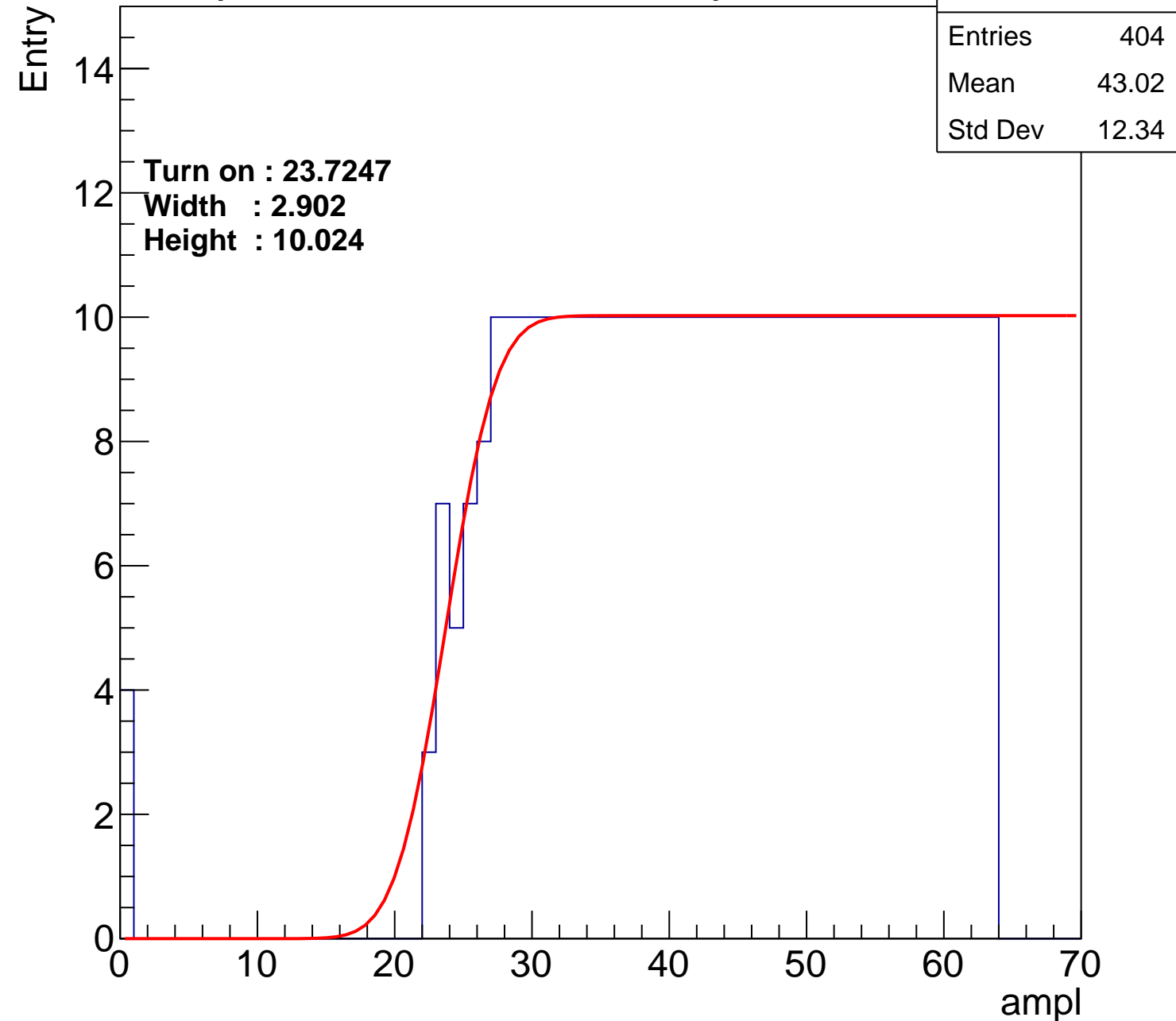
Width : 2.902

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.46
Std Dev	11.86

Turn on : 25.2798

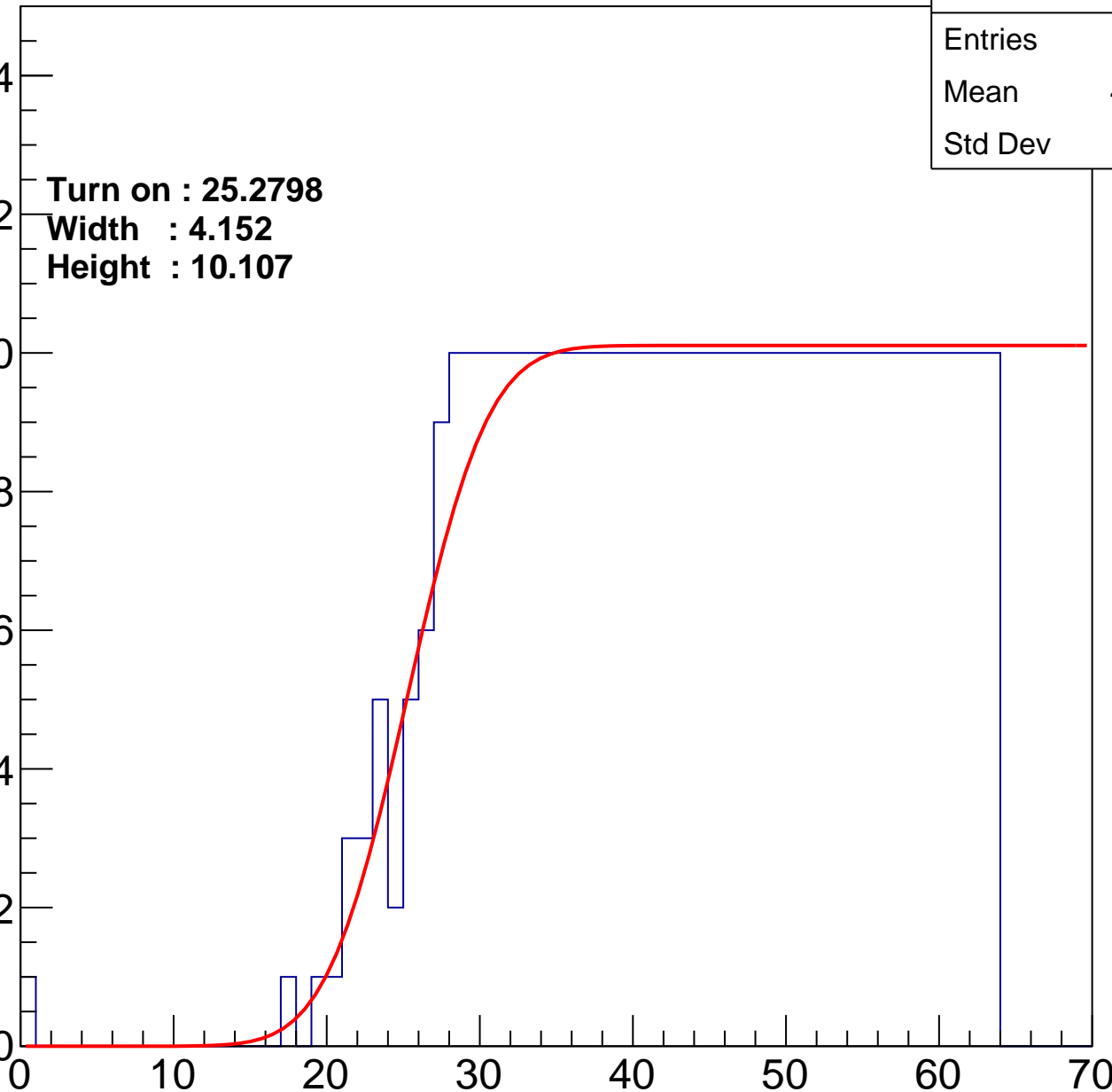
Width : 4.152

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	42.82
Std Dev	12.97

Turn on : 24.8272

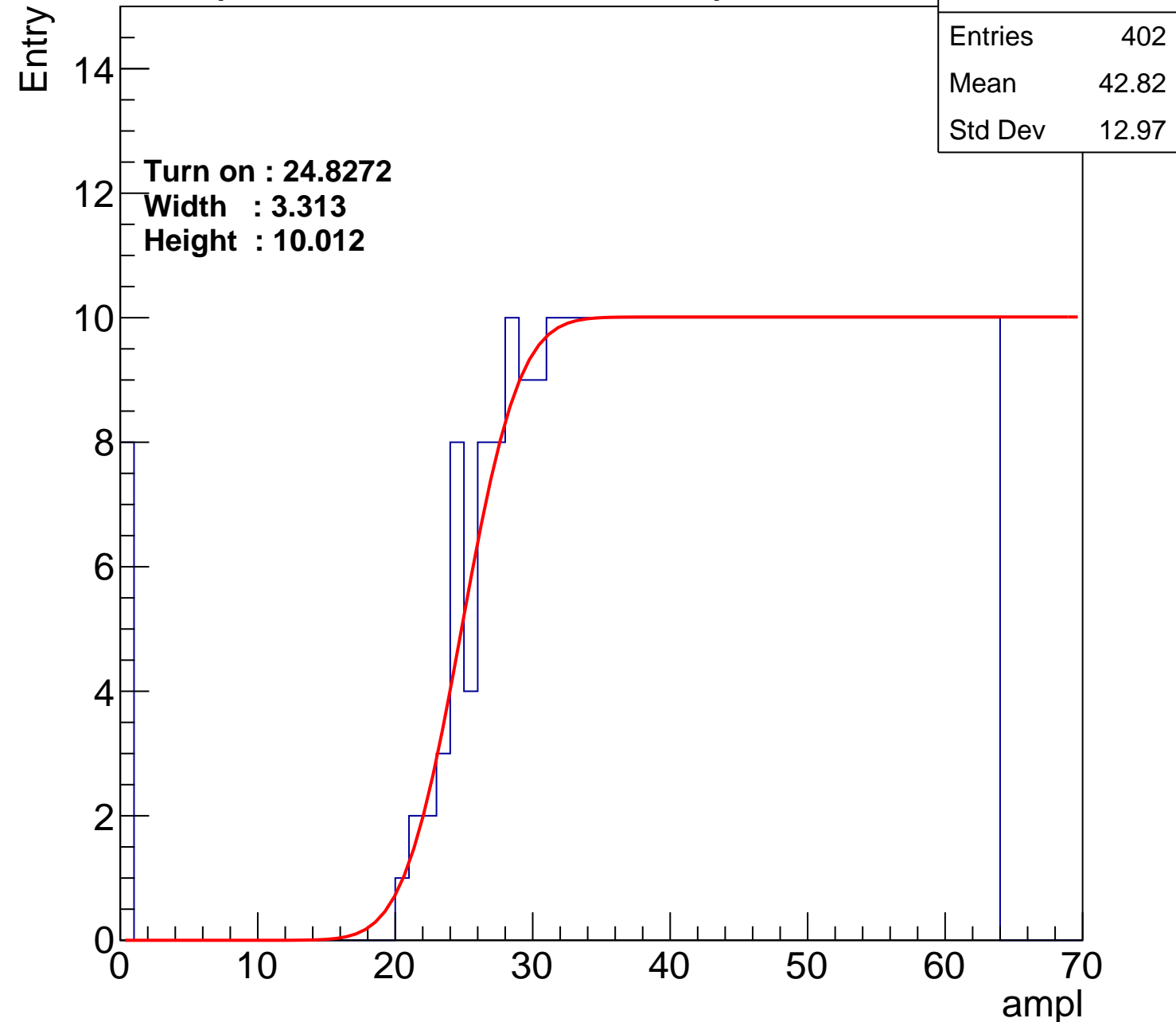
Width : 3.313

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch79

calib_packv5_042523_0143.root, FC#10, port B3

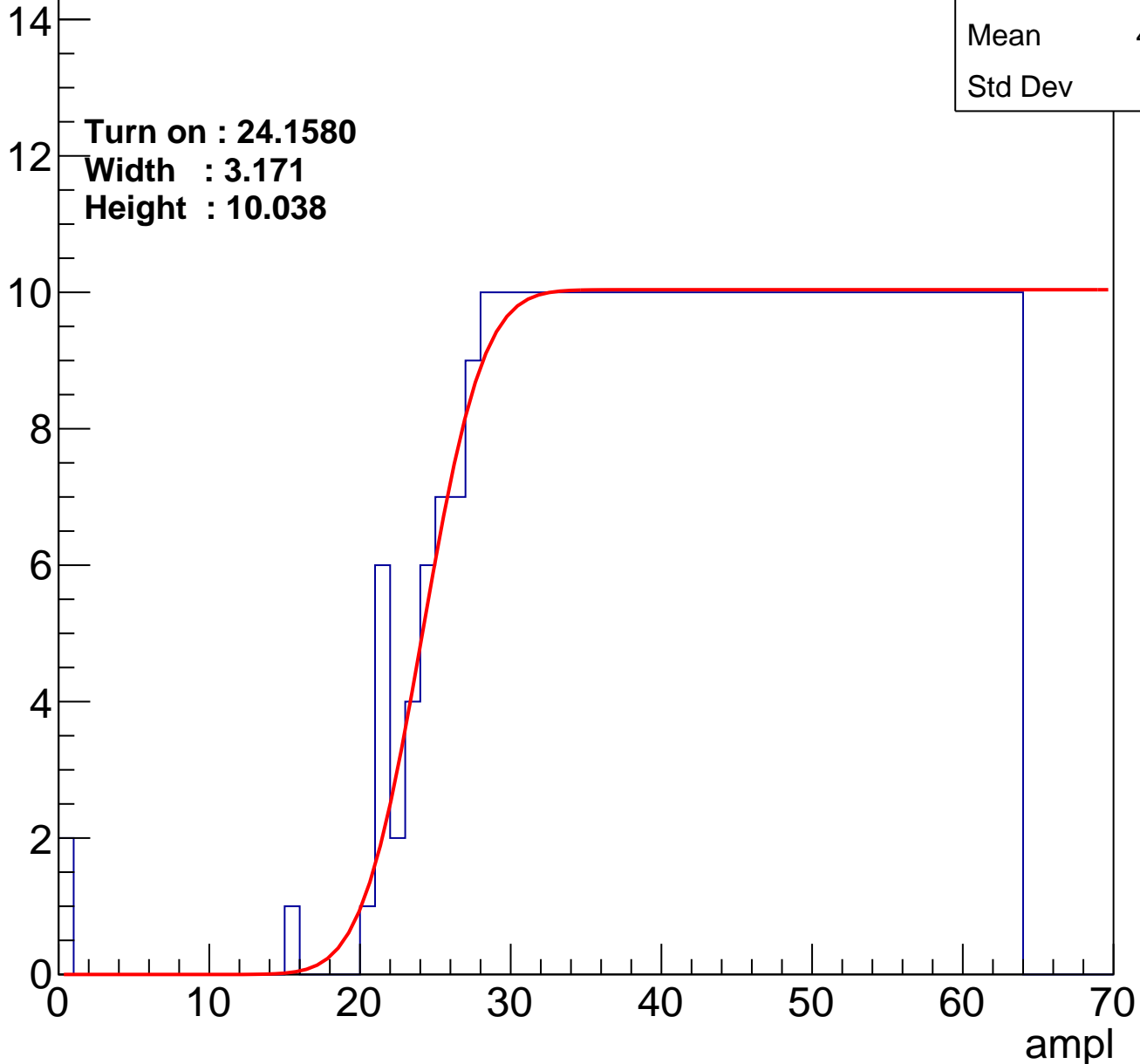
Entries	405
Mean	43.01
Std Dev	12.2

Turn on : 24.1580

Width : 3.171

Height : 10.038

Entry



B1L002S, U14-ch80

calib_packv5_042523_0143.root, FC#10, port B3

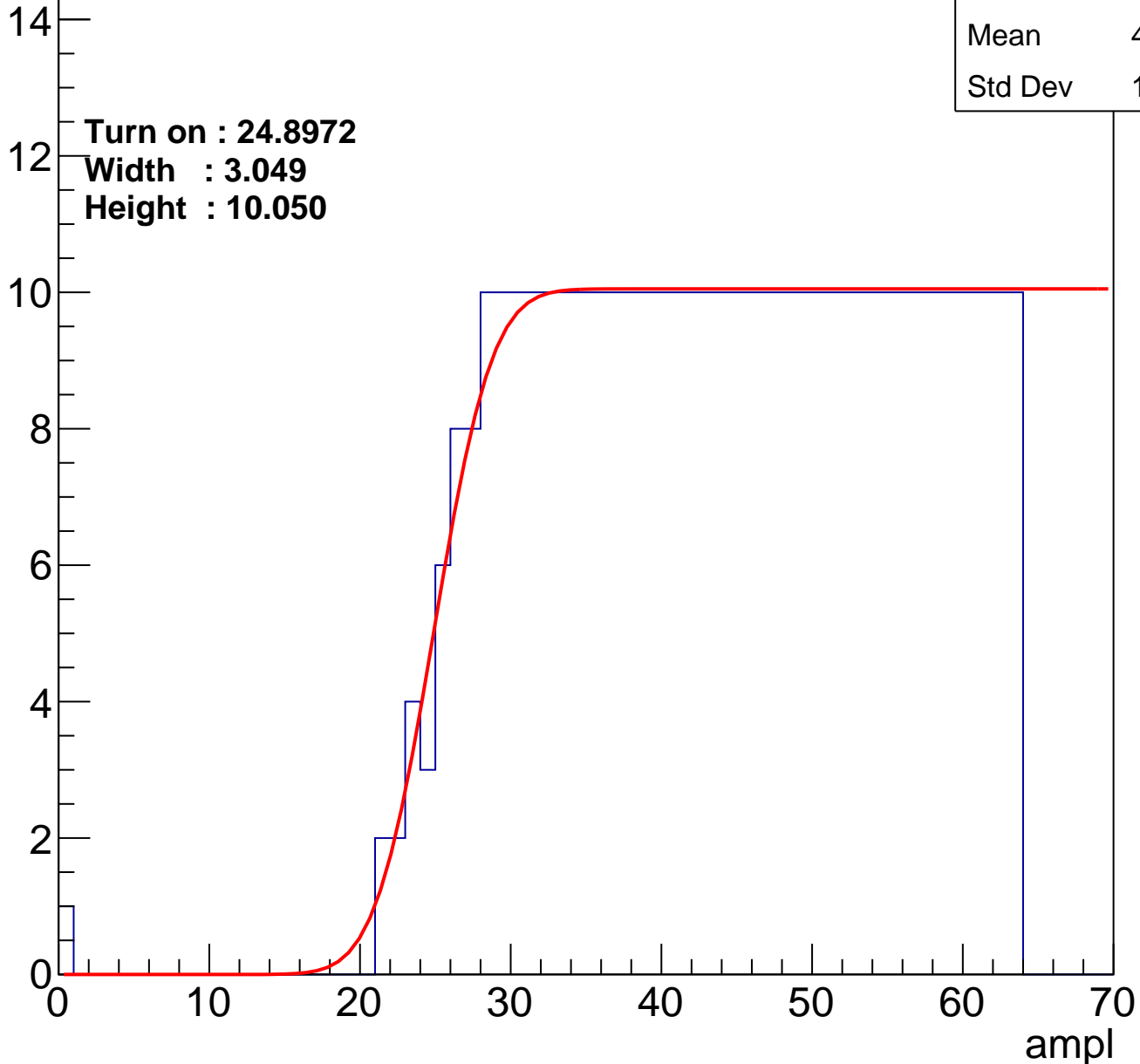
Entries	394
Mean	43.66
Std Dev	11.66

Turn on : 24.8972

Width : 3.049

Height : 10.050

Entry



B1L002S, U14-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.39
Std Dev	11.84

Turn on : 24.5317

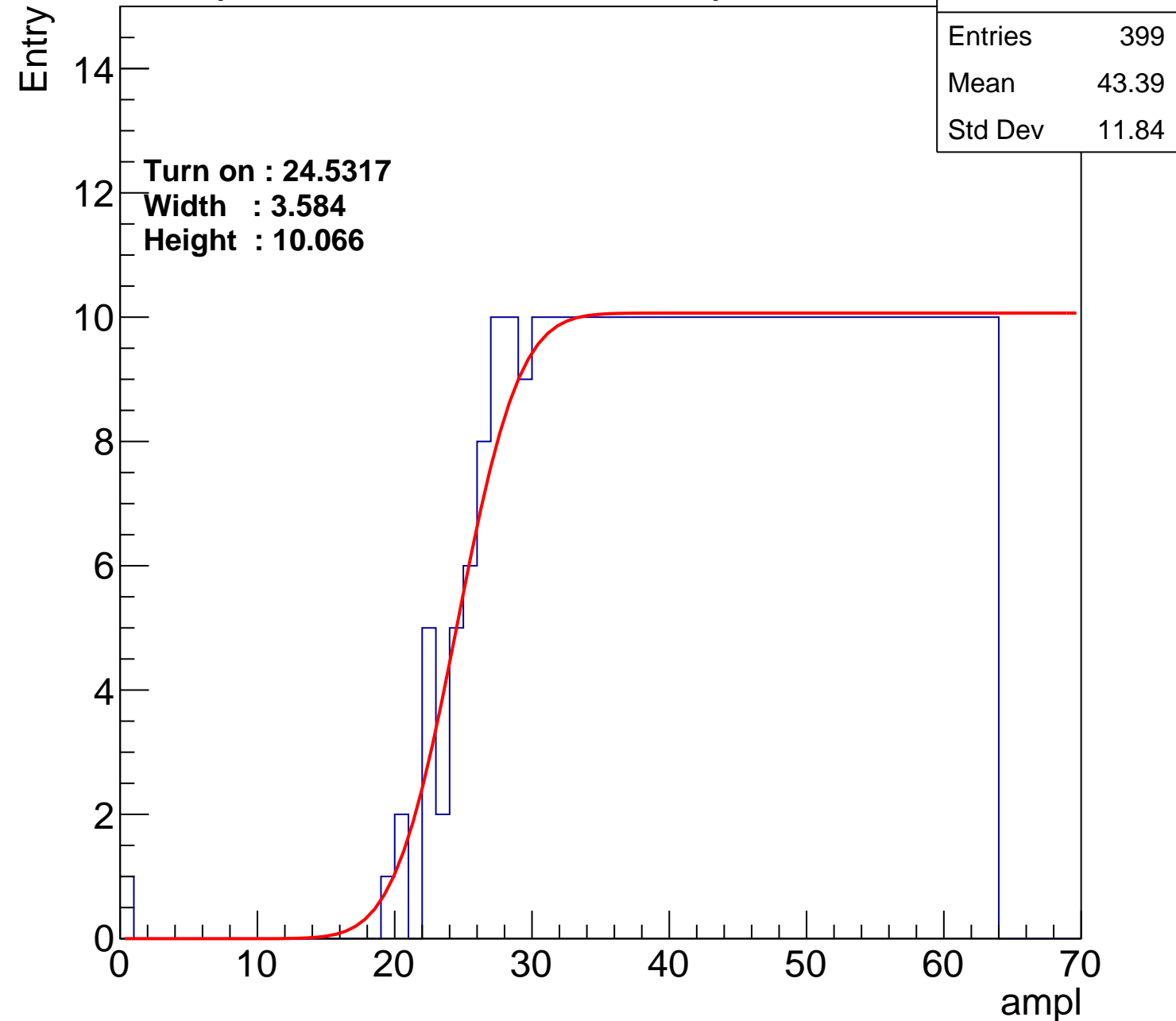
Width : 3.584

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.31
Std Dev	12.24

Turn on : 24.3988

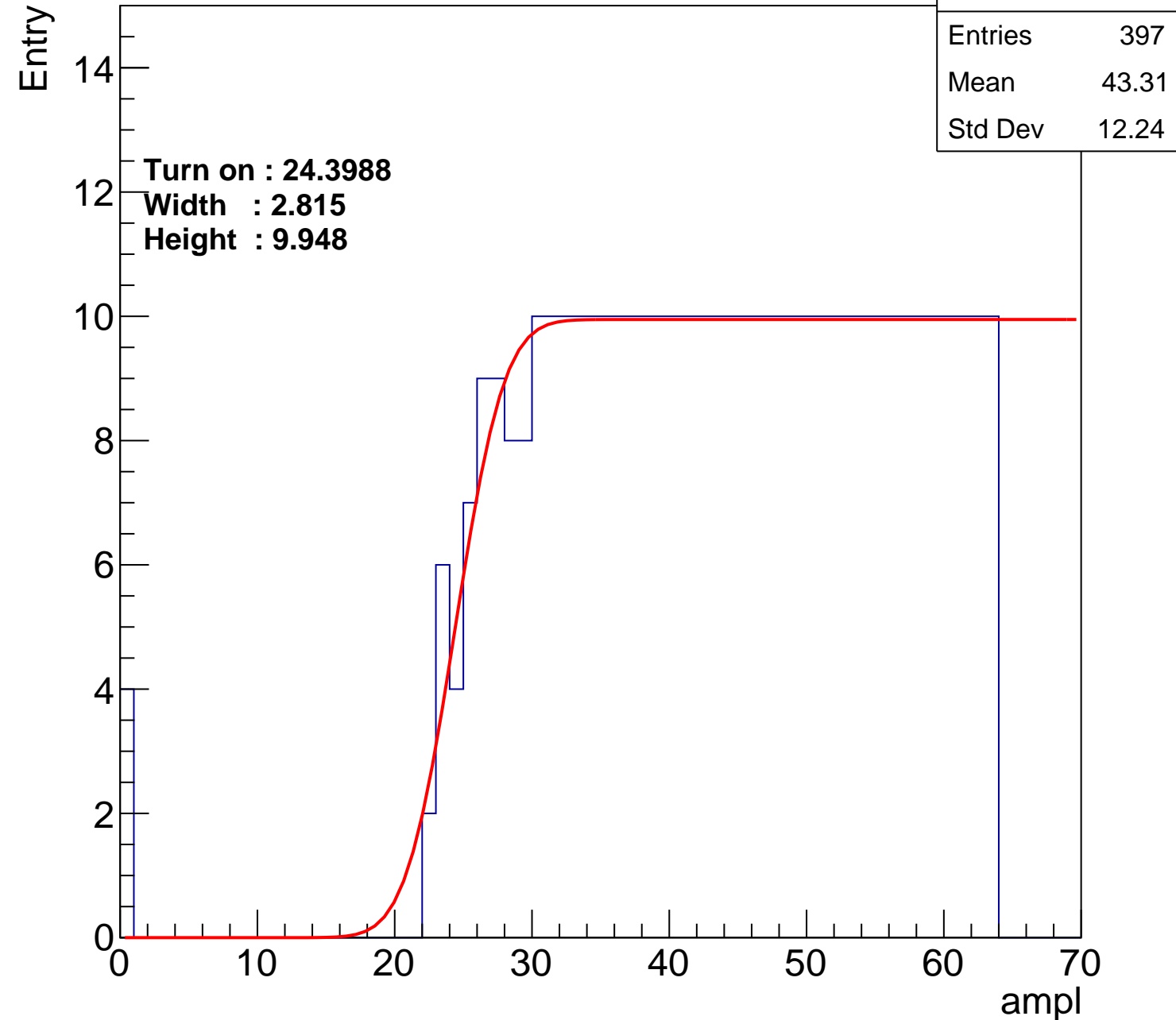
Width : 2.815

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 27.1389

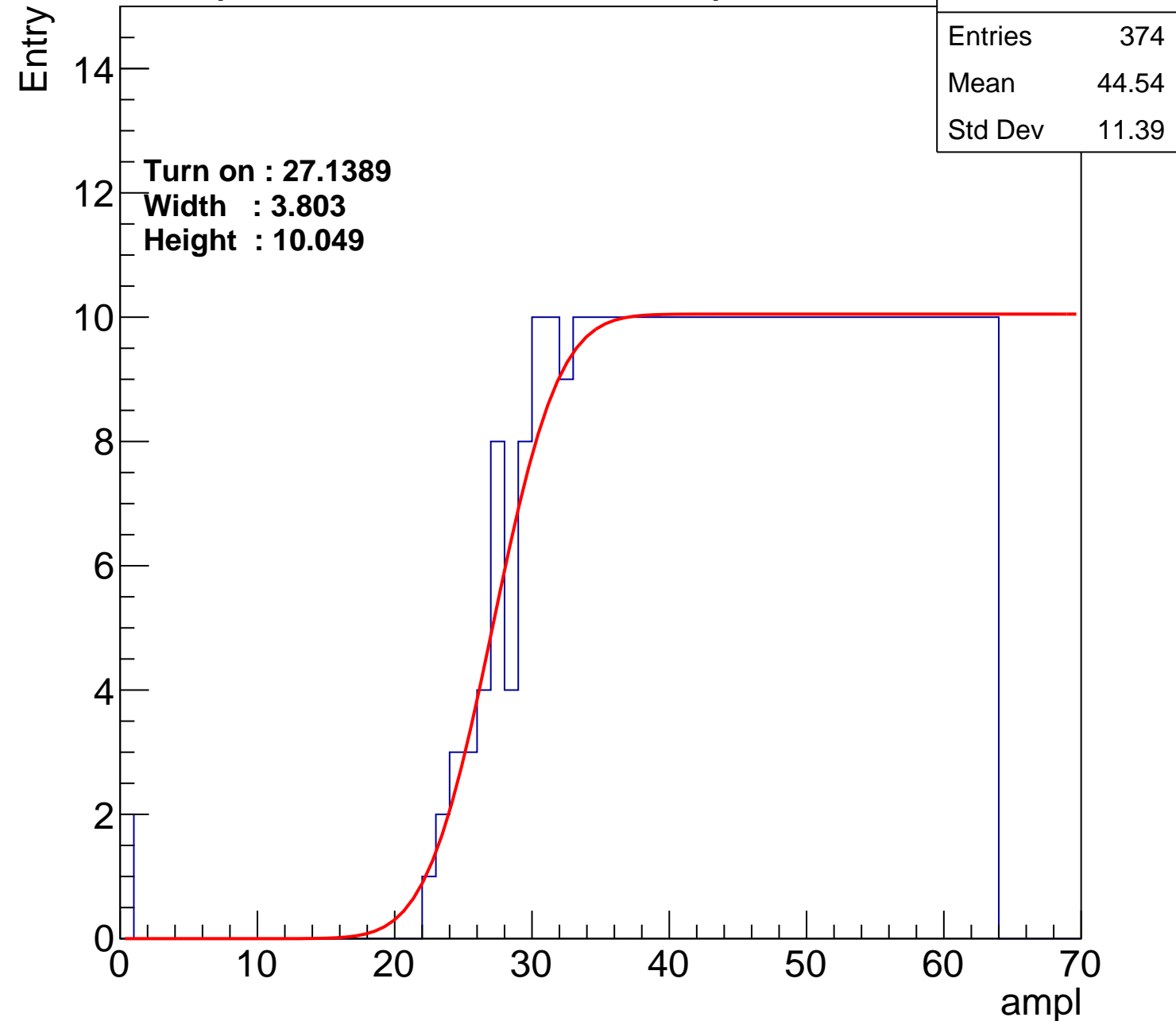
Width : 3.803

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch84

calib_packv5_042523_0143.root, FC#10, port B3

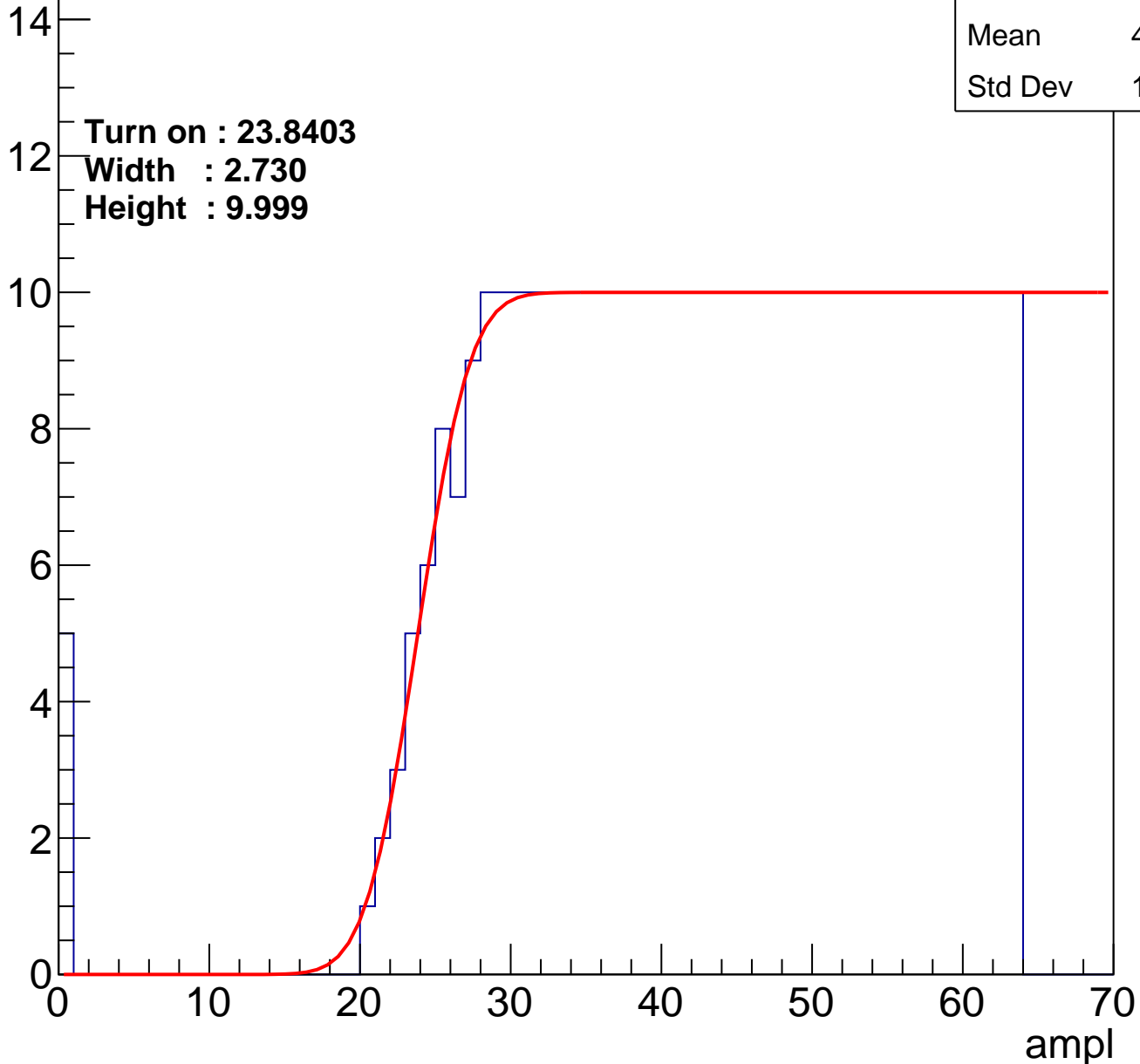
Entries	406
Mean	42.84
Std Dev	12.58

Turn on : 23.8403

Width : 2.730

Height : 9.999

Entry



B1L002S, U14-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.33
Std Dev	11.83

Turn on : 23.8142

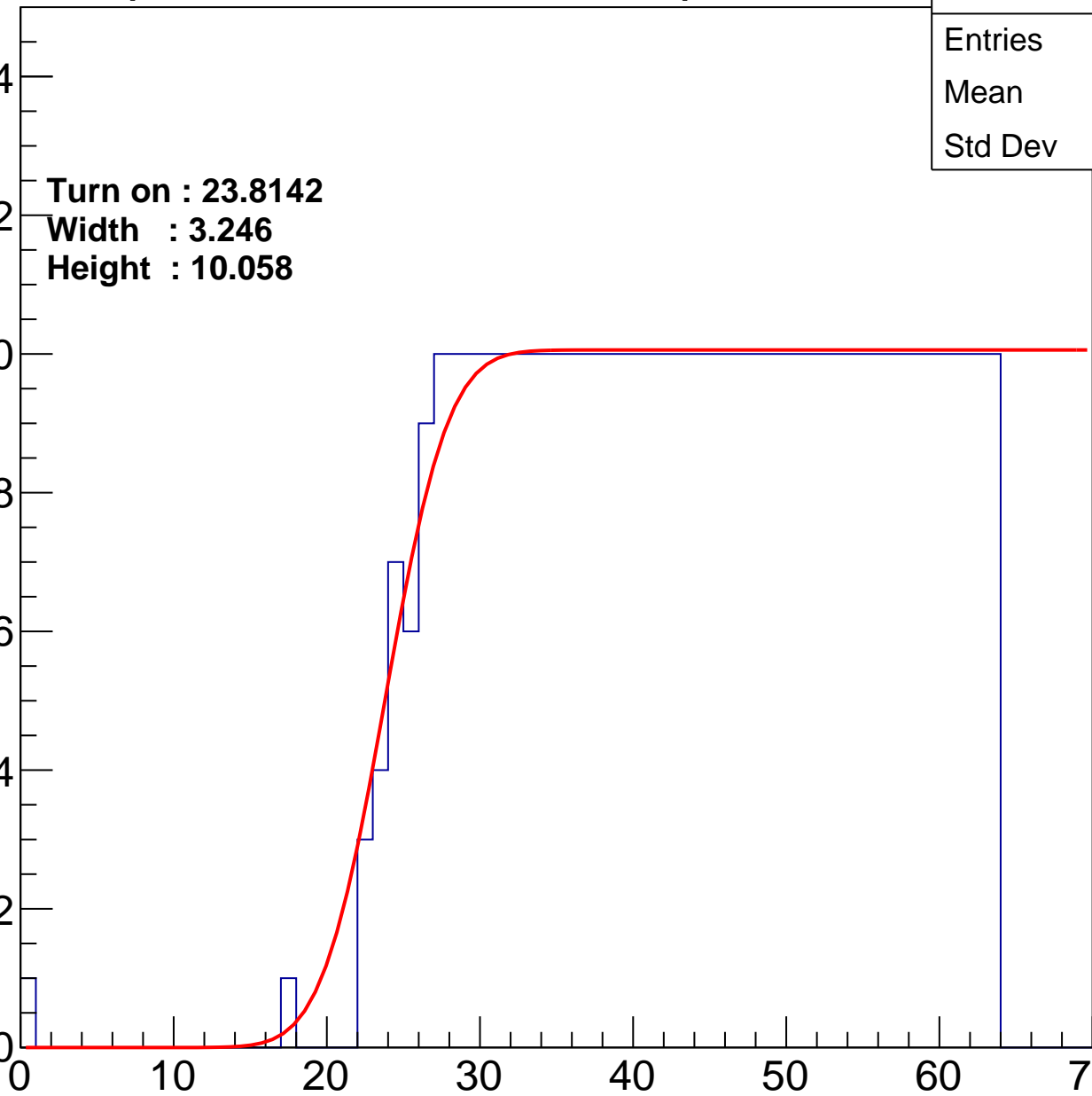
Width : 3.246

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 25.0240

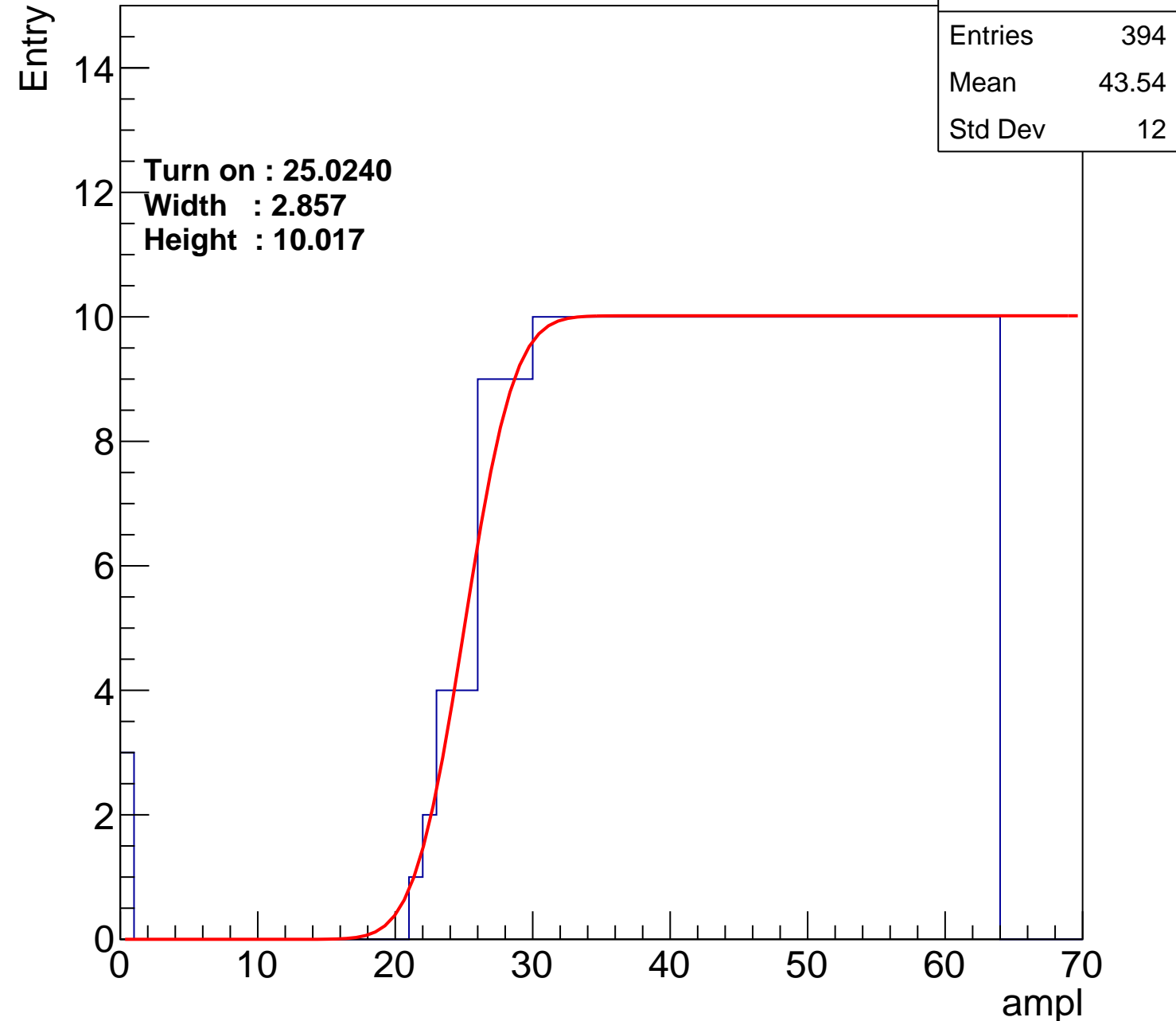
Width : 2.857

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch87

calib_packv5_042523_0143.root, FC#10, port B3

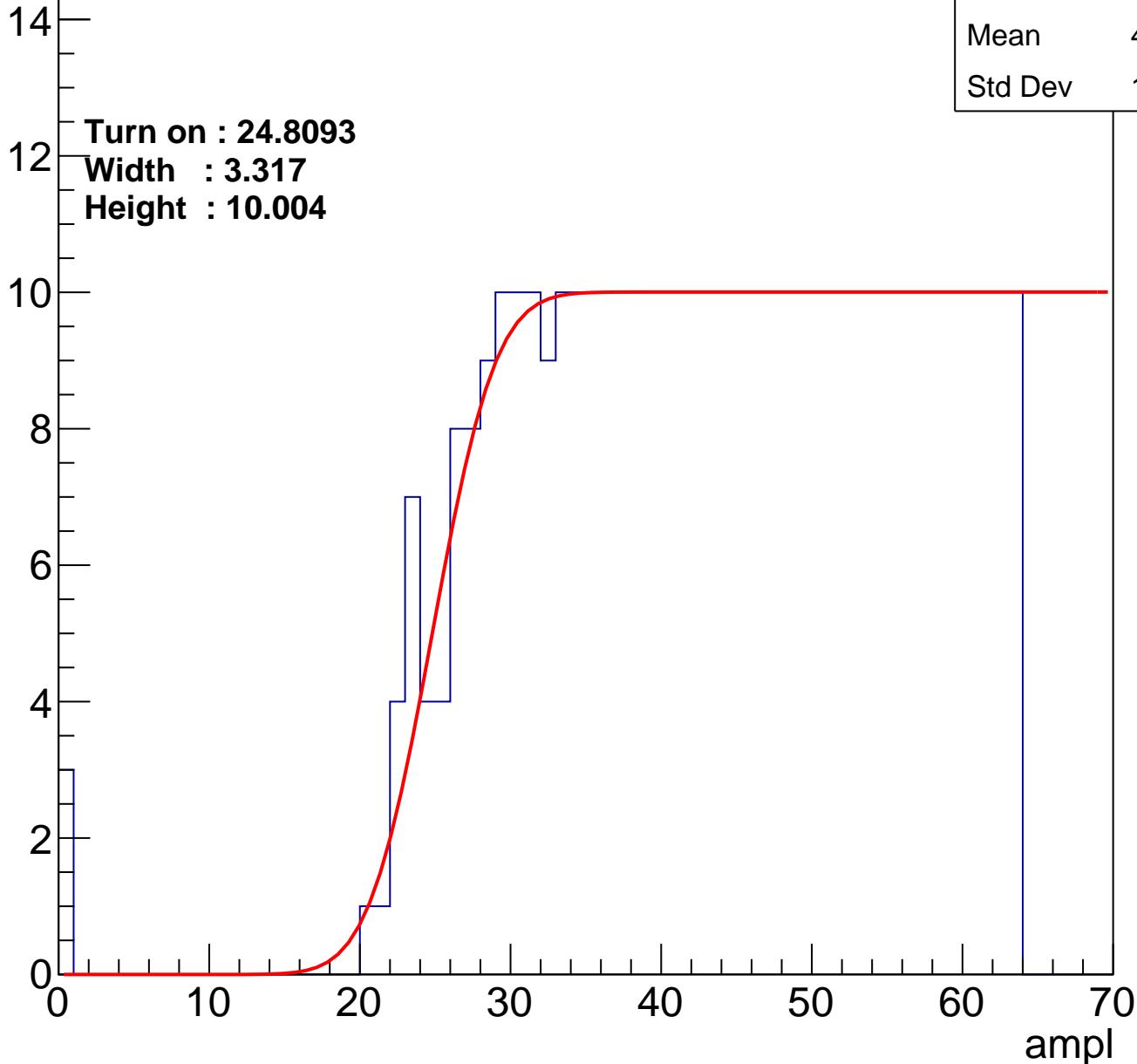
Entries	398
Mean	43.29
Std Dev	12.17

Turn on : 24.8093

Width : 3.317

Height : 10.004

Entry



B1L002S, U14-ch88

calib_packv5_042523_0143.root, FC#10, port B3

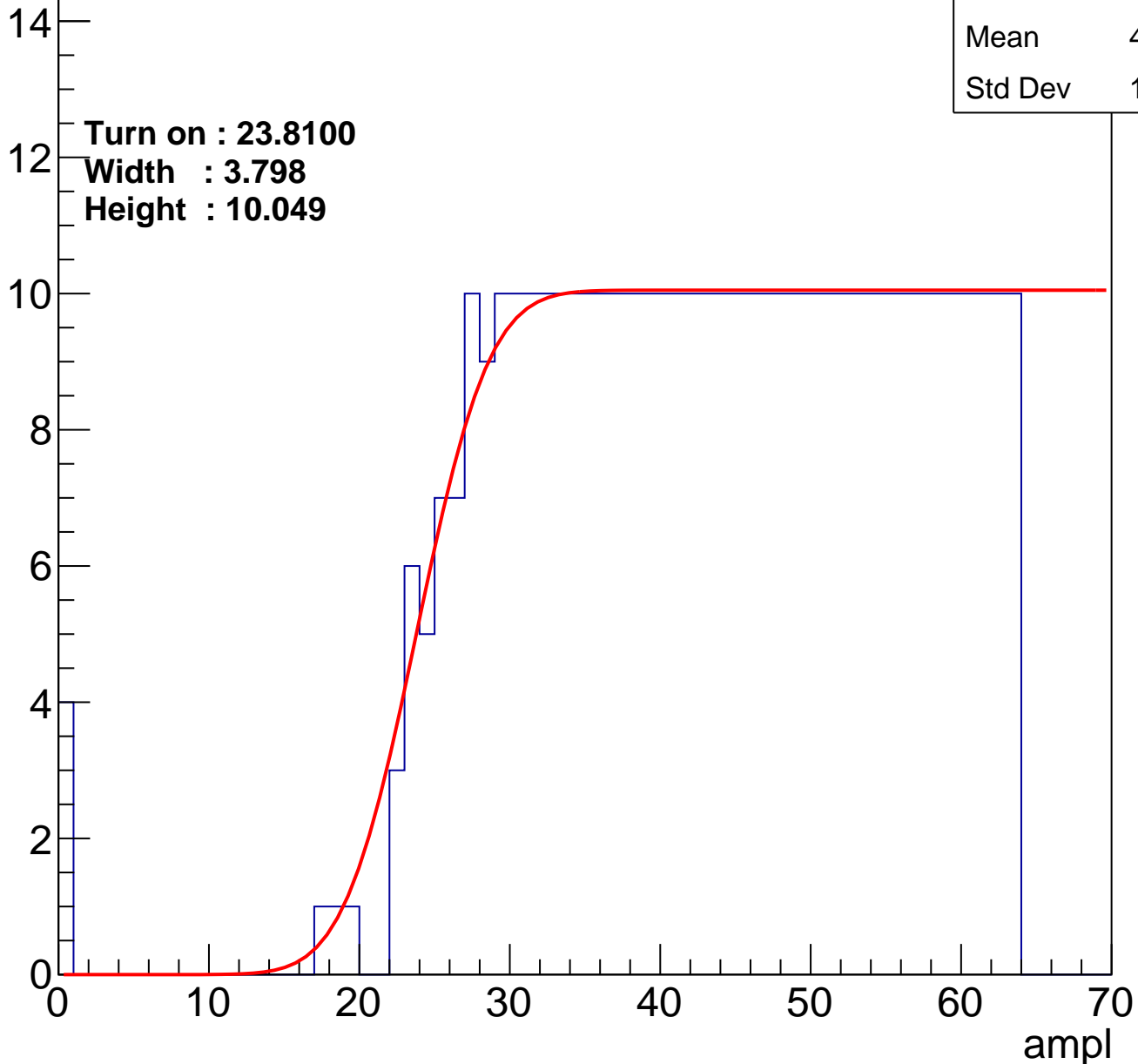
Entries	404
Mean	42.96
Std Dev	12.44

Turn on : 23.8100

Width : 3.798

Height : 10.049

Entry



B1L002S, U14-ch89

calib_packv5_042523_0143.root, FC#10, port B3

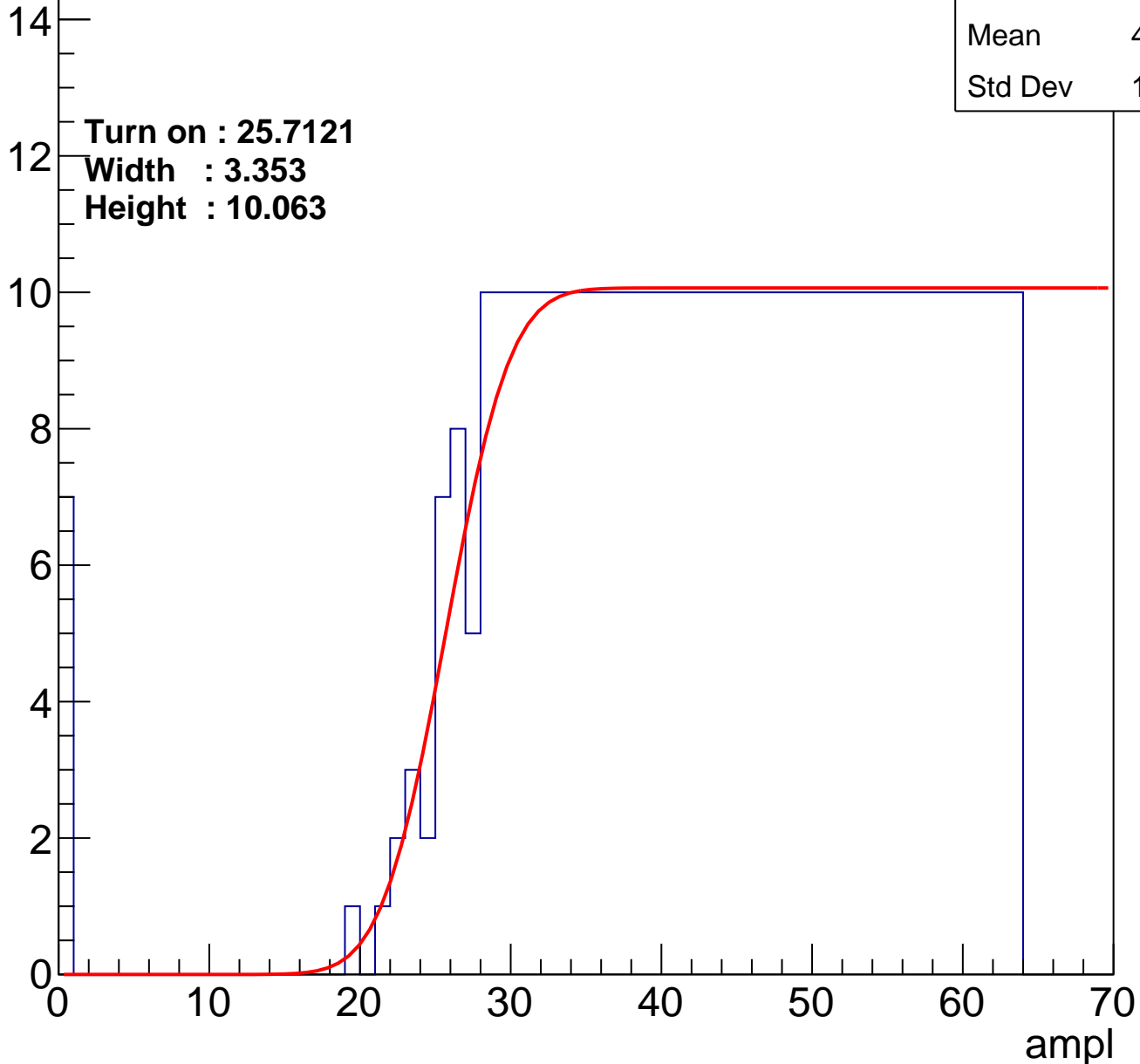
Entries	396
Mean	43.18
Std Dev	12.69

Turn on : 25.7121

Width : 3.353

Height : 10.063

Entry



B1L002S, U14-ch90

calib_packv5_042523_0143.root, FC#10, port B3

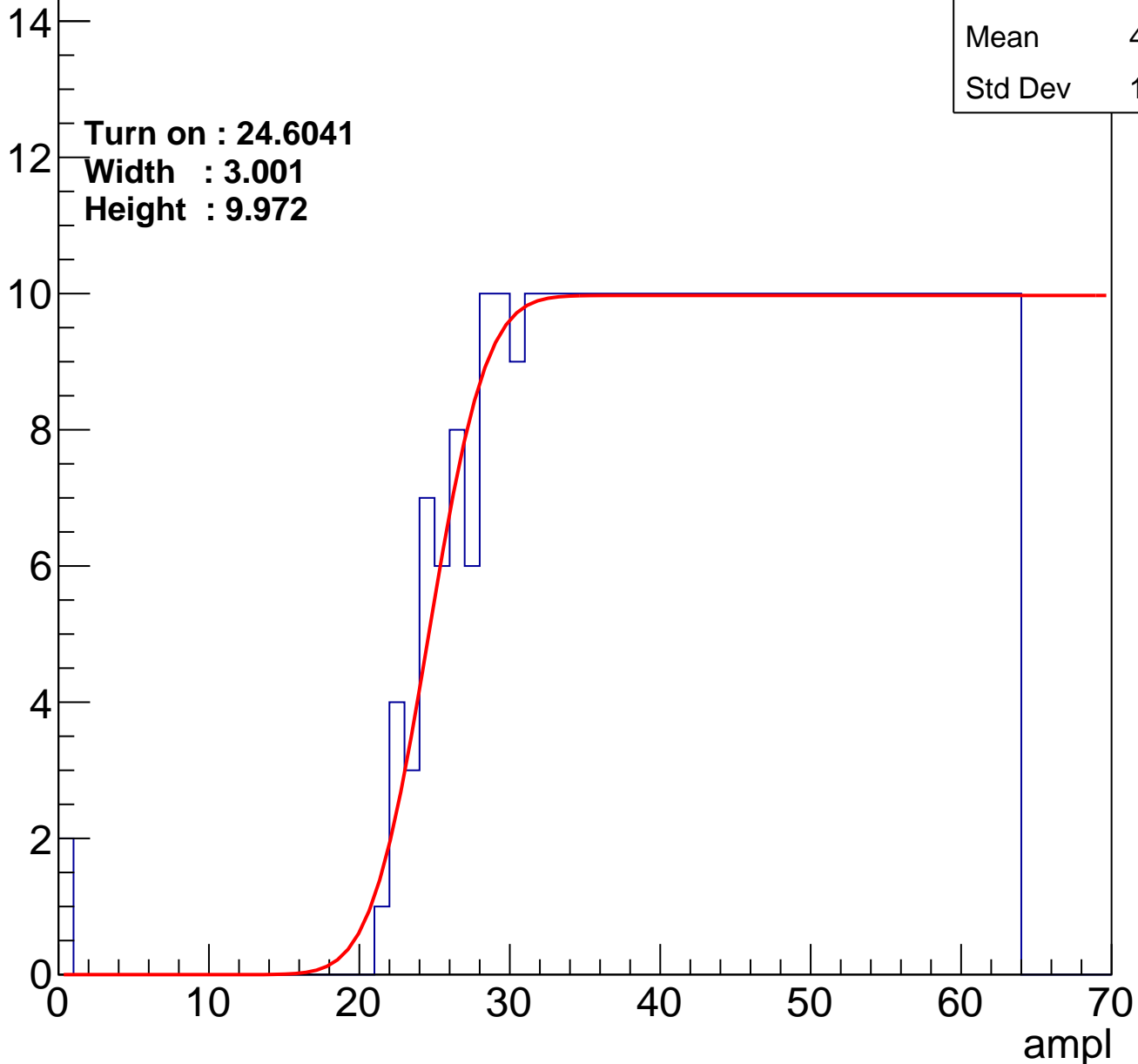
Entries	396
Mean	43.47
Std Dev	11.92

Turn on : 24.6041

Width : 3.001

Height : 9.972

Entry



B1L002S, U14-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.26
Std Dev	12.13

Turn on : 24.8652

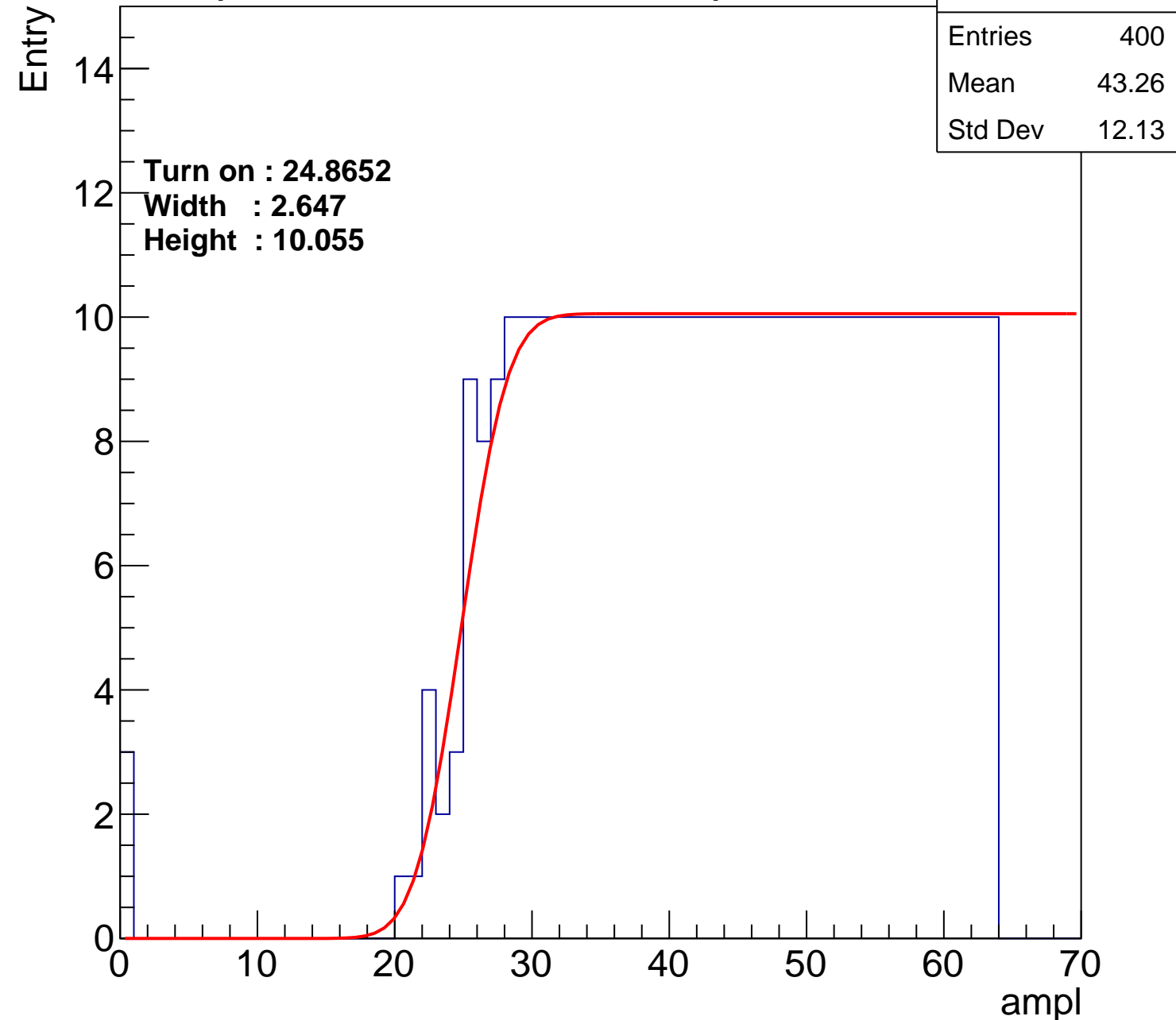
Width : 2.647

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch92

calib_packv5_042523_0143.root, FC#10, port B3

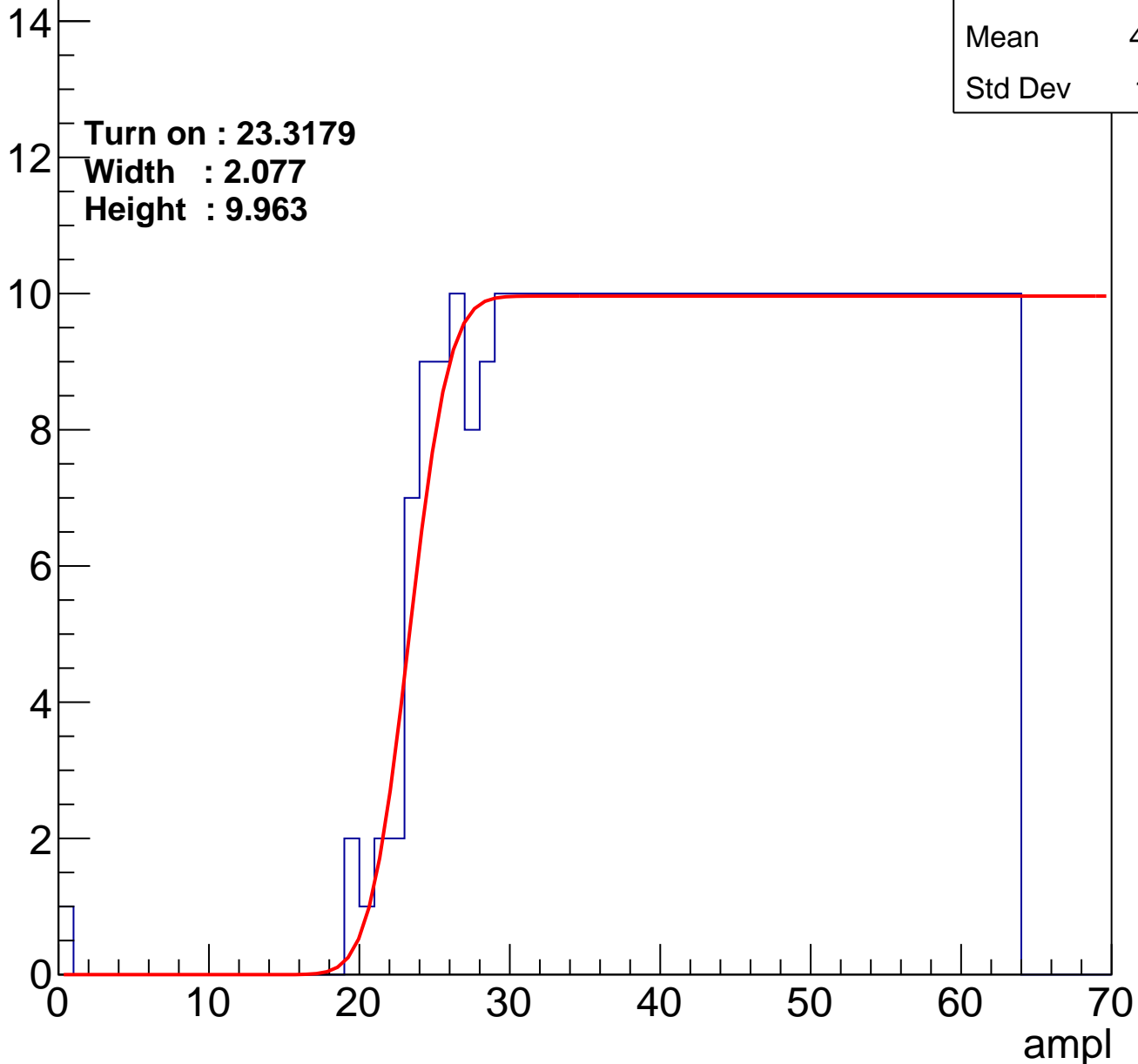
Entries	410
Mean	42.86
Std Dev	12.11

Turn on : 23.3179

Width : 2.077

Height : 9.963

Entry



B1L002S, U14-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.06
Std Dev	11.88

Turn on : 26.2300

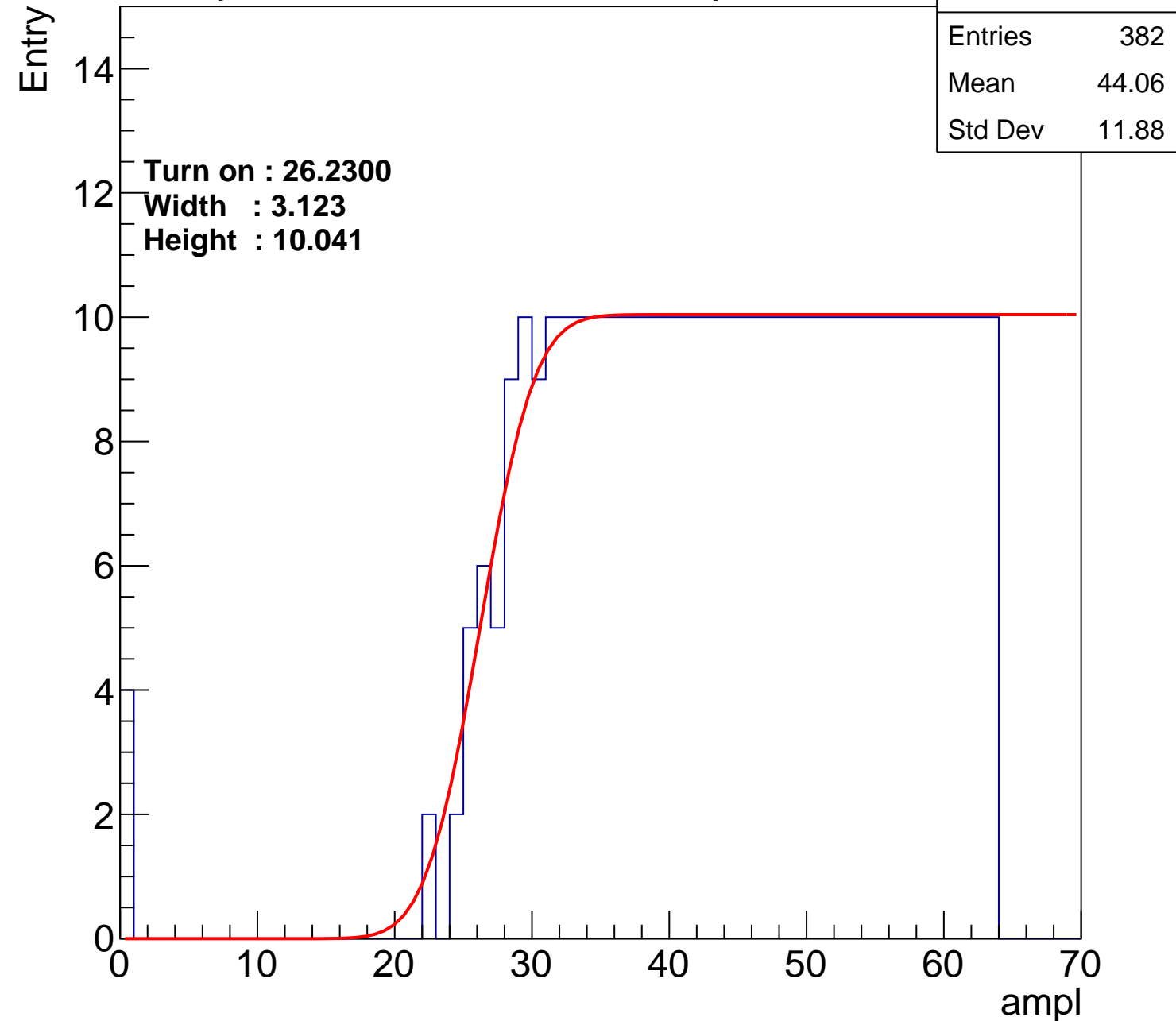
Width : 3.123

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch94

calib_packv5_042523_0143.root, FC#10, port B3

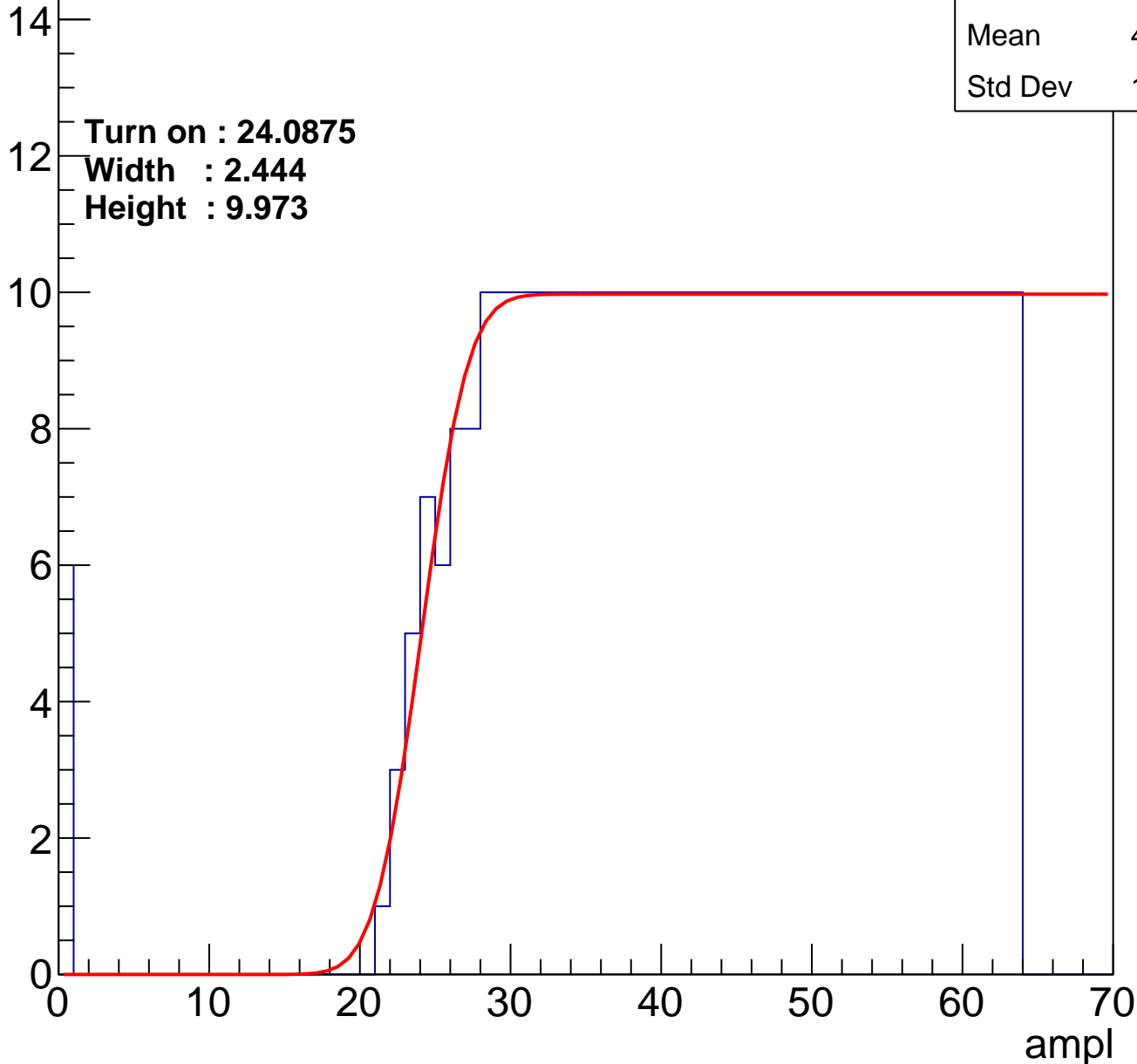
Entries	404
Mean	42.88
Std Dev	12.67

Turn on : 24.0875

Width : 2.444

Height : 9.973

Entry



B1L002S, U14-ch95

calib_packv5_042523_0143.root, FC#10, port B3

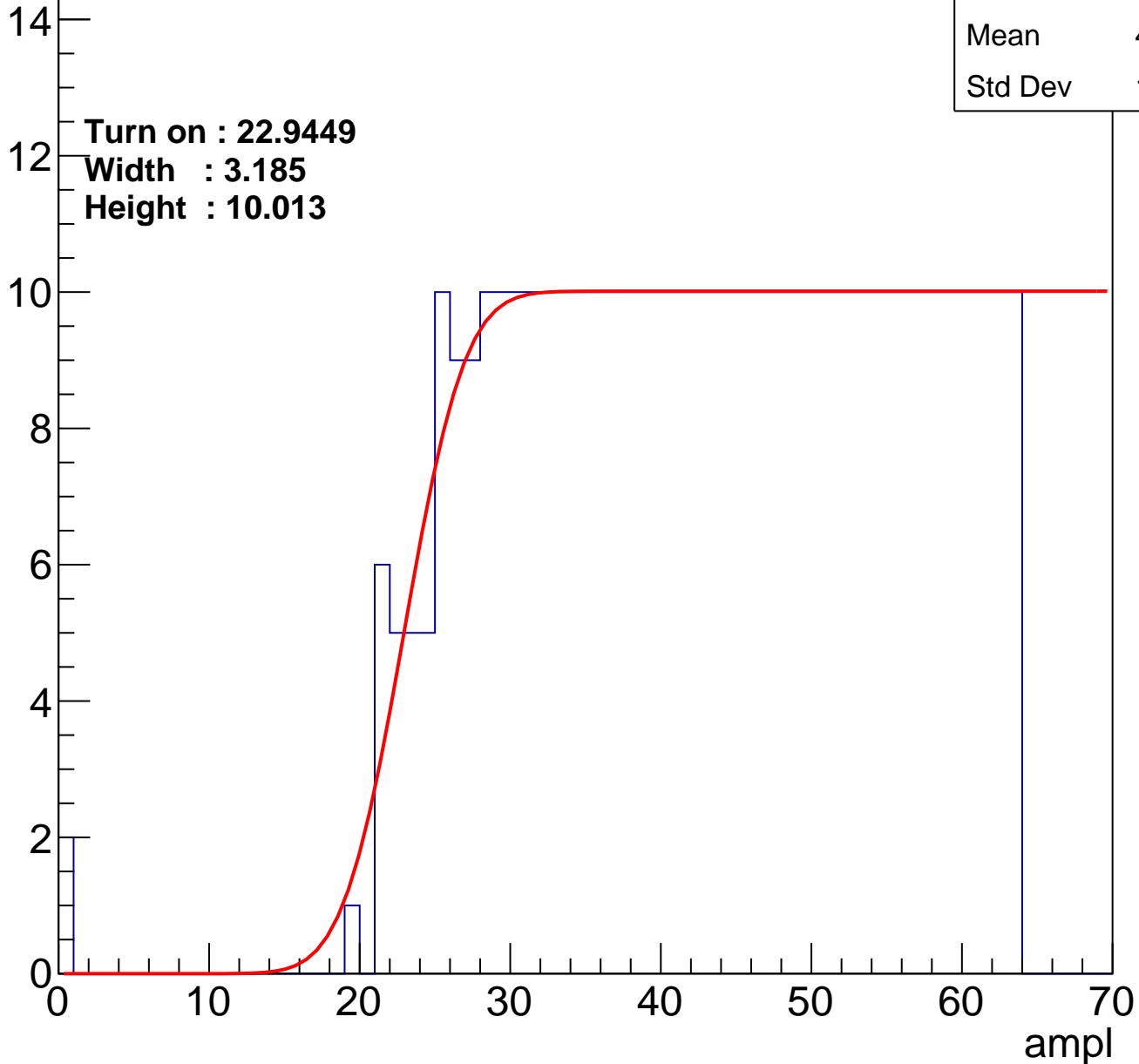
Entries	412
Mean	42.71
Std Dev	12.31

Turn on : 22.9449

Width : 3.185

Height : 10.013

Entry



B1L002S, U14-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.22
Std Dev	12.3

Turn on : 24.6344

Width : 3.314

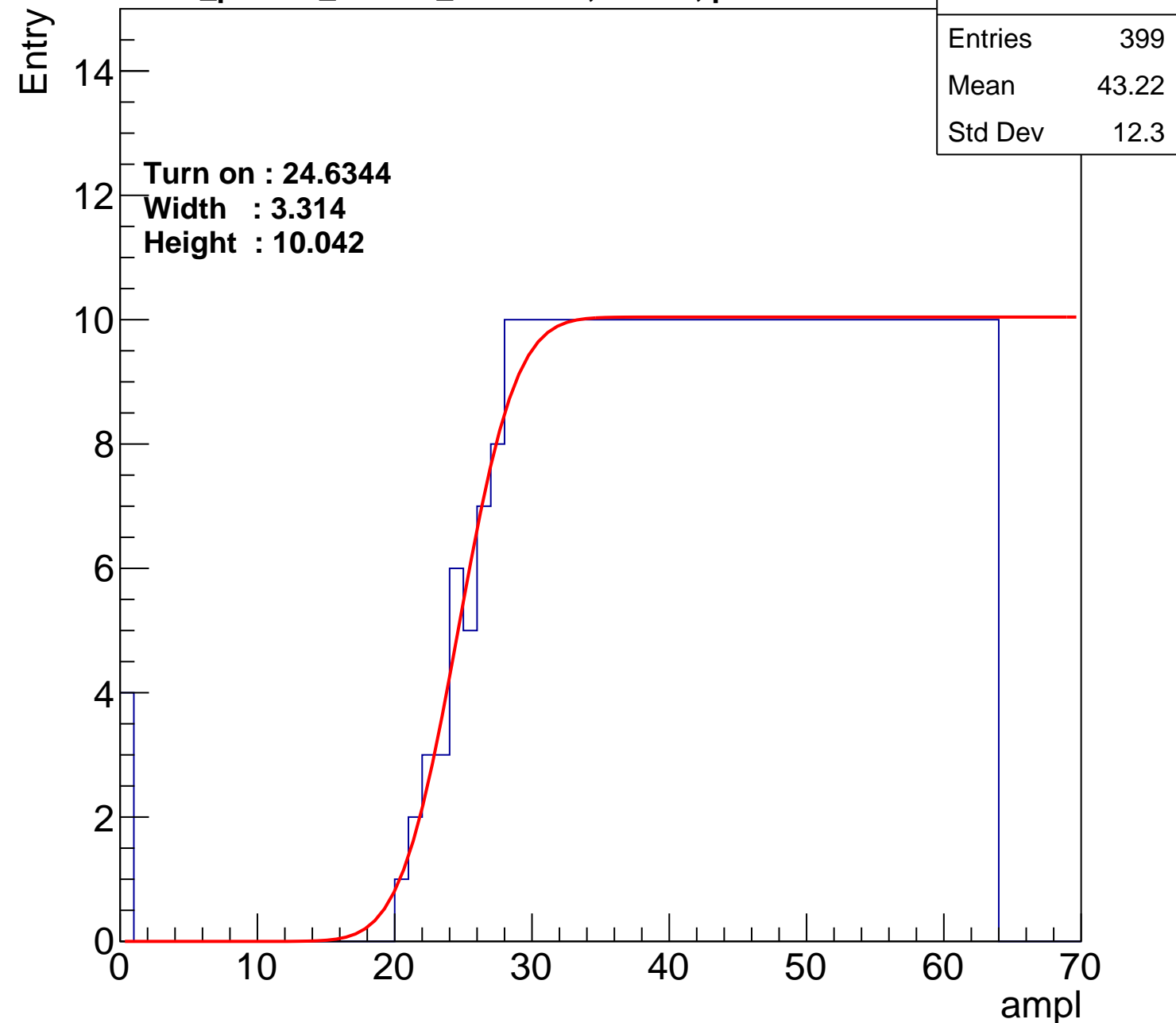
Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U14-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.8393

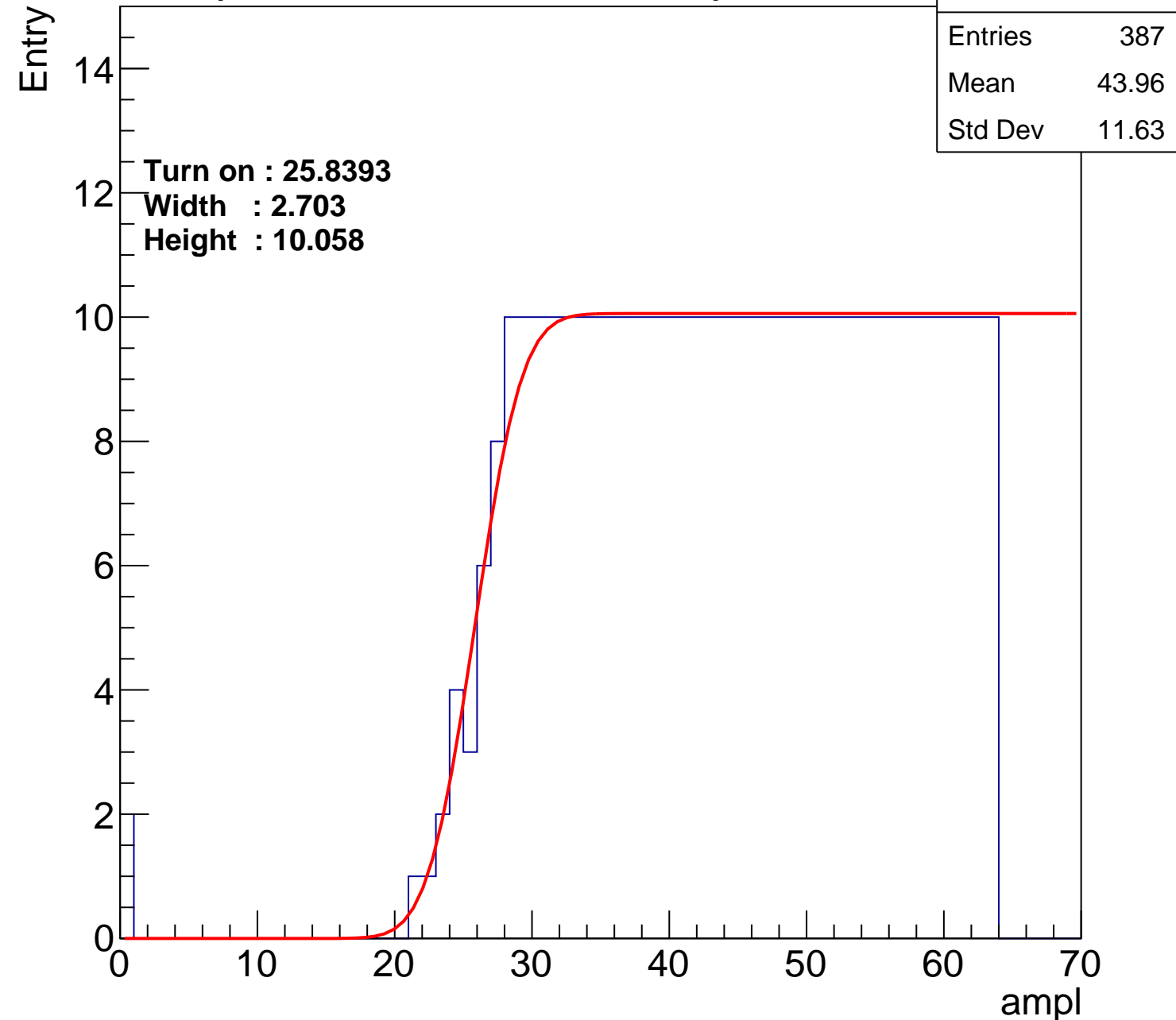
Width : 2.703

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.47
Std Dev	11.77

Turn on : 24.1189

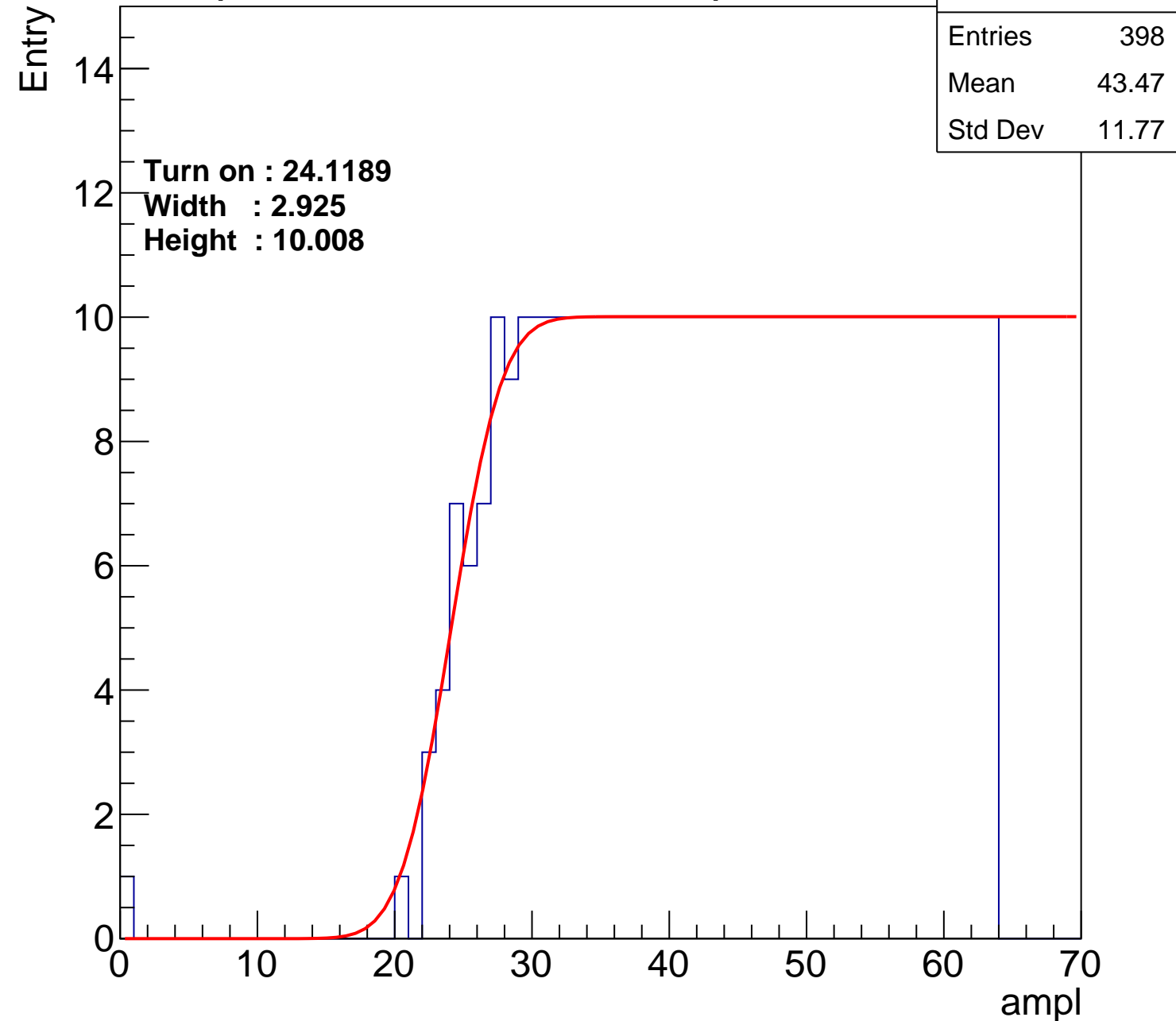
Width : 2.925

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch99

calib_packv5_042523_0143.root, FC#10, port B3

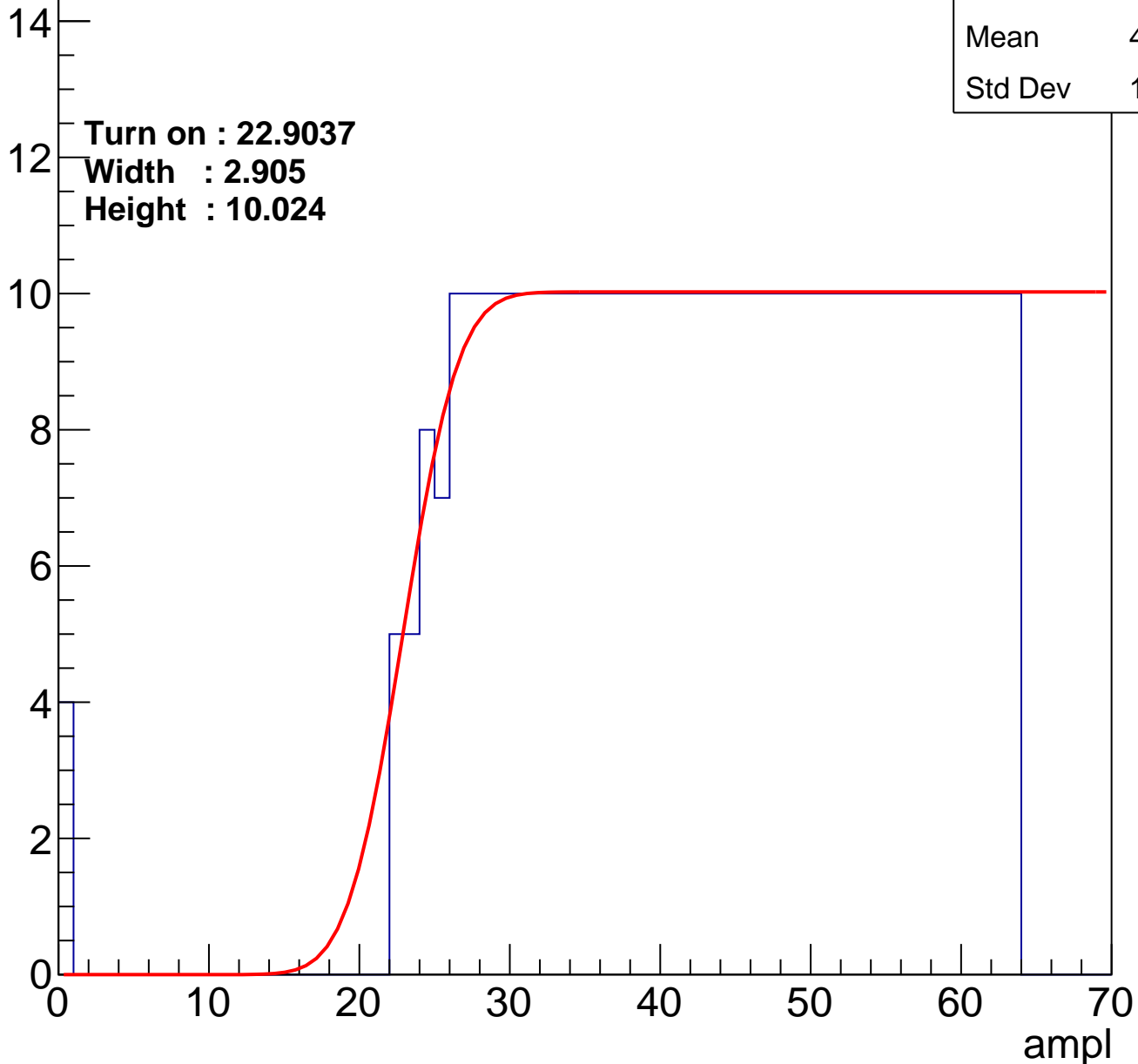
Entries	409
Mean	42.79
Std Dev	12.44

Turn on : 22.9037

Width : 2.905

Height : 10.024

Entry



B1L002S, U14-ch100

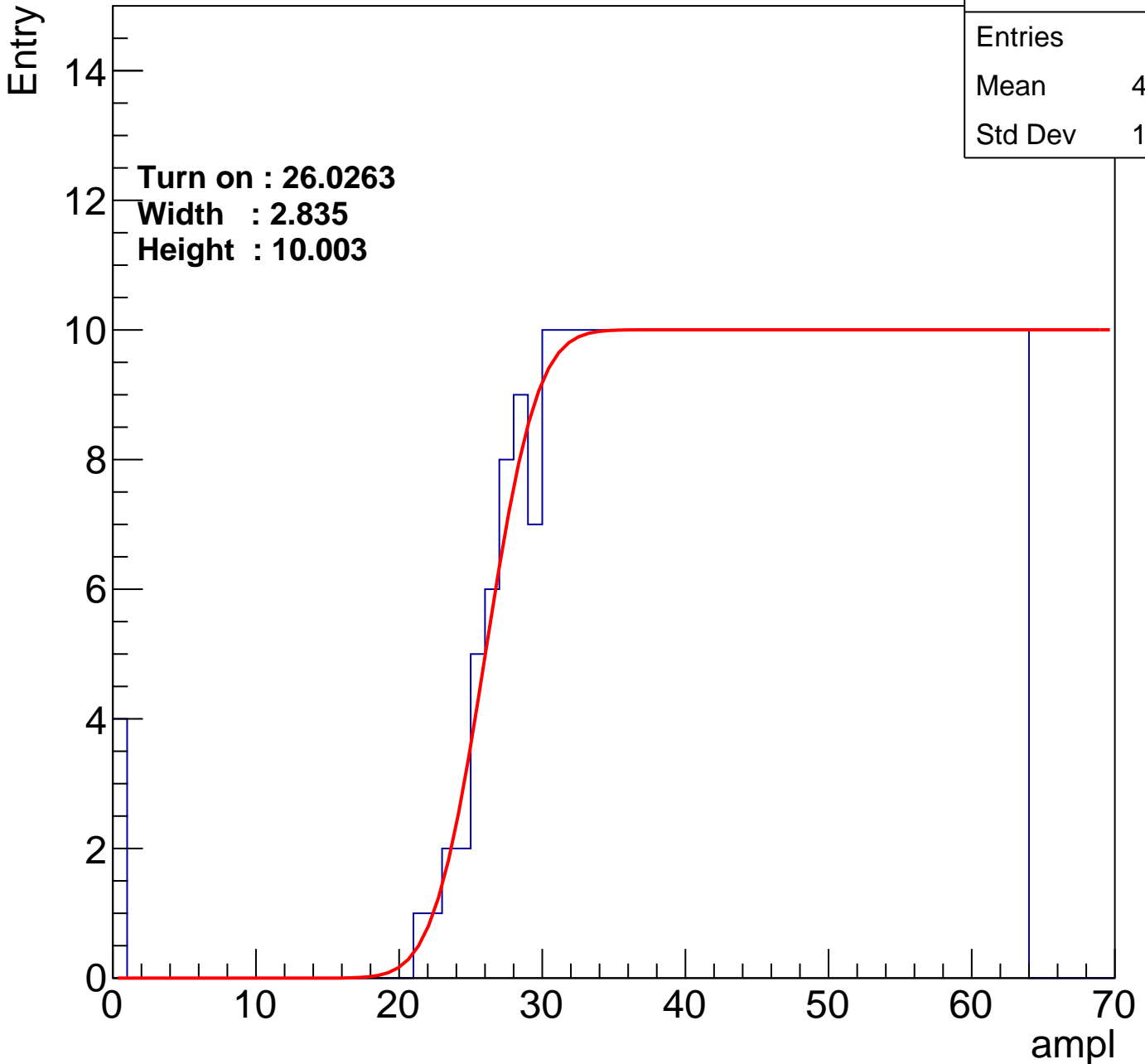
calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.89
Std Dev	11.98

Turn on : 26.0263

Width : 2.835

Height : 10.003



B1L002S, U14-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.7360

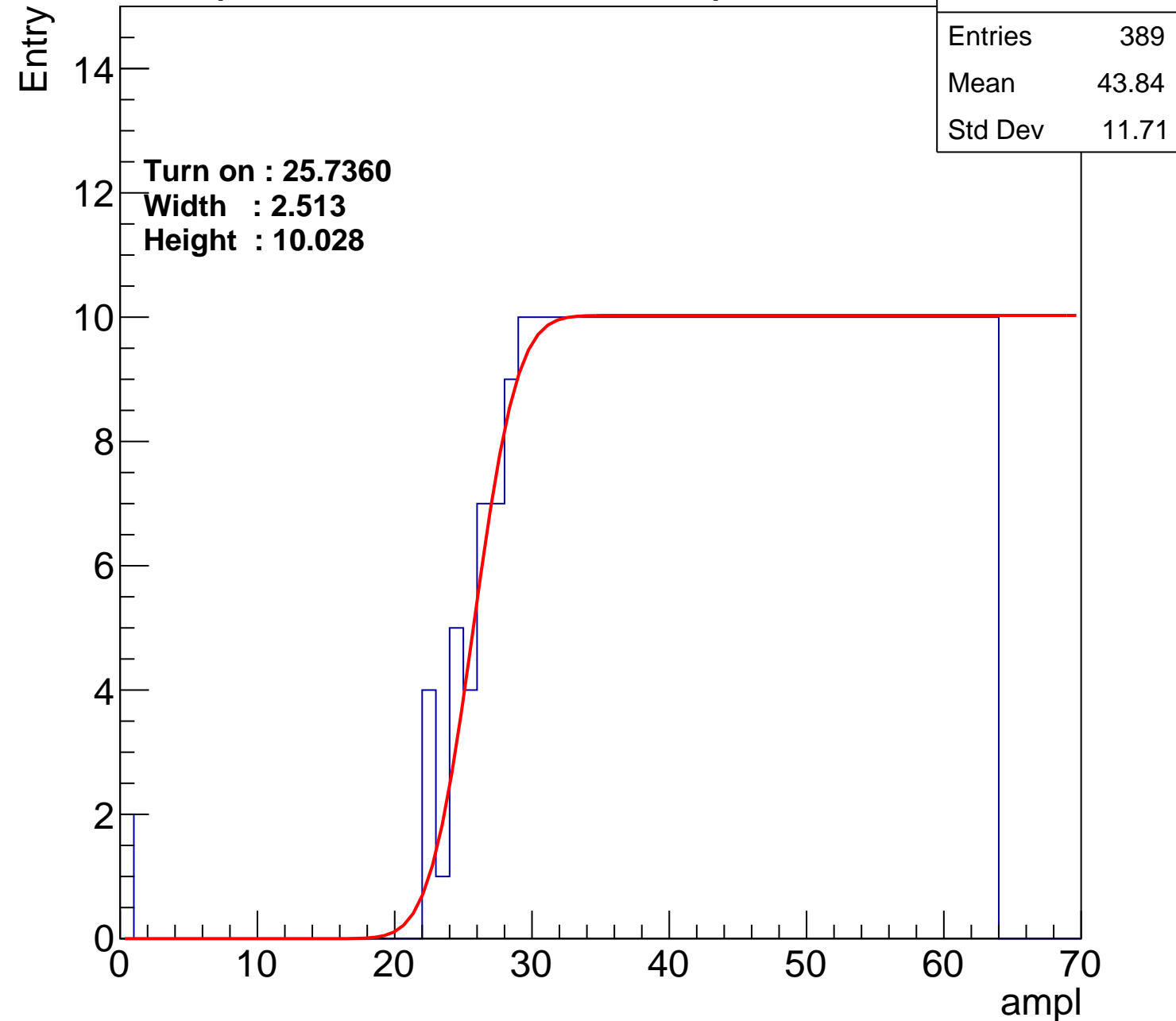
Width : 2.513

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.49
Std Dev	12.31

Turn on : 25.7651

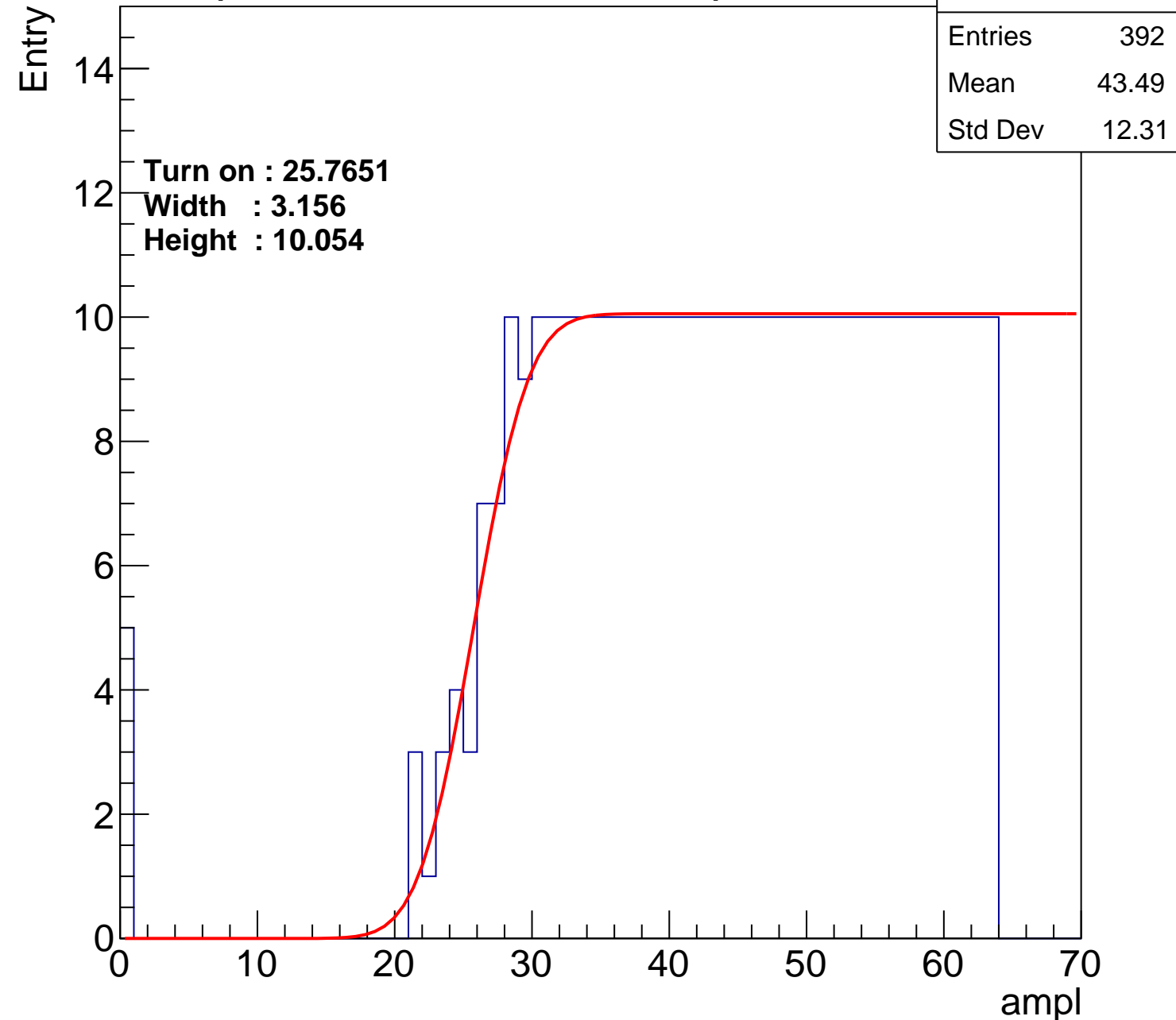
Width : 3.156

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch103

calib_packv5_042523_0143.root, FC#10, port B3

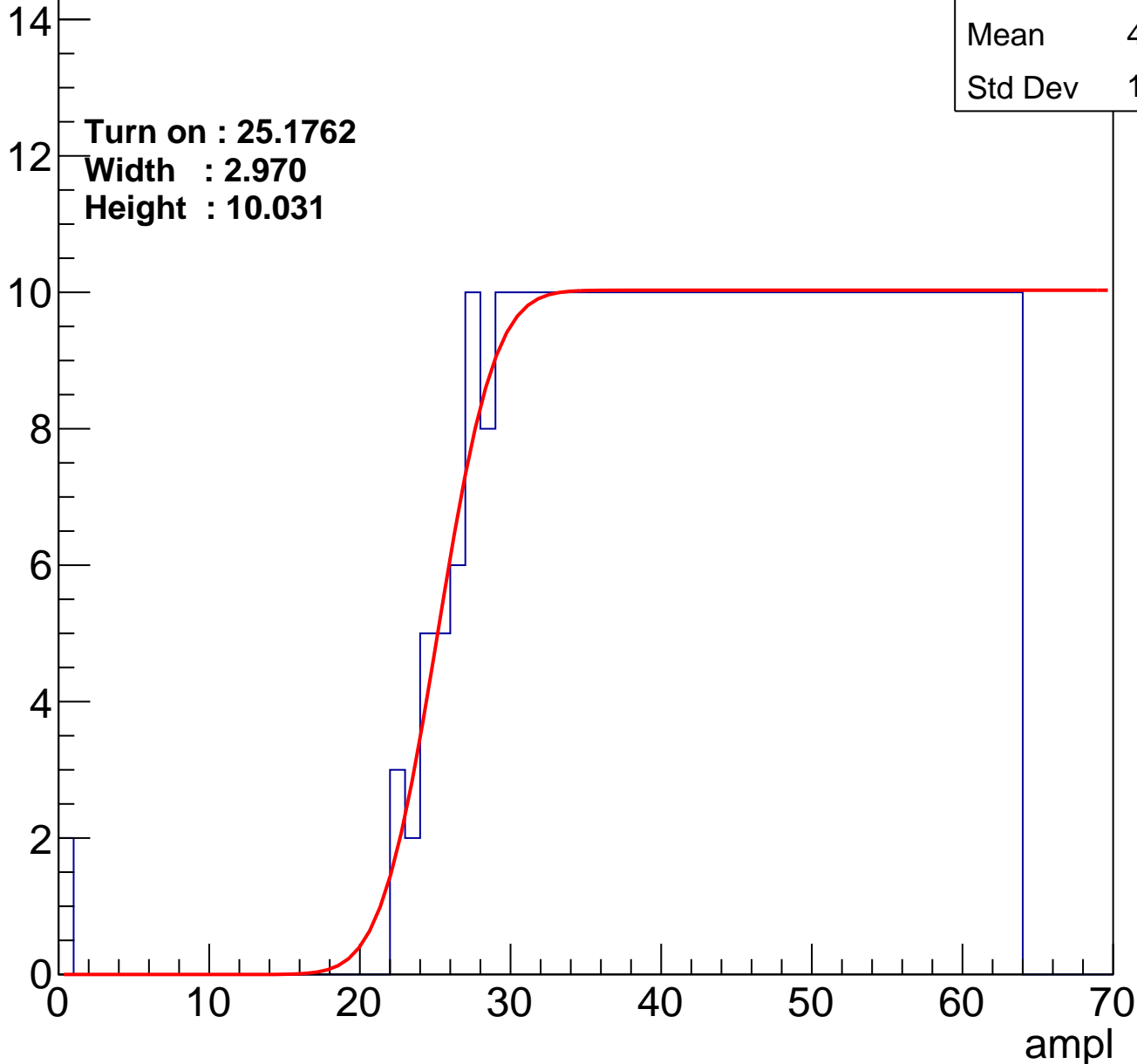
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.1762

Width : 2.970

Height : 10.031

Entry



B1L002S, U14-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.07
Std Dev	11.47

Turn on : 25.3771

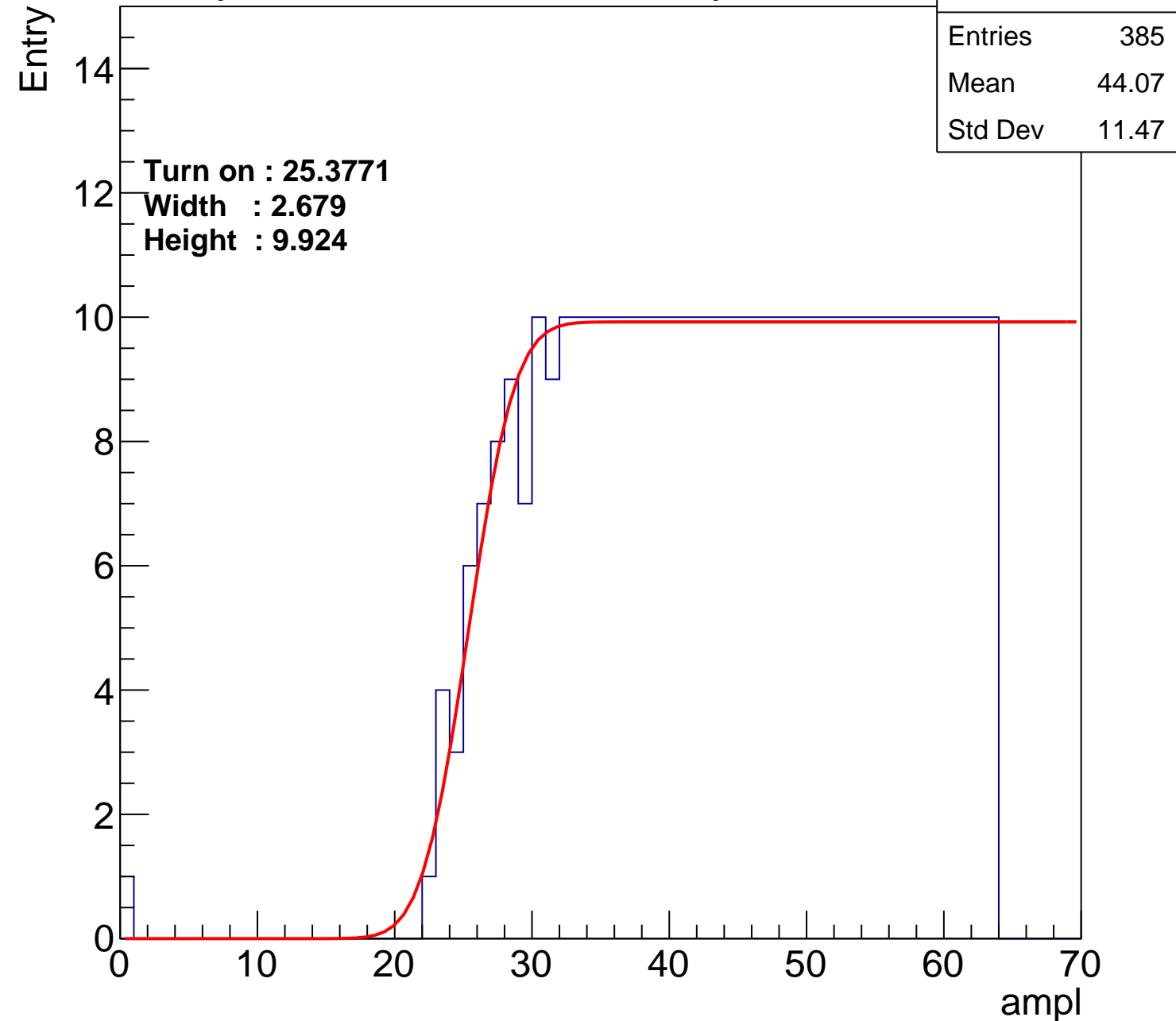
Width : 2.679

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.47
Std Dev	11.95

Turn on : 24.9891

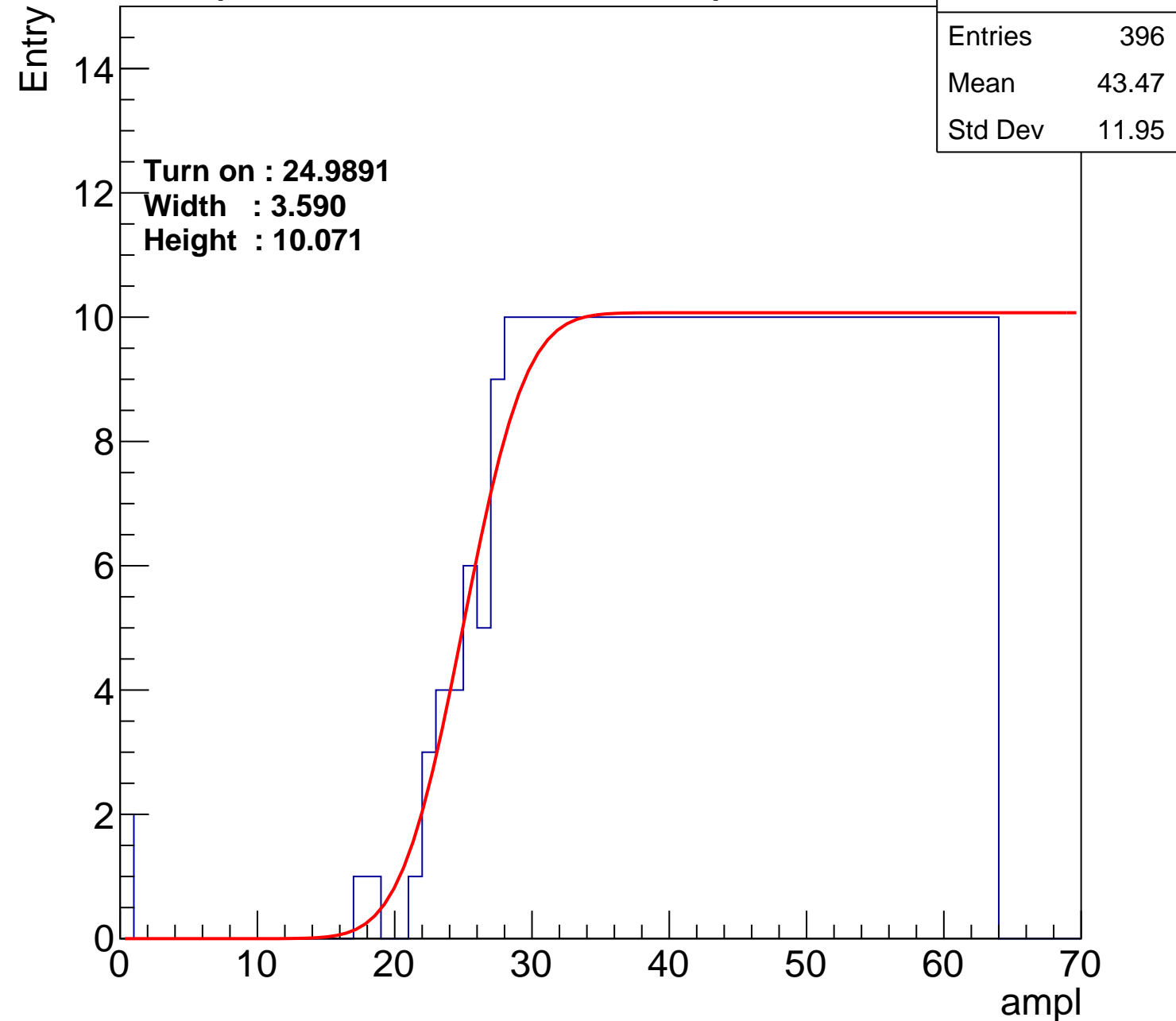
Width : 3.590

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.84
Std Dev	11.59

Turn on : 25.5791

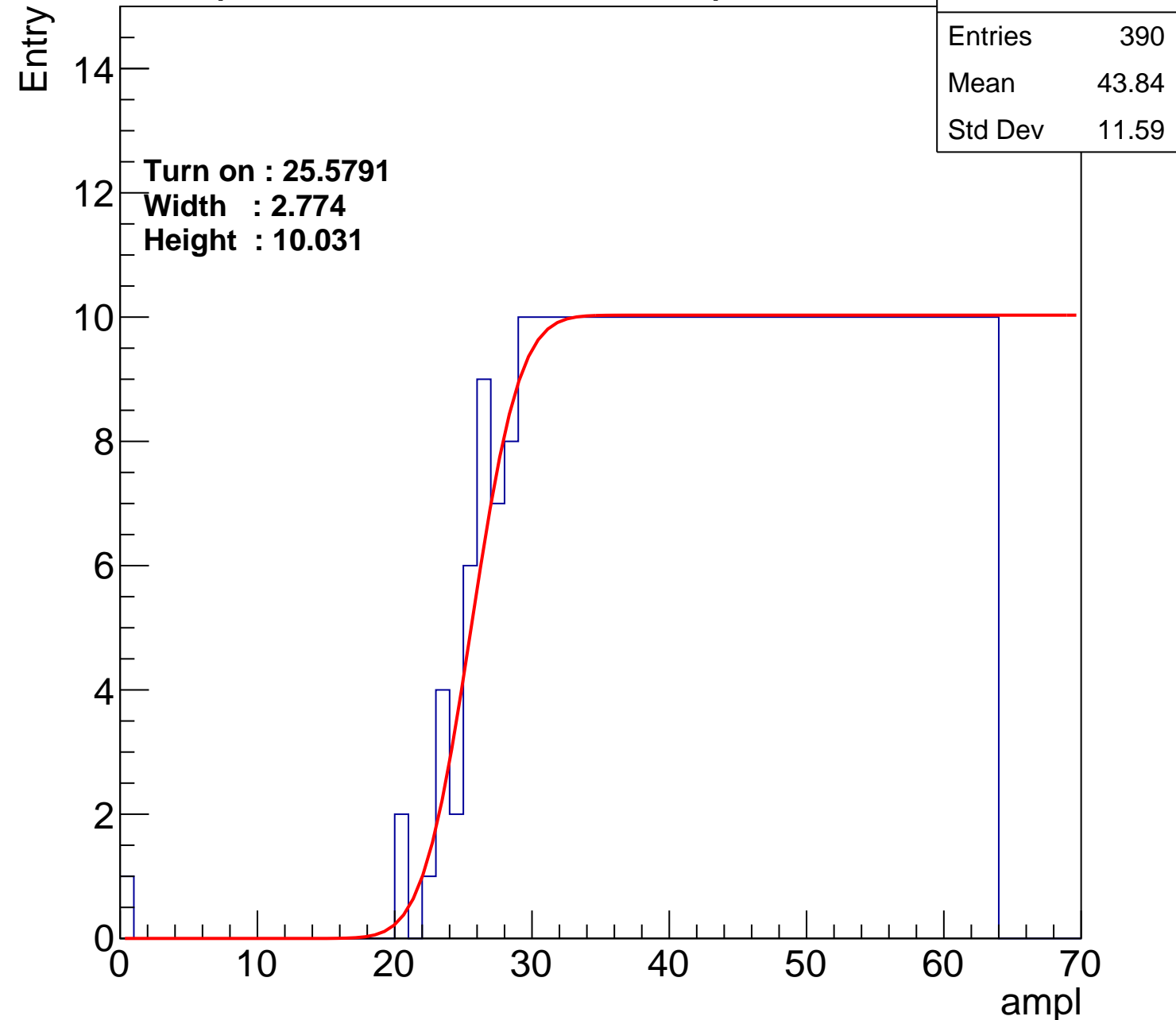
Width : 2.774

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch107

calib_packv5_042523_0143.root, FC#10, port B3

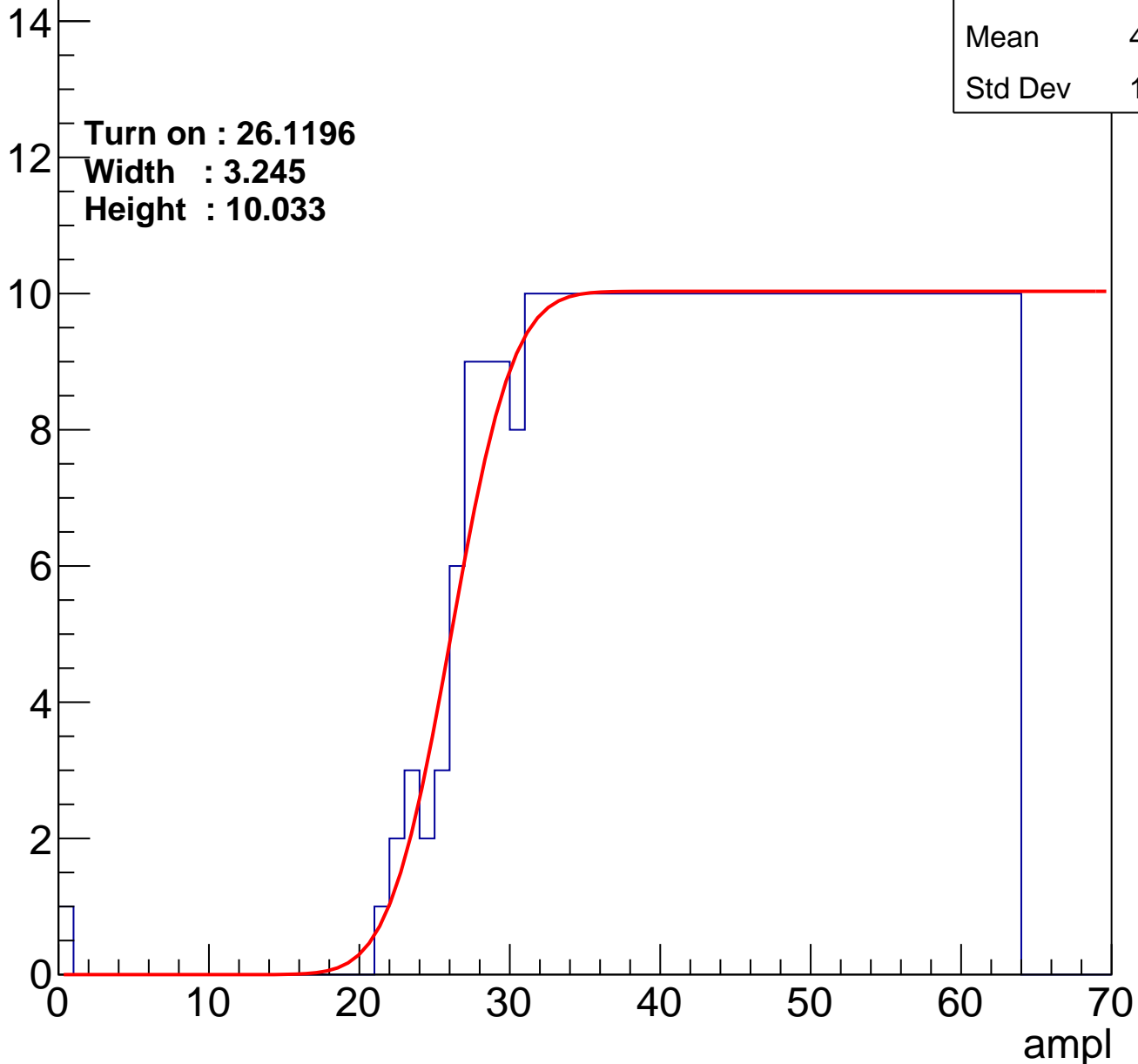
Entries	383
Mean	44.17
Std Dev	11.42

Turn on : 26.1196

Width : 3.245

Height : 10.033

Entry



B1L002S, U14-ch108

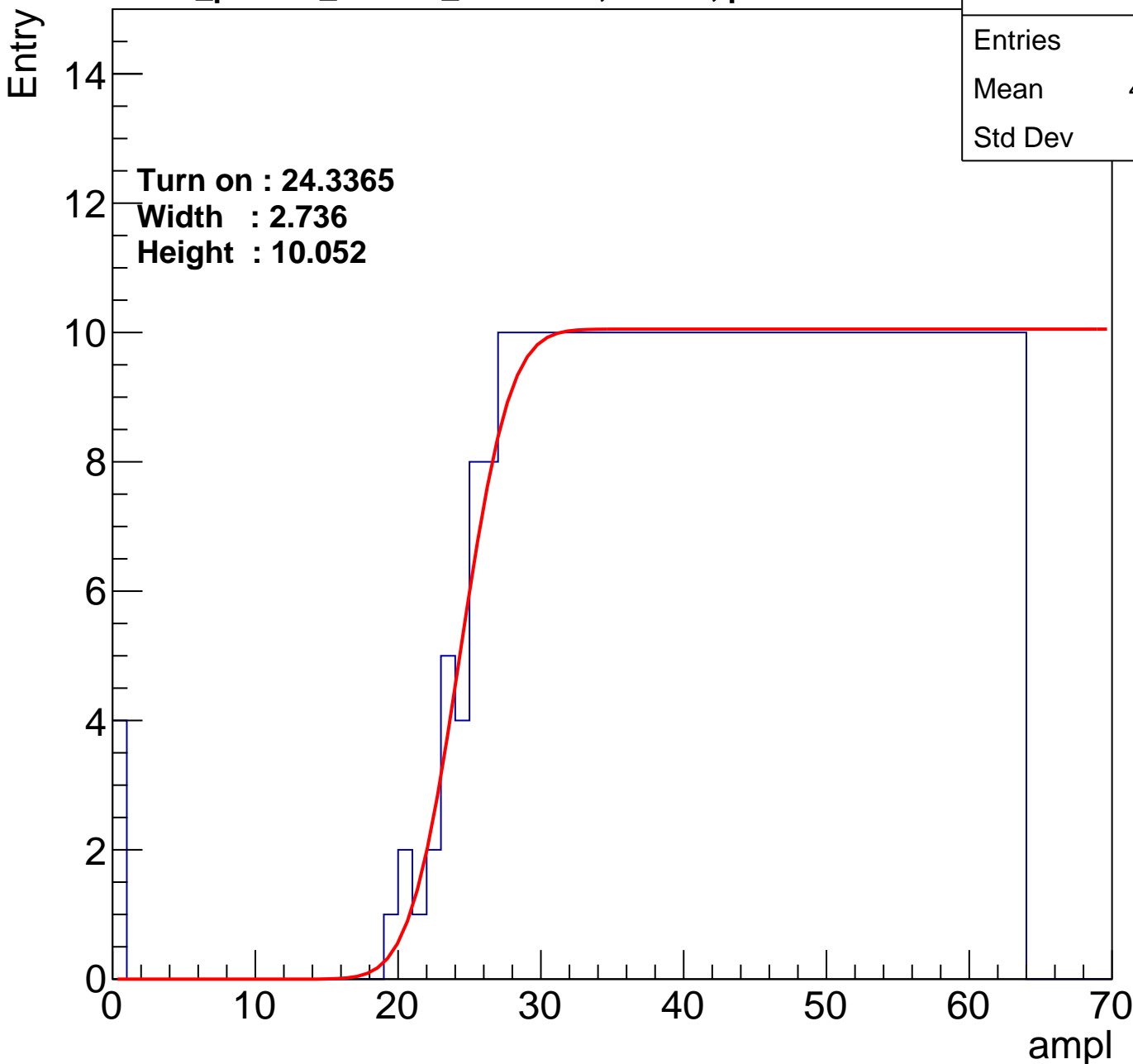
calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.95
Std Dev	12.41

Turn on : 24.3365

Width : 2.736

Height : 10.052



B1L002S, U14-ch109

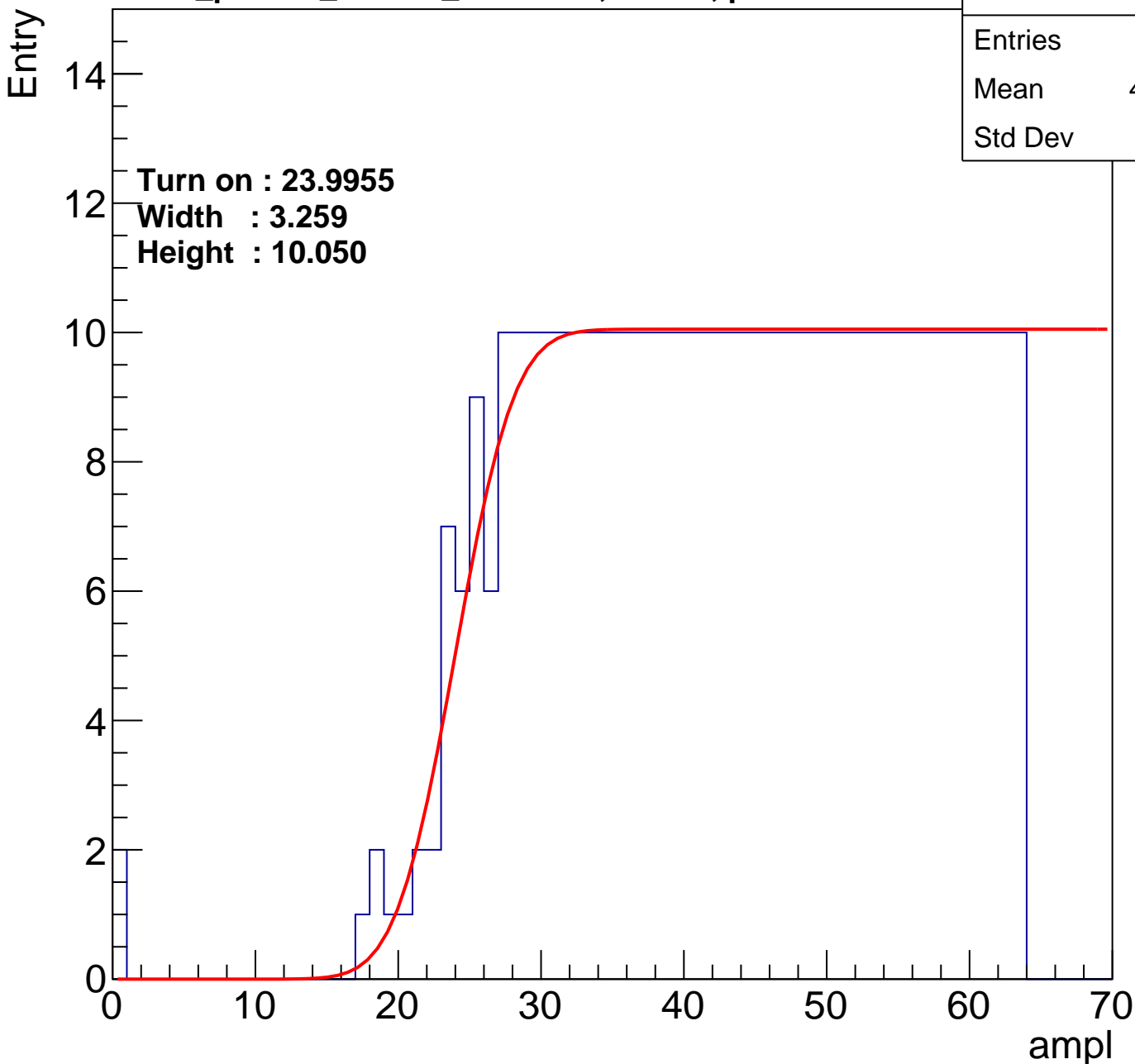
calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.82
Std Dev	12.3

Turn on : 23.9955

Width : 3.259

Height : 10.050



B1L002S, U14-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.81
Std Dev	11.71

Turn on : 24.9603

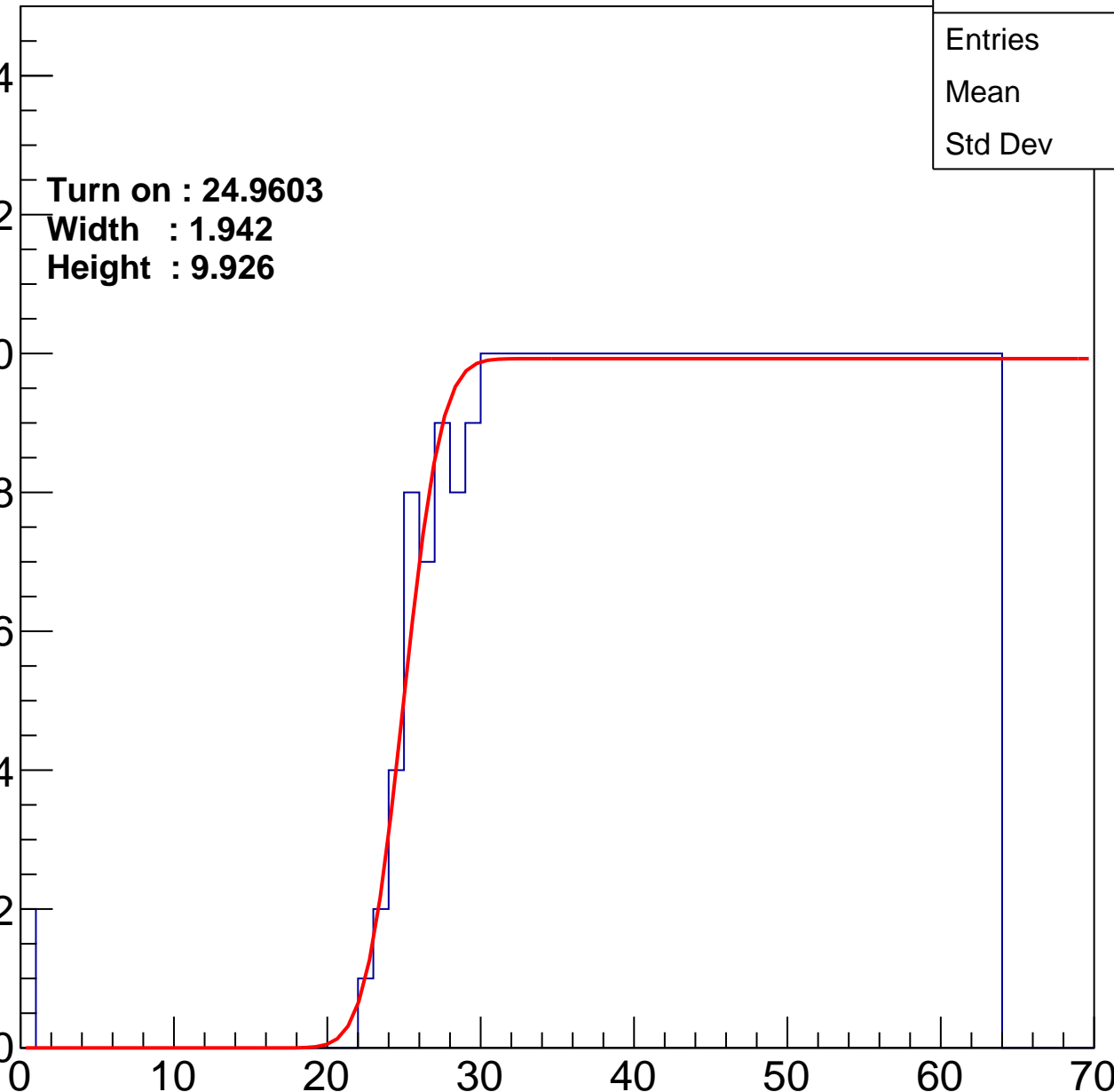
Width : 1.942

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.87
Std Dev	11.9

Turn on : 26.0710

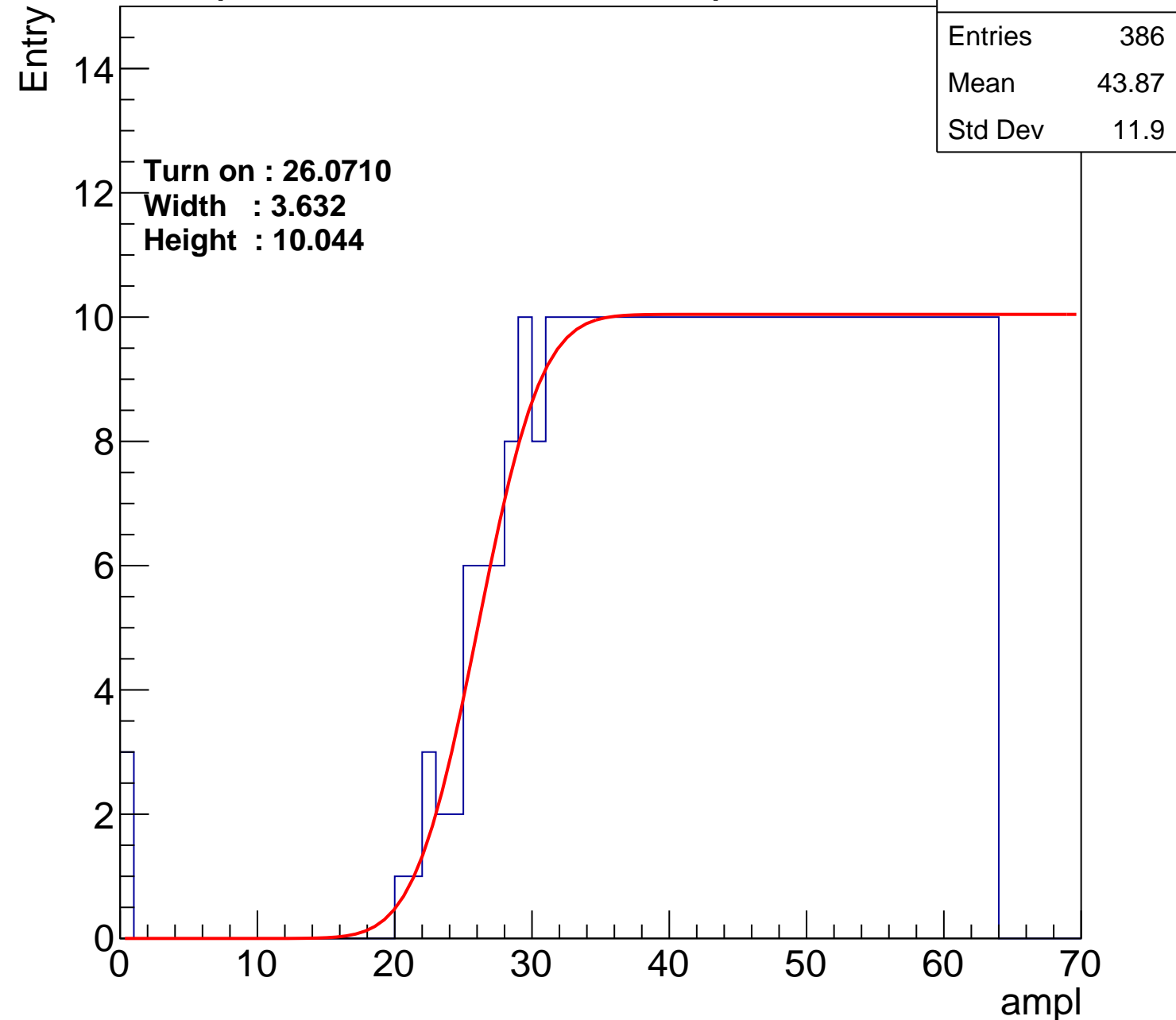
Width : 3.632

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
---------	-----

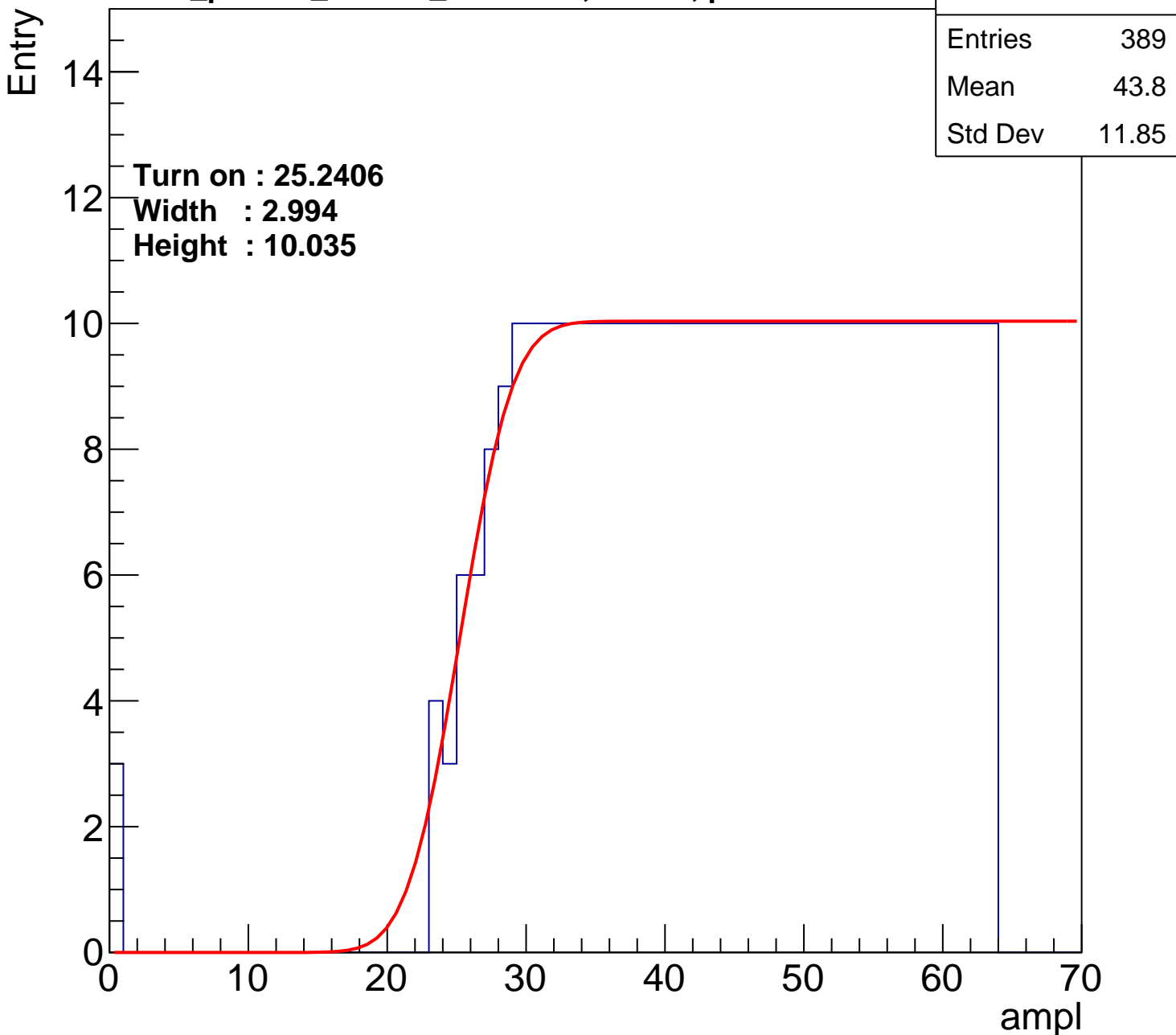
Mean	43.8
------	------

Std Dev	11.85
---------	-------

Turn on : 25.2406

Width : 2.994

Height : 10.035



B1L002S, U14-ch113

calib_packv5_042523_0143.root, FC#10, port B3

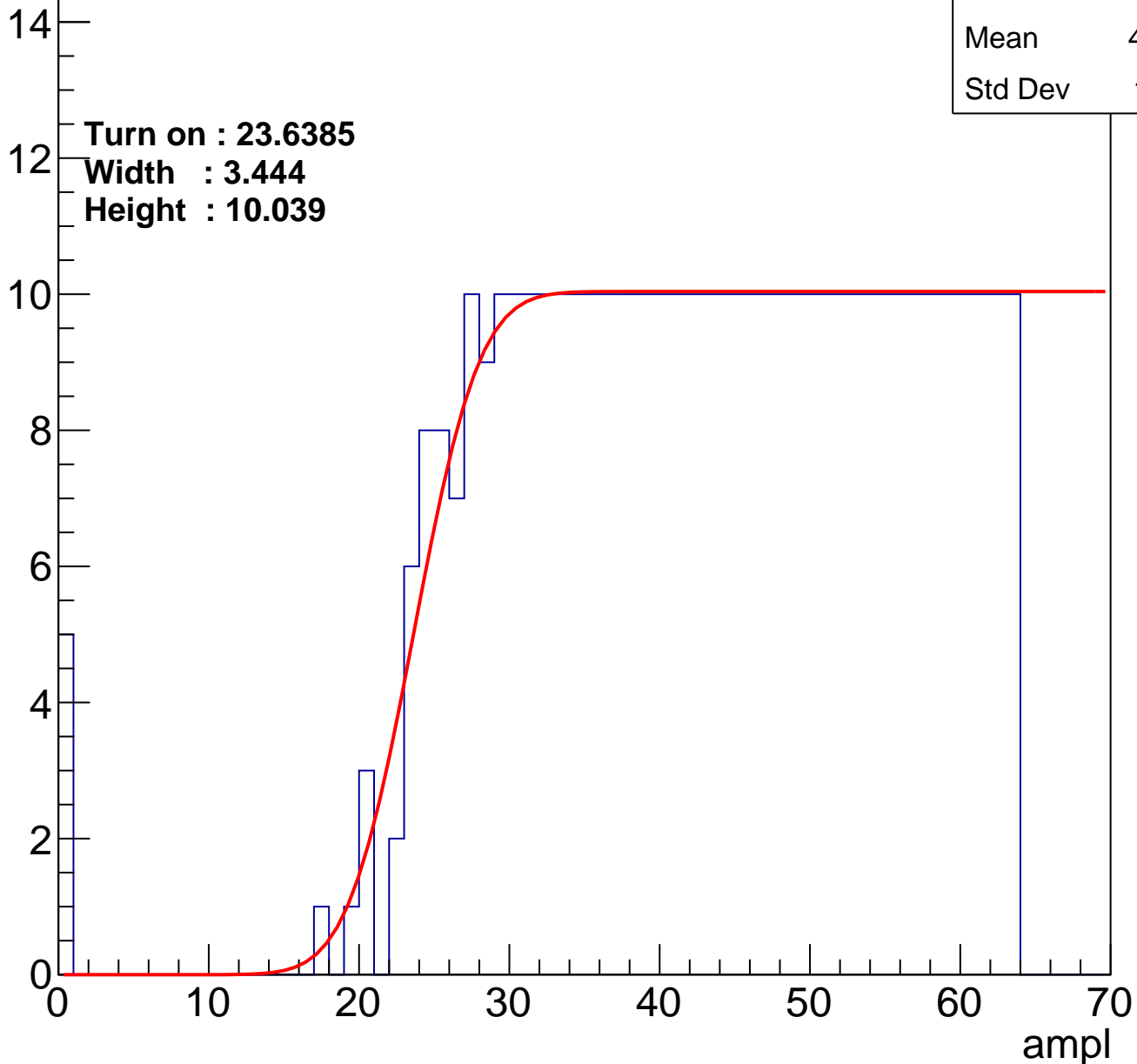
Entries	410
Mean	42.62
Std Dev	12.71

Turn on : 23.6385

Width : 3.444

Height : 10.039

Entry



B1L002S, U14-ch114

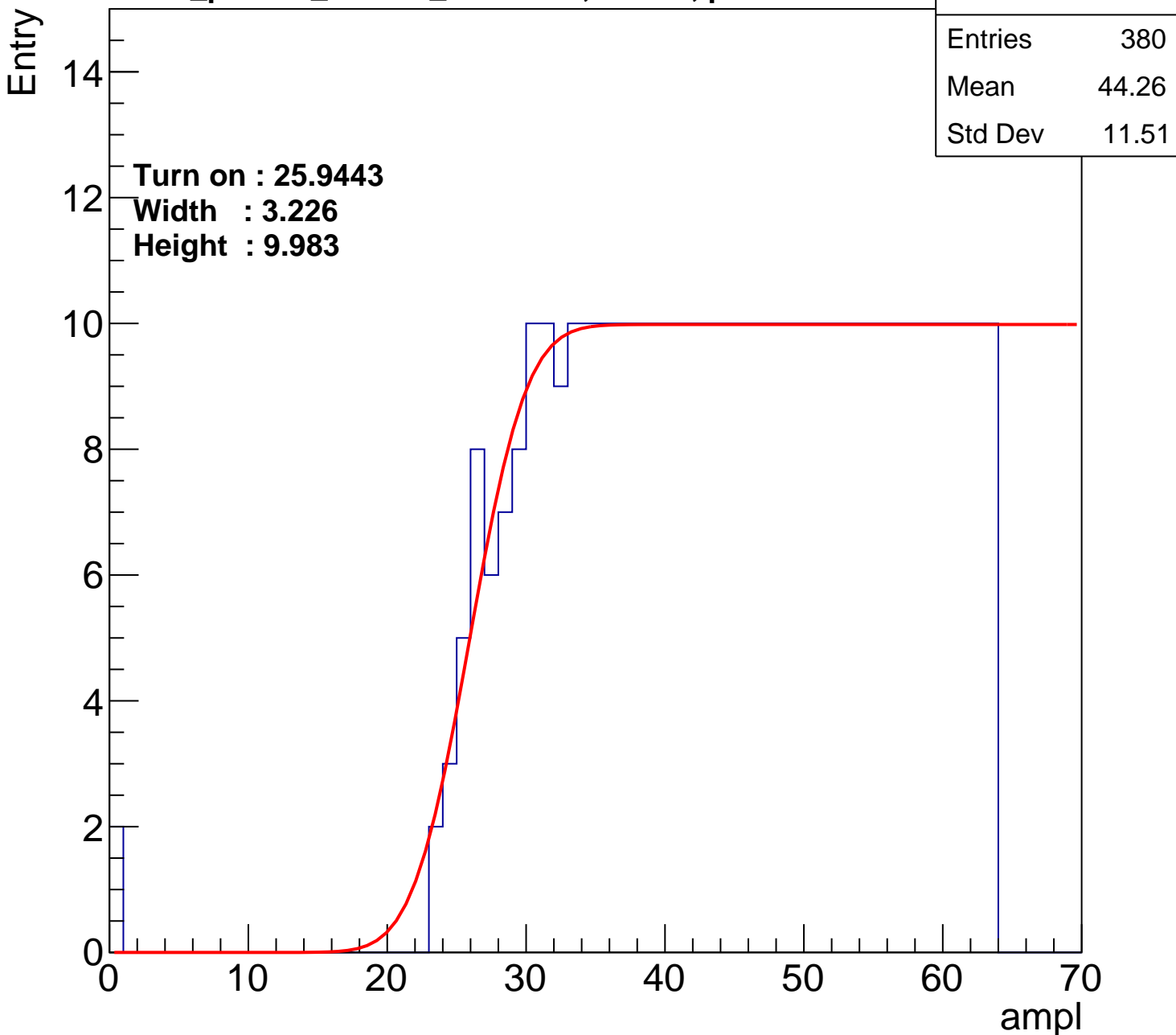
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.9443

Width : 3.226

Height : 9.983

Entries	380
Mean	44.26
Std Dev	11.51



B1L002S, U14-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.88
Std Dev	12.56

Turn on : 24.4655

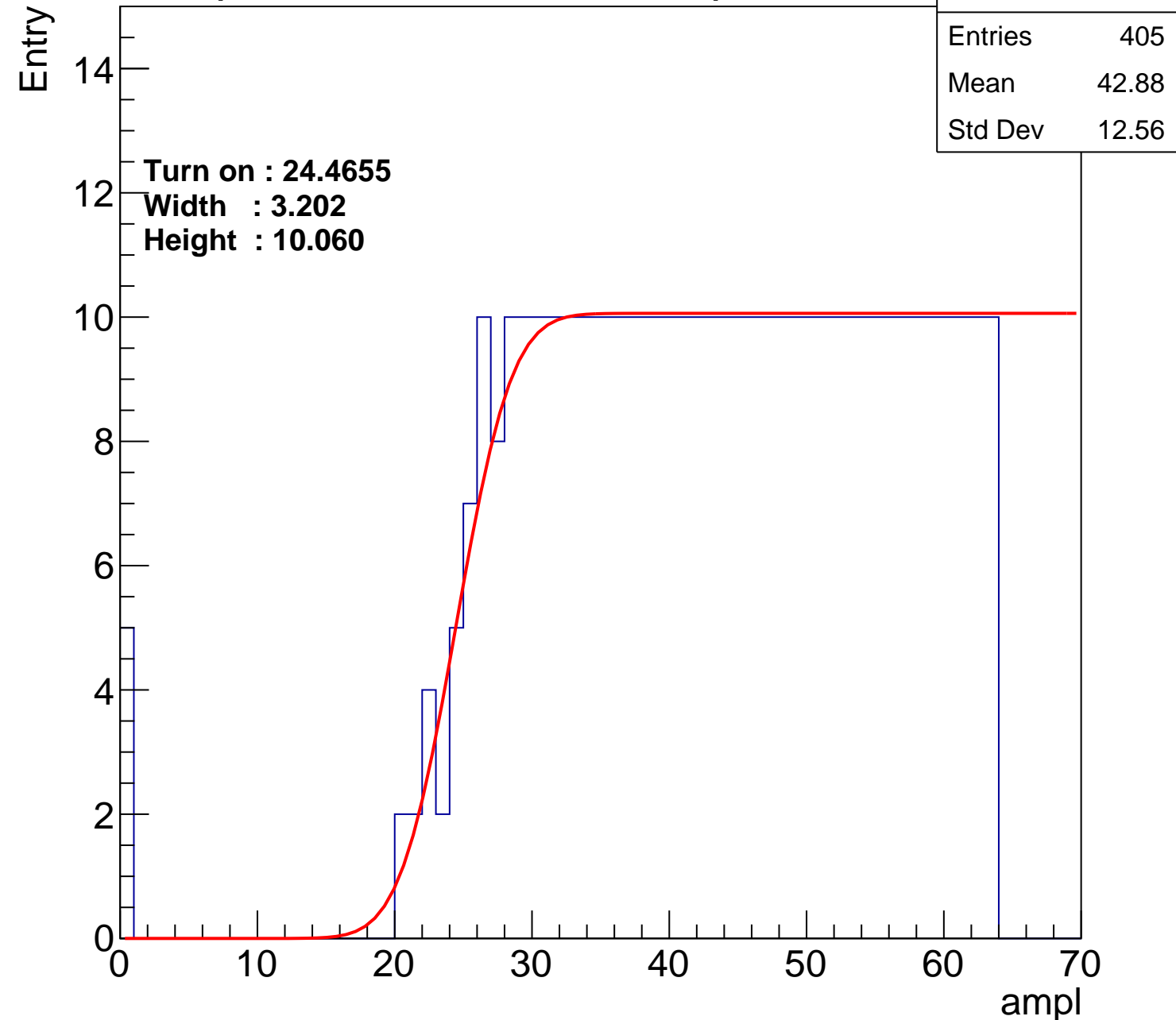
Width : 3.202

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.6
Std Dev	11.96

Turn on : 24.9821

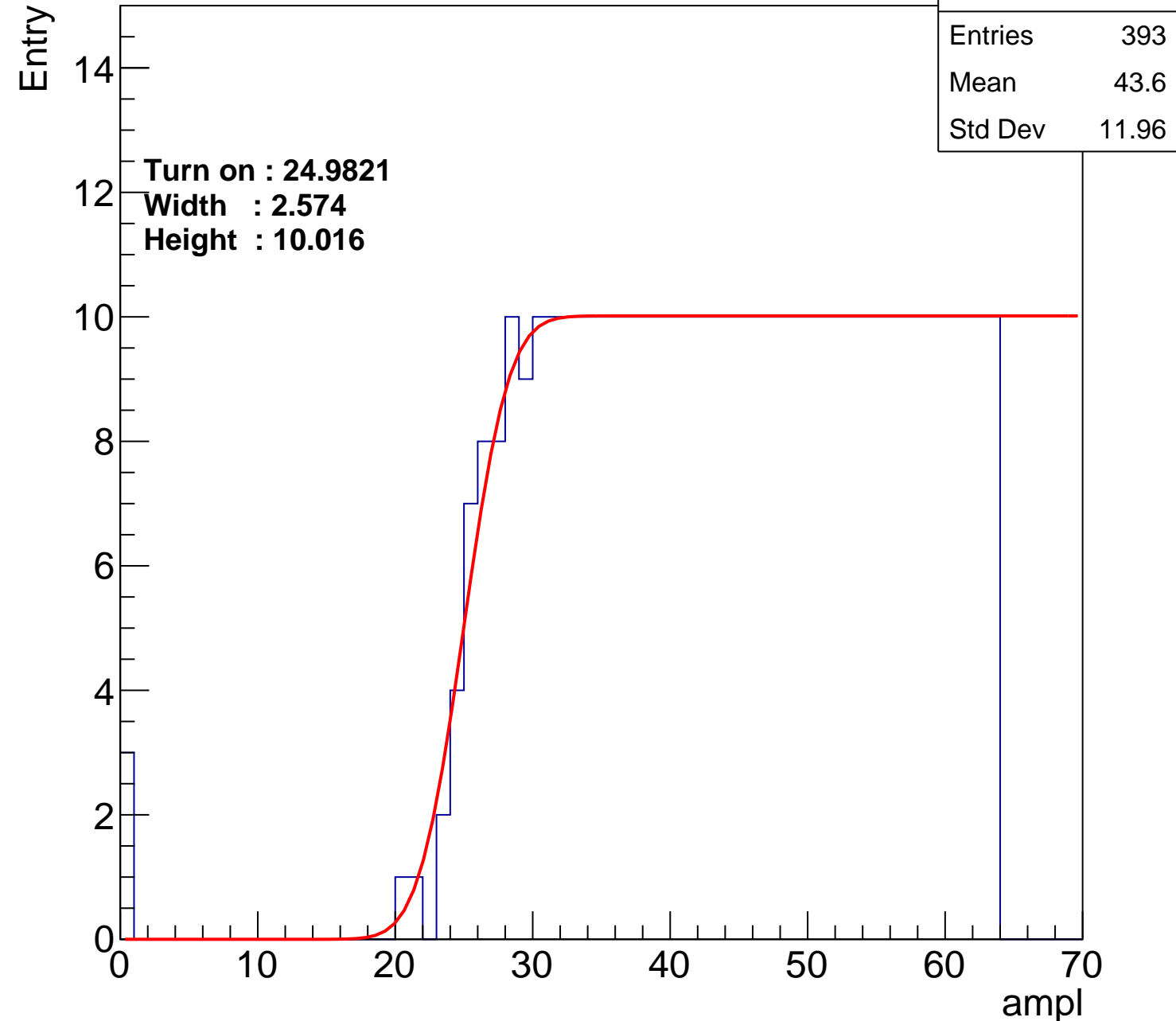
Width : 2.574

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.05
Std Dev	12.35

Turn on : 24.1243

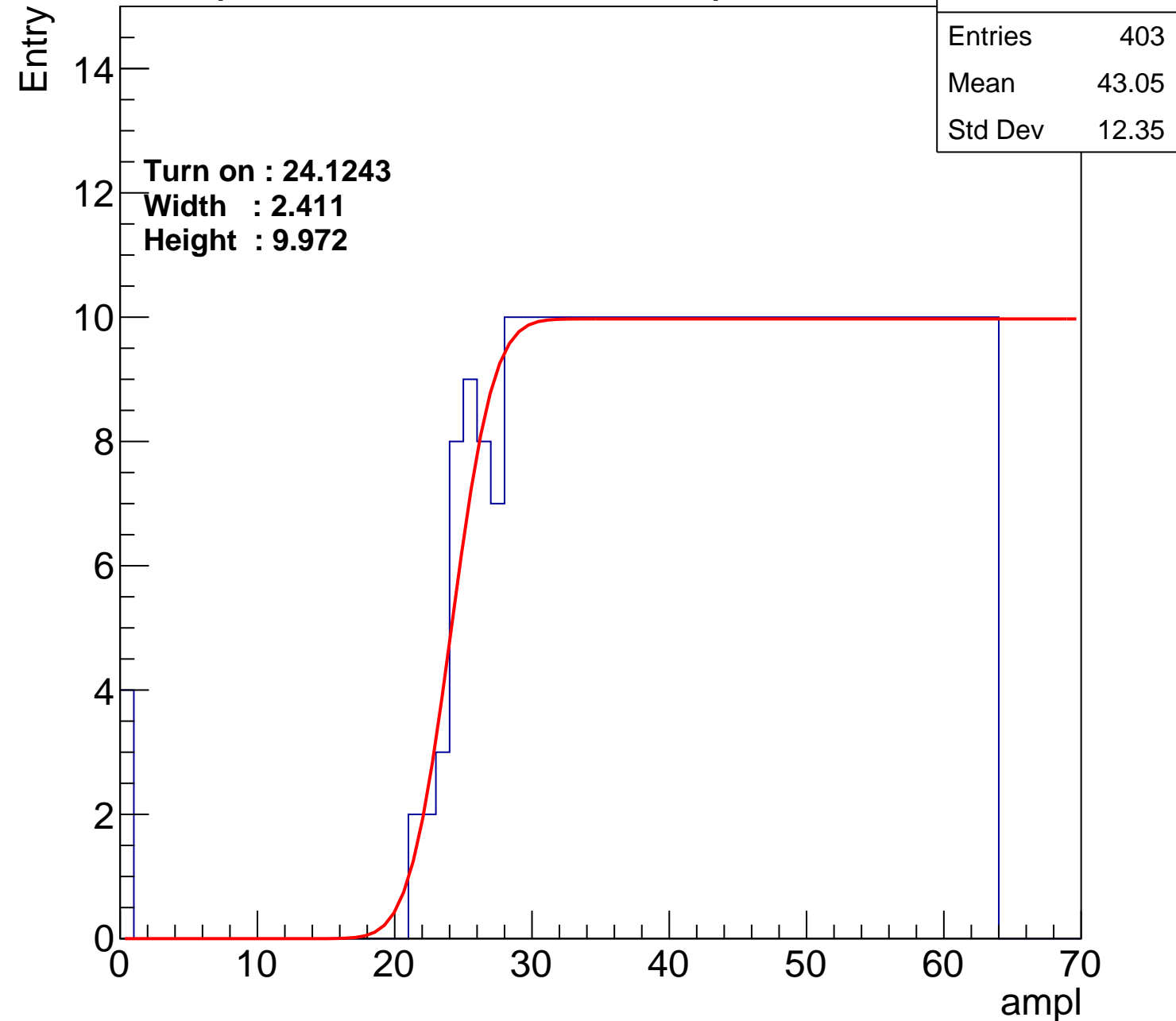
Width : 2.411

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch118

calib_packv5_042523_0143.root, FC#10, port B3

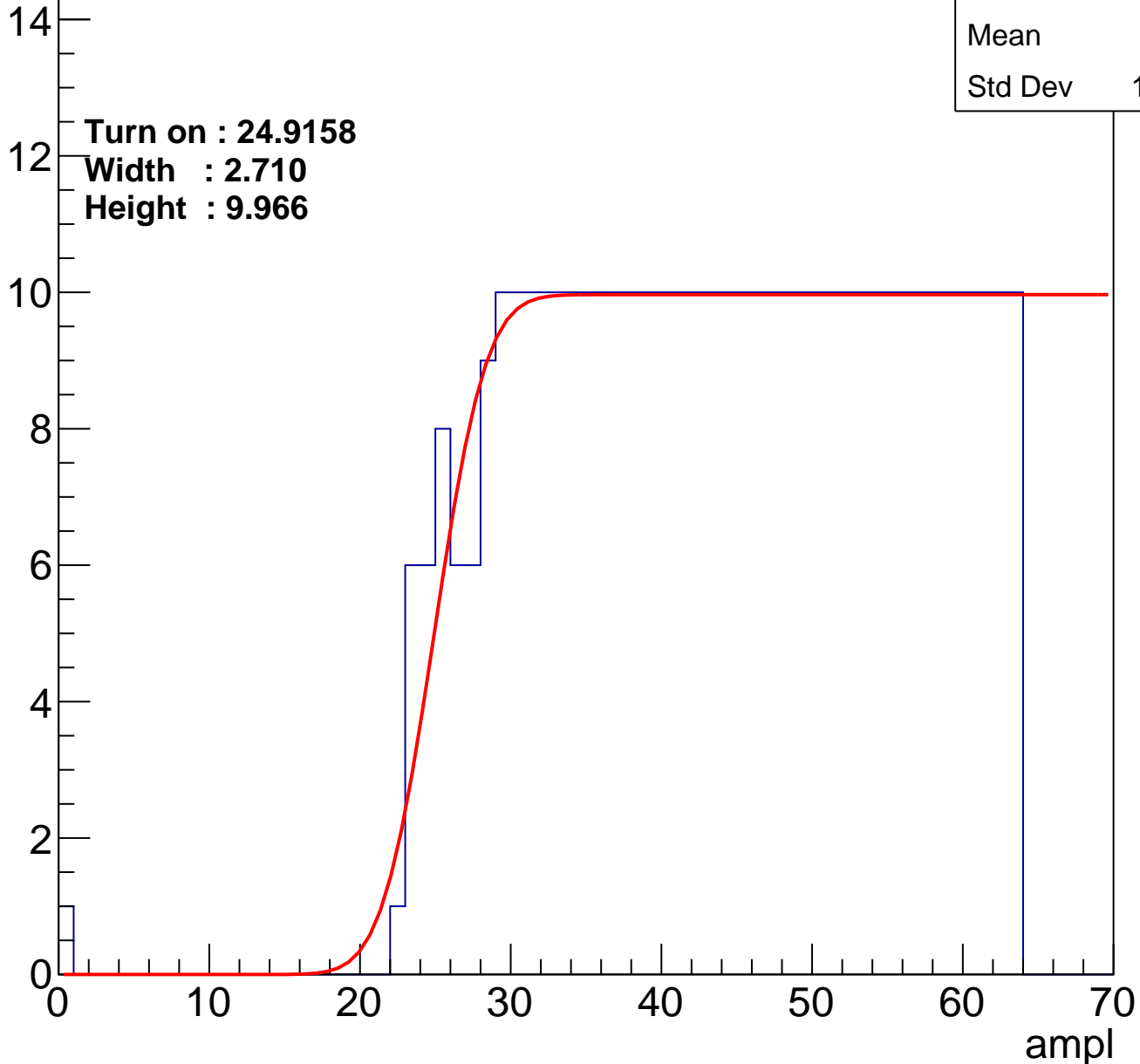
Entries	393
Mean	43.7
Std Dev	11.66

Turn on : 24.9158

Width : 2.710

Height : 9.966

Entry



B1L002S, U14-ch119

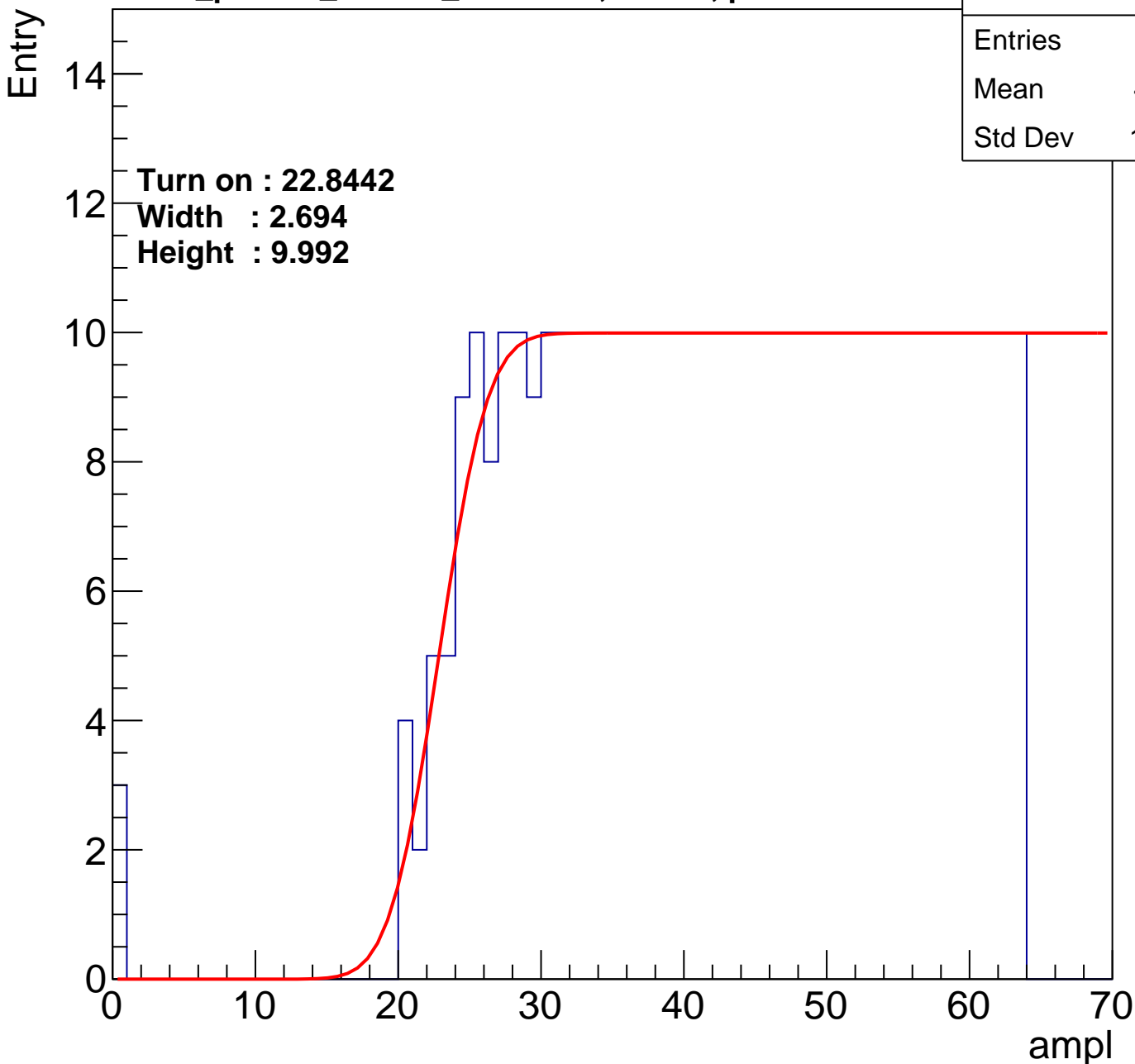
calib_packv5_042523_0143.root, FC#10, port B3

Entries	415
Mean	42.51
Std Dev	12.52

Turn on : 22.8442

Width : 2.694

Height : 9.992



B1L002S, U14-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.41
Std Dev	12.04

Turn on : 24.6239

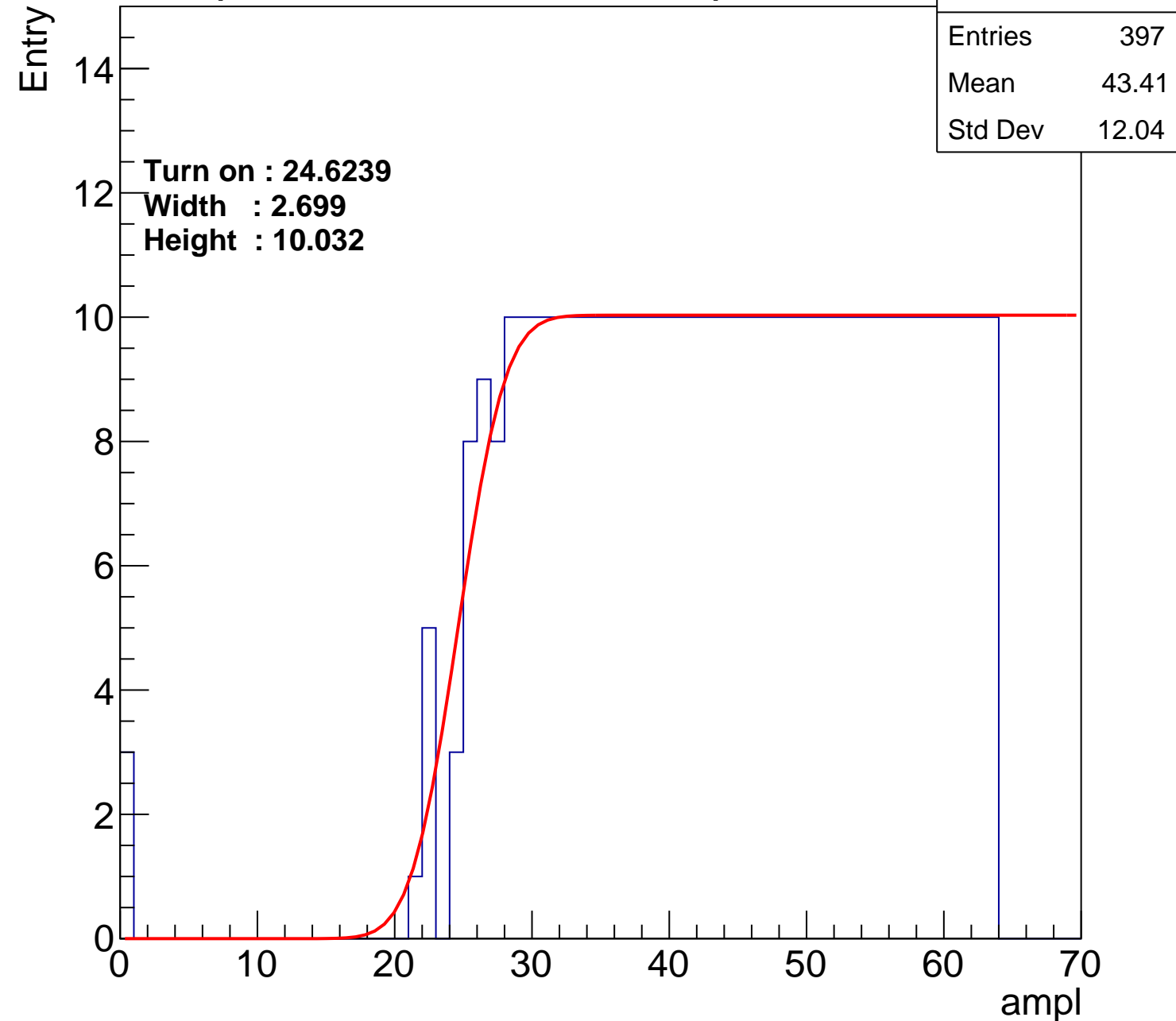
Width : 2.699

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.48
Std Dev	12.66

Turn on : 22.9603

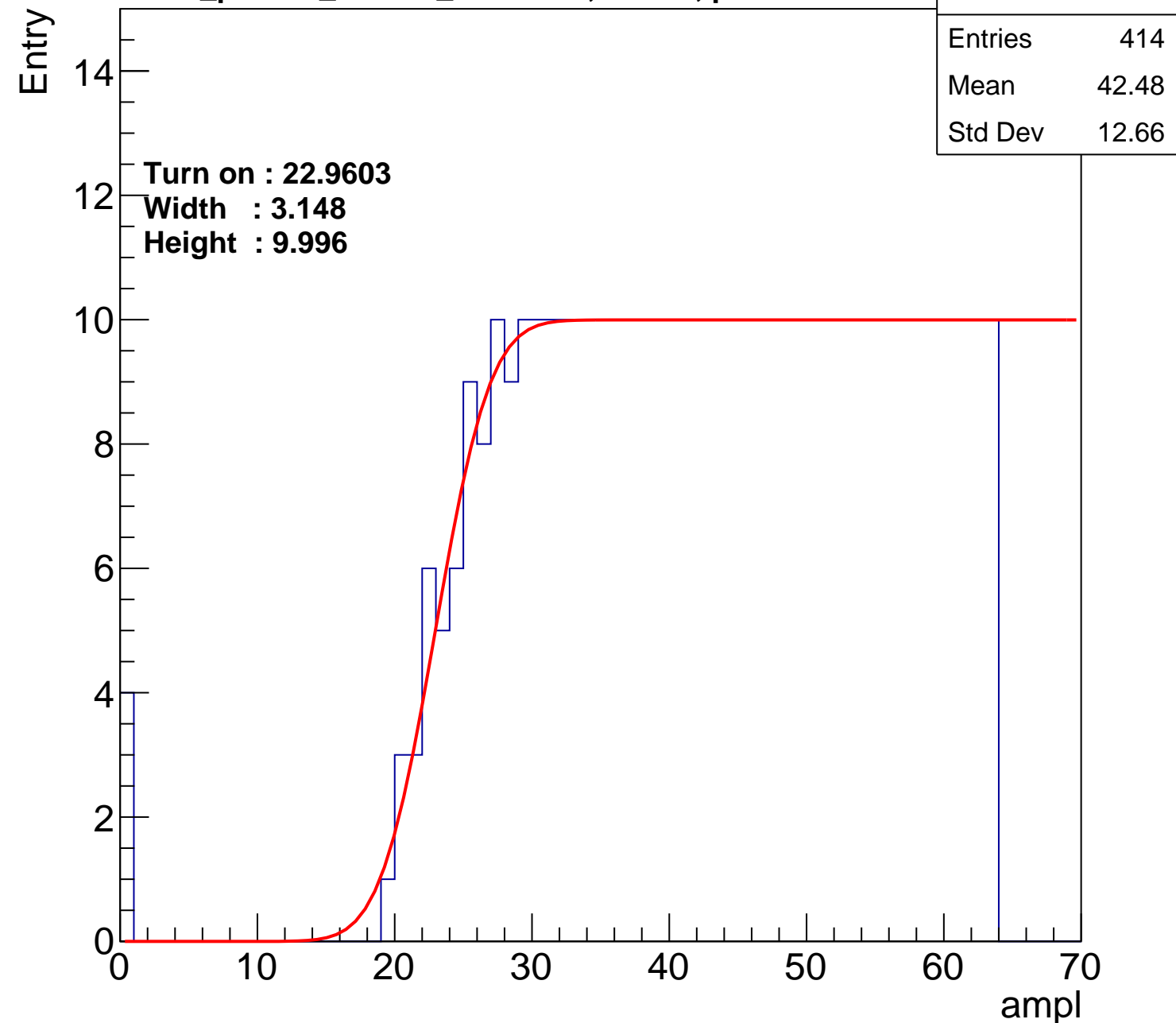
Width : 3.148

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.3
Std Dev	11.99

Turn on : 24.6914

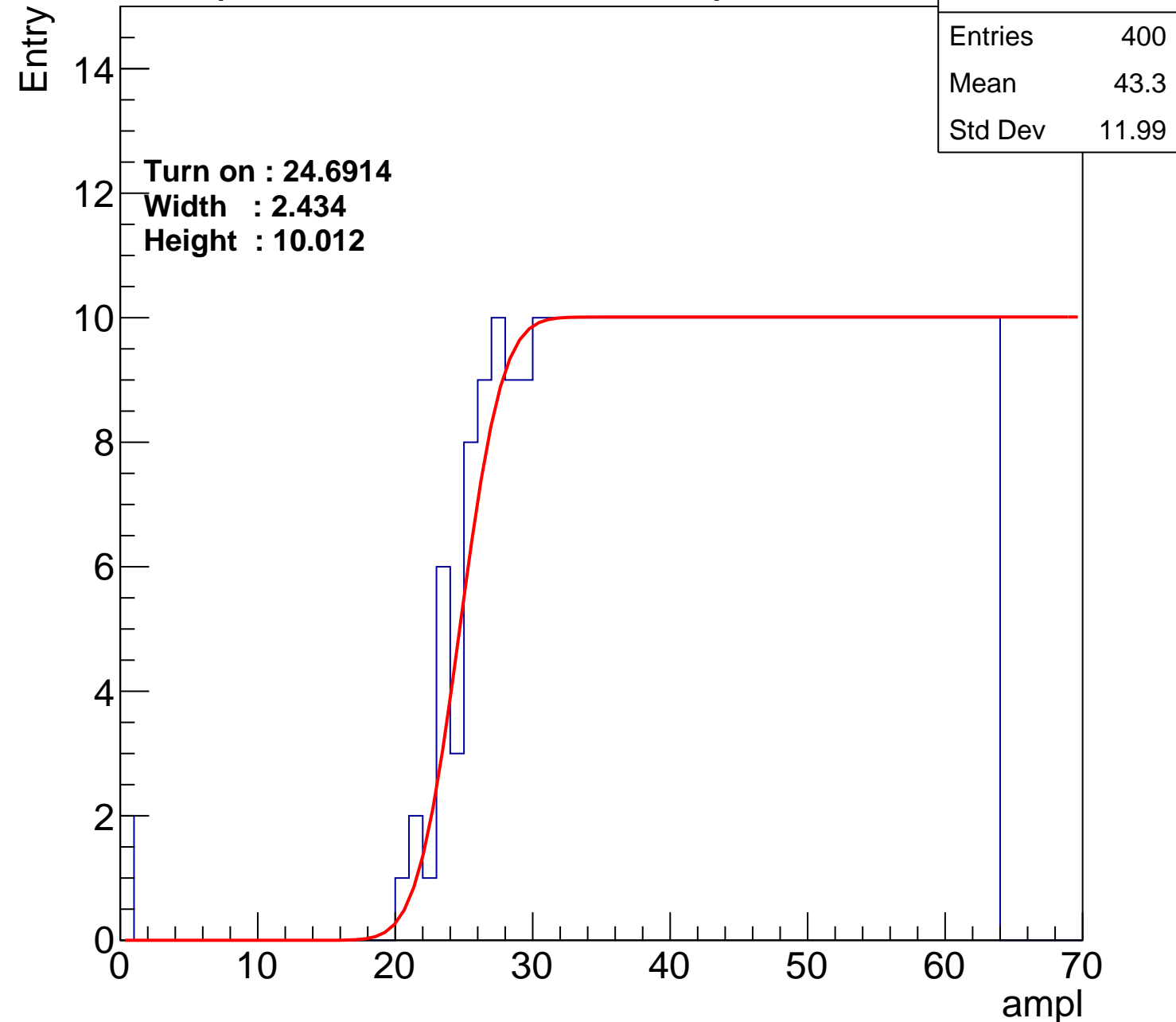
Width : 2.434

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.71
Std Dev	12.04

Turn on : 26.1125

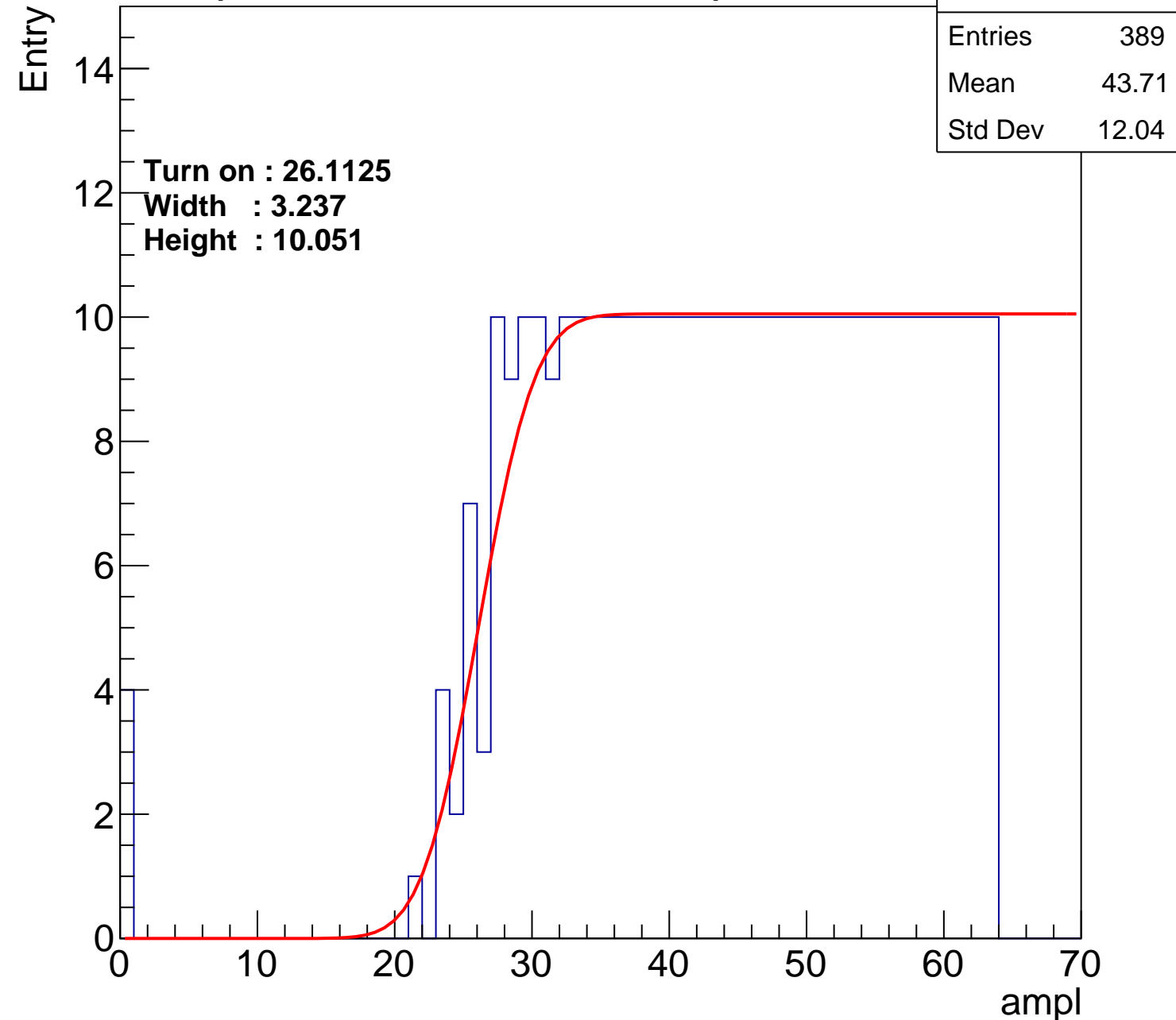
Width : 3.237

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.85
Std Dev	12.24

Turn on : 23.0439

Width : 2.645

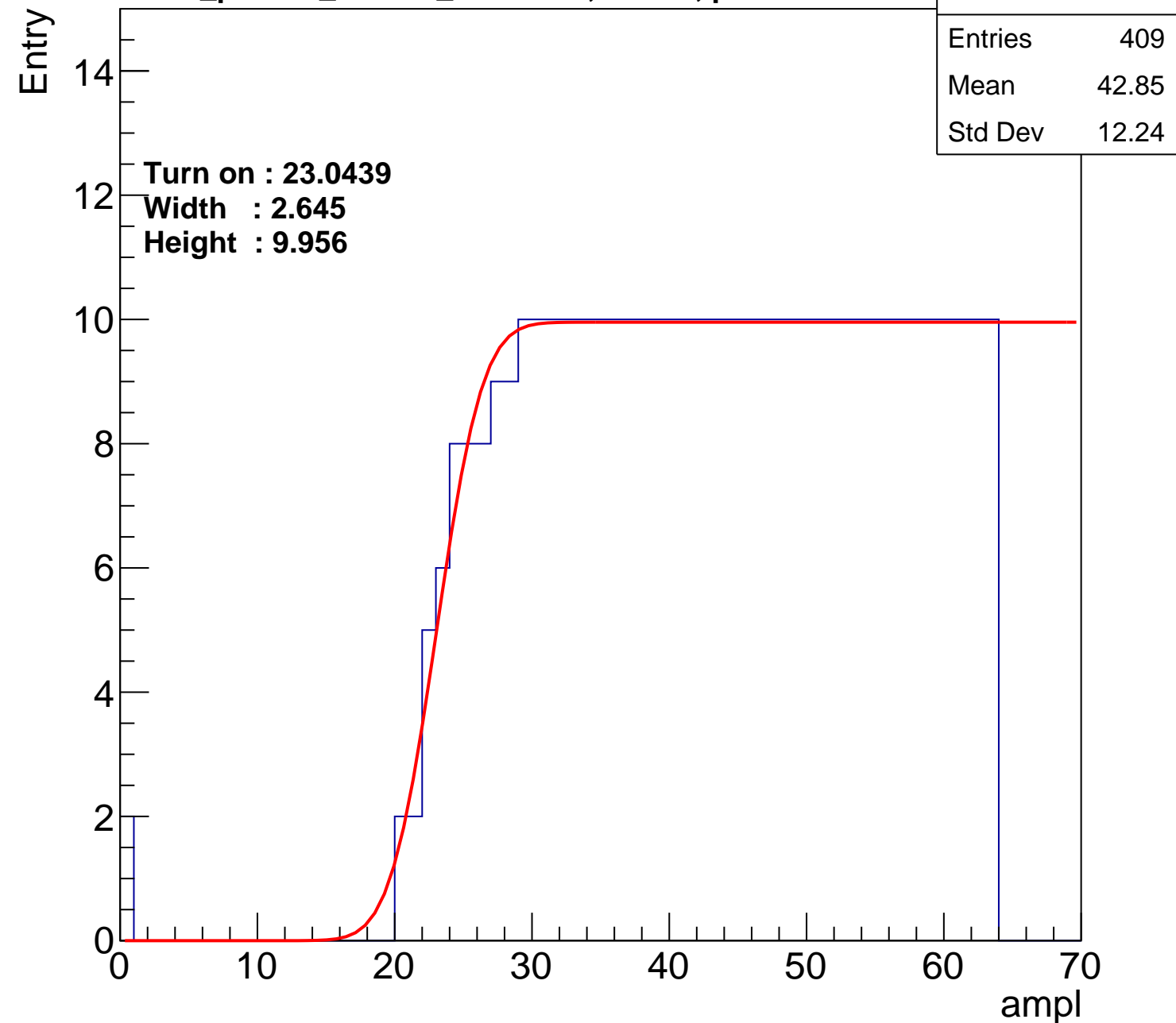
Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U14-ch125

calib_packv5_042523_0143.root, FC#10, port B3

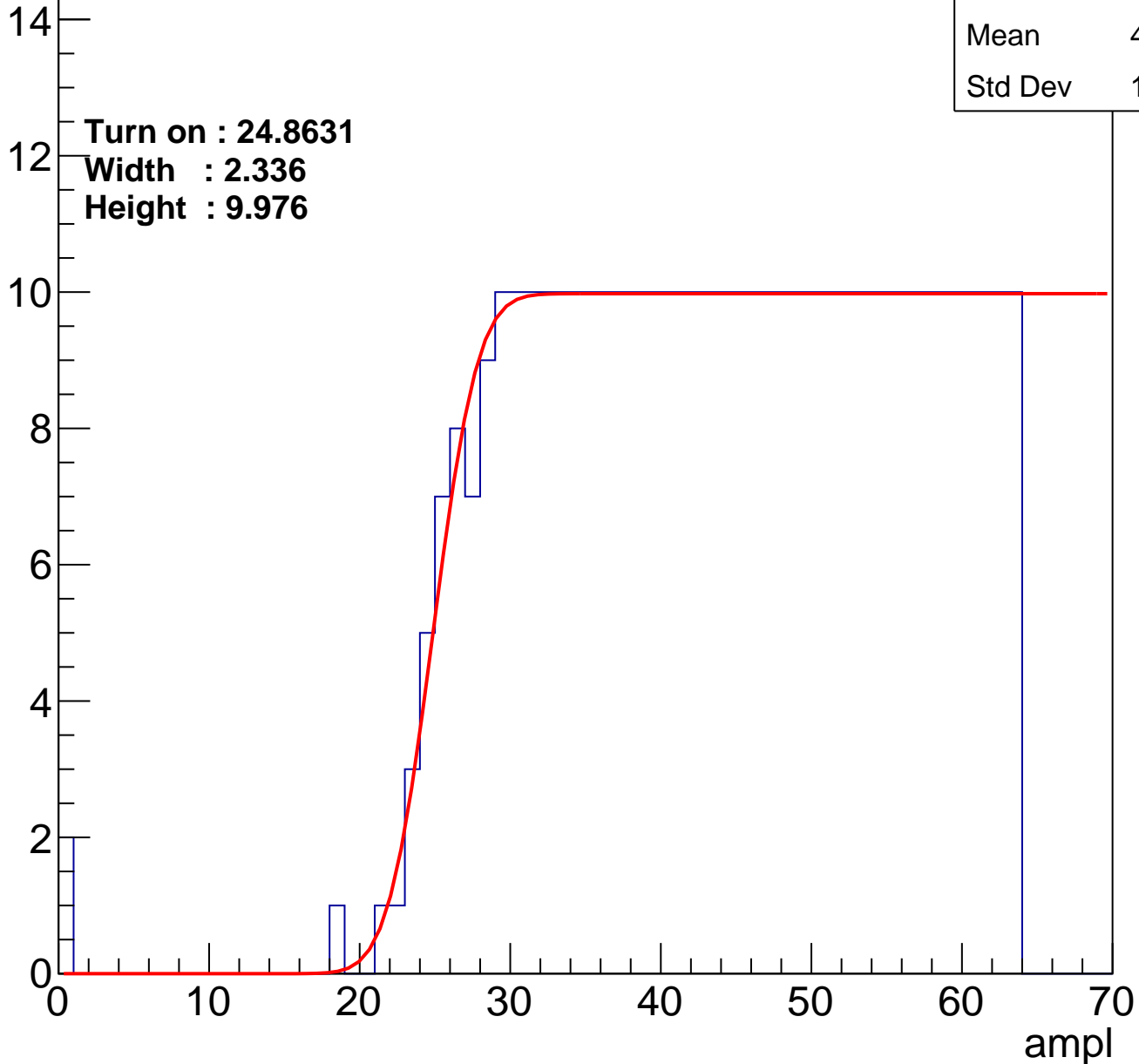
Entries	394
Mean	43.59
Std Dev	11.85

Turn on : 24.8631

Width : 2.336

Height : 9.976

Entry



B1L002S, U14-ch126

calib_packv5_042523_0143.root, FC#10, port B3

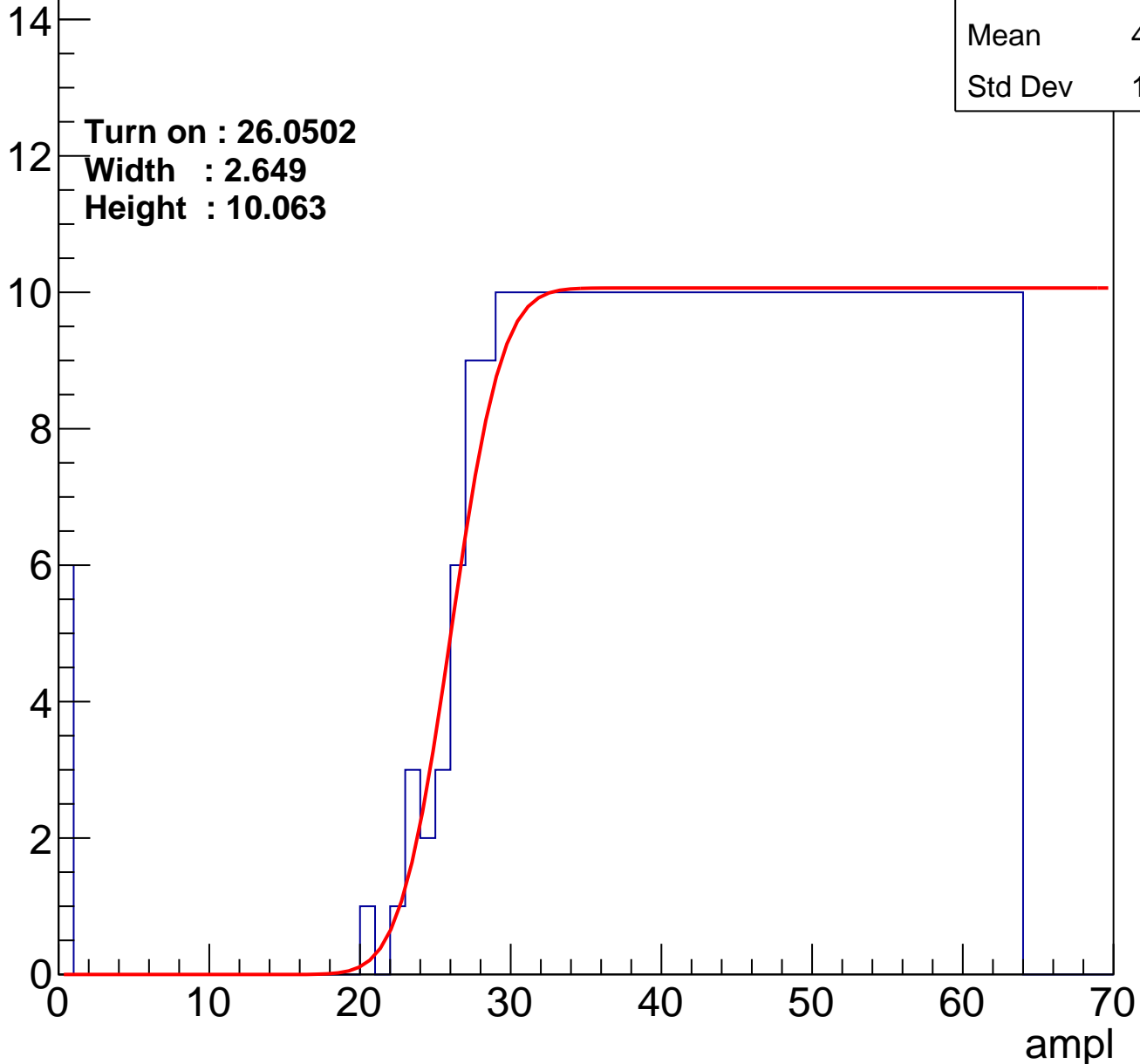
Entries	390
Mean	43.55
Std Dev	12.38

Turn on : 26.0502

Width : 2.649

Height : 10.063

Entry



B1L002S, U14-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.6551

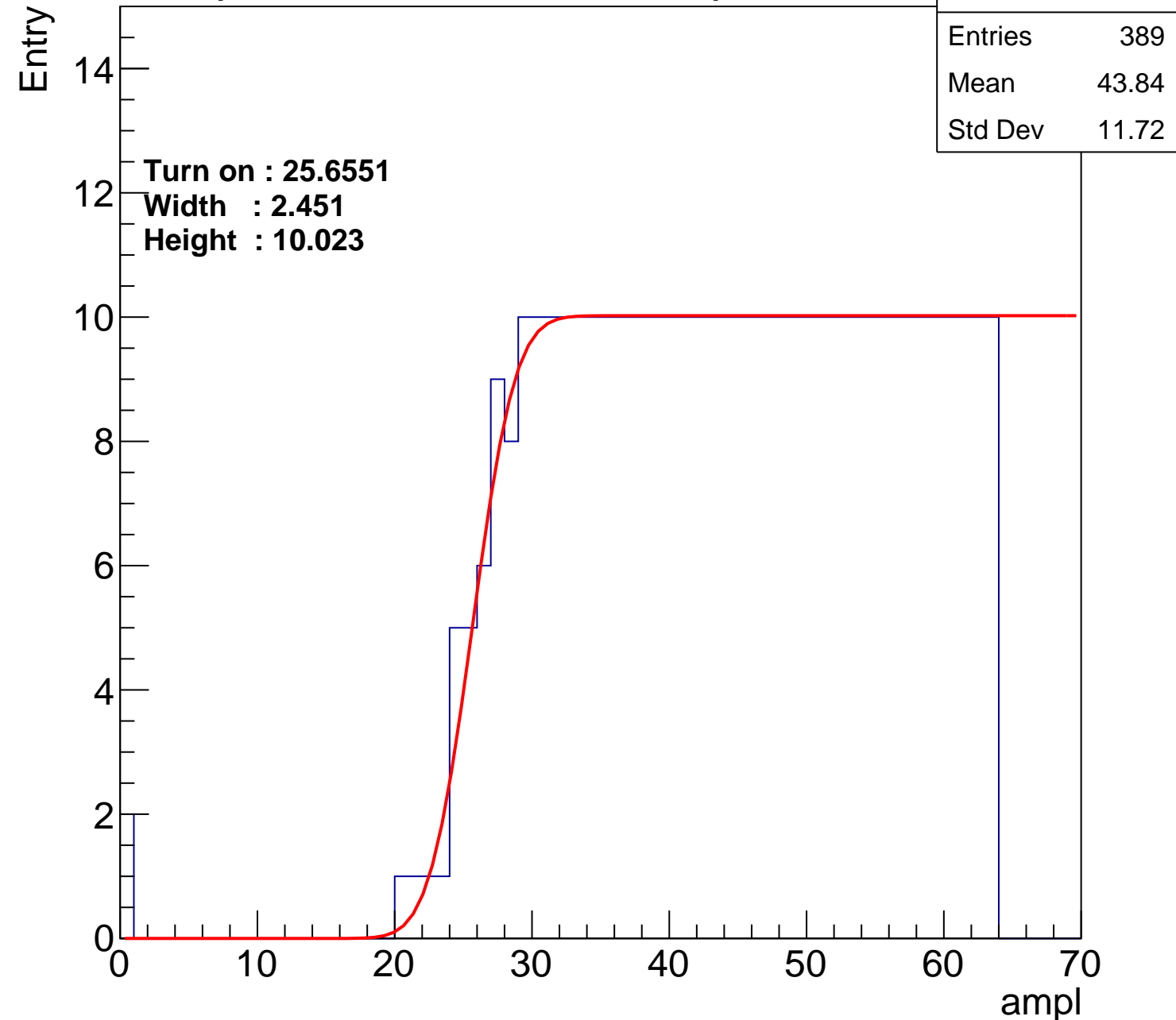
Width : 2.451

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U14-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.6551

Width : 2.451

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl

