

B1L000S, U19-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
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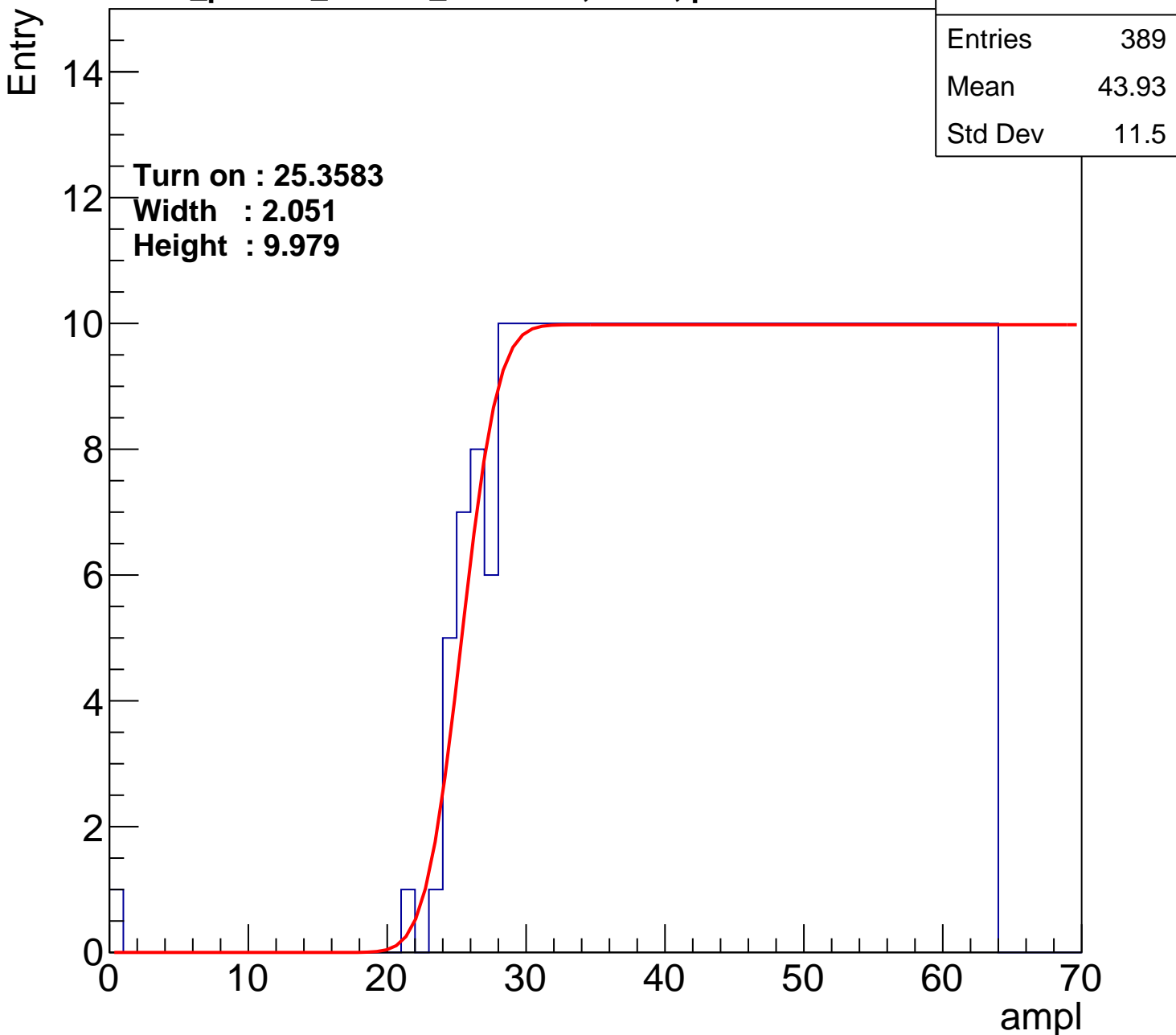
Mean	43.93
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Std Dev	11.5
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Turn on : 25.3583

Width : 2.051

Height : 9.979



B1L000S, U19-ch1

calib_packv5_042523_0143.root, FC#3, port B3

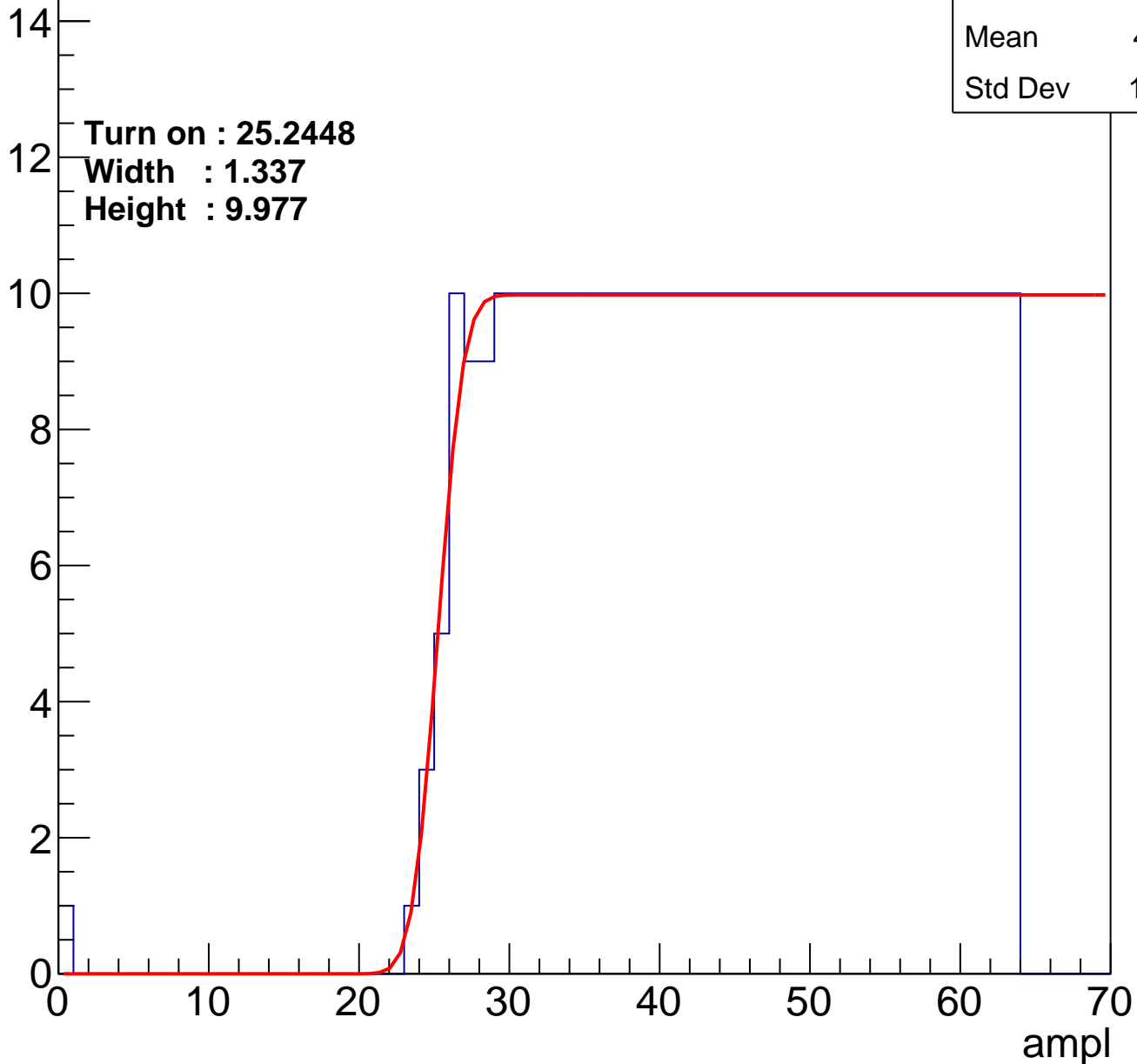
Entries	388
Mean	44.01
Std Dev	11.42

Turn on : 25.2448

Width : 1.337

Height : 9.977

Entry



B1L000S, U19-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44.02
Std Dev	11.47

Turn on : 25.7287

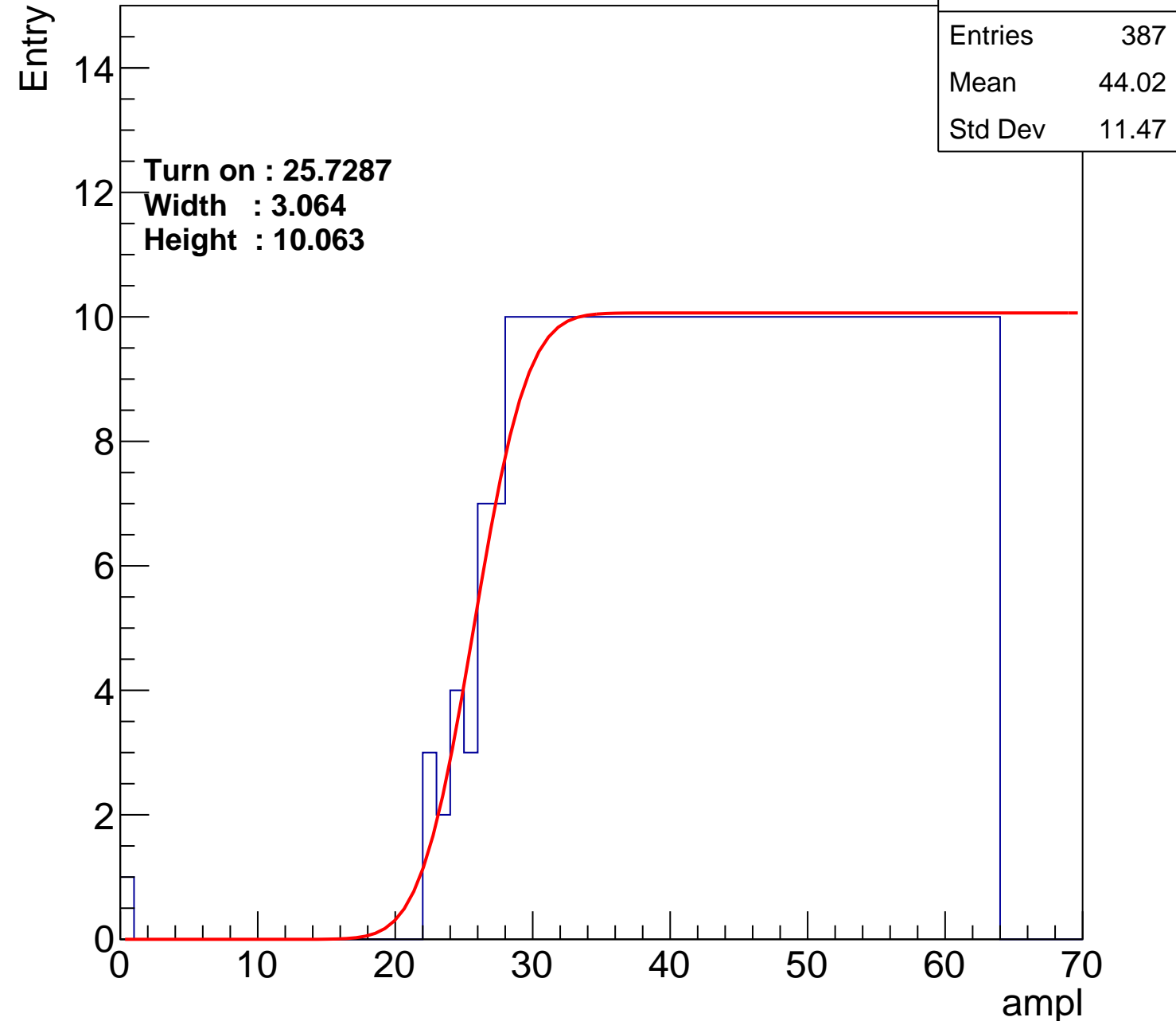
Width : 3.064

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch3

calib_packv5_042523_0143.root, FC#3, port B3

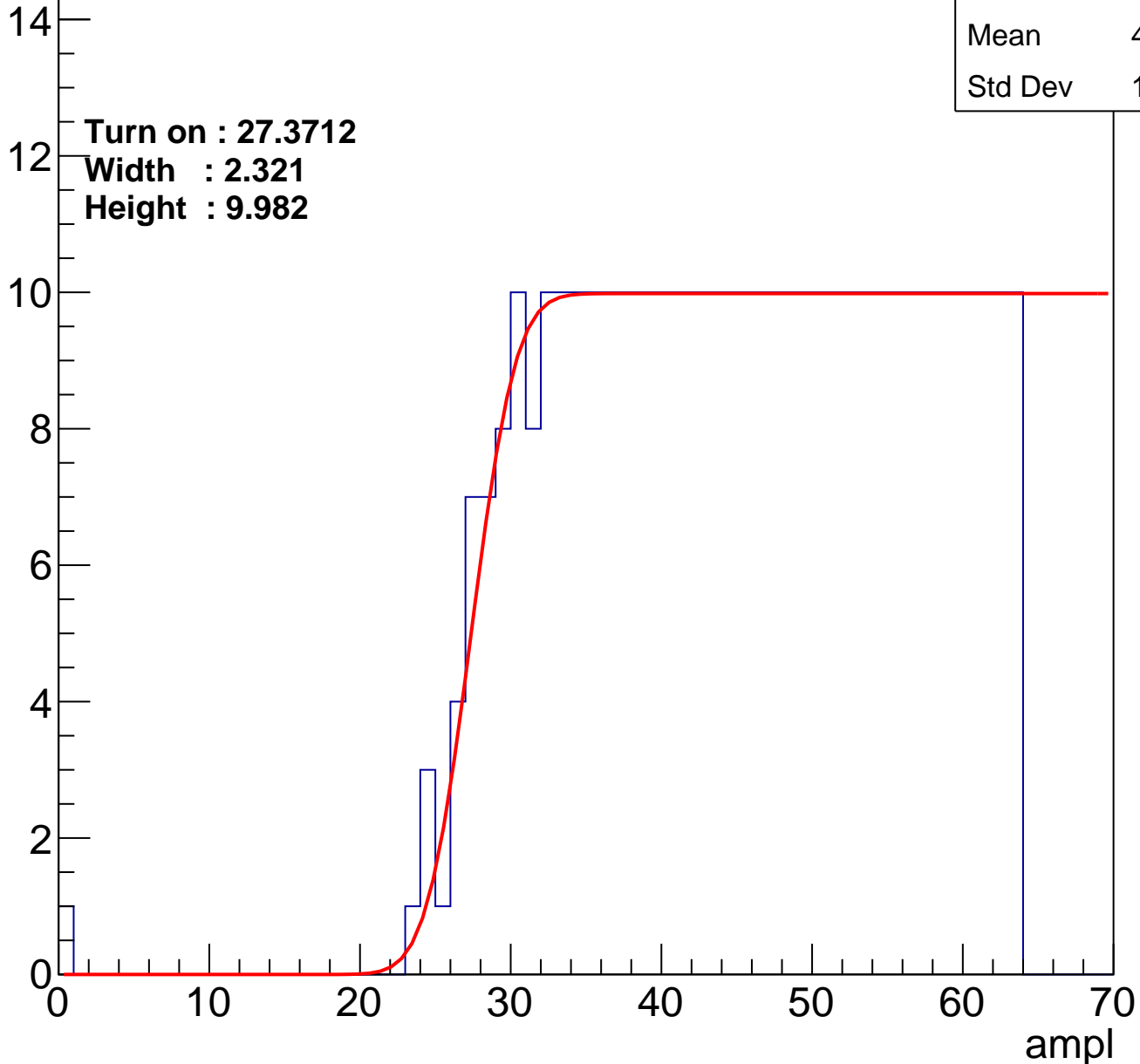
Entries	370
Mean	44.84
Std Dev	11.04

Turn on : 27.3712

Width : 2.321

Height : 9.982

Entry



B1L000S, U19-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	45
Std Dev	10.94

Turn on : 27.2421

Width : 2.912

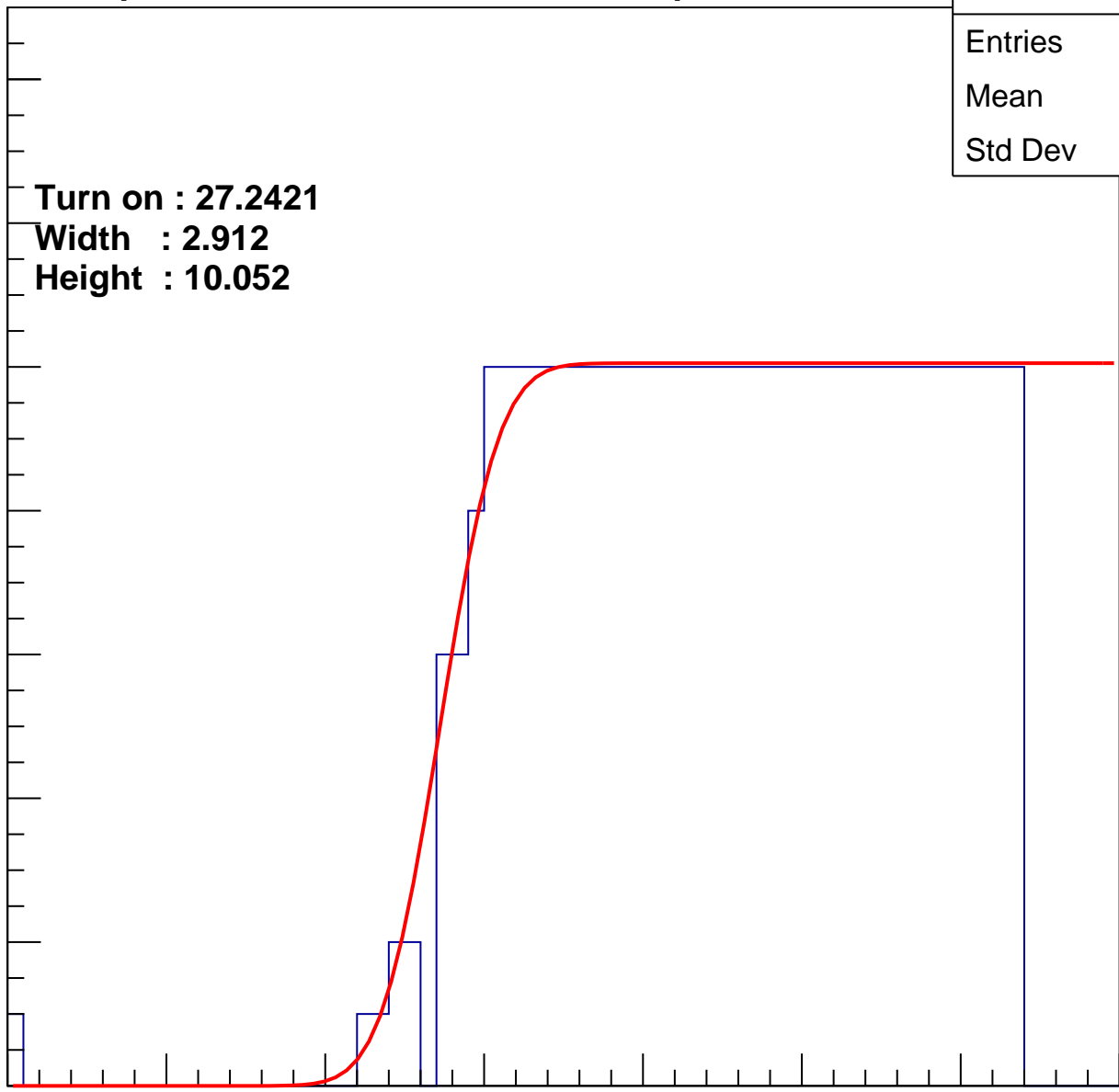
Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U19-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.7
Std Dev	11.24

Turn on : 26.5417

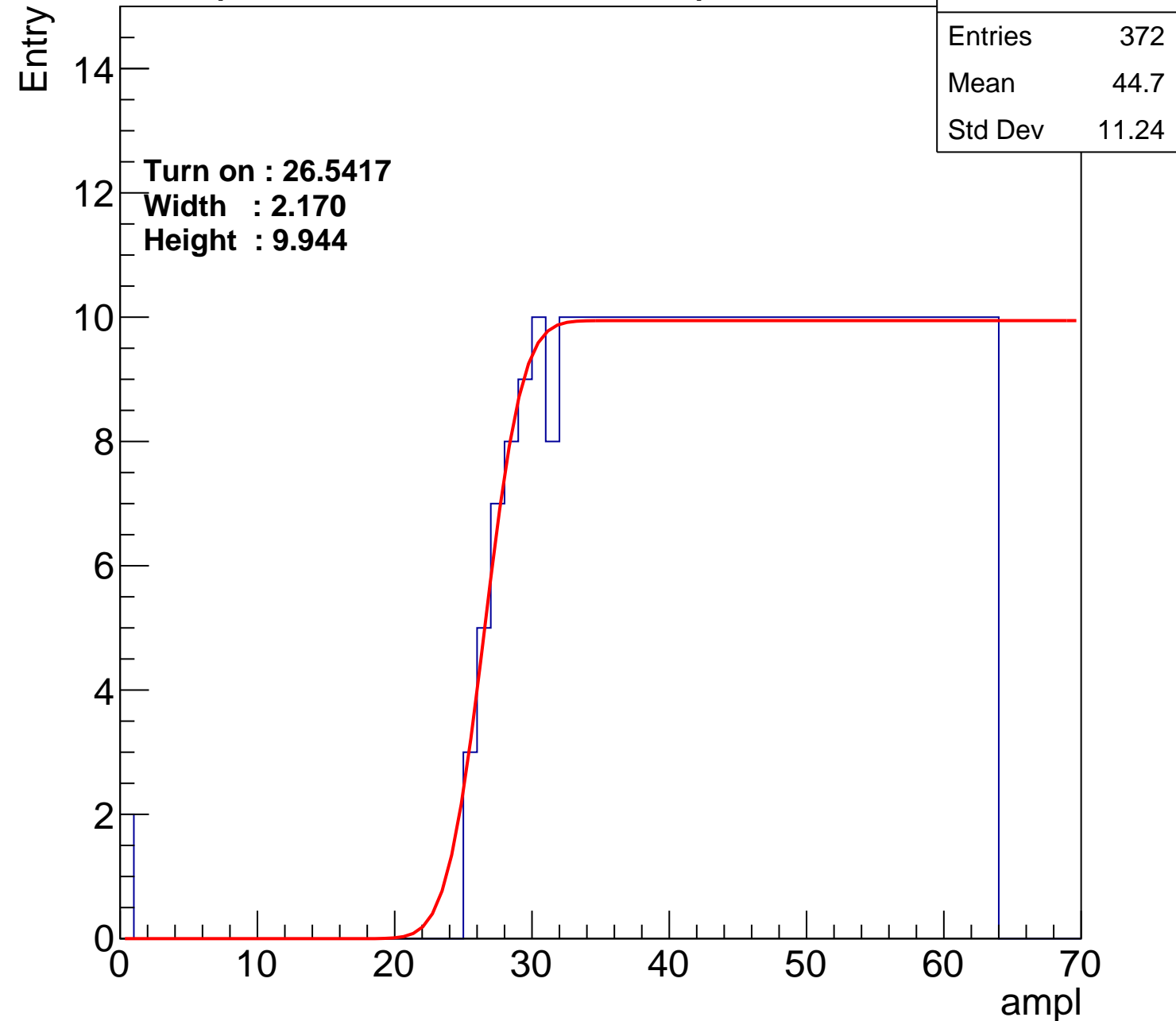
Width : 2.170

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch6

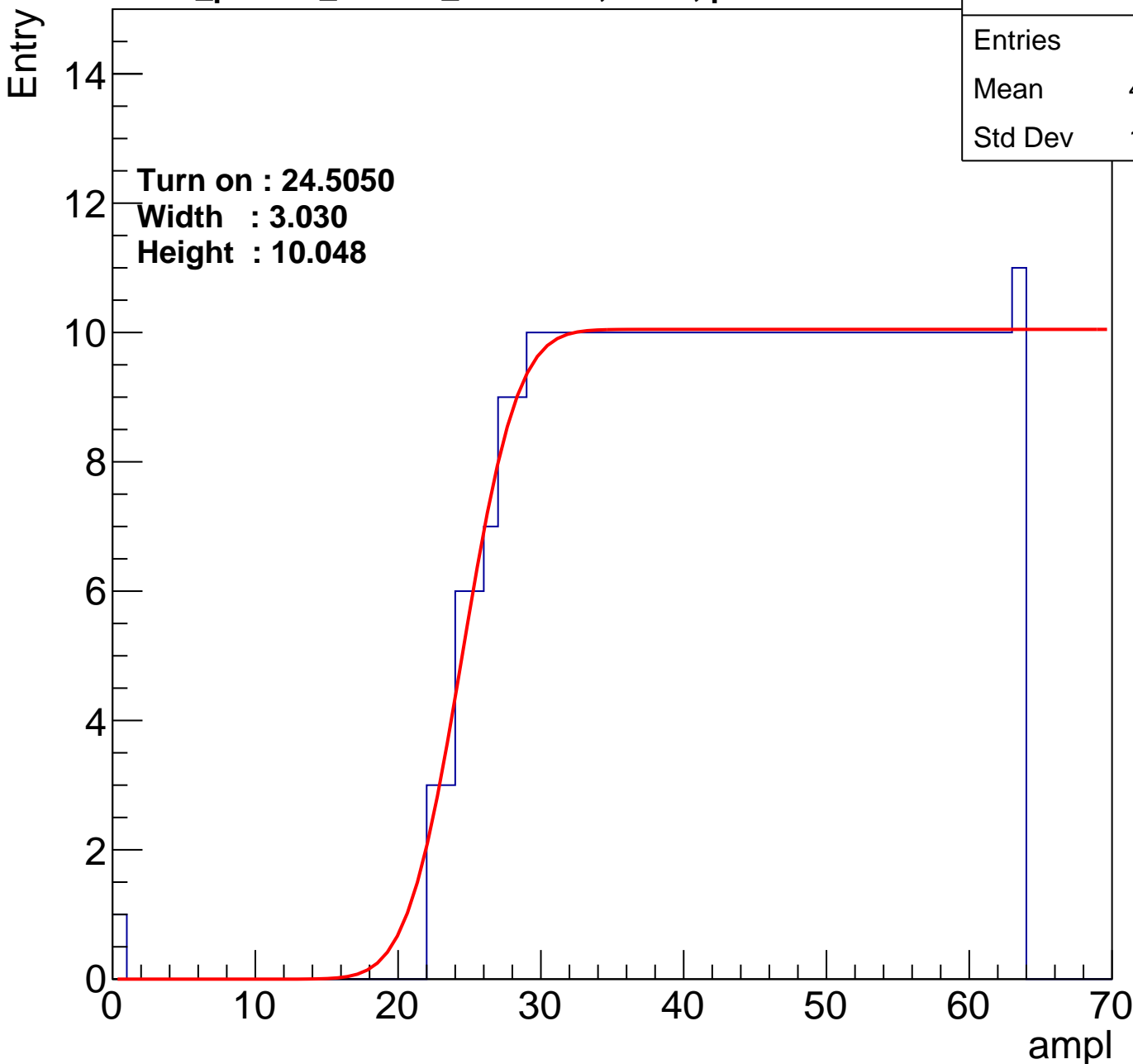
calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.72
Std Dev	11.68

Turn on : 24.5050

Width : 3.030

Height : 10.048



B1L000S, U19-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.76
Std Dev	11.79

Turn on : 25.4898

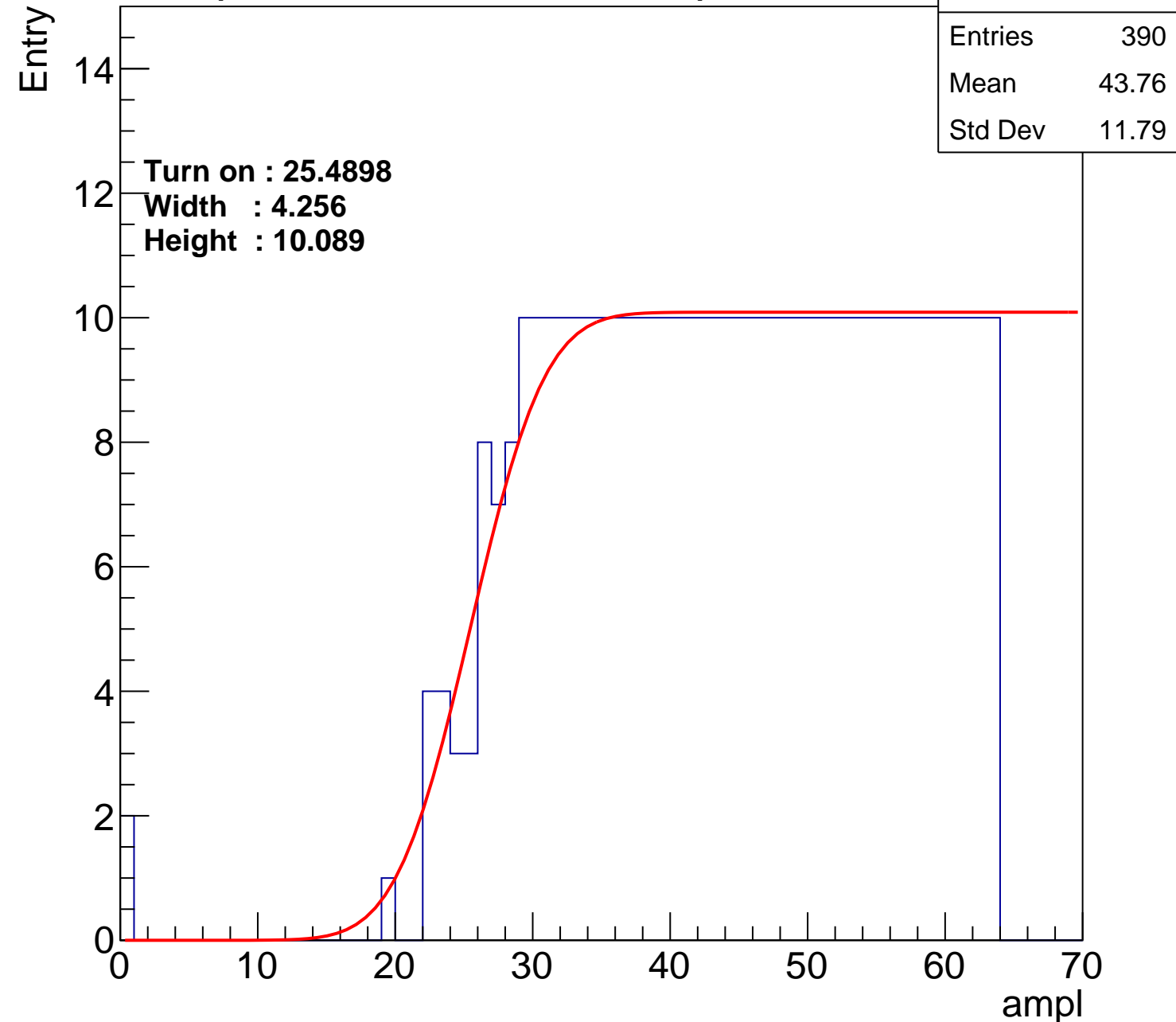
Width : 4.256

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.6
Std Dev	11.1

Turn on : 26.5130

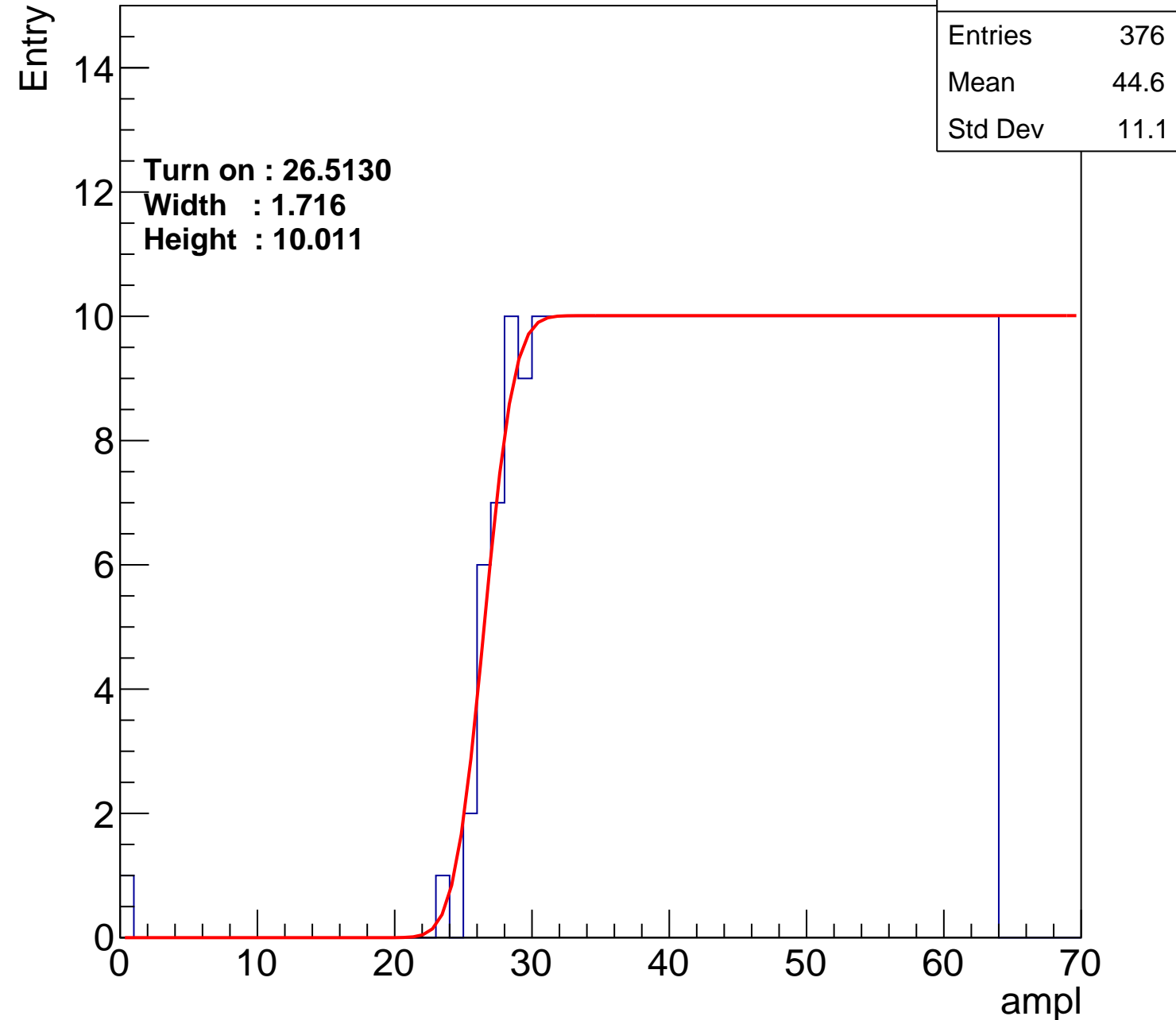
Width : 1.716

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 25.3125

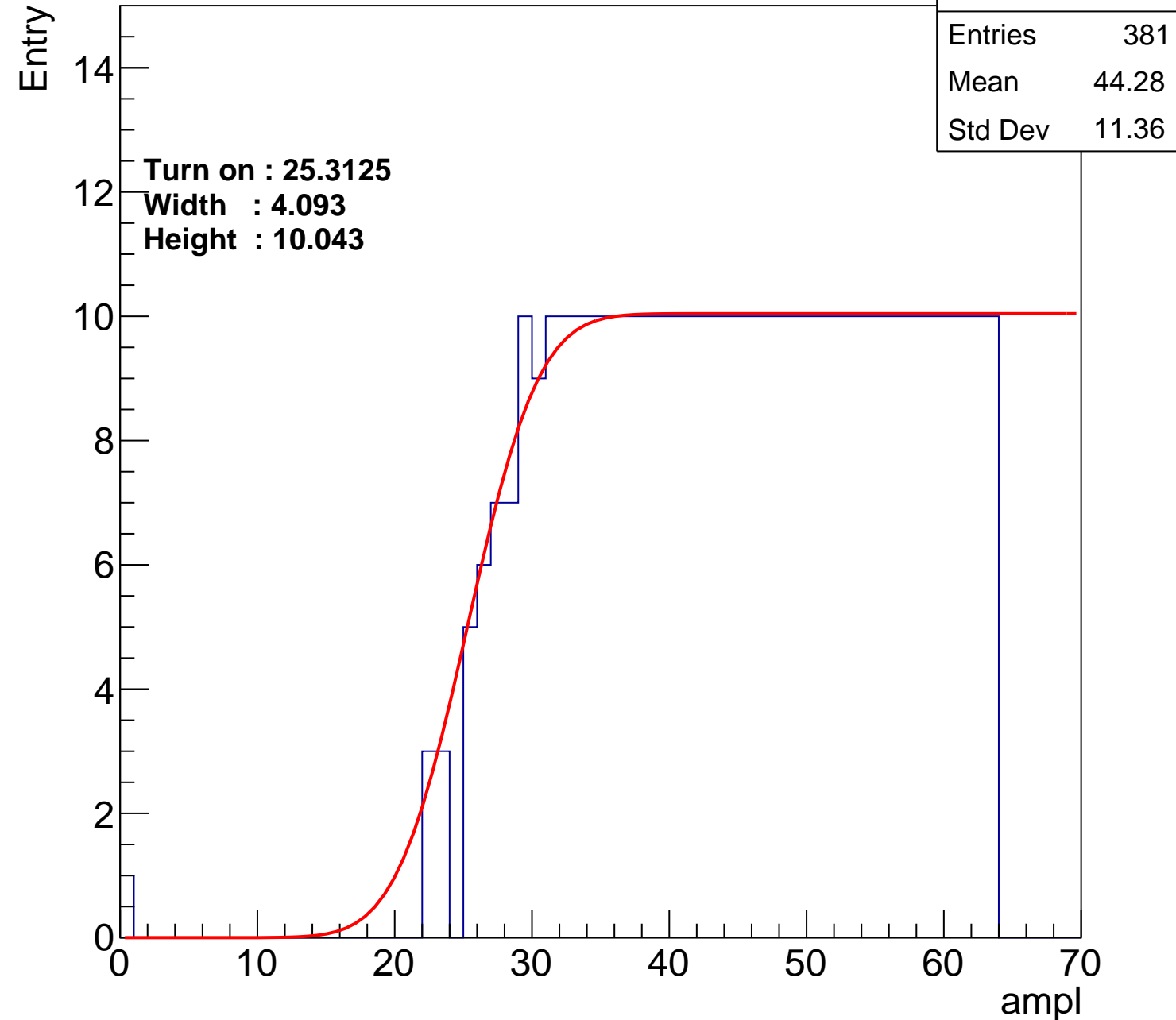
Width : 4.093

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.3
Std Dev	11.63

Std Dev	11.63
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Height : 10.001



B1L000S, U19-ch11

calib_packv5_042523_0143.root, FC#3, port B3

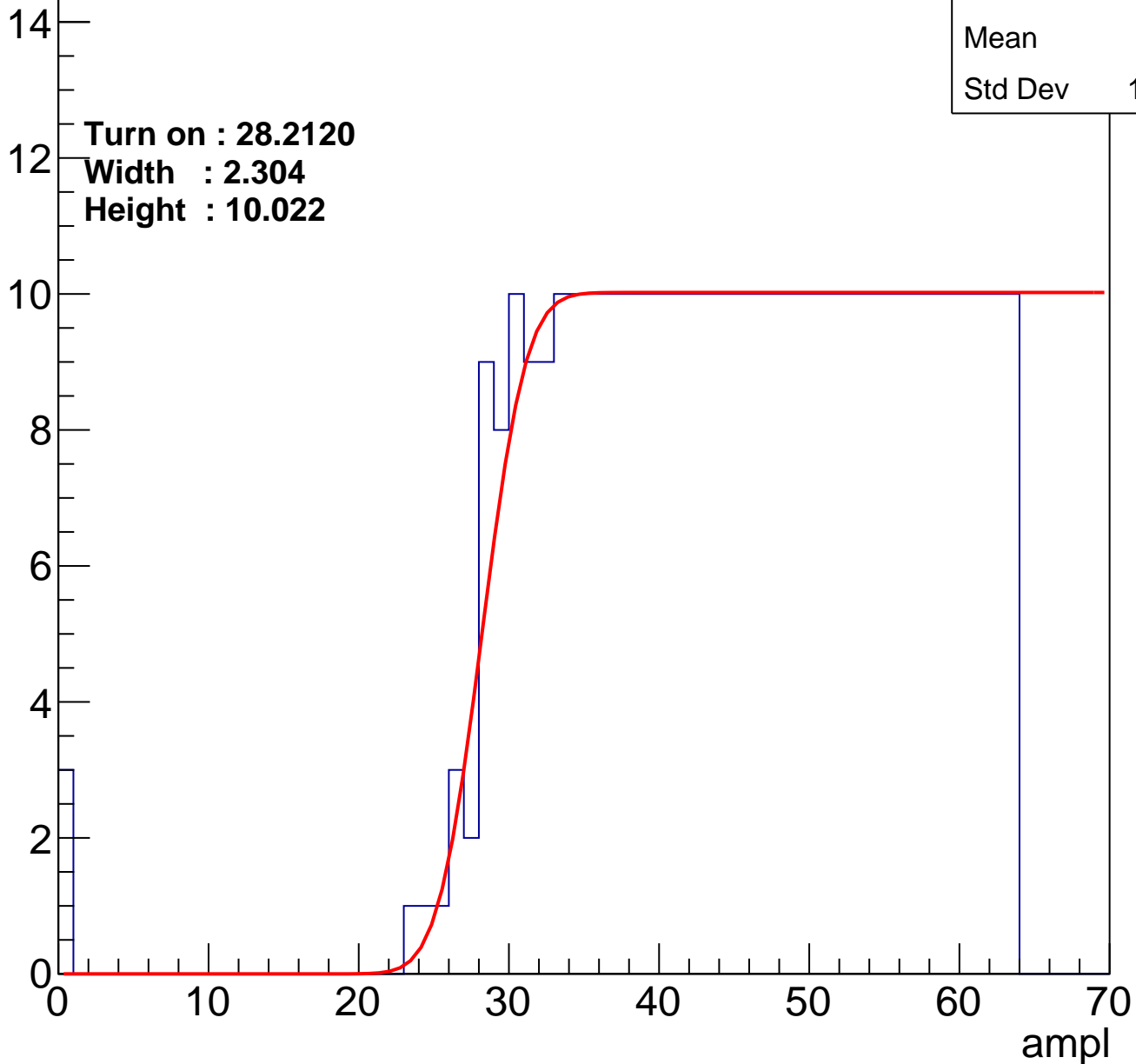
Entry

Entries	366
Mean	44.9
Std Dev	11.32

Turn on : 28.2120

Width : 2.304

Height : 10.022



B1L000S, U19-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 26.9254

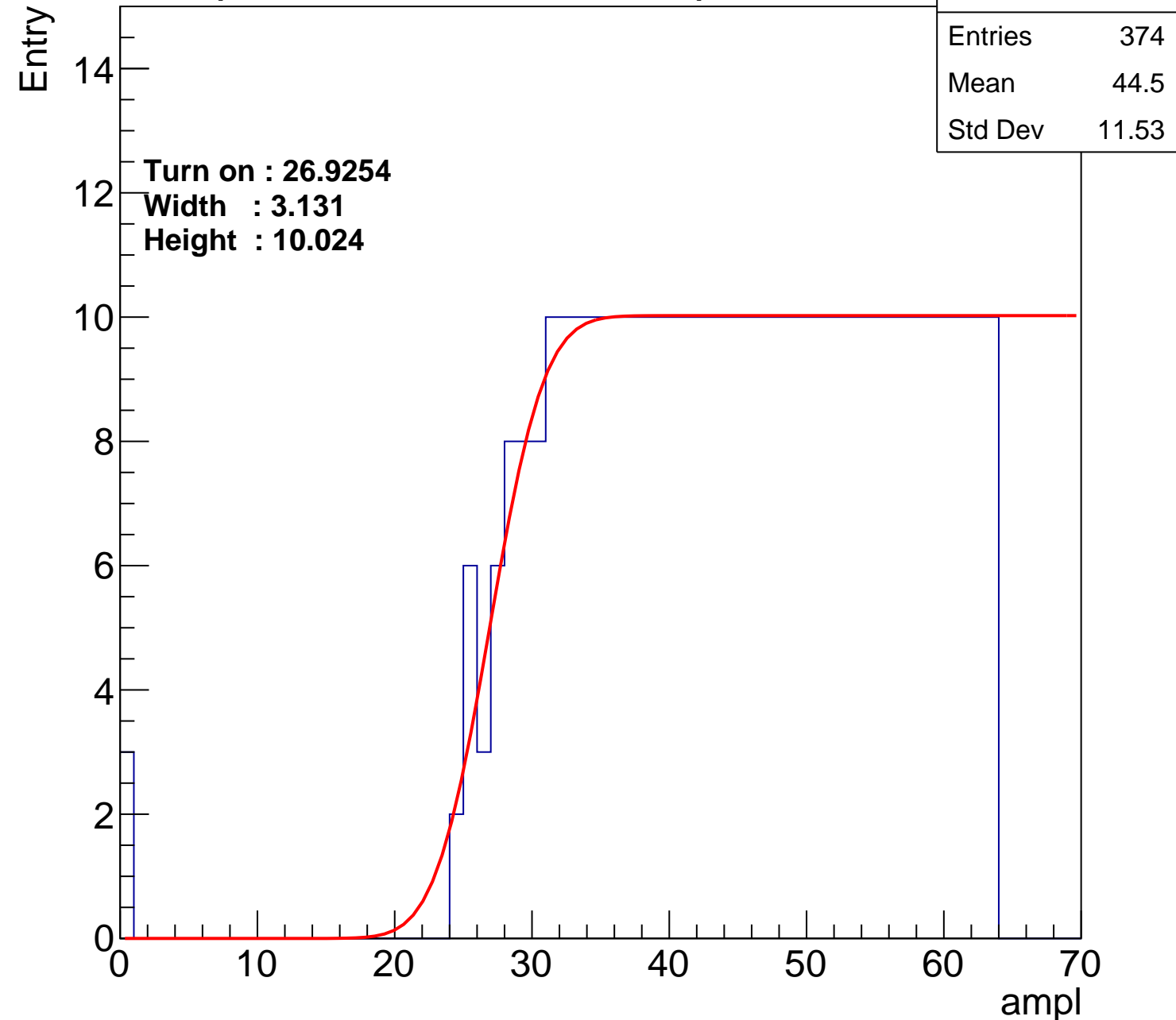
Width : 3.131

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.24

Turn on : 27.2565

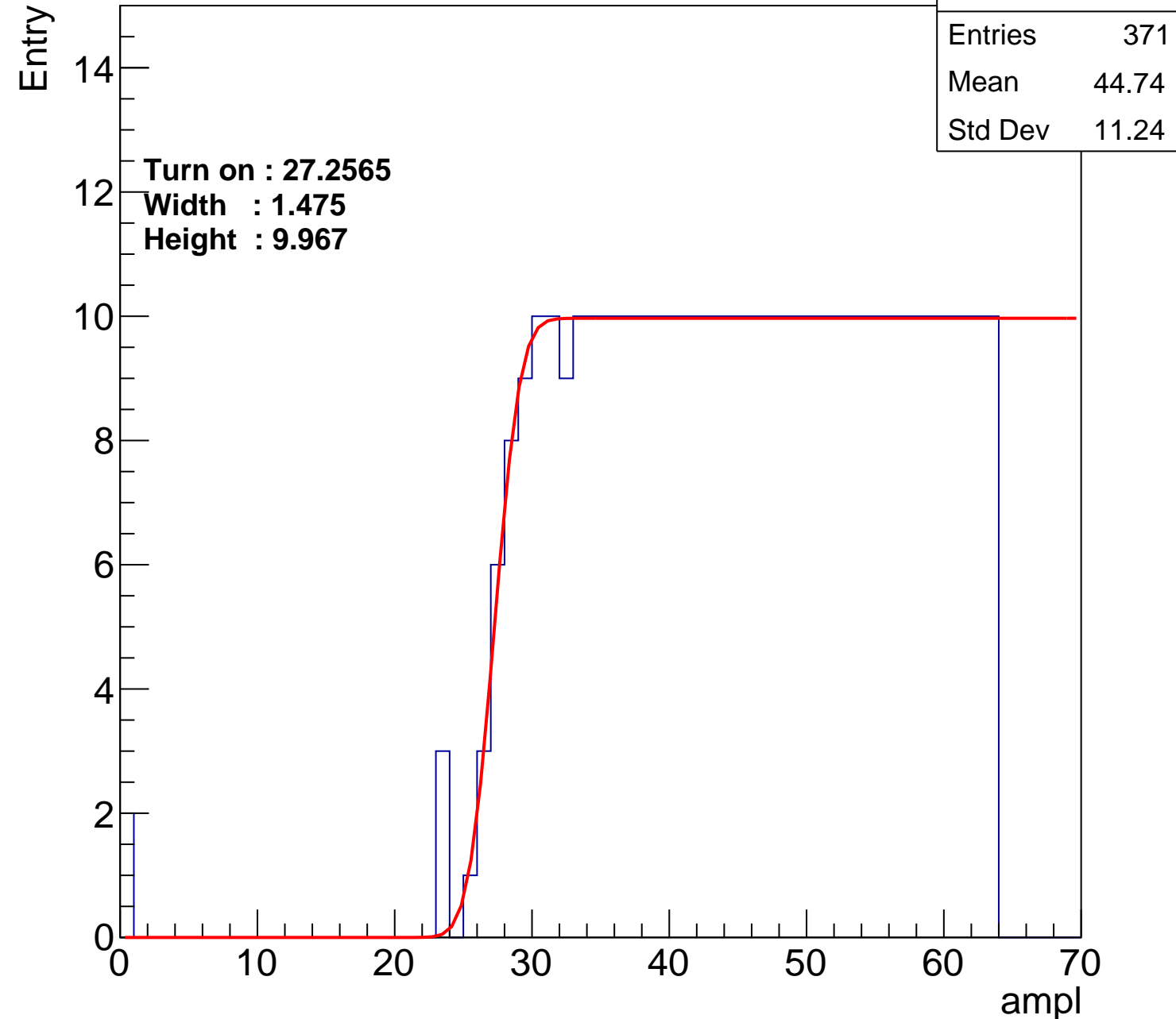
Width : 1.475

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.8518

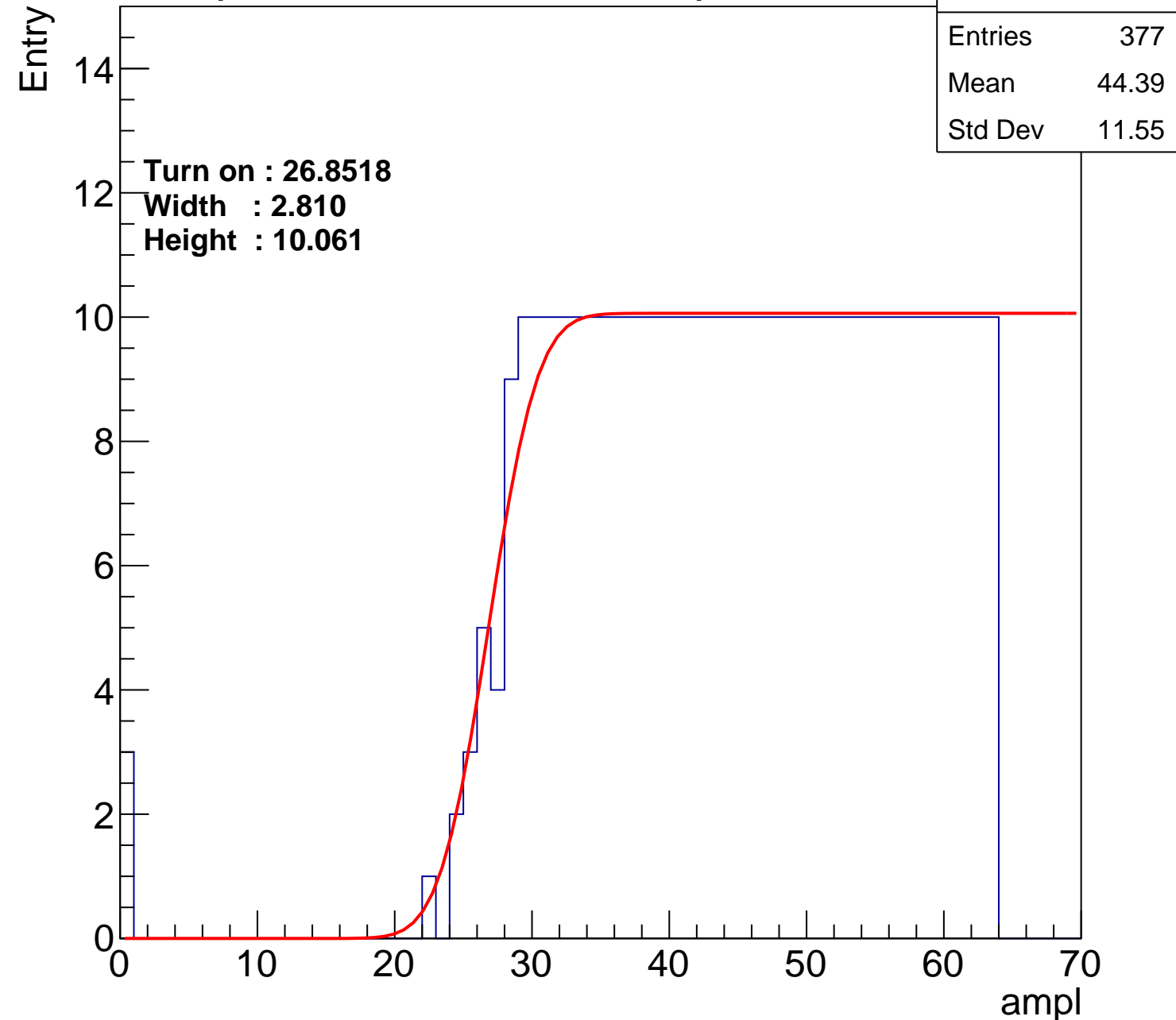
Width : 2.810

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch15

calib_packv5_042523_0143.root, FC#3, port B3

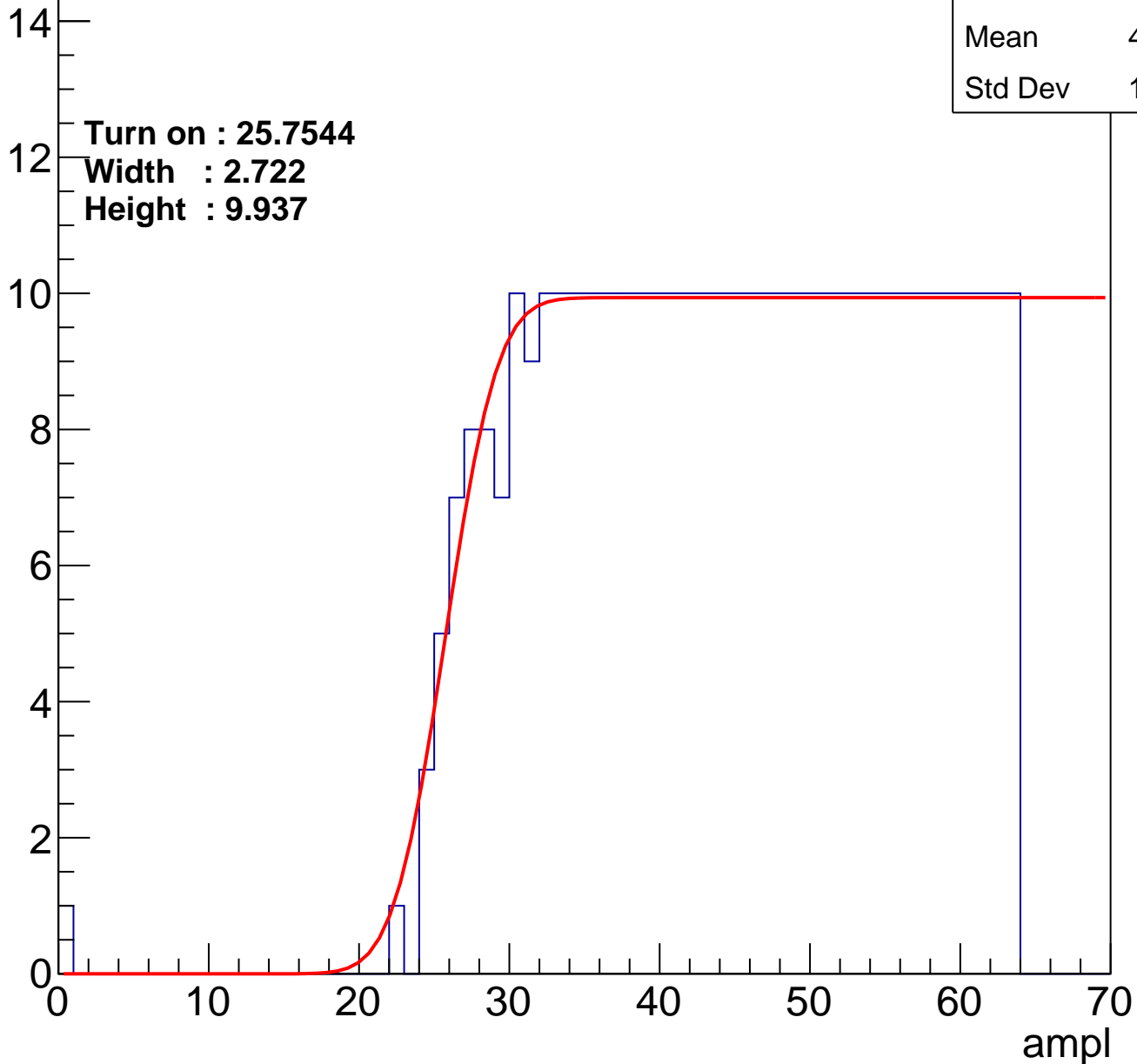
Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 25.7544

Width : 2.722

Height : 9.937

Entry



B1L000S, U19-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 26.1650

Width : 2.200

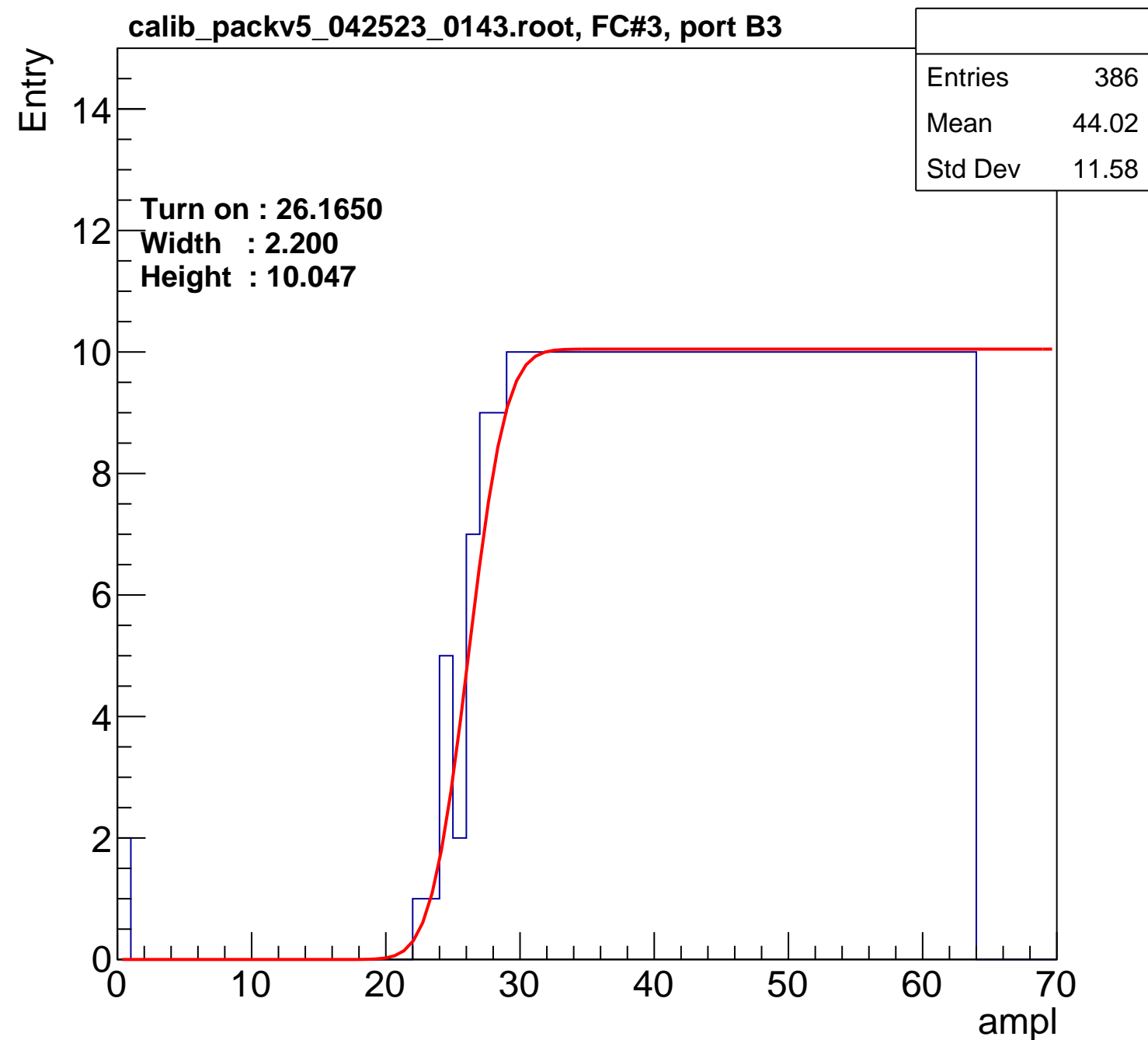
Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U19-ch17

calib_packv5_042523_0143.root, FC#3, port B3

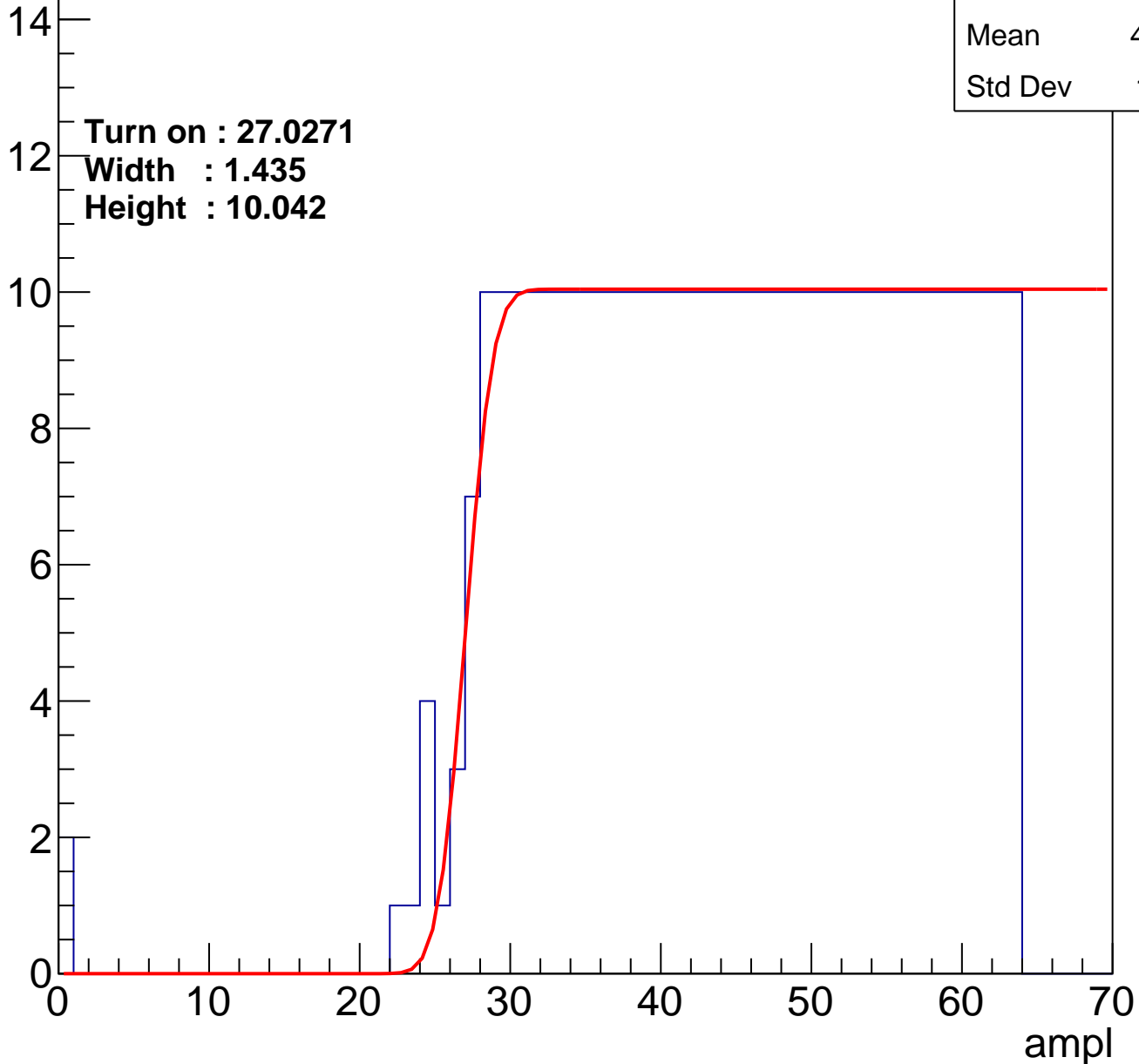
Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 27.0271

Width : 1.435

Height : 10.042

Entry



B1L000S, U19-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.98
Std Dev	11.64

Turn on : 25.5652

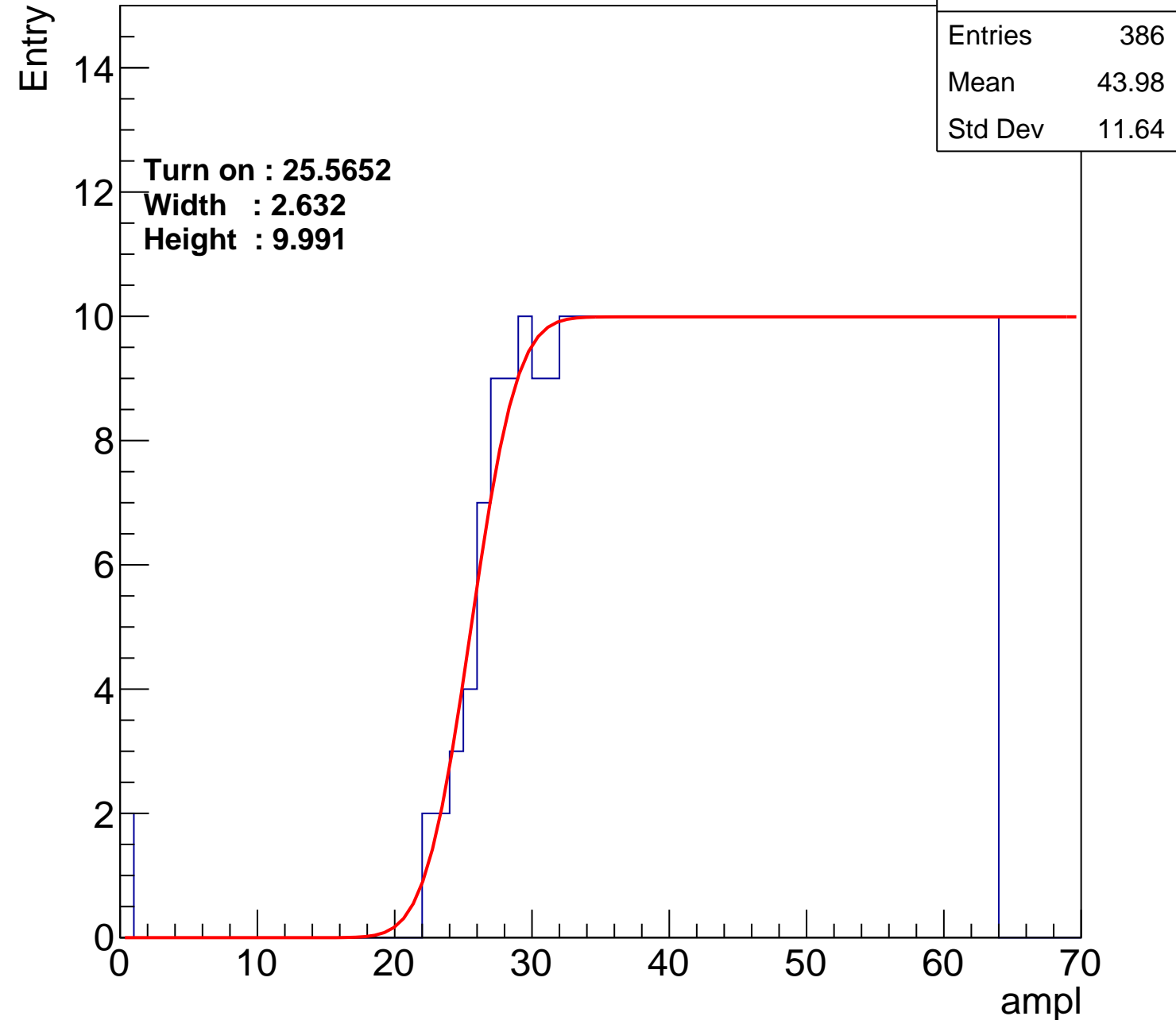
Width : 2.632

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch19

calib_packv5_042523_0143.root, FC#3, port B3

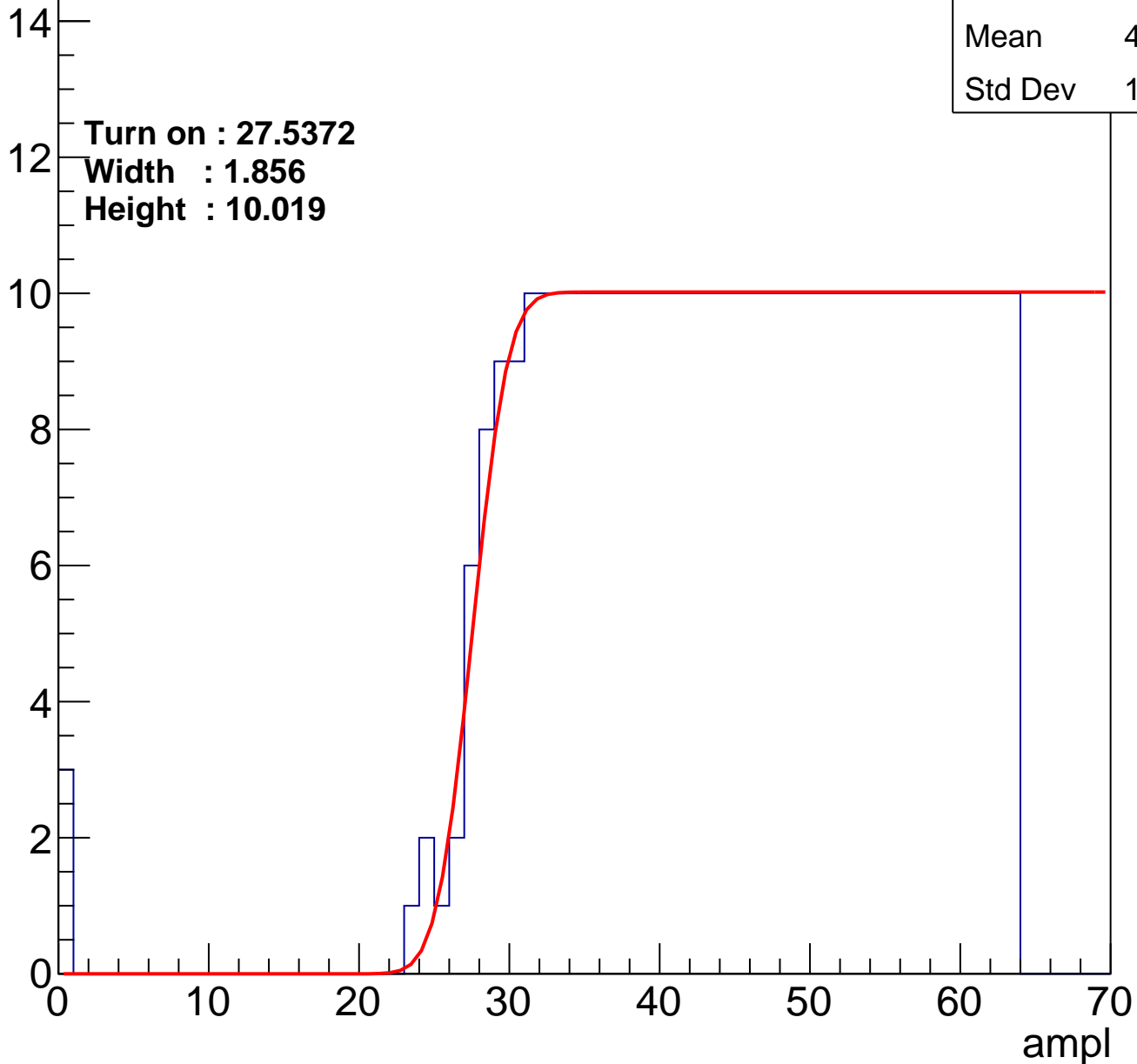
Entries	371
Mean	44.68
Std Dev	11.42

Turn on : 27.5372

Width : 1.856

Height : 10.019

Entry



B1L000S, U19-ch20

calib_packv5_042523_0143.root, FC#3, port B3

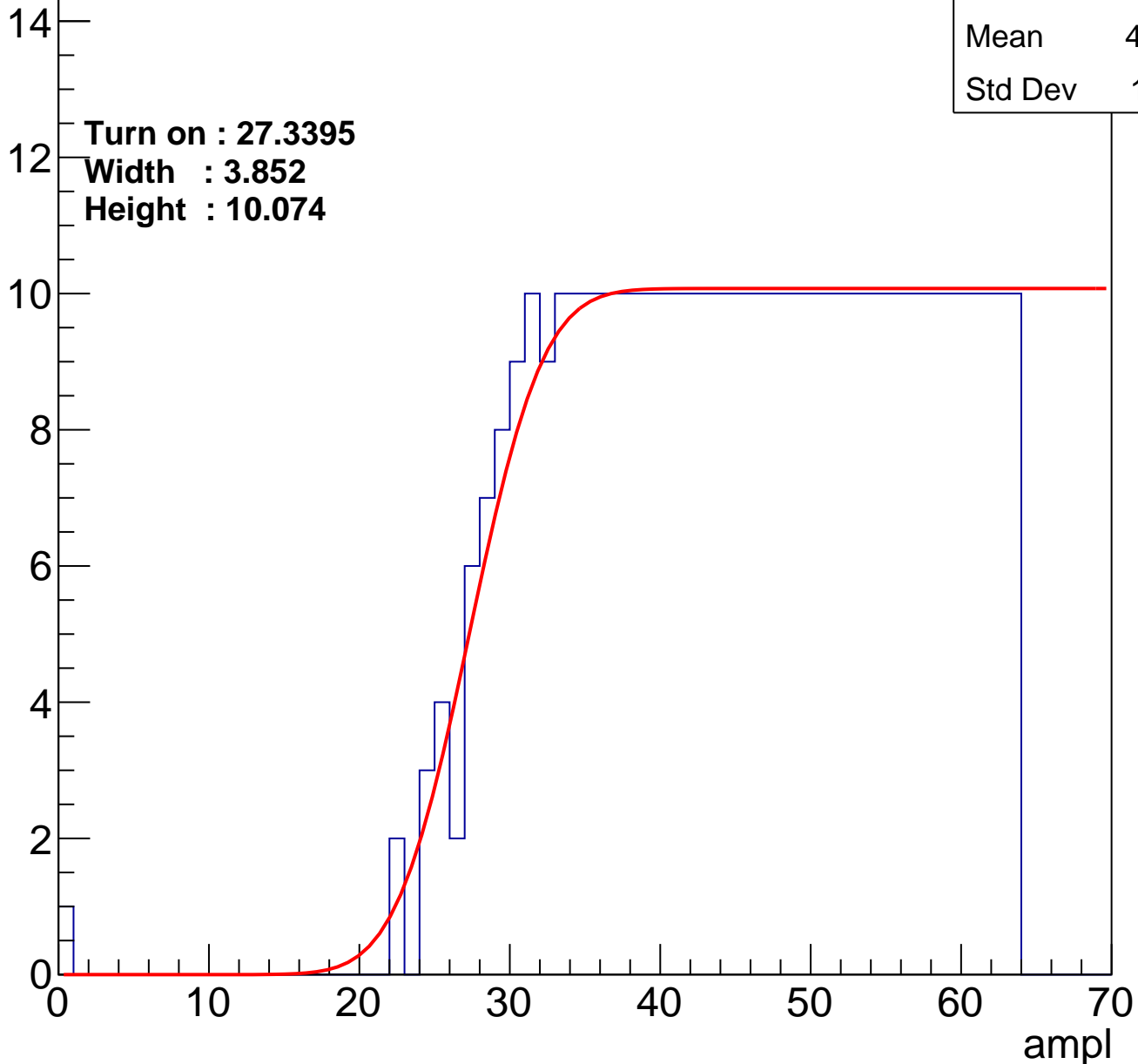
Entries	371
Mean	44.76
Std Dev	11.11

Turn on : 27.3395

Width : 3.852

Height : 10.074

Entry



B1L000S, U19-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.16
Std Dev	11.52

Turn on : 26.2566

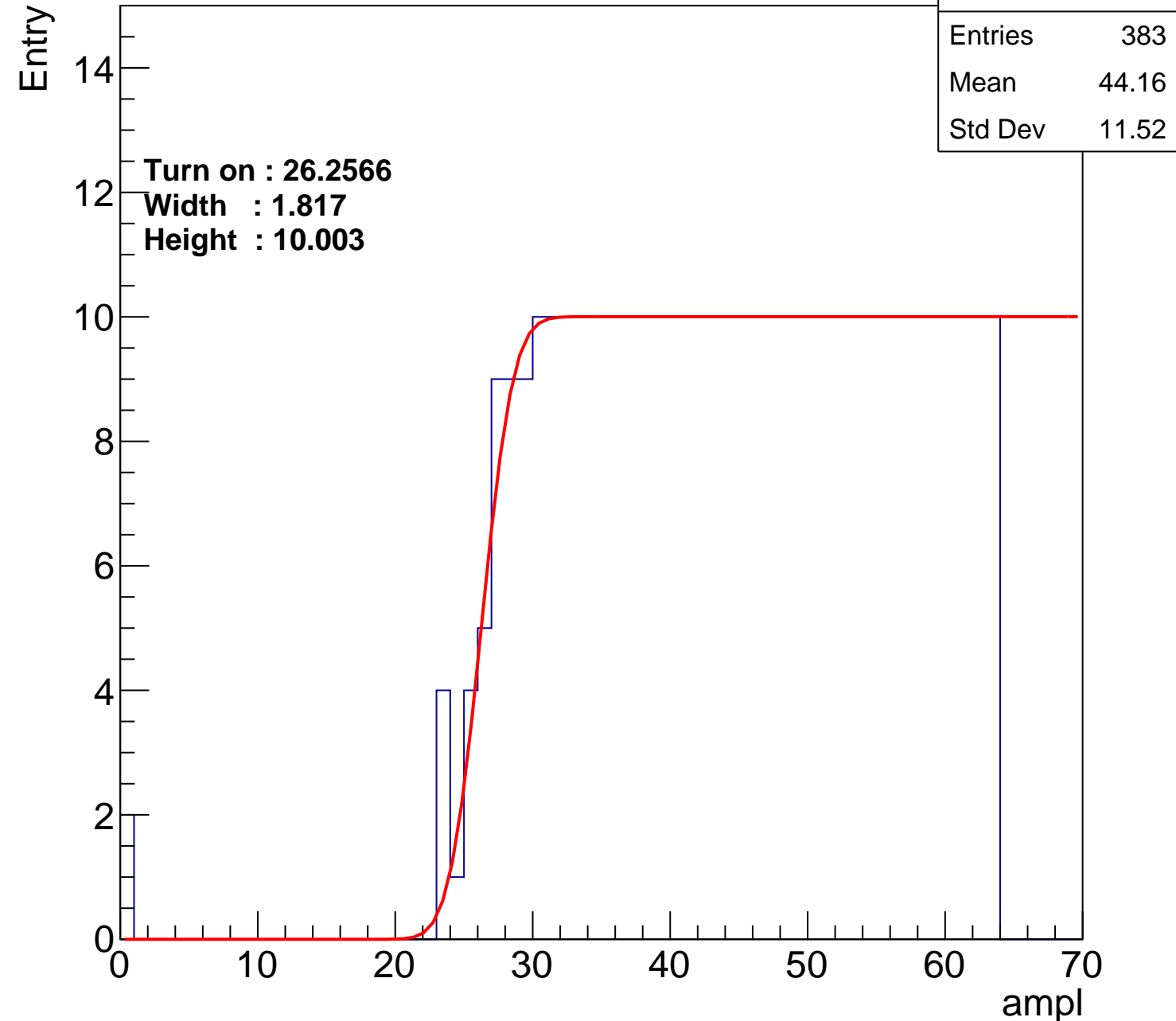
Width : 1.817

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.83
Std Dev	11.68

Turn on : 25.2491

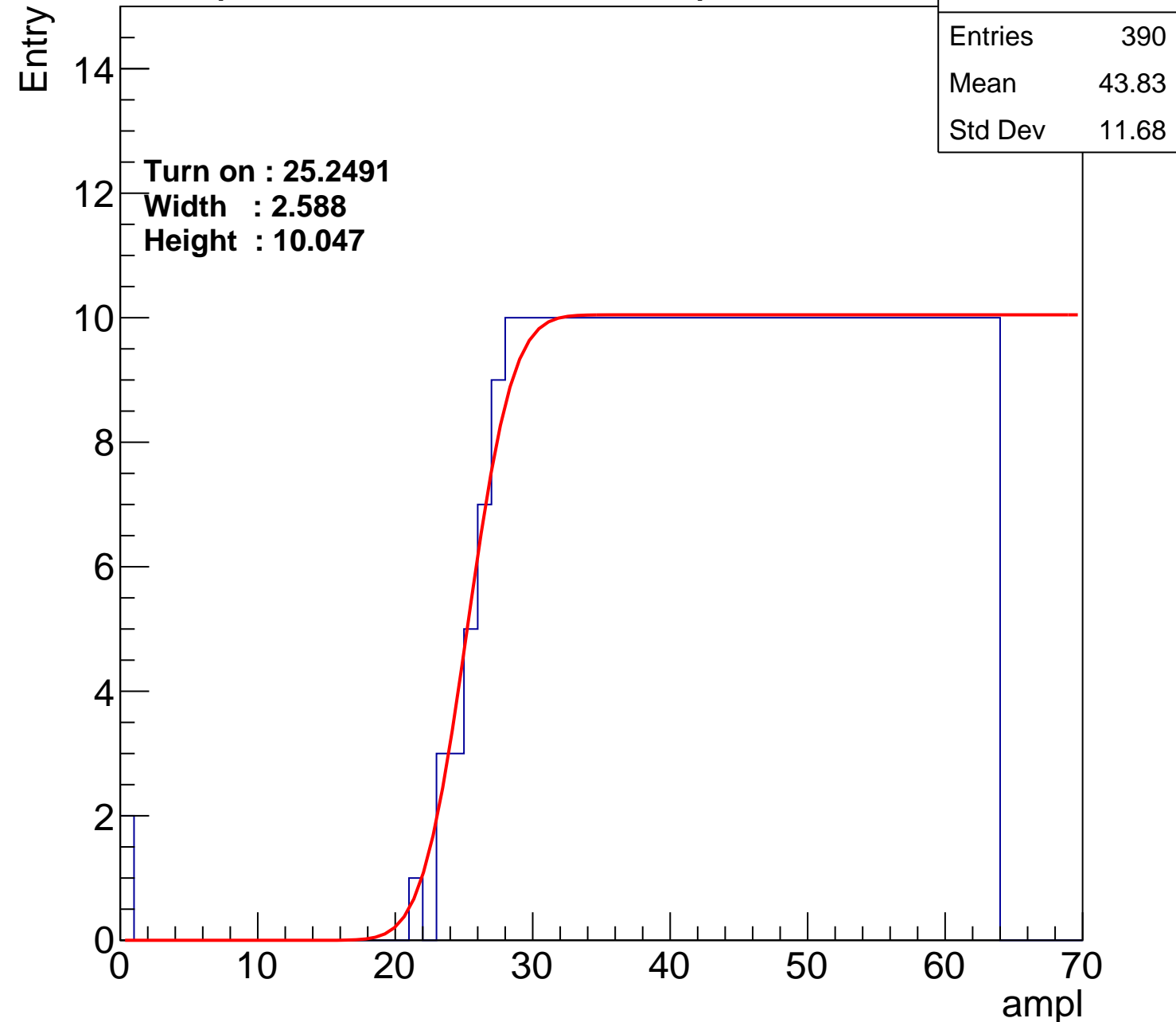
Width : 2.588

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch23

calib_packv5_042523_0143.root, FC#3, port B3

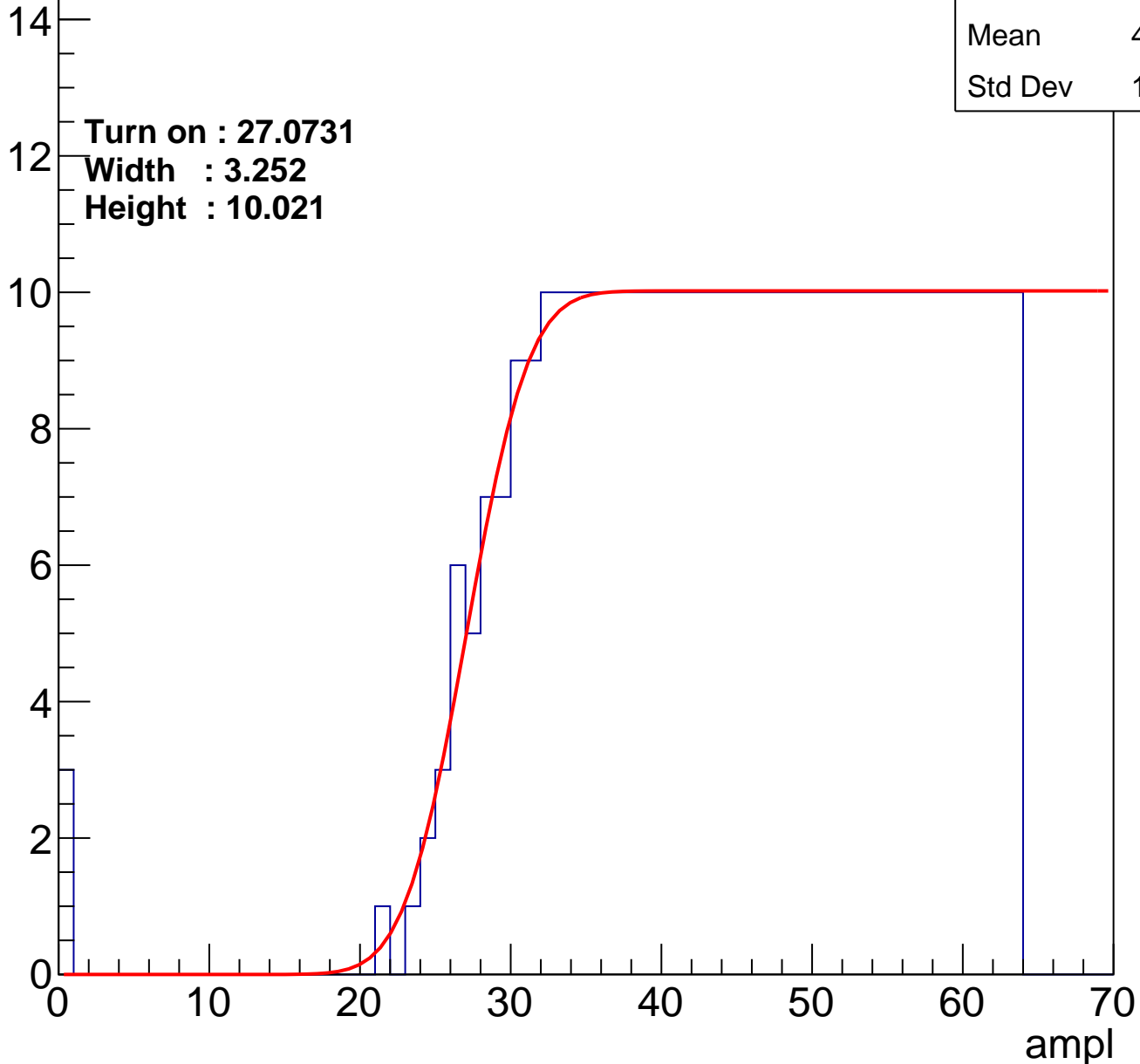
Entries	373
Mean	44.52
Std Dev	11.55

Turn on : 27.0731

Width : 3.252

Height : 10.021

Entry



B1L000S, U19-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.73
Std Dev	11.58

Turn on : 28.2503

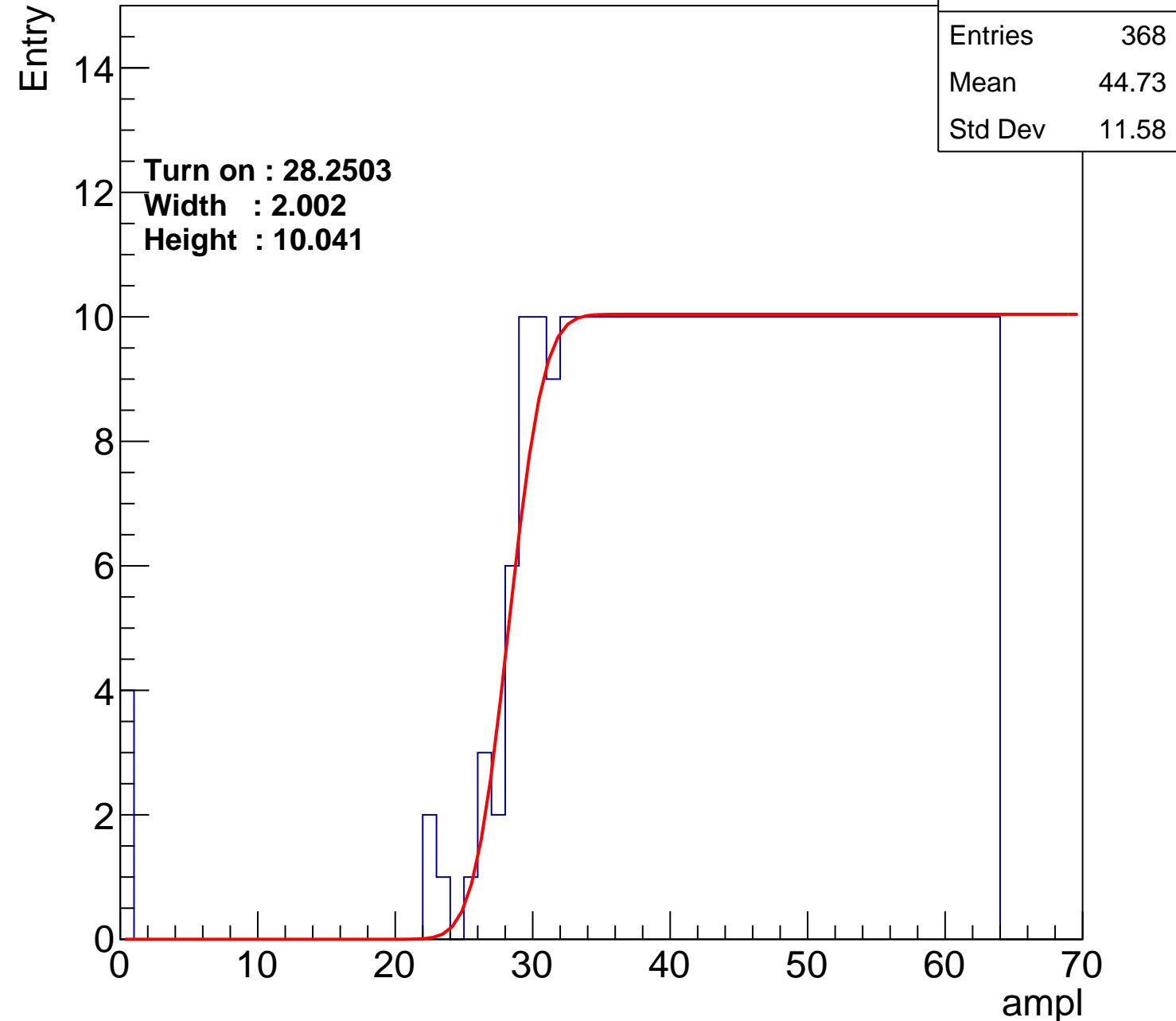
Width : 2.002

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.81
Std Dev	11.02

Turn on : 27.1895

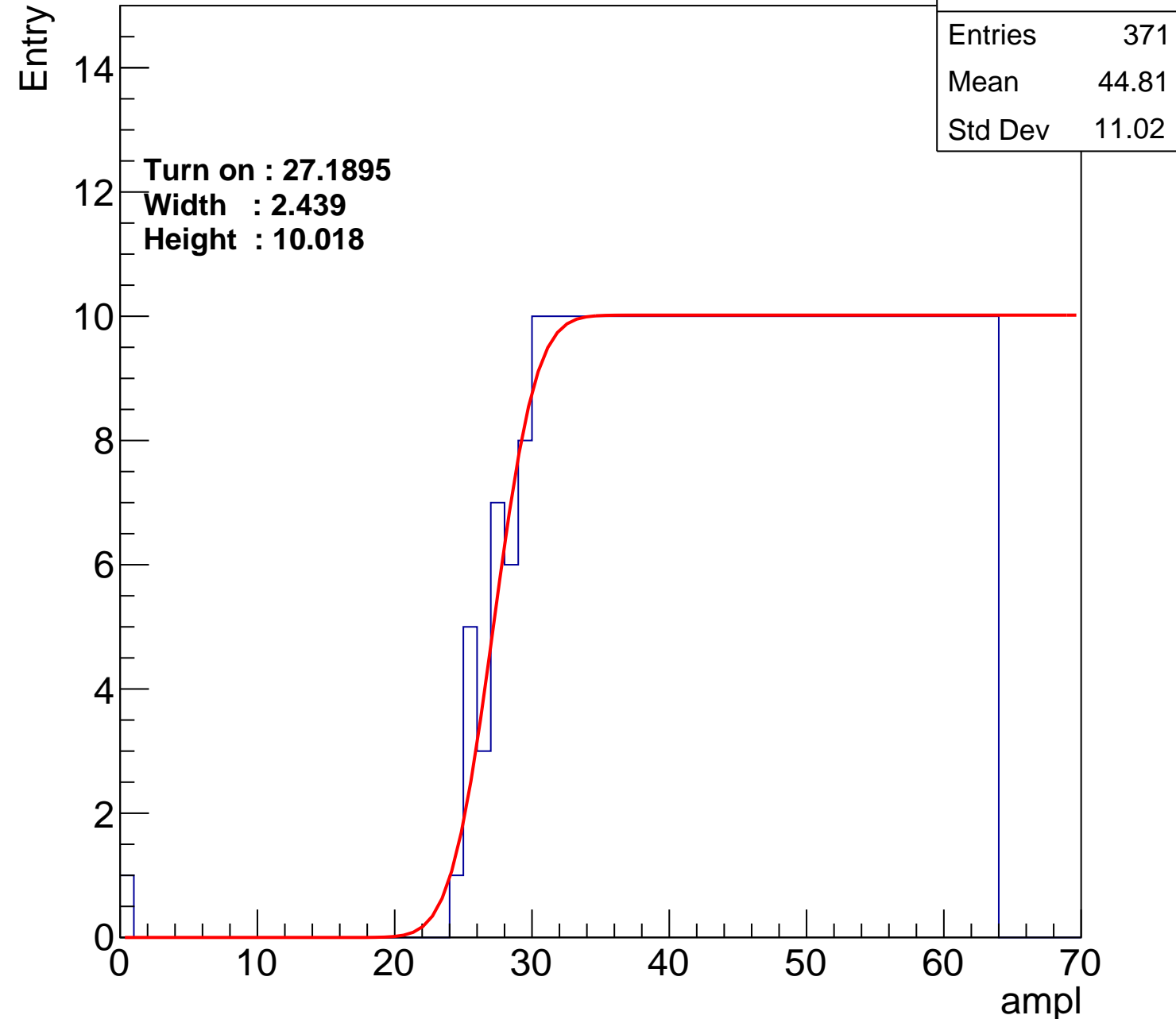
Width : 2.439

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.65
Std Dev	11.27

Turn on : 27.1655

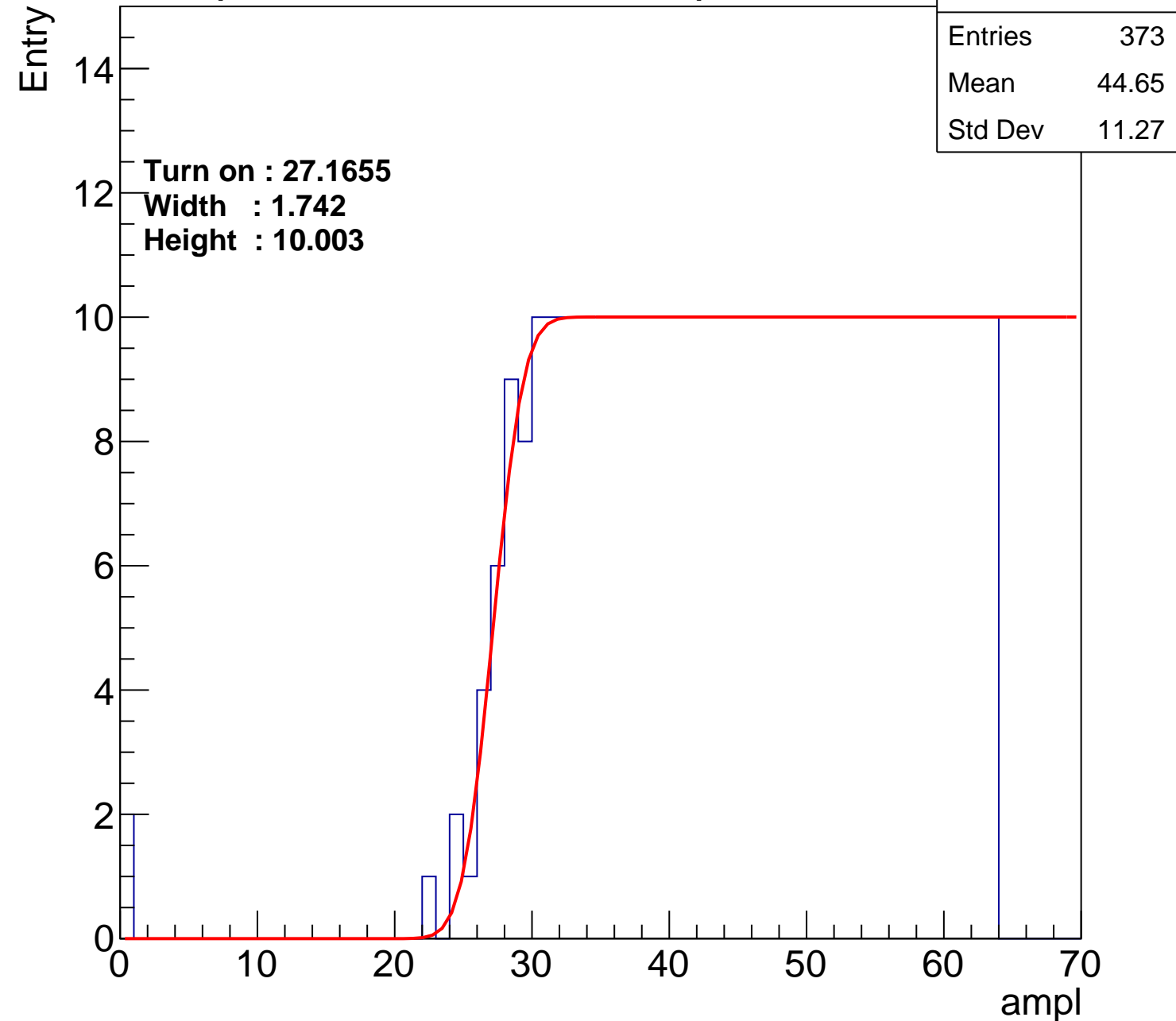
Width : 1.742

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch27

calib_packv5_042523_0143.root, FC#3, port B3

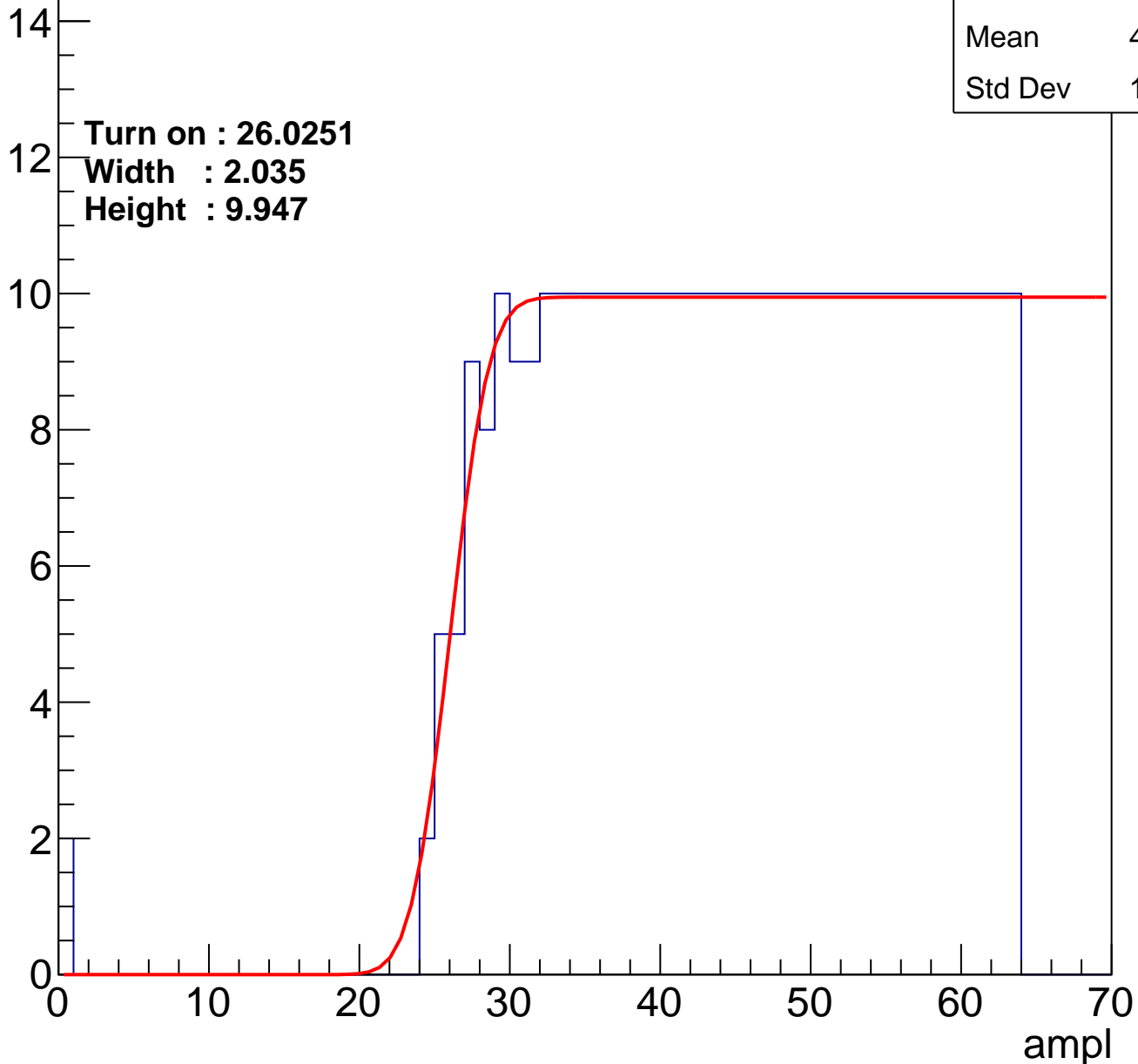
Entries	379
Mean	44.35
Std Dev	11.42

Turn on : 26.0251

Width : 2.035

Height : 9.947

Entry



B1L000S, U19-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.0434

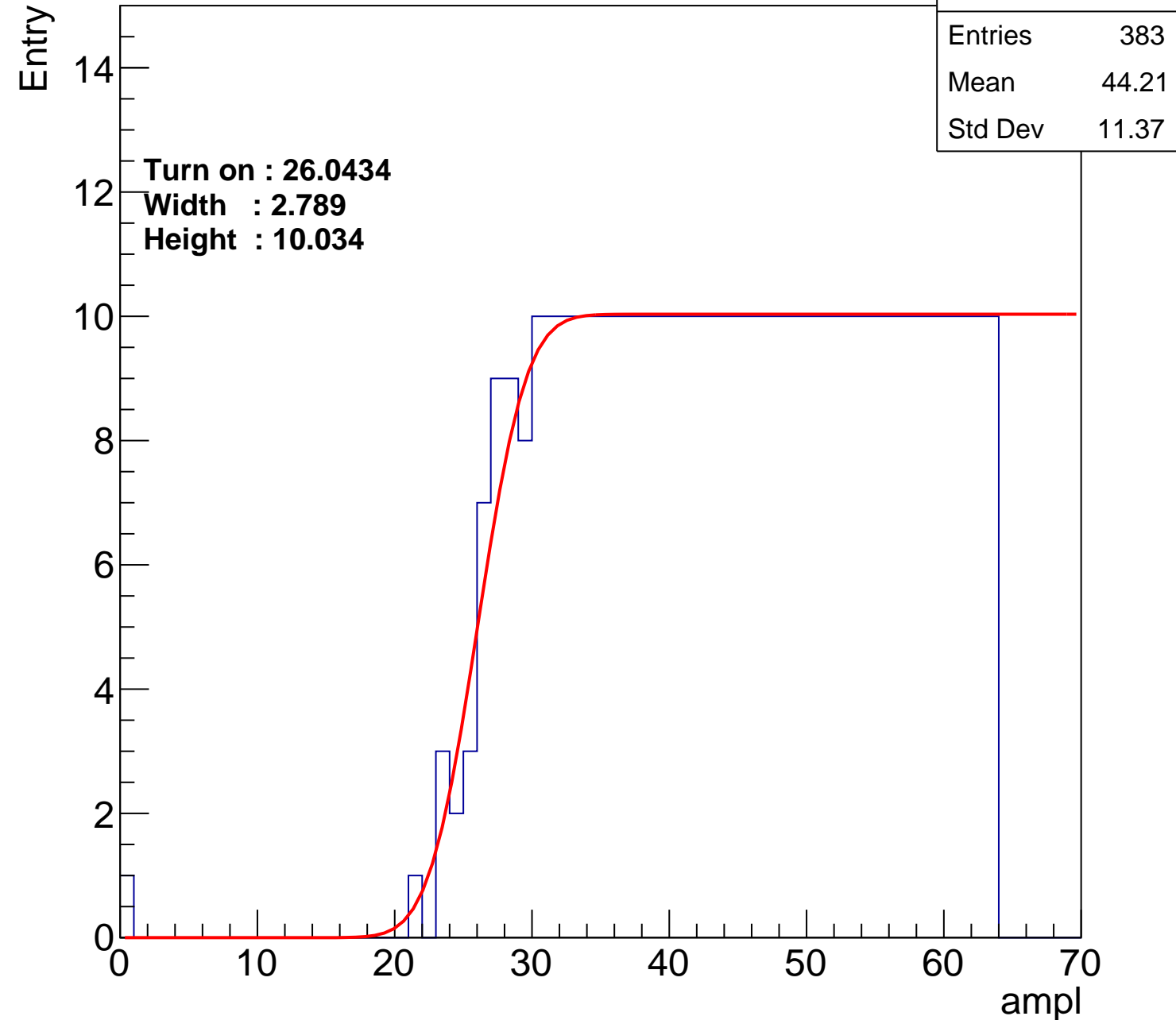
Width : 2.789

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch29

calib_packv5_042523_0143.root, FC#3, port B3

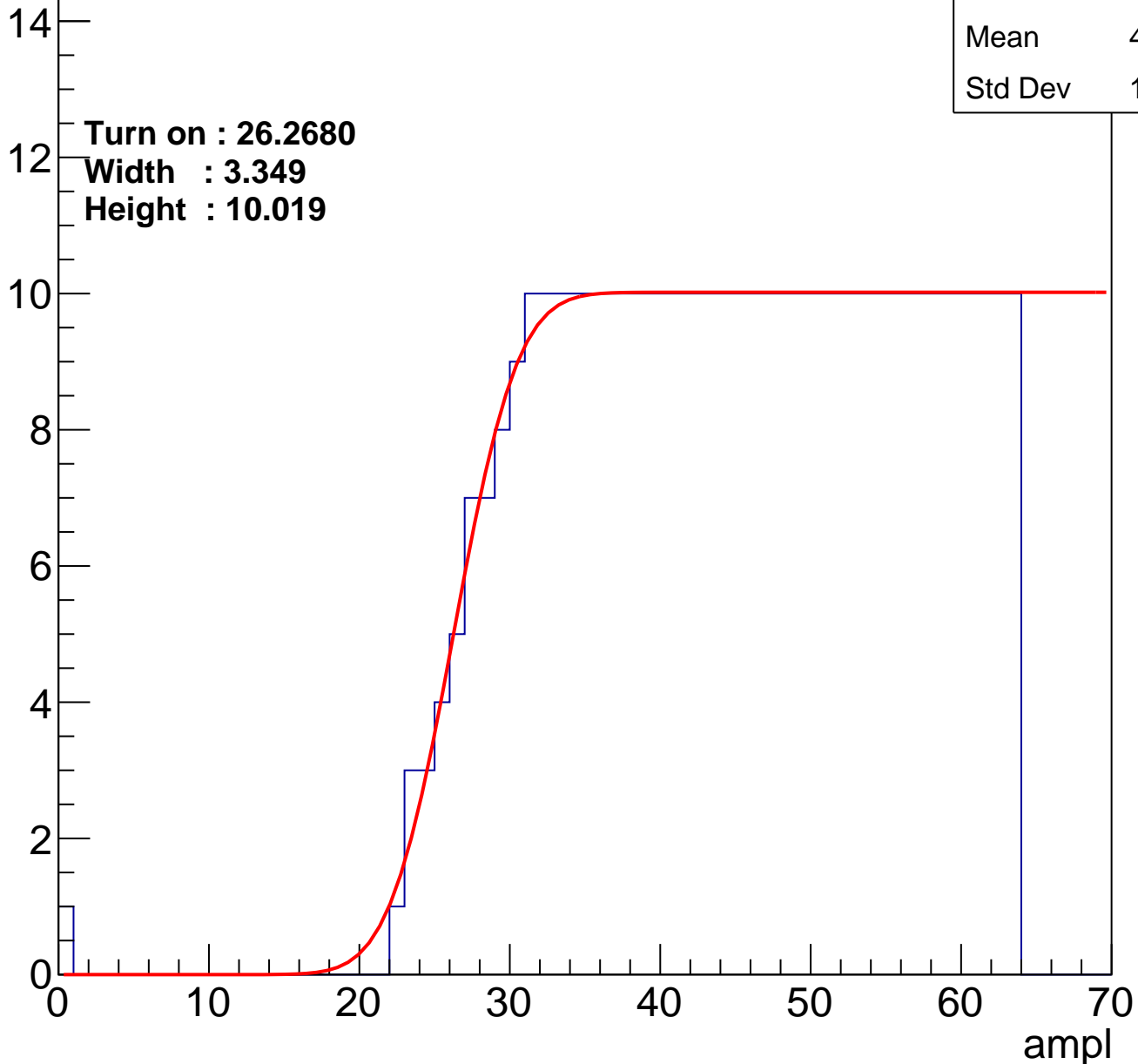
Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.2680

Width : 3.349

Height : 10.019

Entry



B1L000S, U19-ch30

calib_packv5_042523_0143.root, FC#3, port B3

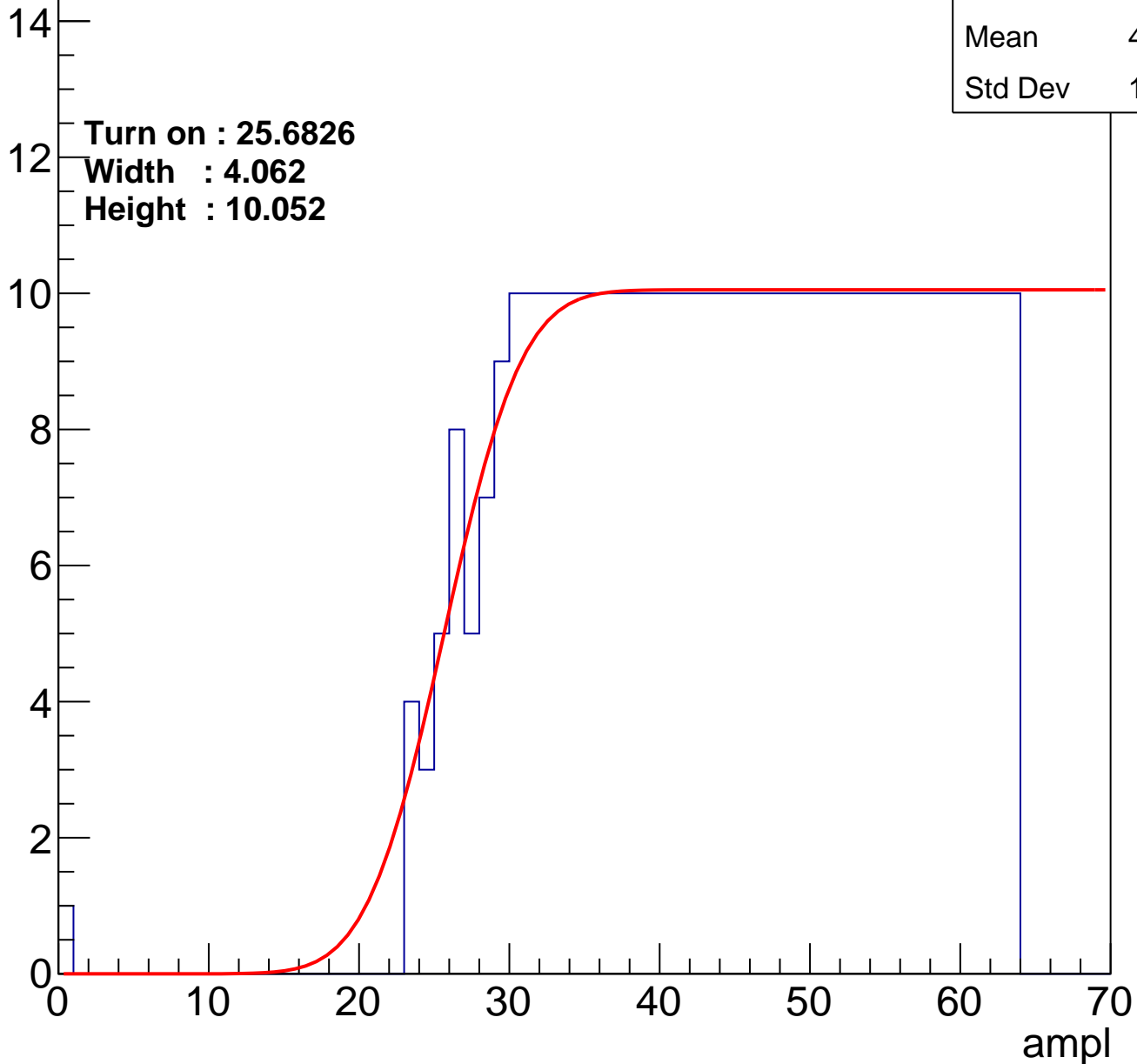
Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.6826

Width : 4.062

Height : 10.052

Entry



B1L000S, U19-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.7190

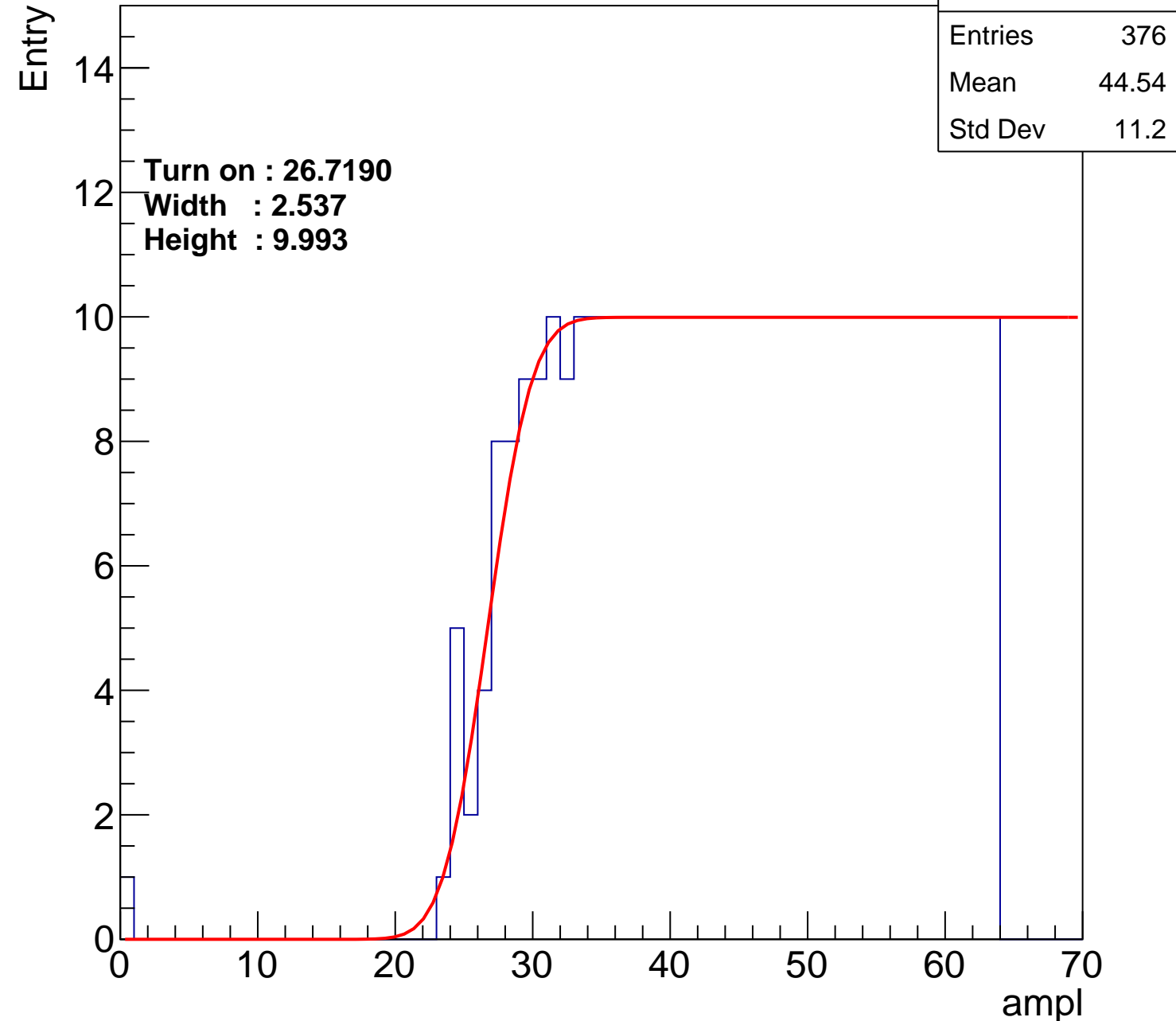
Width : 2.537

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.21
Std Dev	11.36

Turn on : 25.4739

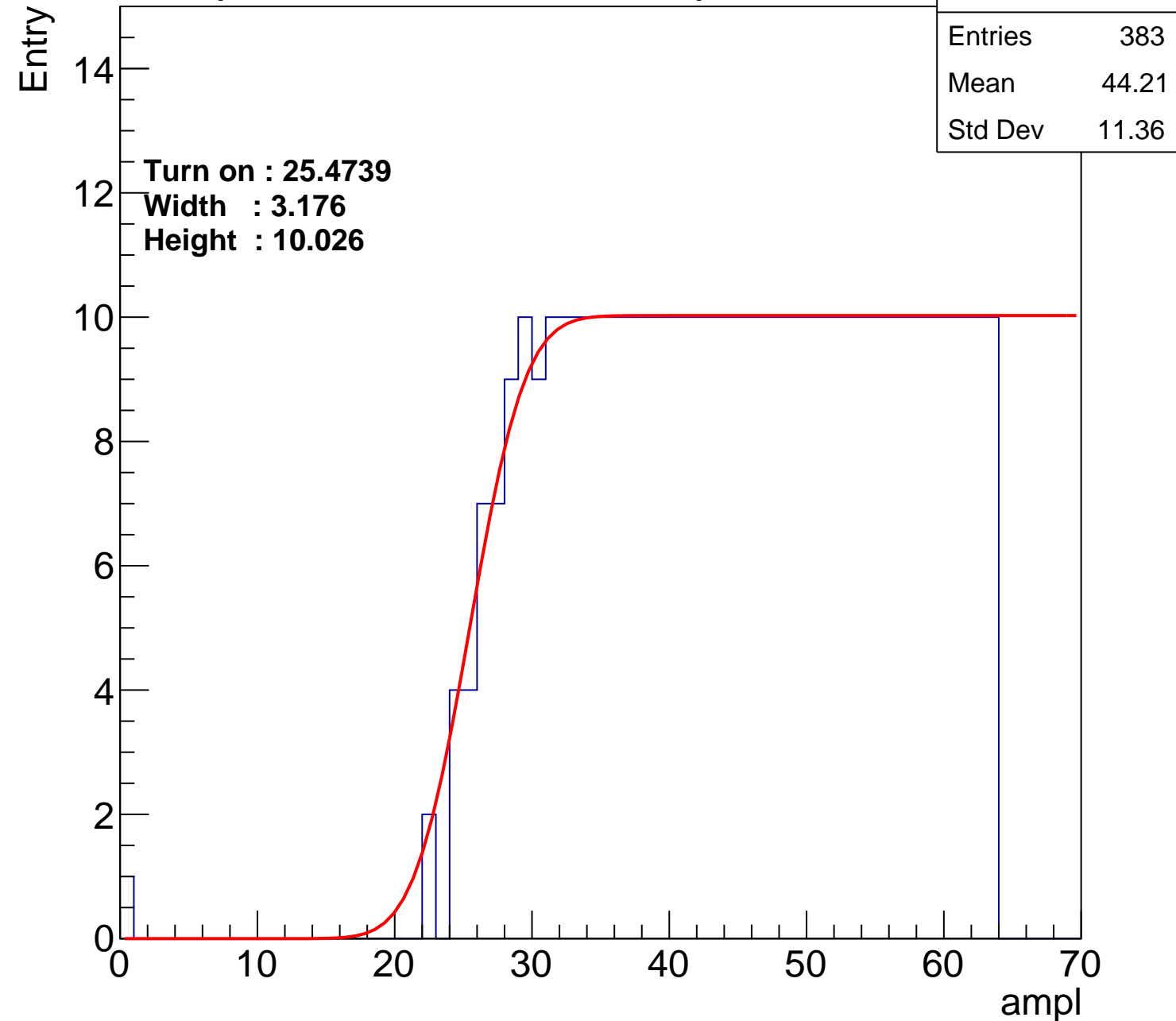
Width : 3.176

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch33

calib_packv5_042523_0143.root, FC#3, port B3

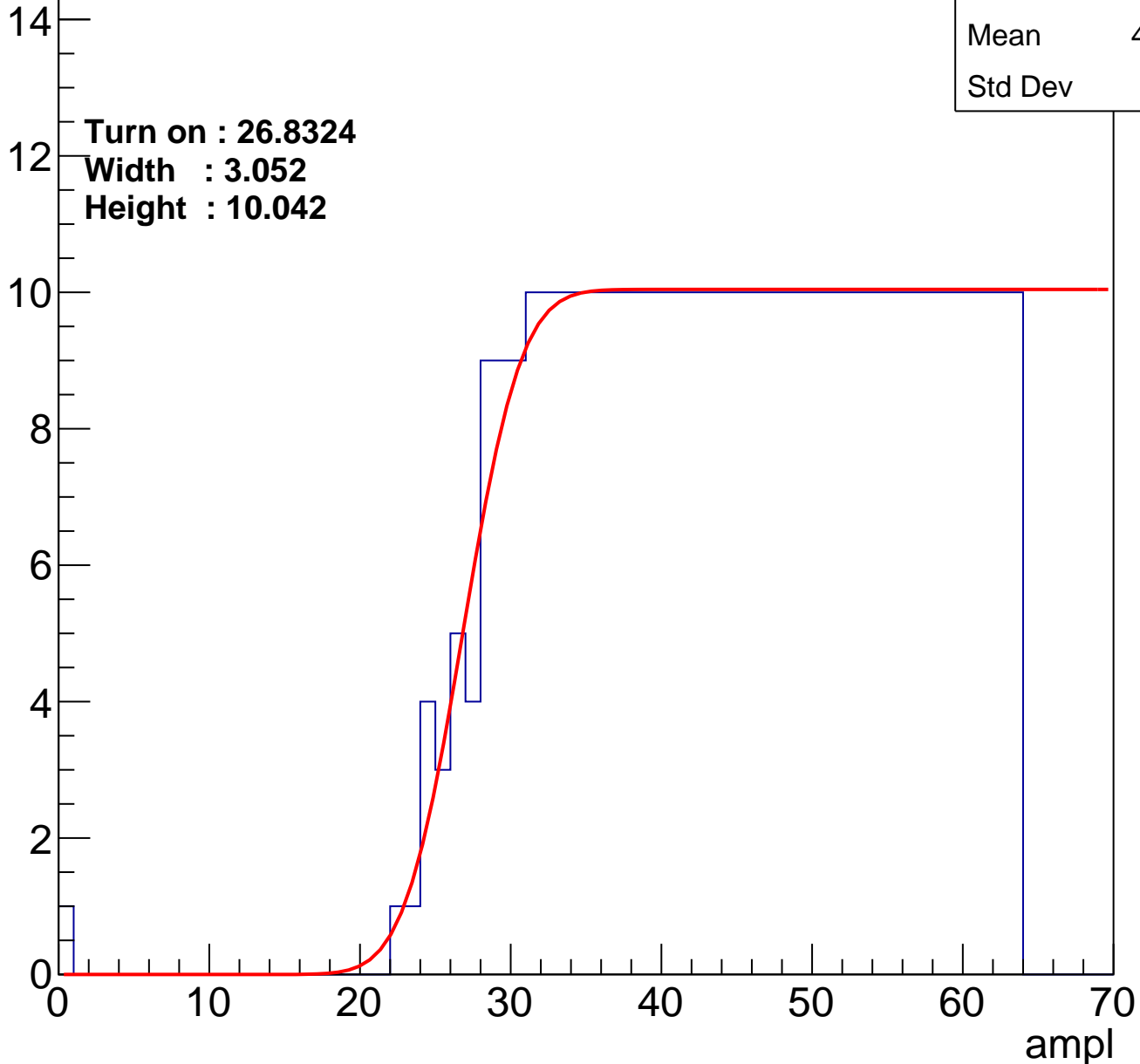
Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.8324

Width : 3.052

Height : 10.042

Entry



B1L000S, U19-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.8
Std Dev	11.89

Turn on : 25.9115

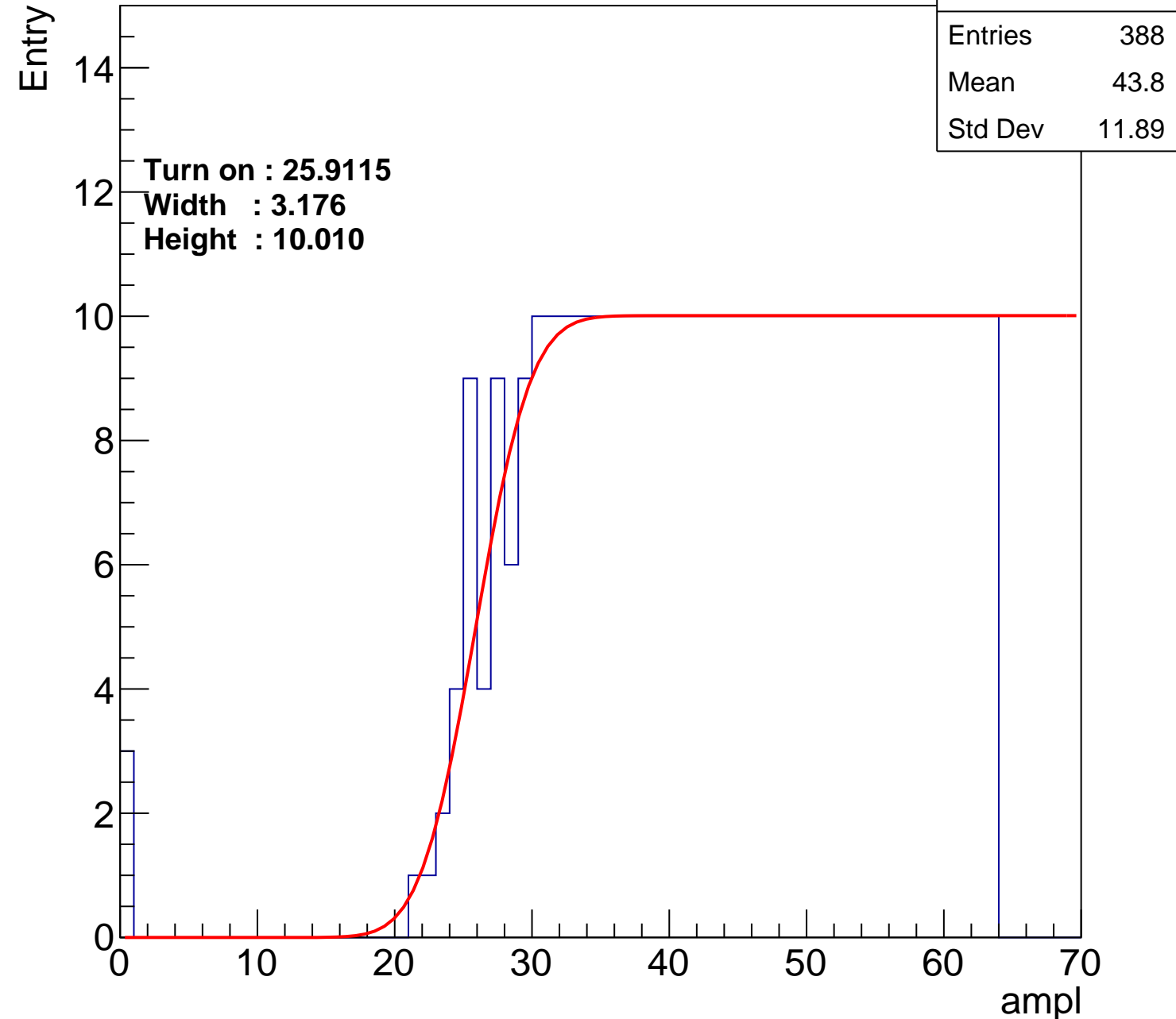
Width : 3.176

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.4605

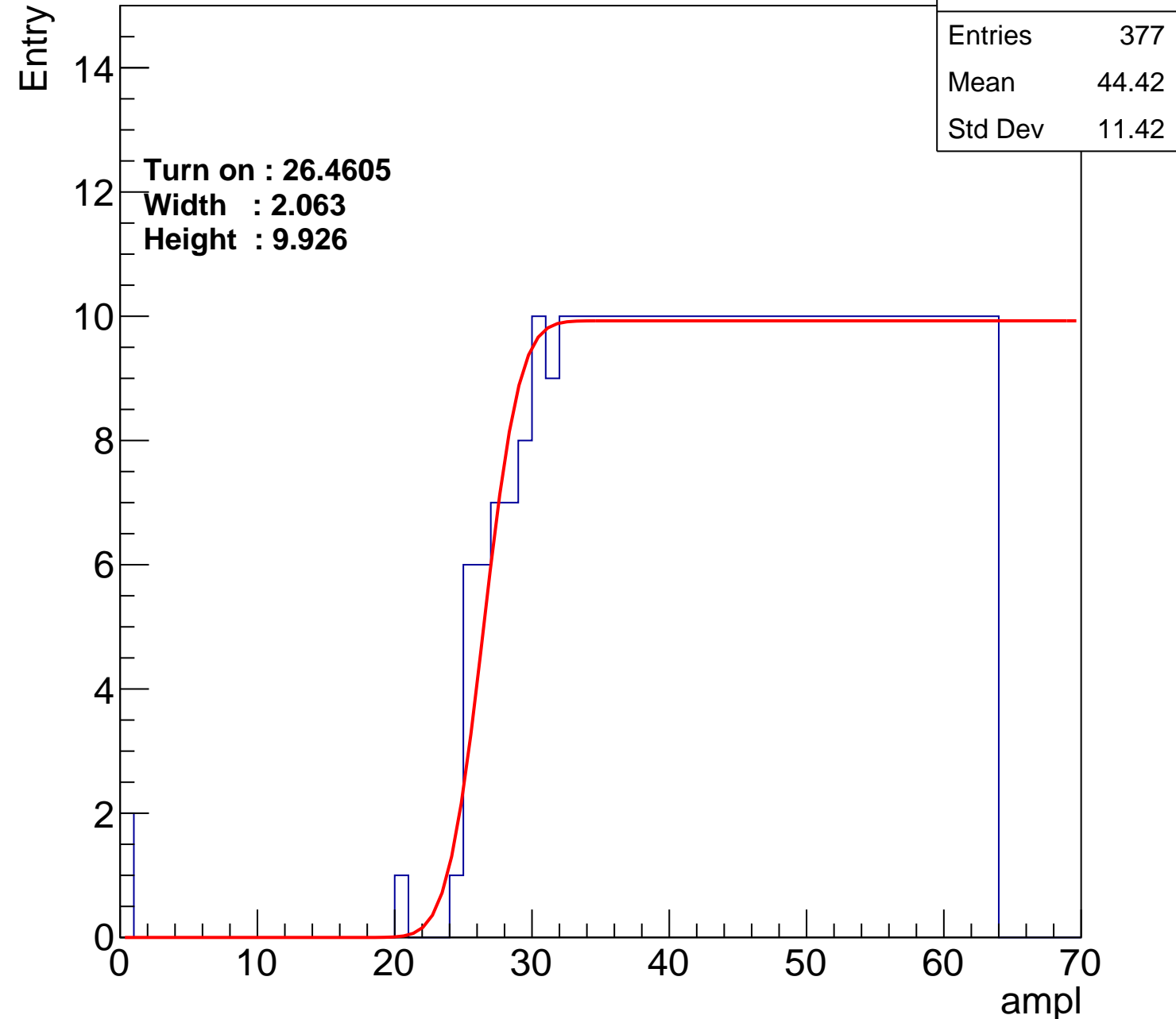
Width : 2.063

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.9395

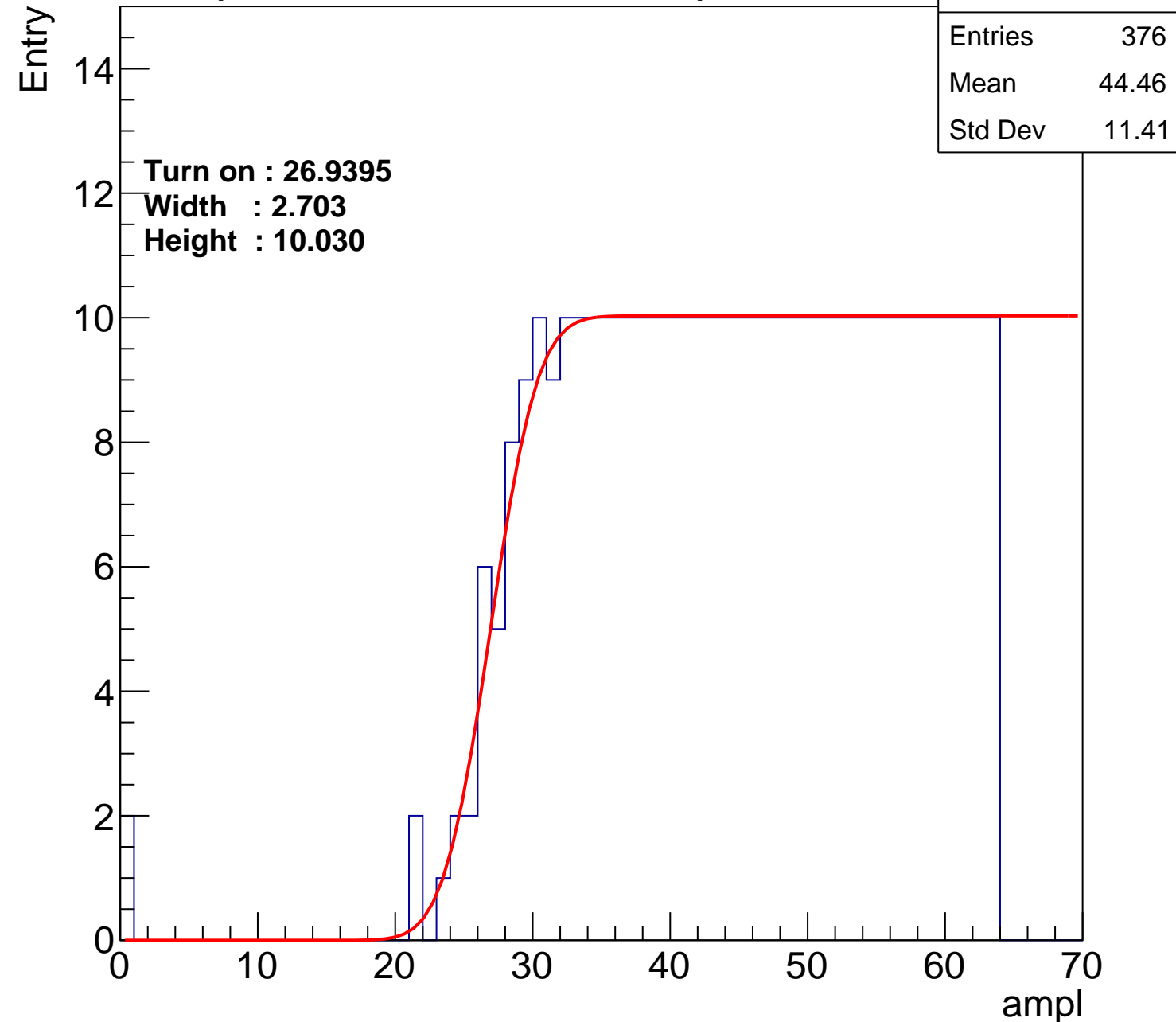
Width : 2.703

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.78
Std Dev	11.23

Turn on : 27.7102

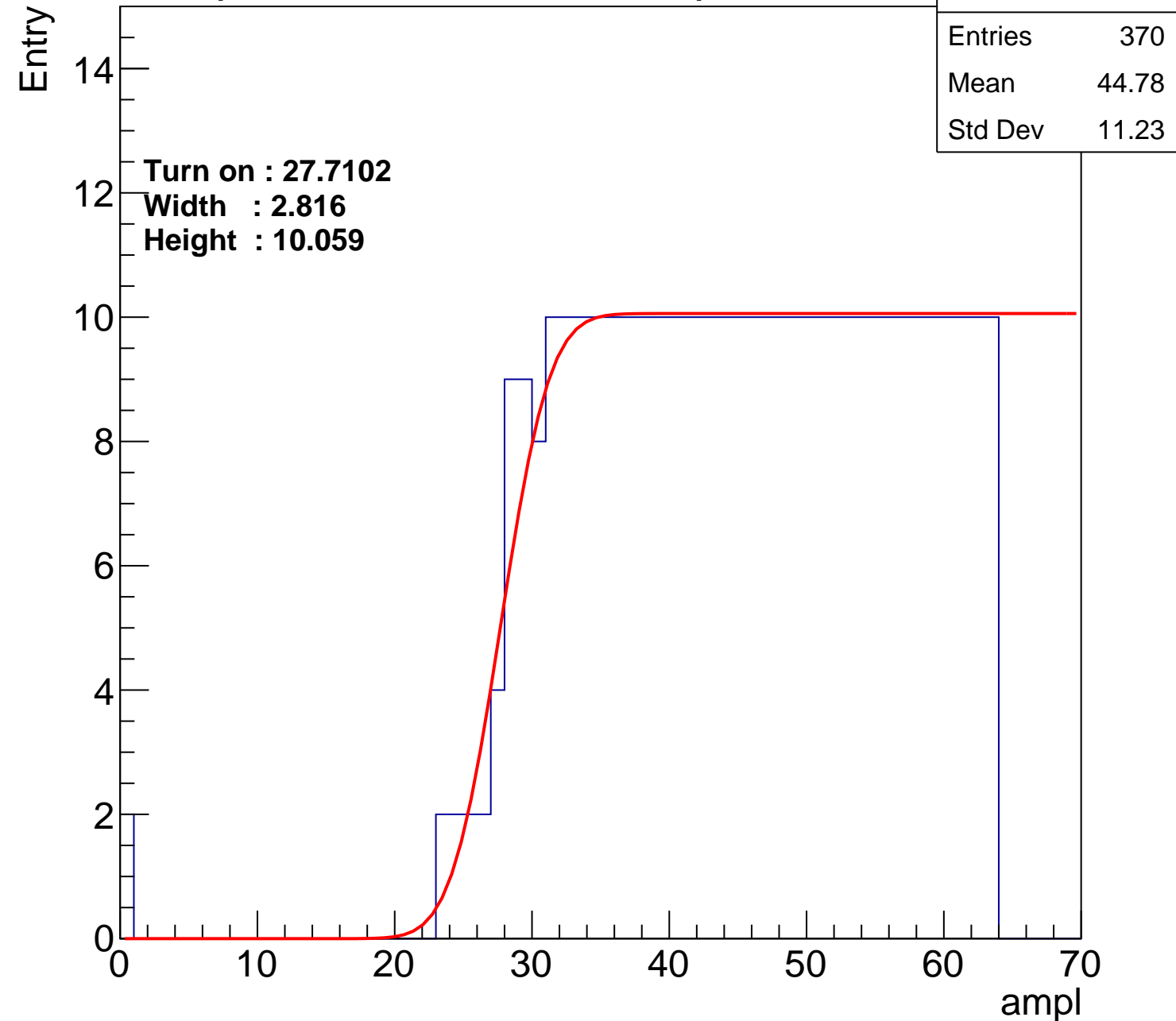
Width : 2.816

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 26.4358

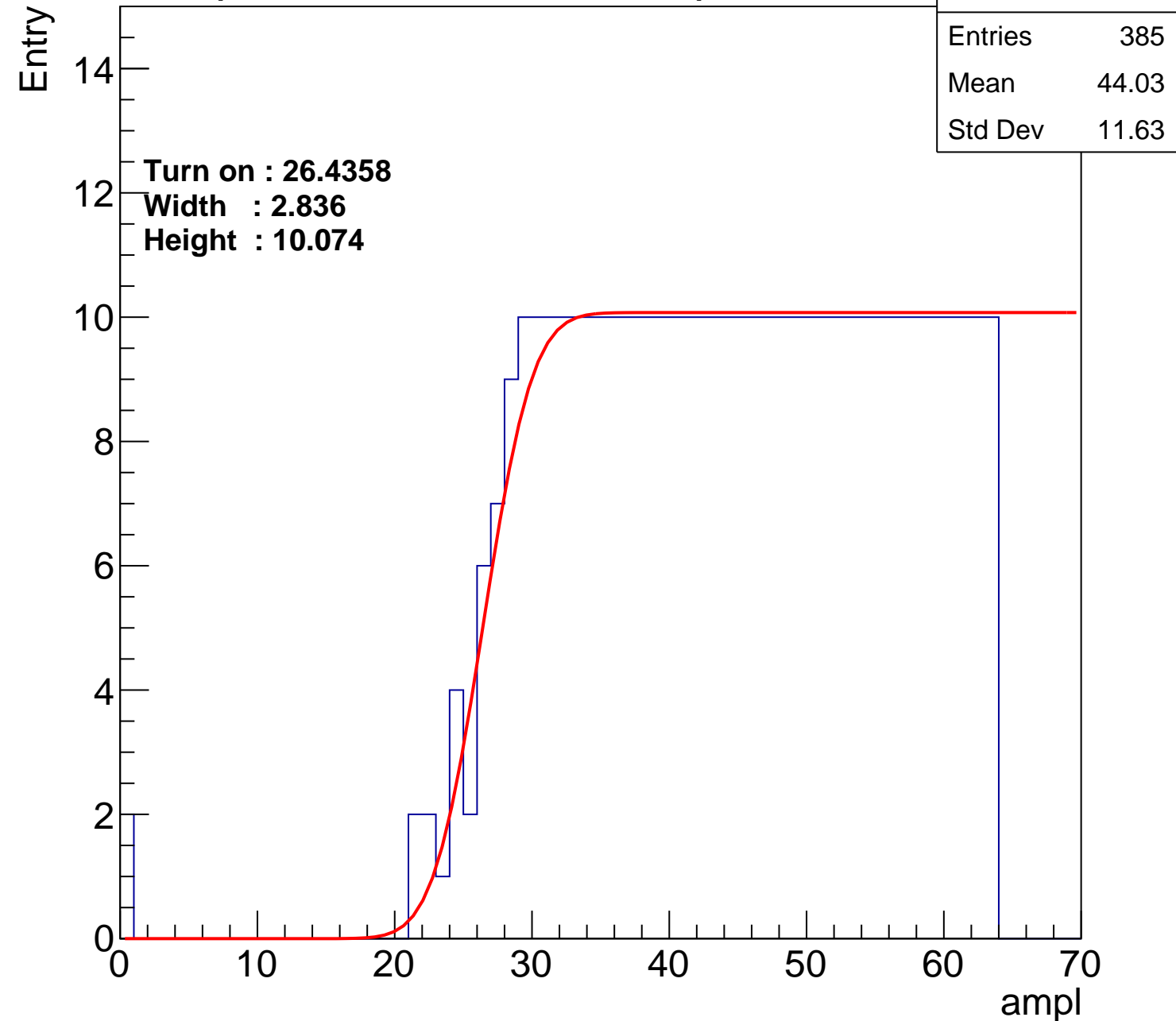
Width : 2.836

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch39

calib_packv5_042523_0143.root, FC#3, port B3

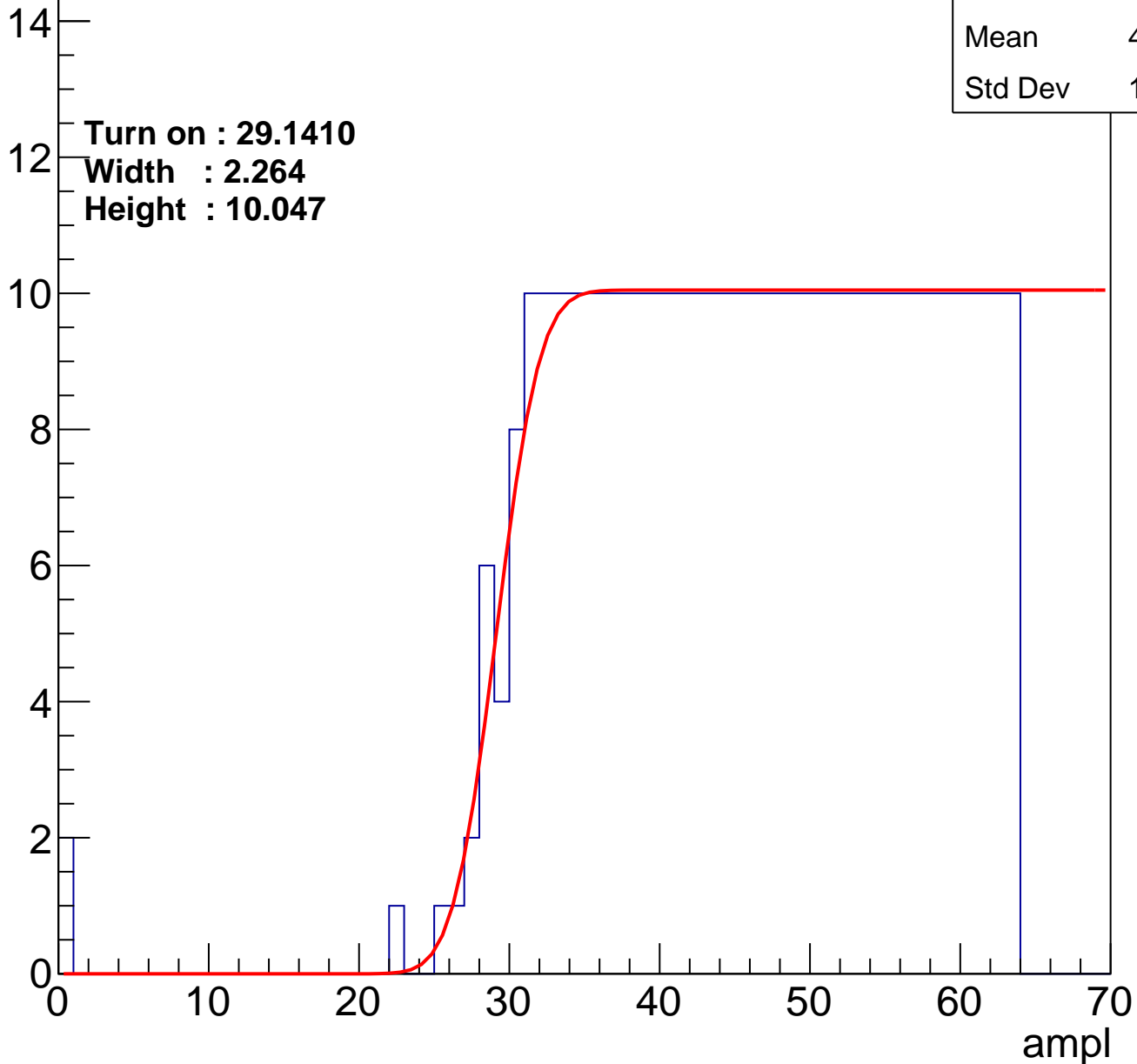
Entries	355
Mean	45.52
Std Dev	10.84

Turn on : 29.1410

Width : 2.264

Height : 10.047

Entry



B1L000S, U19-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.3625

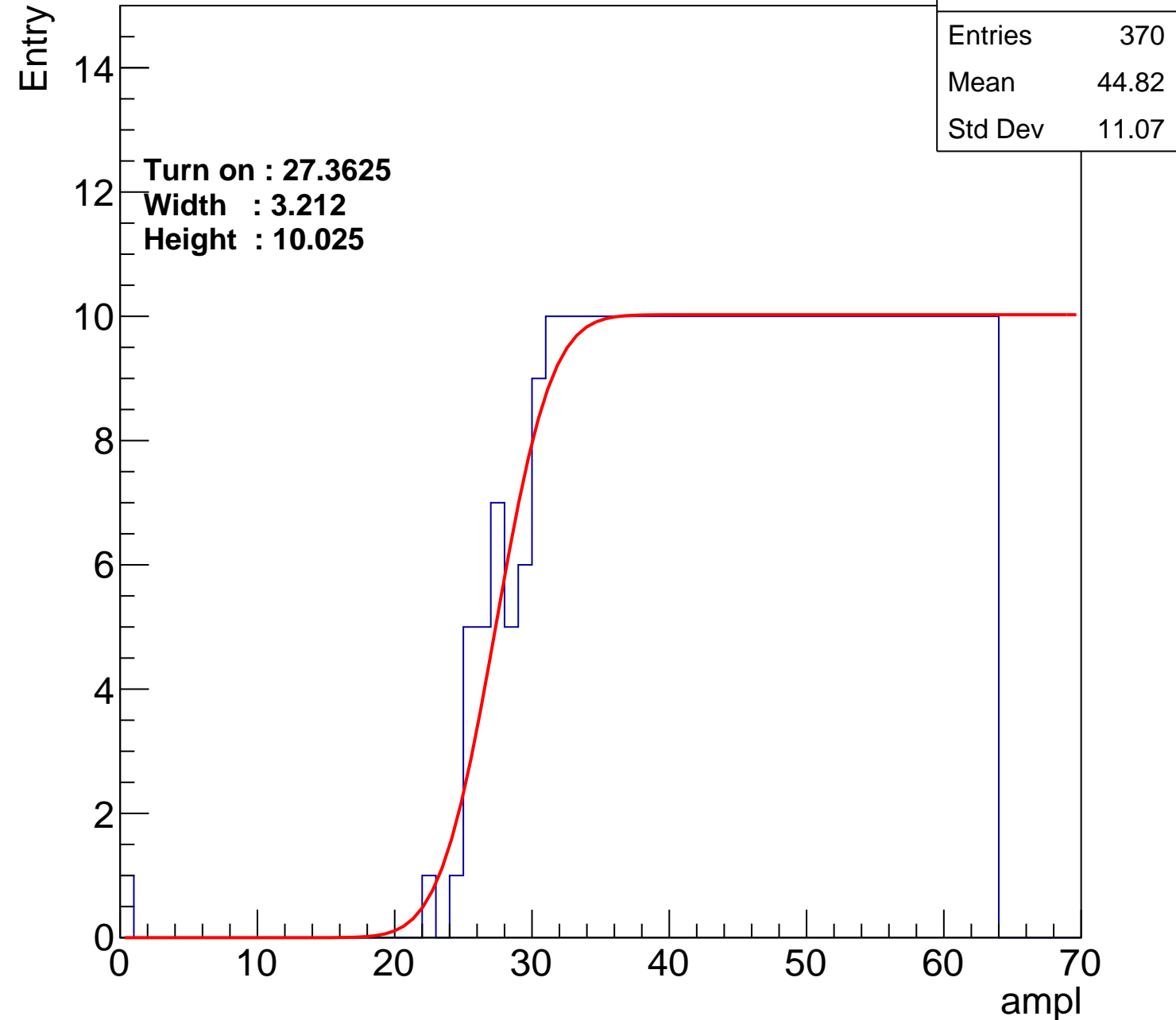
Width : 3.212

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch41

calib_packv5_042523_0143.root, FC#3, port B3

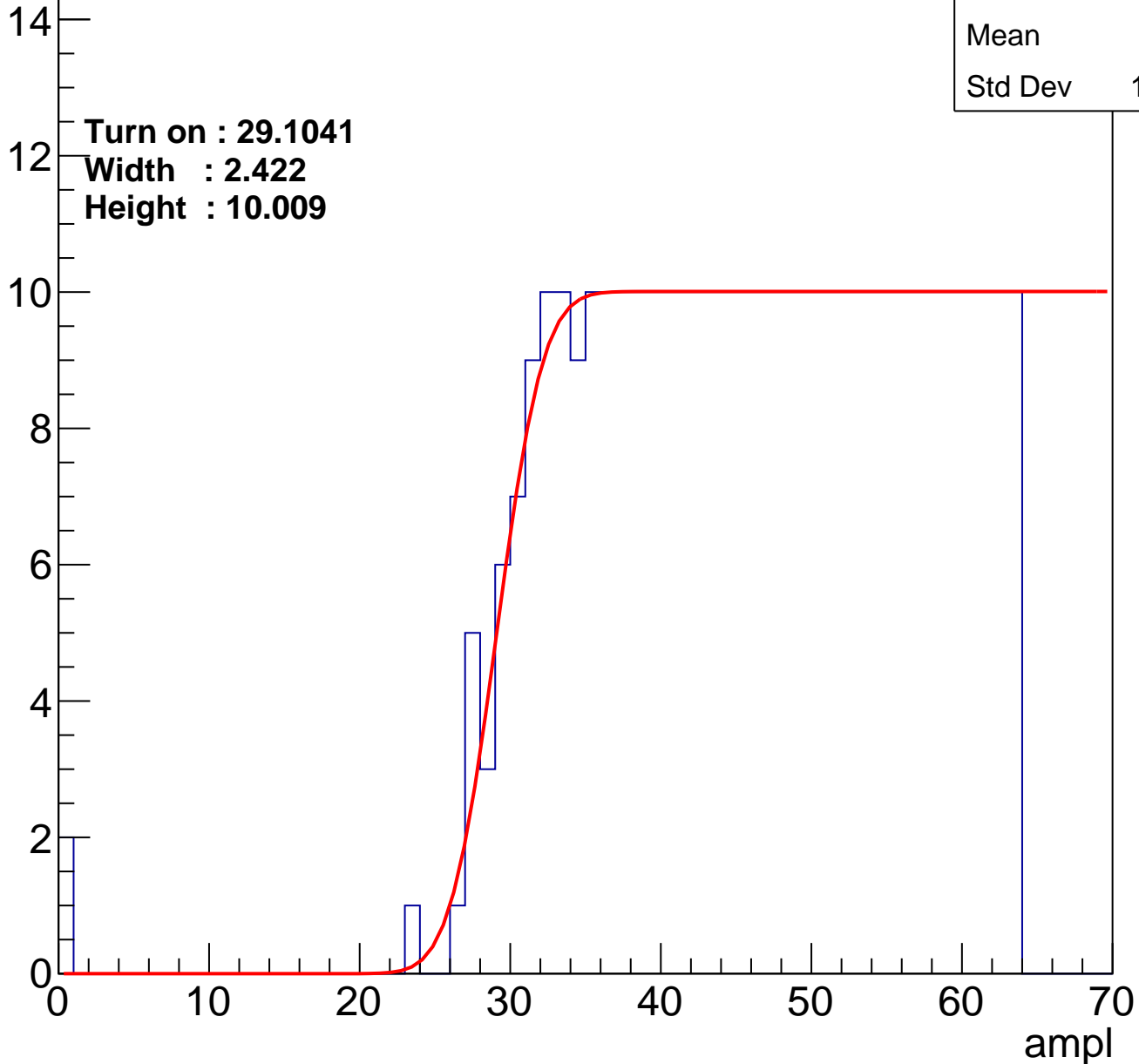
Entries	353
Mean	45.6
Std Dev	10.82

Turn on : 29.1041

Width : 2.422

Height : 10.009

Entry



B1L000S, U19-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 26.1856

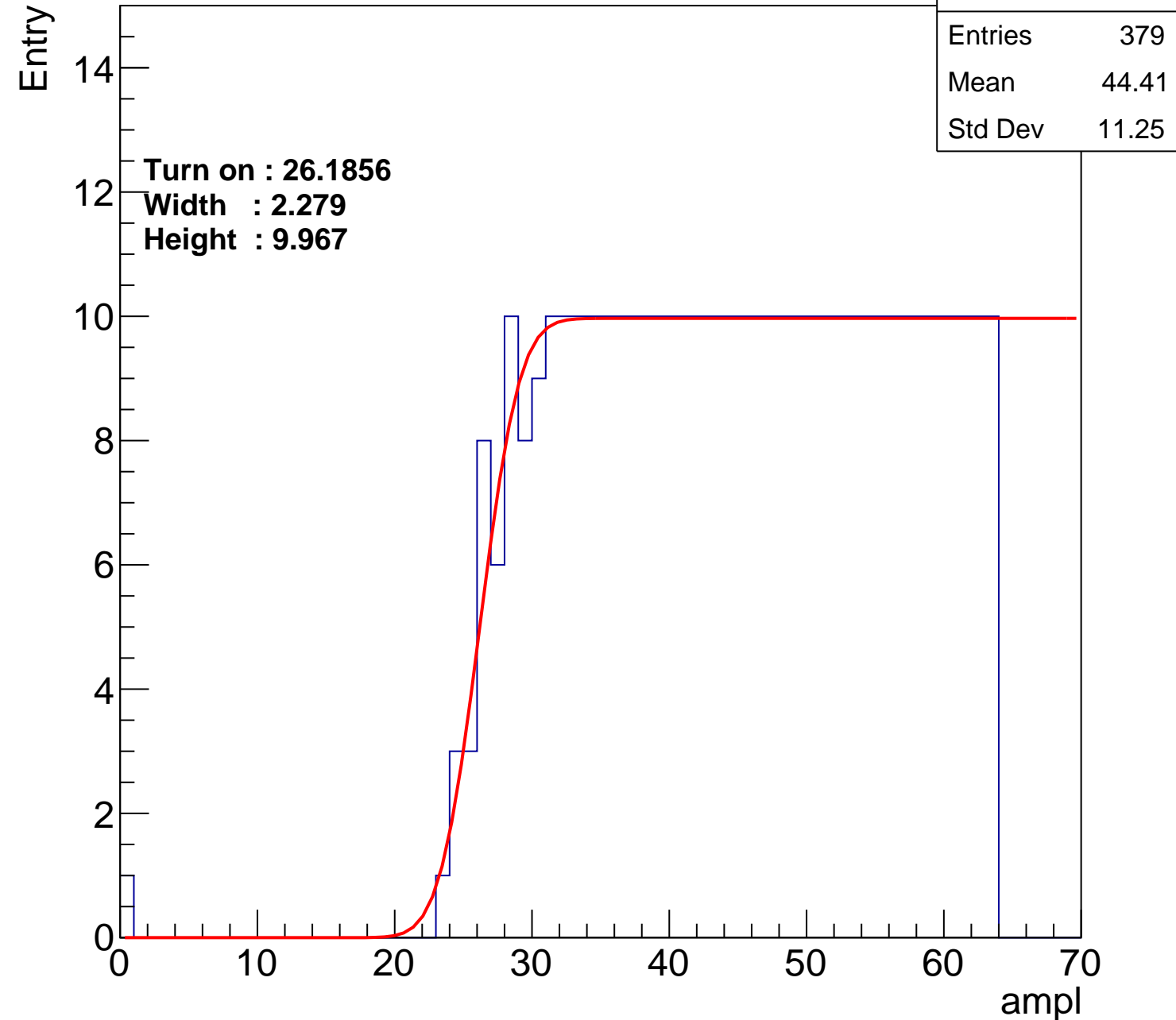
Width : 2.279

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.65
Std Dev	11.83

Turn on : 25.5211

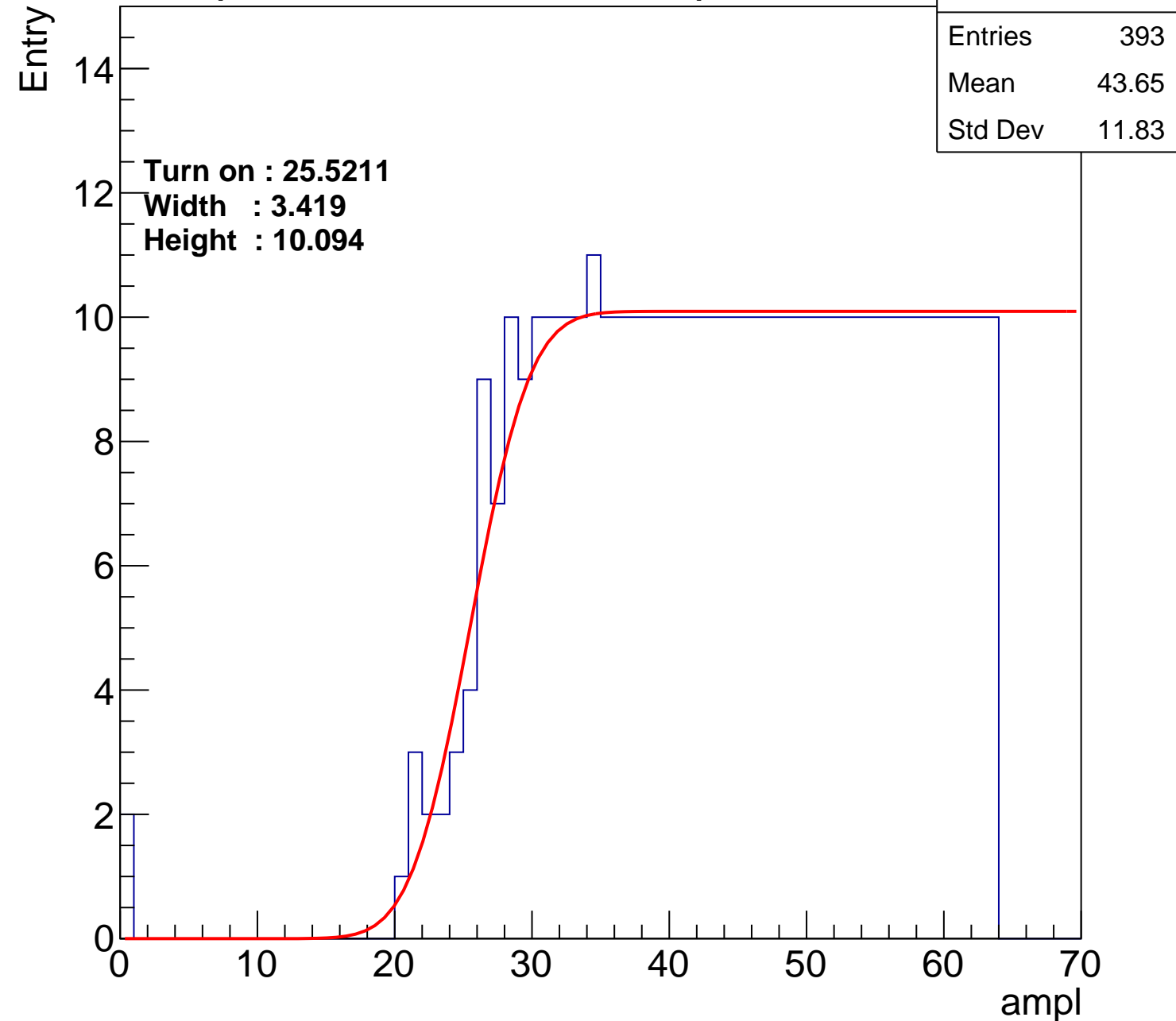
Width : 3.419

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.53
Std Dev	11.85

Turn on : 24.9299

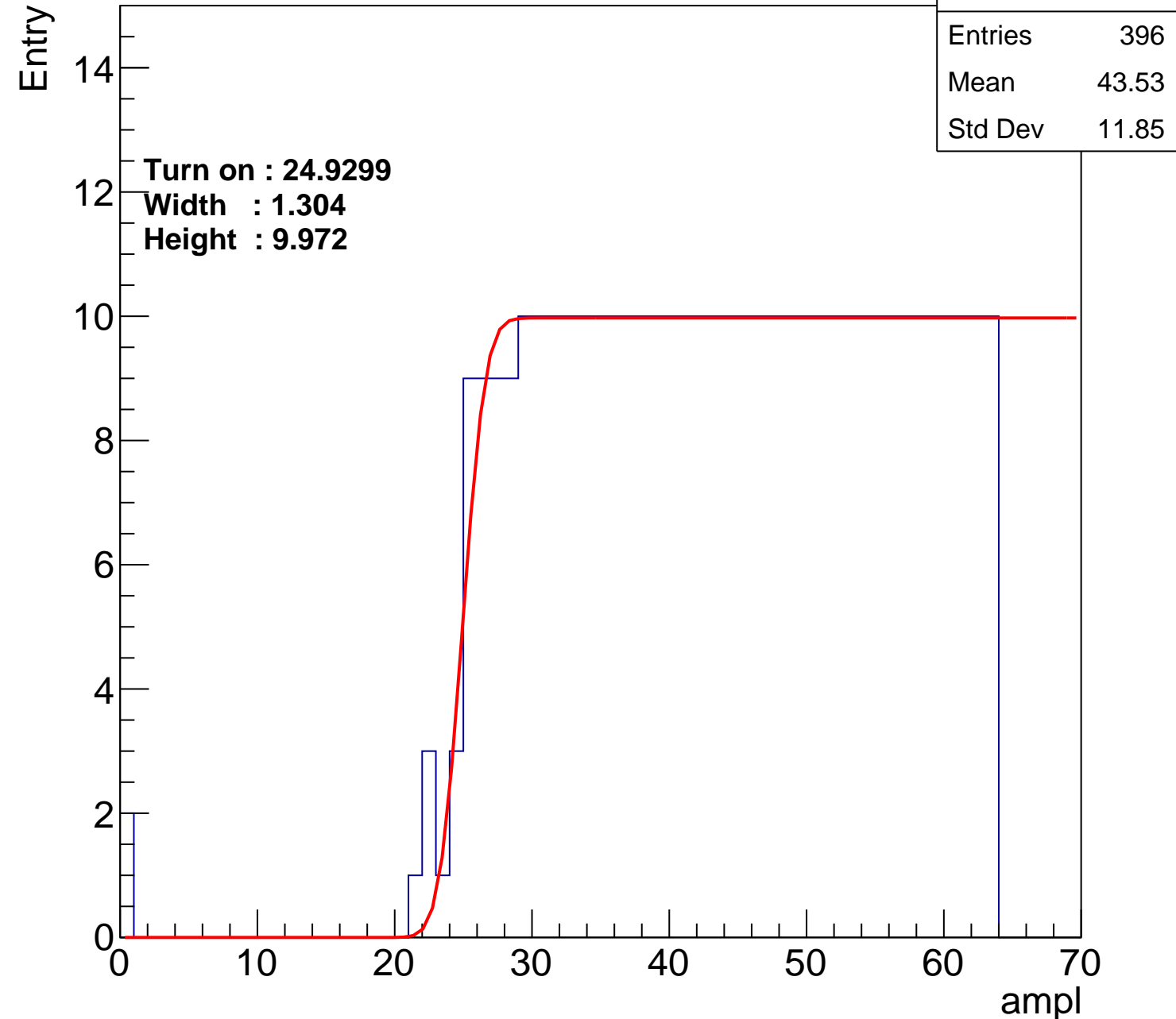
Width : 1.304

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch45

calib_packv5_042523_0143.root, FC#3, port B3

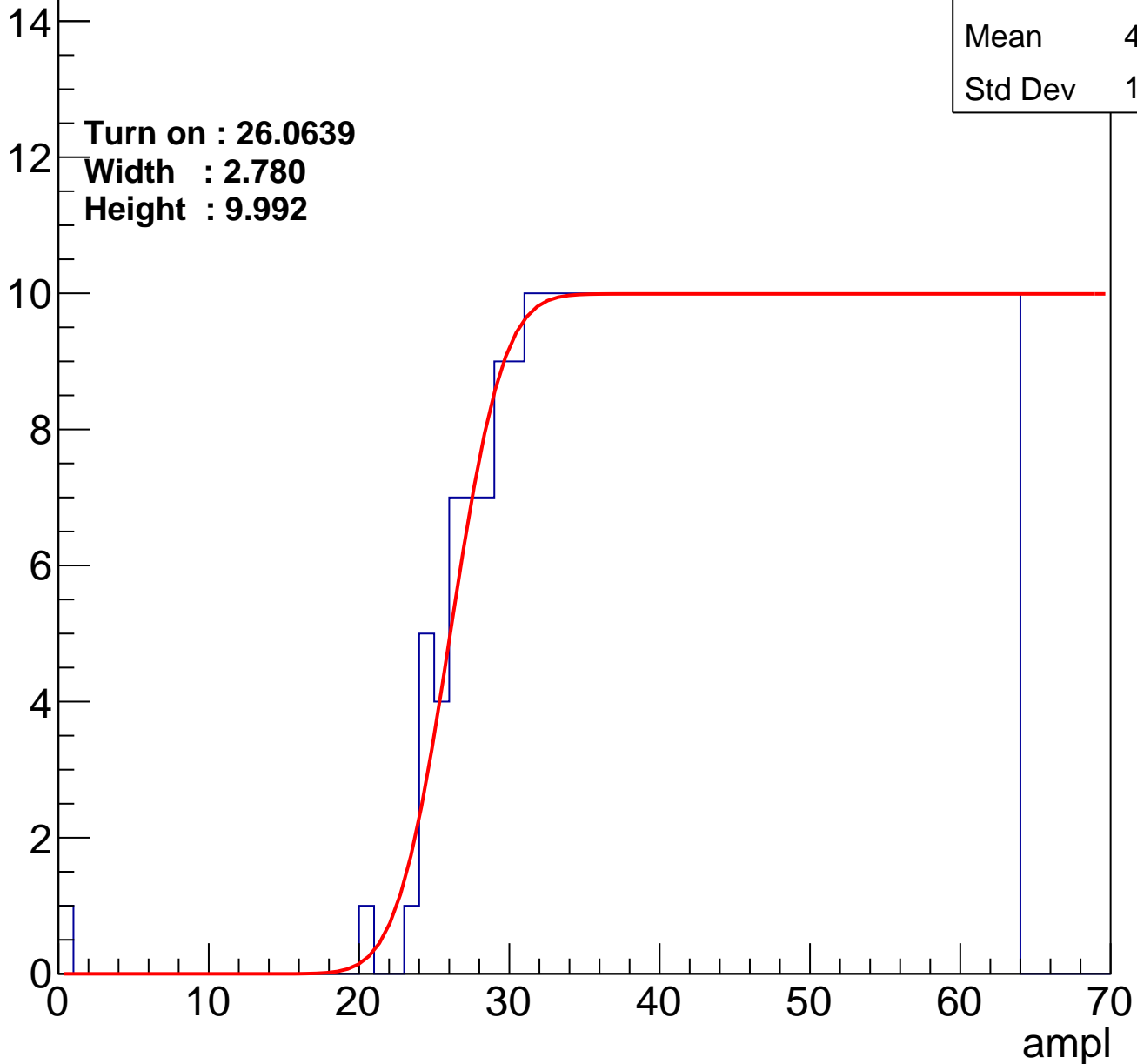
Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.0639

Width : 2.780

Height : 9.992

Entry



B1L000S, U19-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
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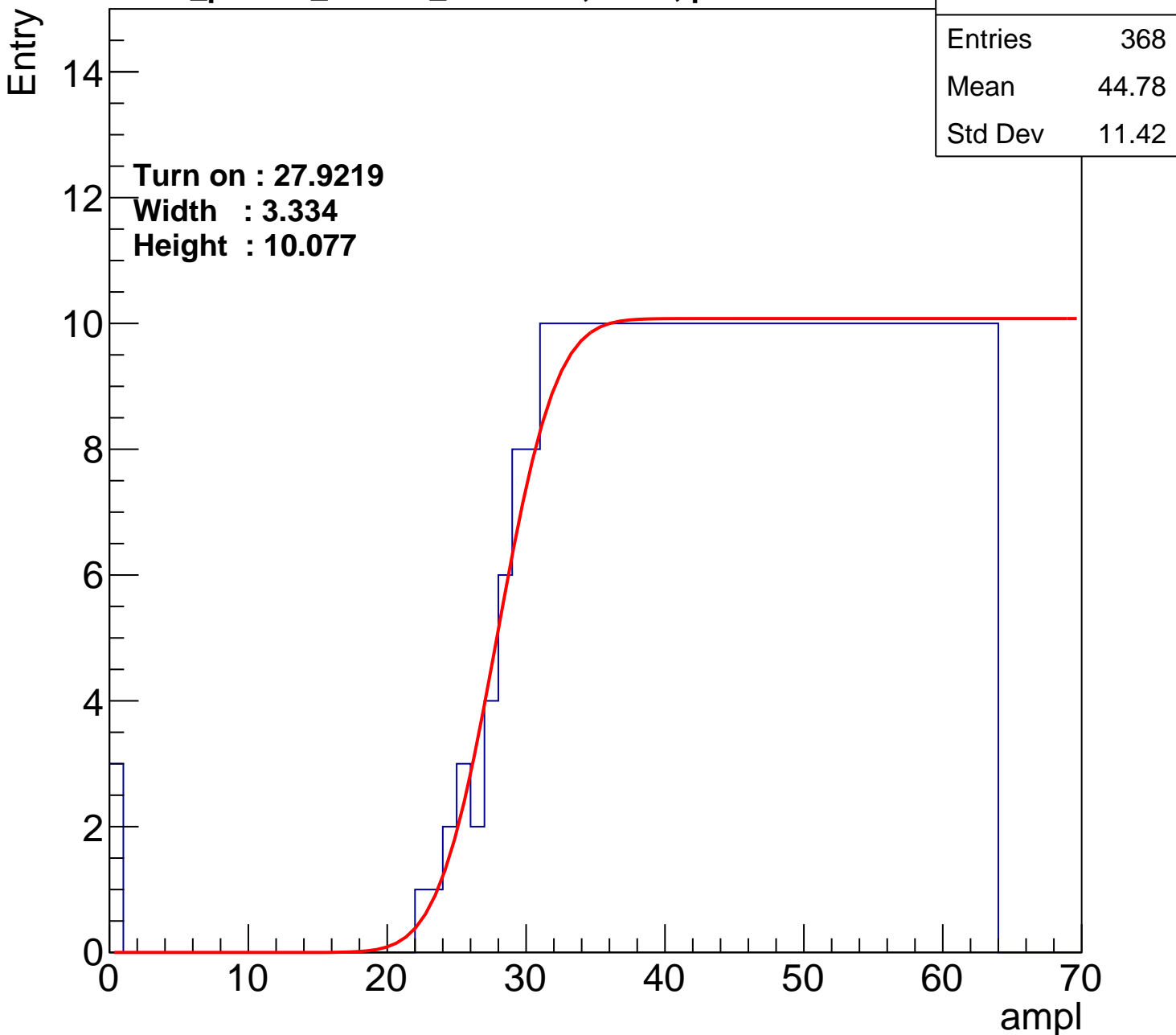
Mean	44.78
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Std Dev	11.42
---------	-------

Turn on : 27.9219

Width : 3.334

Height : 10.077



B1L000S, U19-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.04
Std Dev	11.78

Turn on : 26.6561

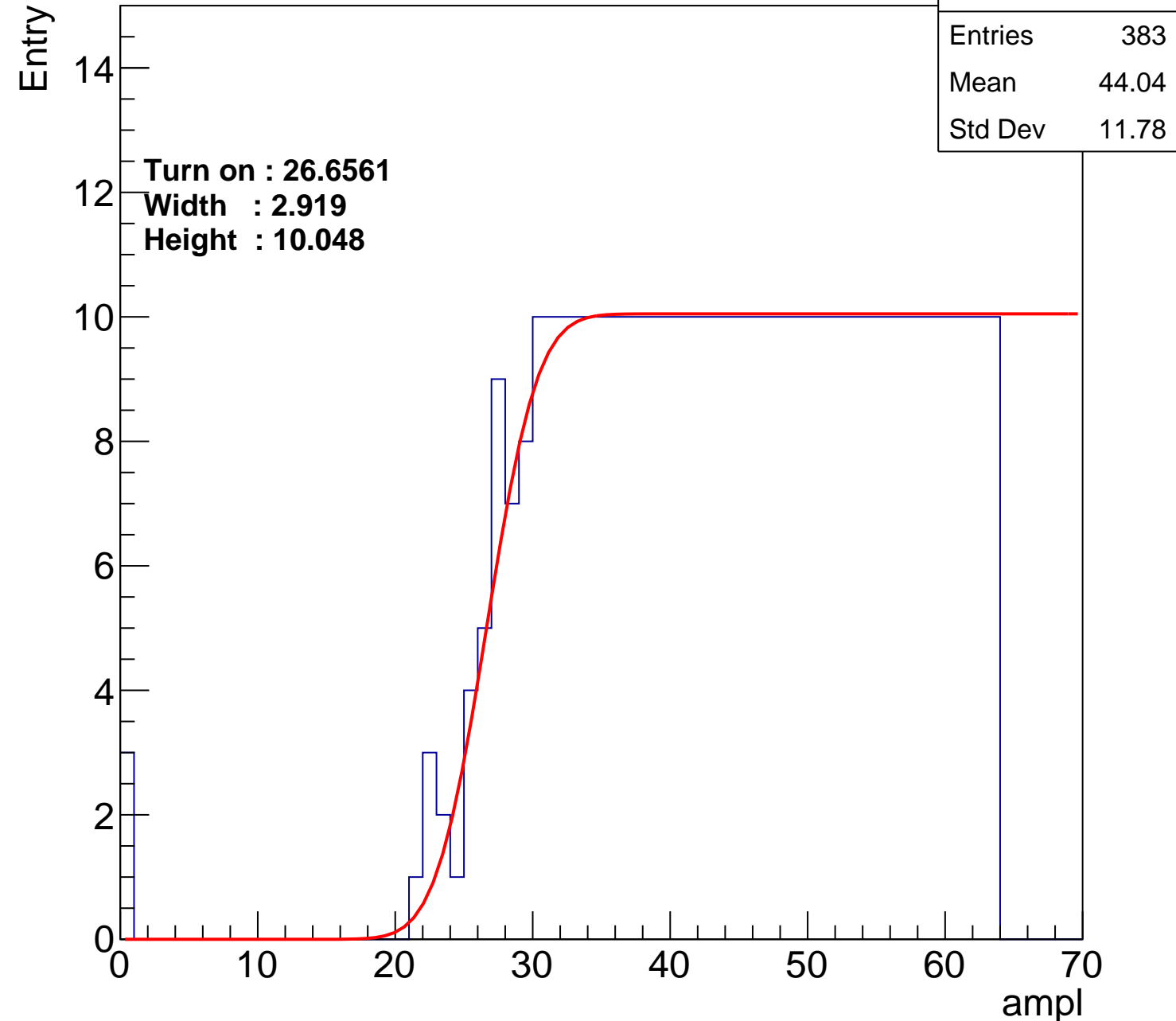
Width : 2.919

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch48

calib_packv5_042523_0143.root, FC#3, port B3

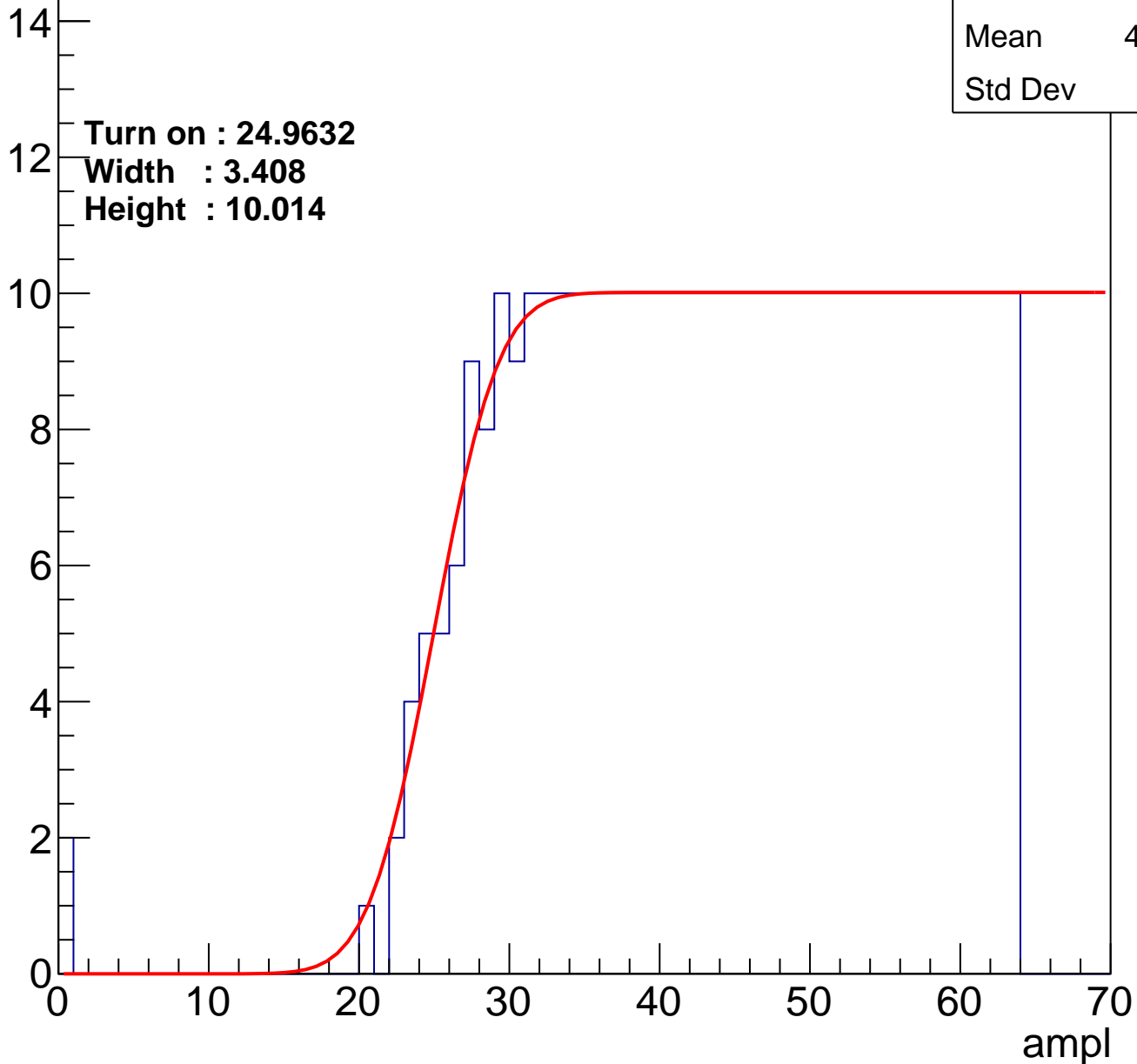
Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 24.9632

Width : 3.408

Height : 10.014

Entry



B1L000S, U19-ch49

calib_packv5_042523_0143.root, FC#3, port B3

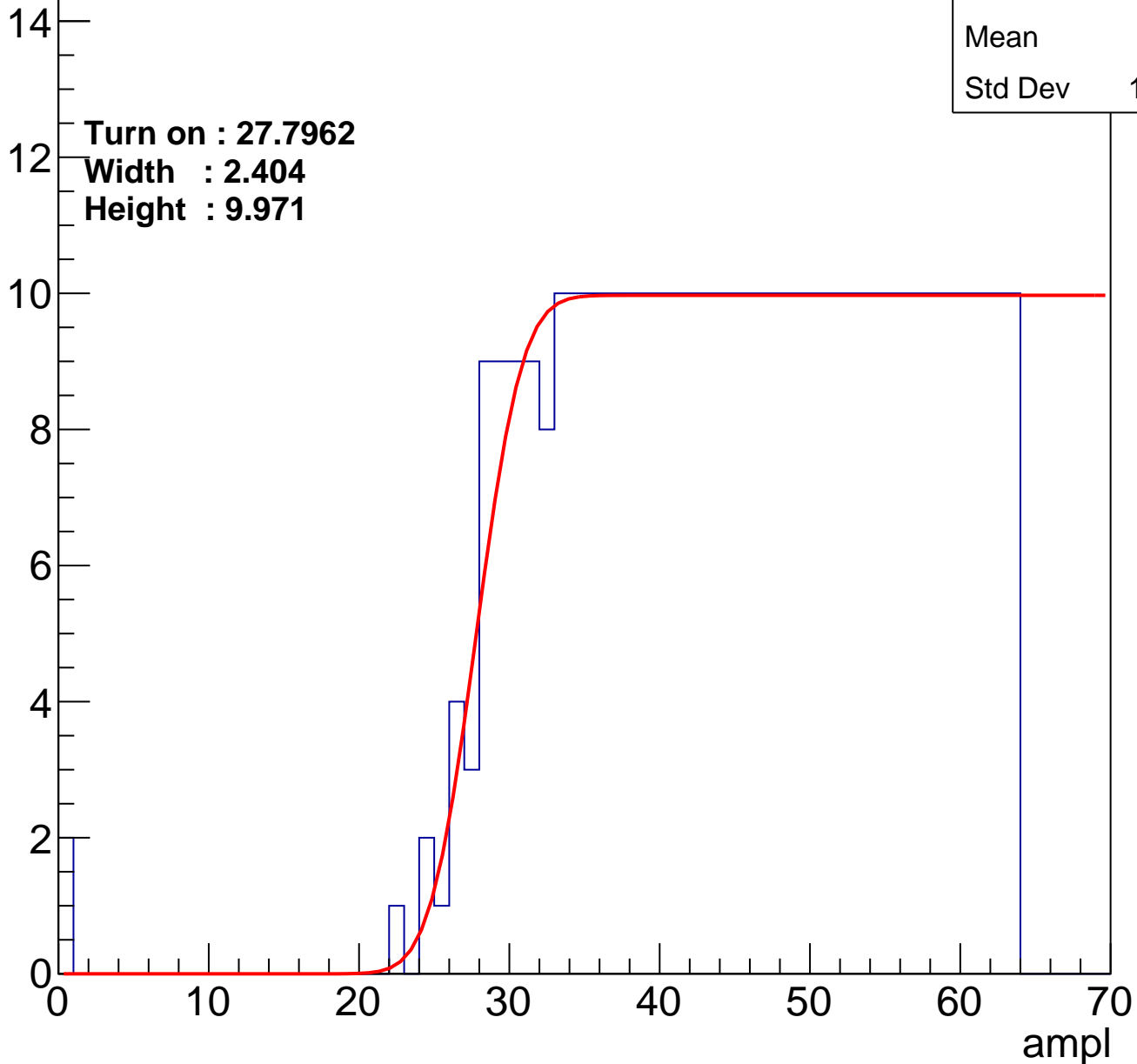
Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.7962

Width : 2.404

Height : 9.971

Entry



B1L000S, U19-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.2678

Width : 3.308

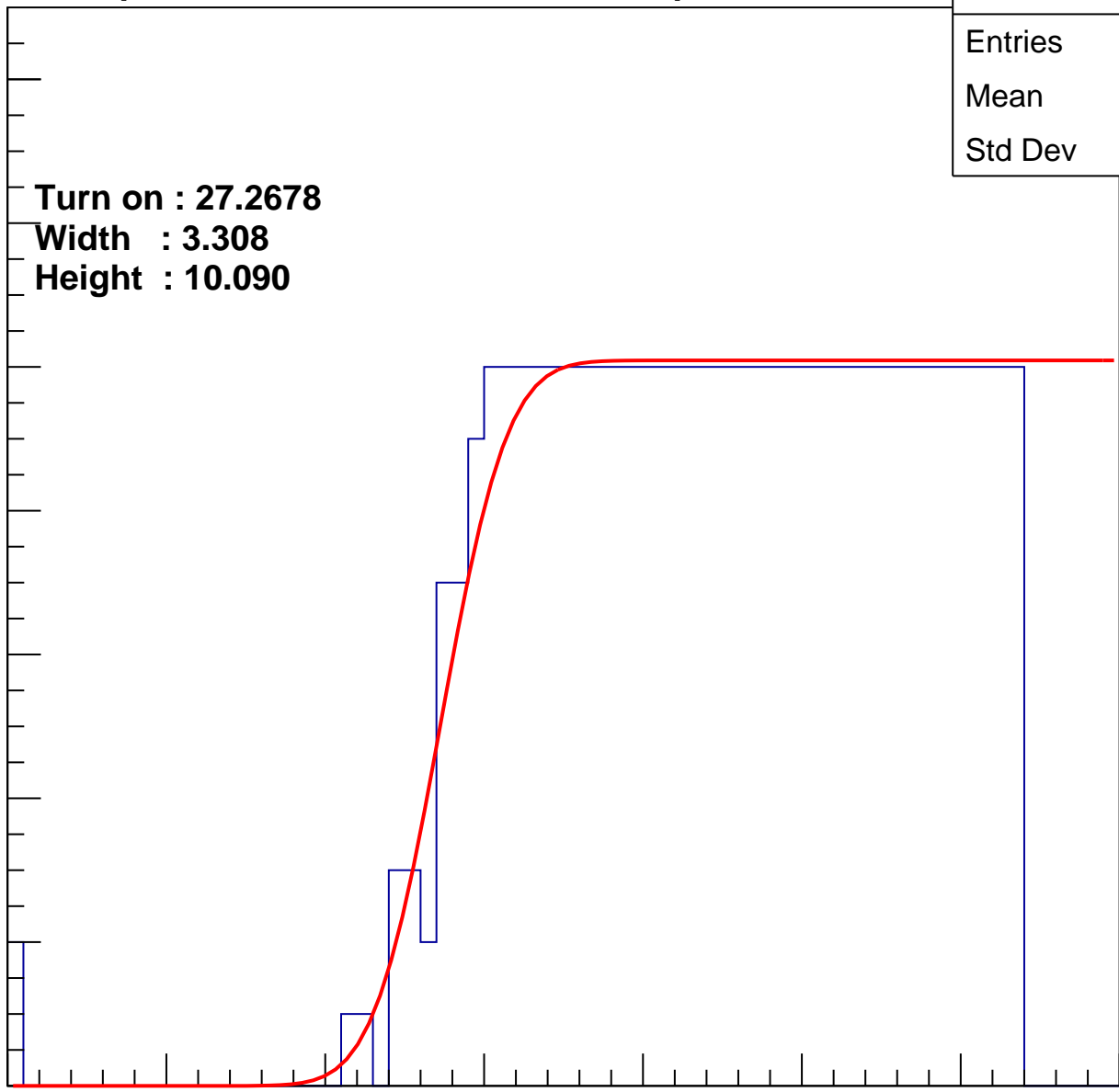
Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U19-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.14
Std Dev	10.87

Turn on : 28.5831

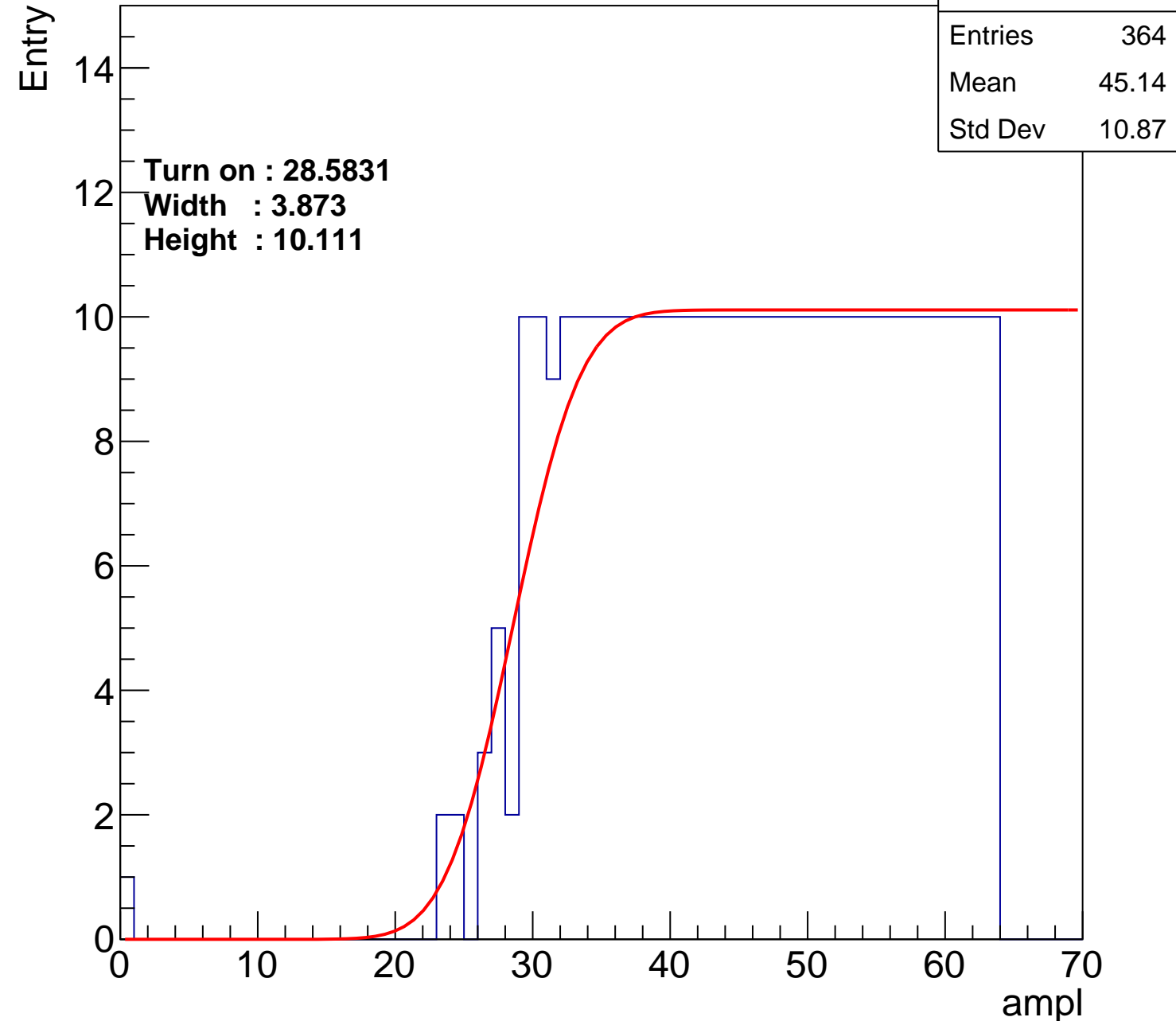
Width : 3.873

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch52

calib_packv5_042523_0143.root, FC#3, port B3

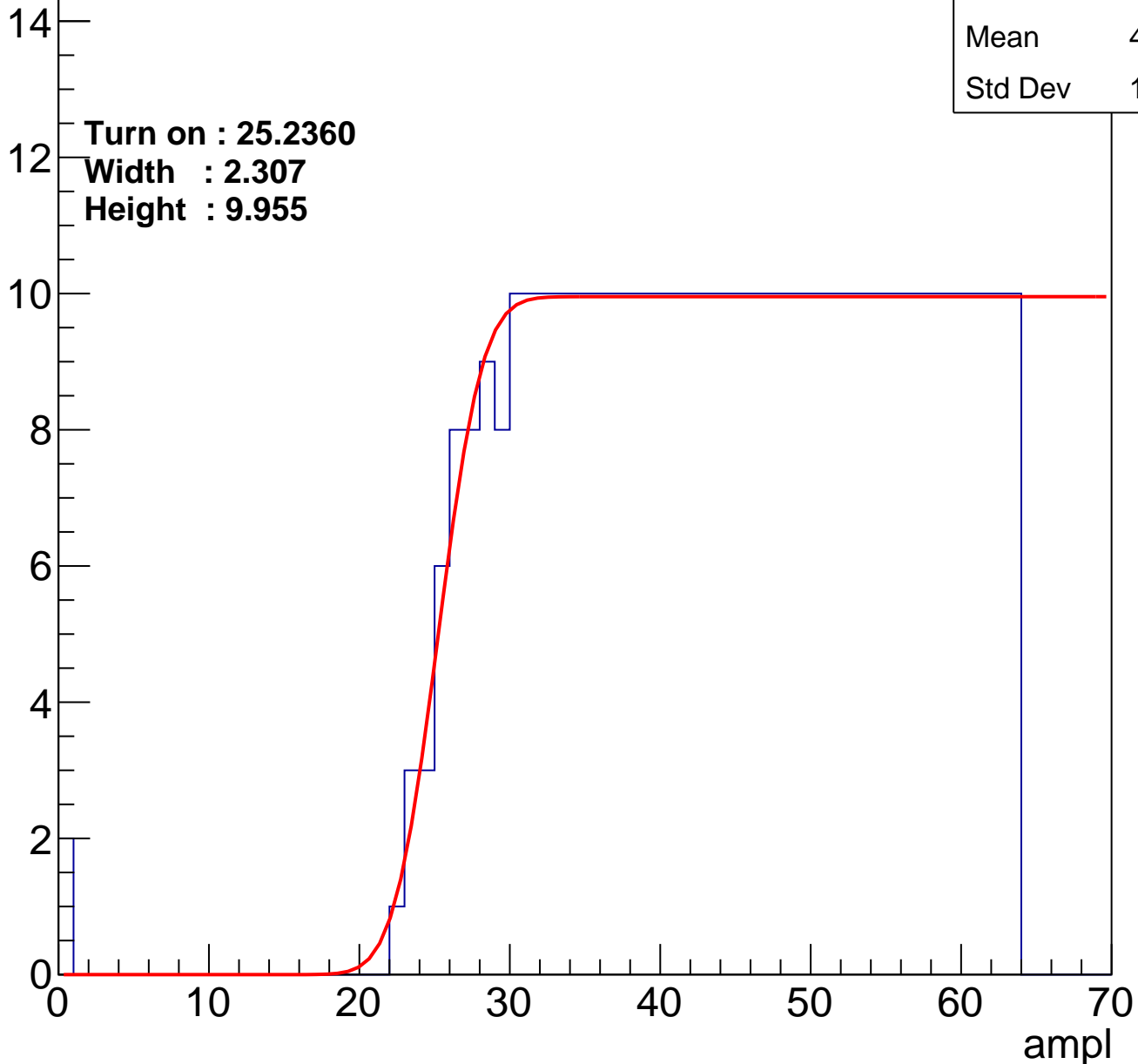
Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.2360

Width : 2.307

Height : 9.955

Entry



B1L000S, U19-ch53

calib_packv5_042523_0143.root, FC#3, port B3

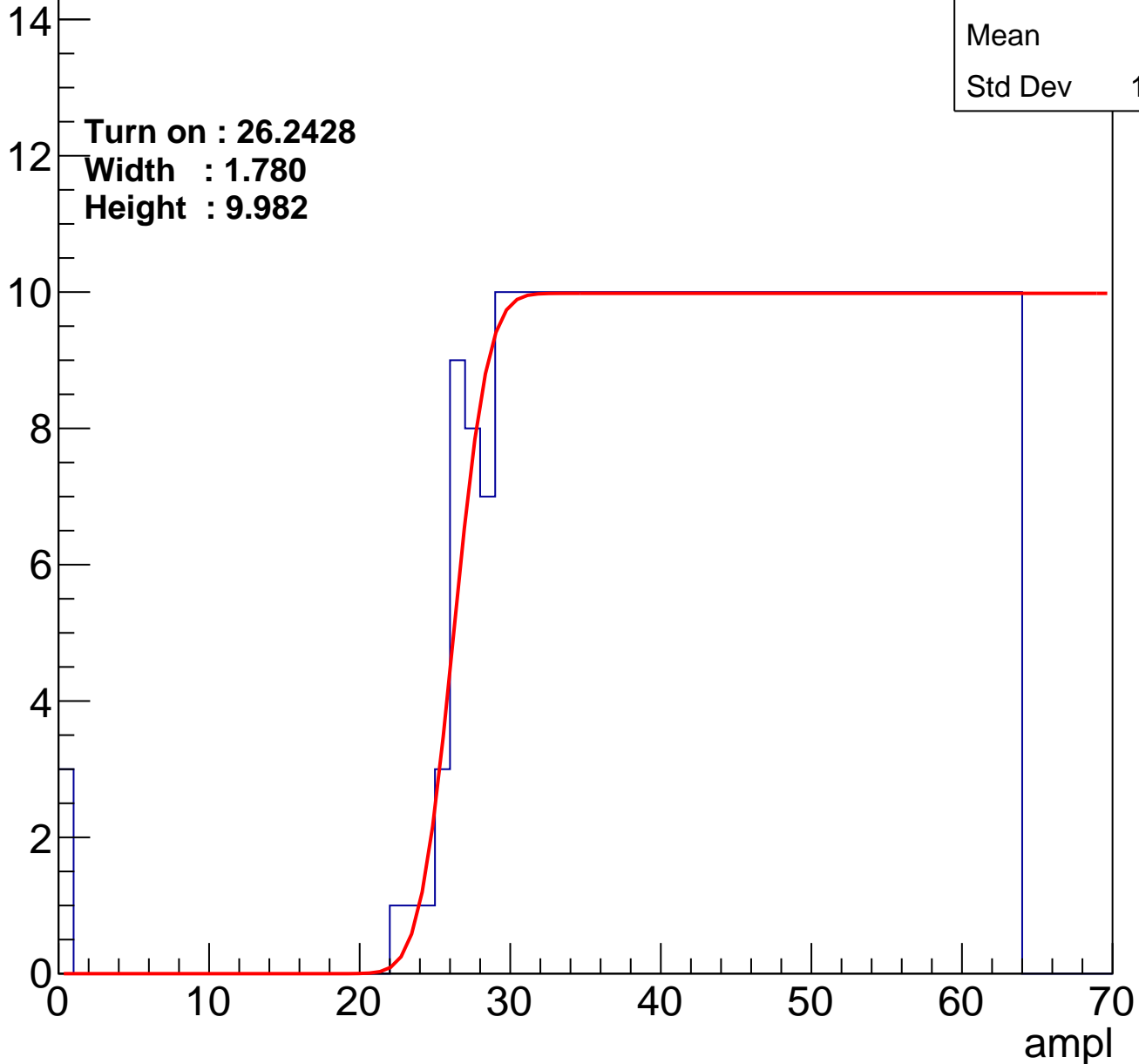
Entries	383
Mean	44.1
Std Dev	11.69

Turn on : 26.2428

Width : 1.780

Height : 9.982

Entry



B1L000S, U19-ch54

calib_packv5_042523_0143.root, FC#3, port B3

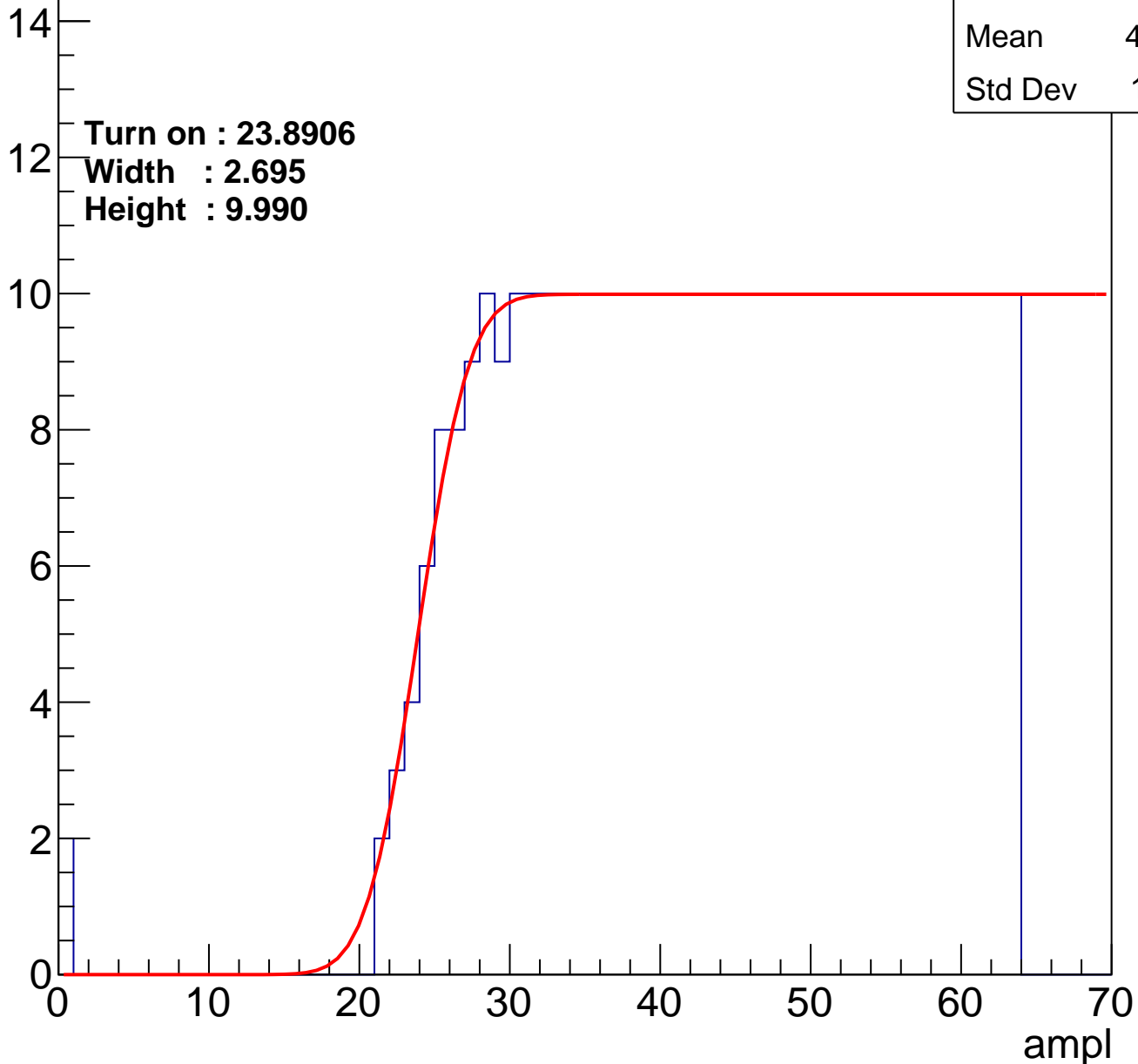
Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 23.8906

Width : 2.695

Height : 9.990

Entry



B1L000S, U19-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.49
Std Dev	11.22

Turn on : 25.6096

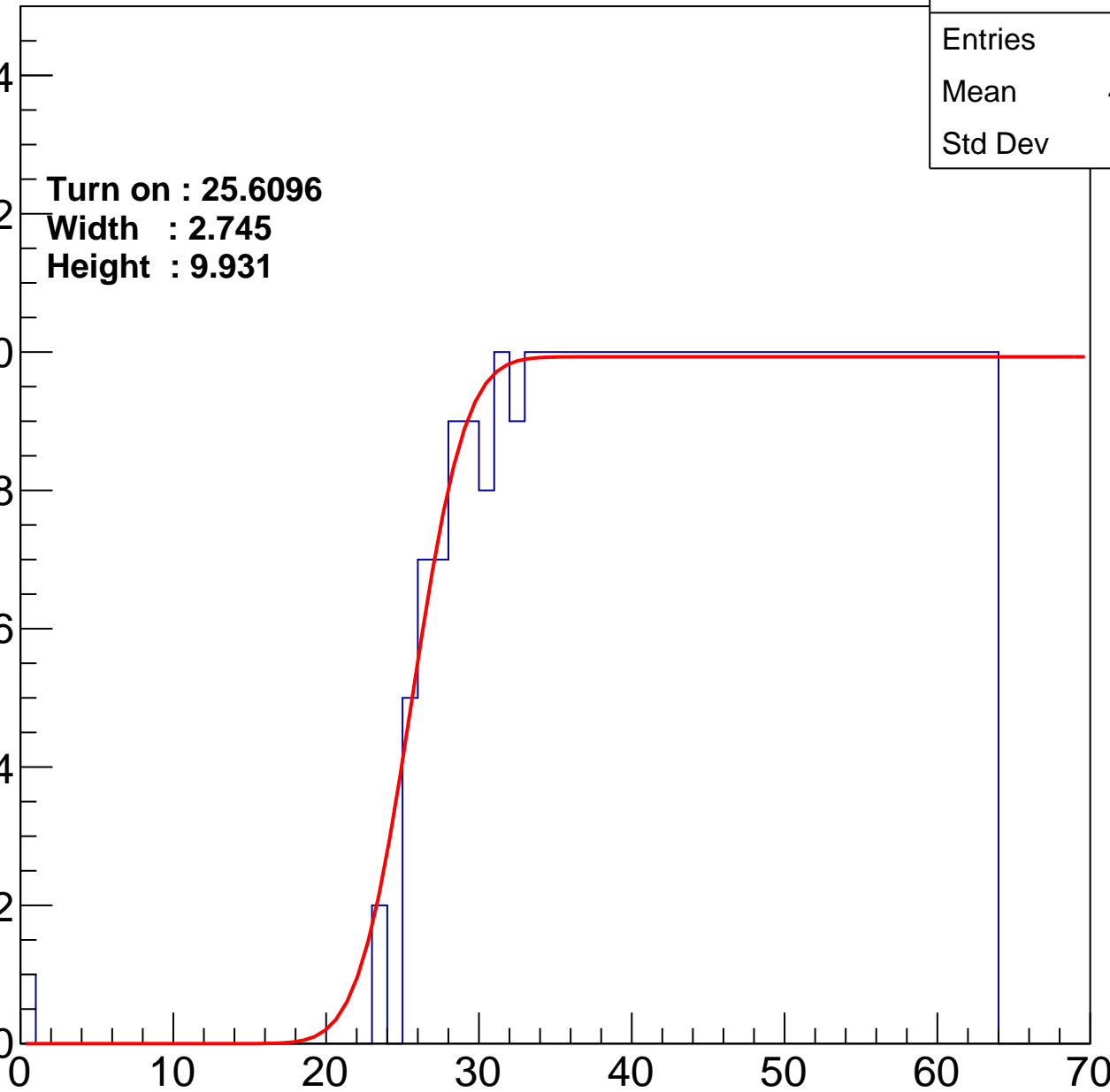
Width : 2.745

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch56

calib_packv5_042523_0143.root, FC#3, port B3

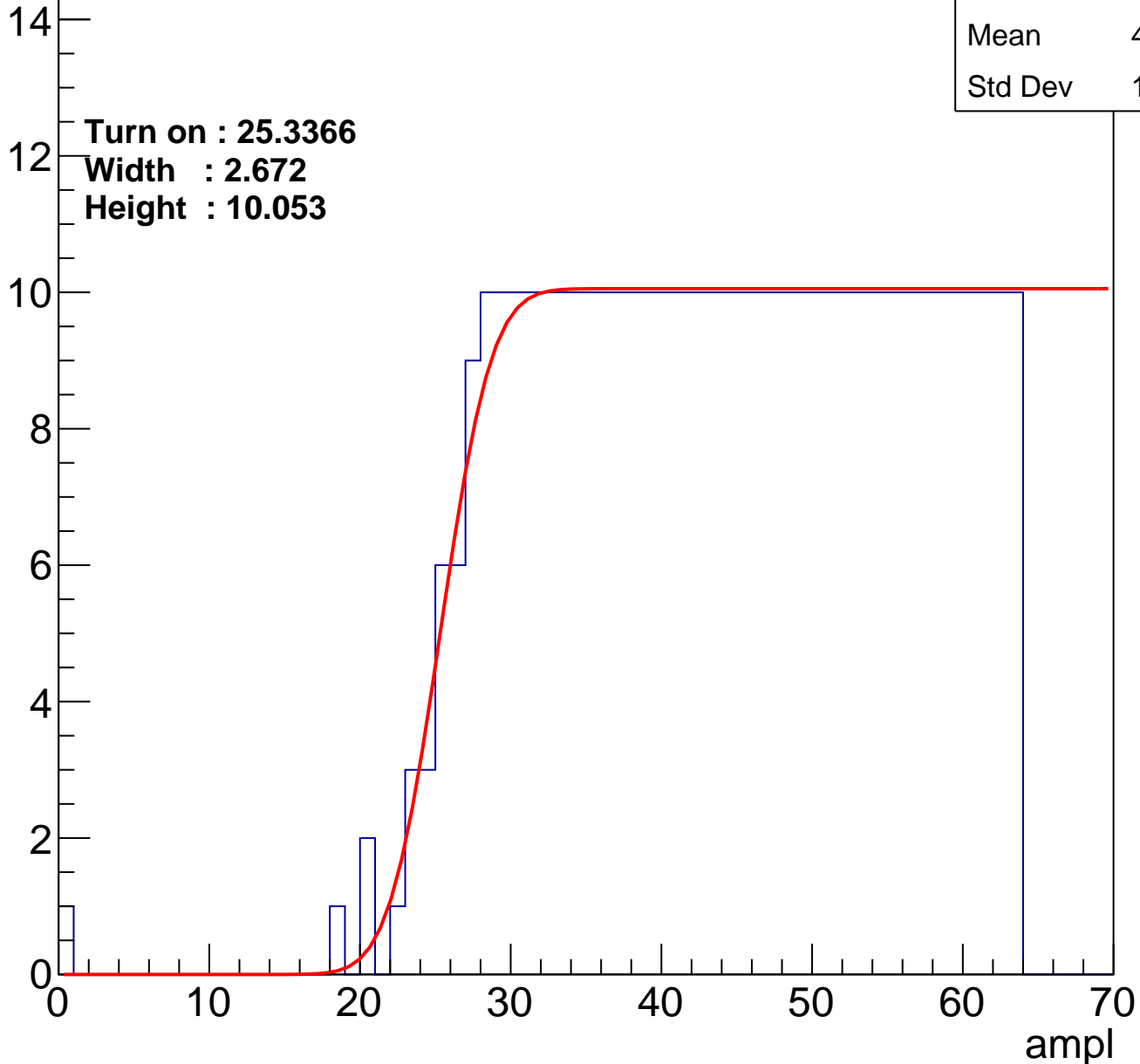
Entries	392
Mean	43.75
Std Dev	11.64

Turn on : 25.3366

Width : 2.672

Height : 10.053

Entry



B1L000S, U19-ch57

calib_packv5_042523_0143.root, FC#3, port B3

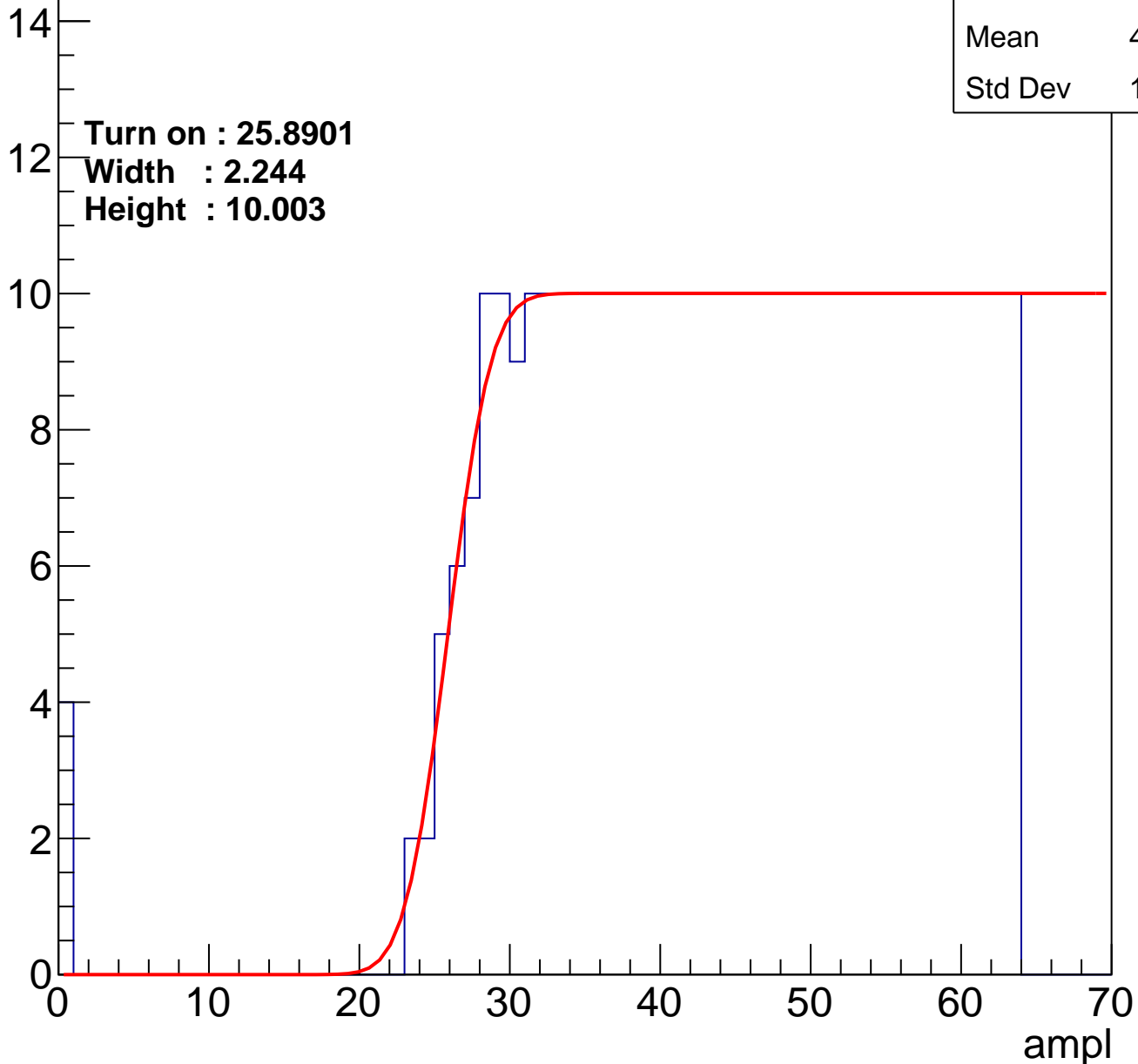
Entries	385
Mean	43.93
Std Dev	11.92

Turn on : 25.8901

Width : 2.244

Height : 10.003

Entry



B1L000S, U19-ch58

calib_packv5_042523_0143.root, FC#3, port B3

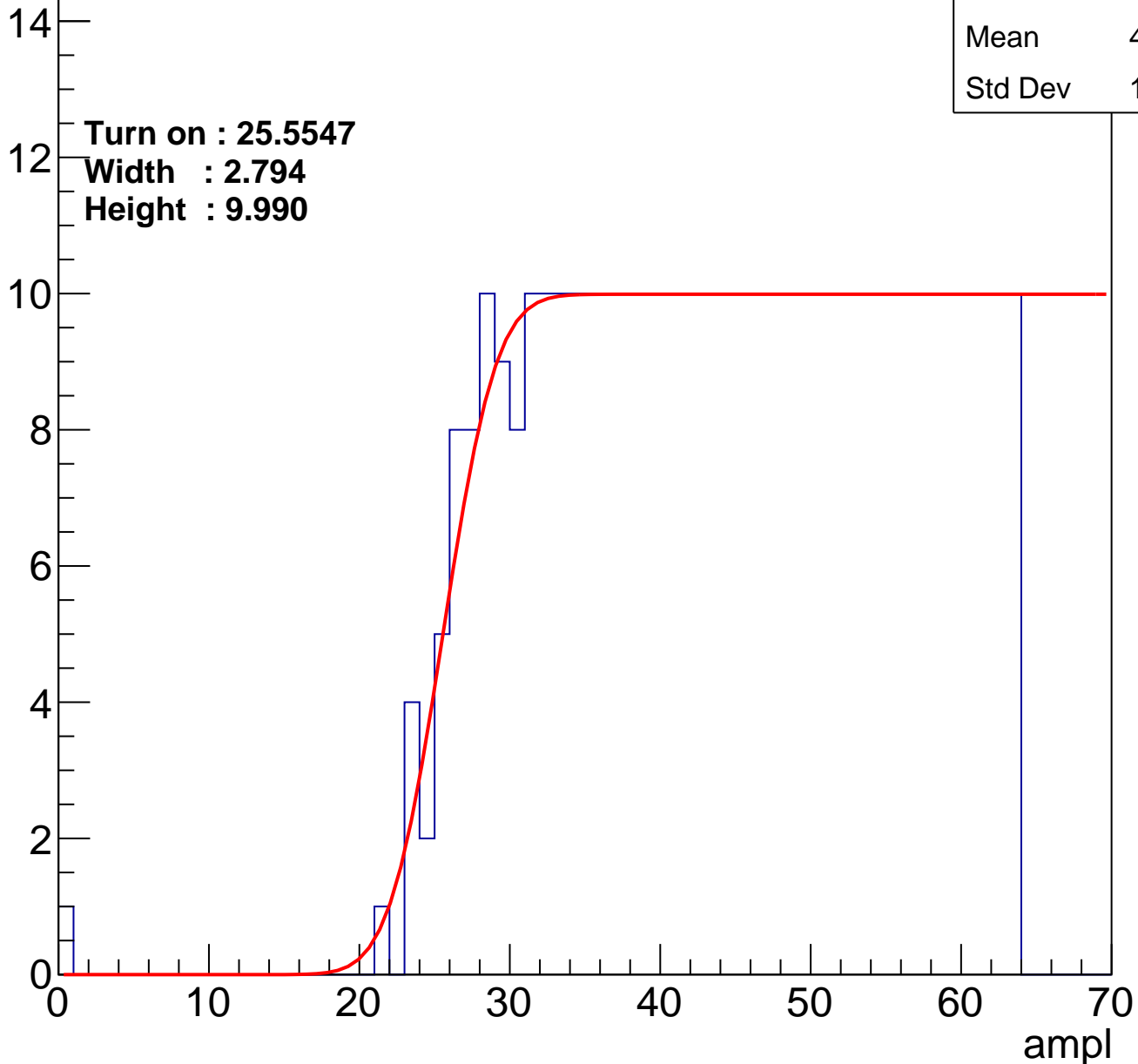
Entries	386
Mean	44.04
Std Dev	11.47

Turn on : 25.5547

Width : 2.794

Height : 9.990

Entry



B1L000S, U19-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.29
Std Dev	10.93

Turn on : 28.4740

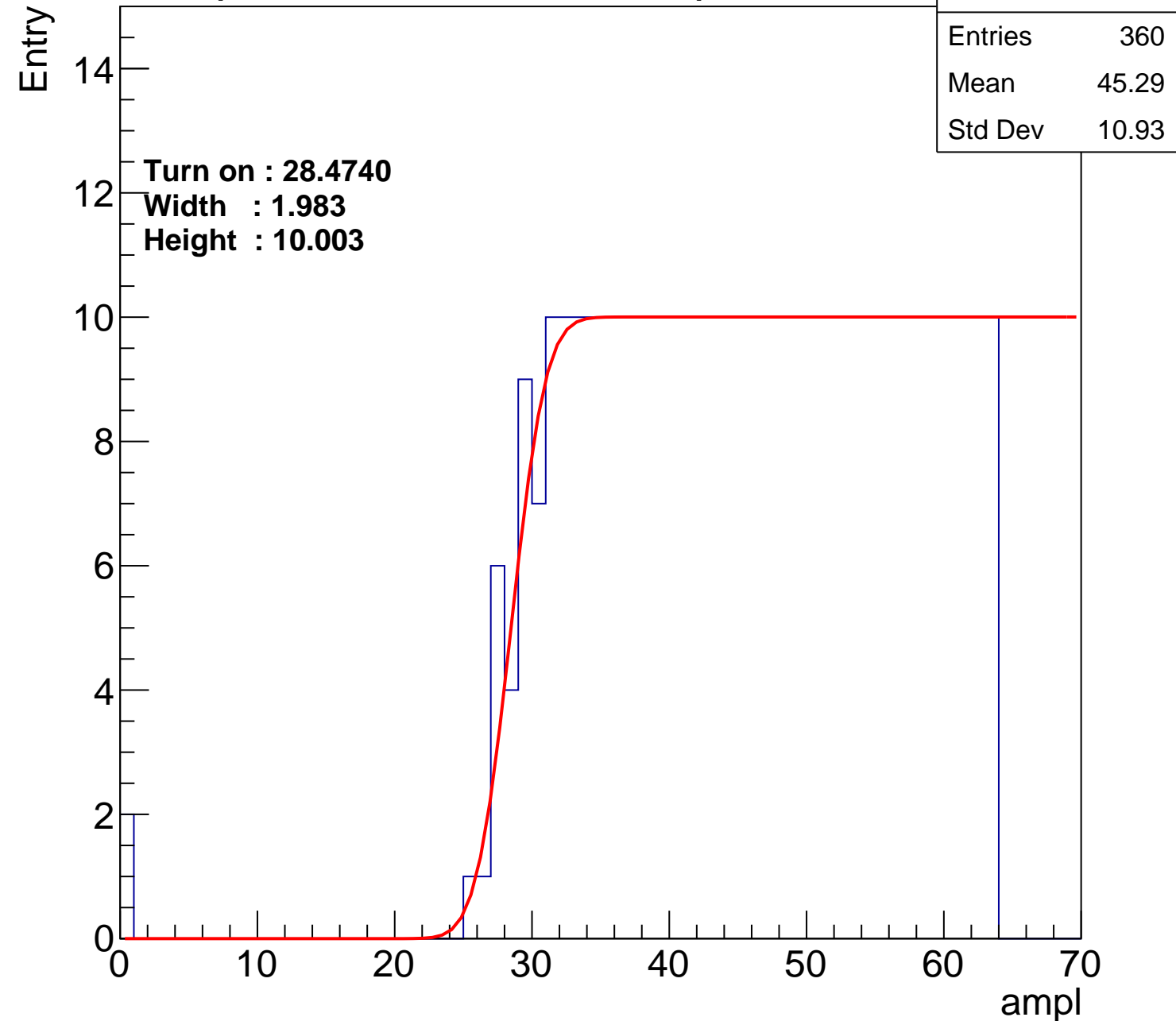
Width : 1.983

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.58
Std Dev	11.52

Turn on : 27.1994

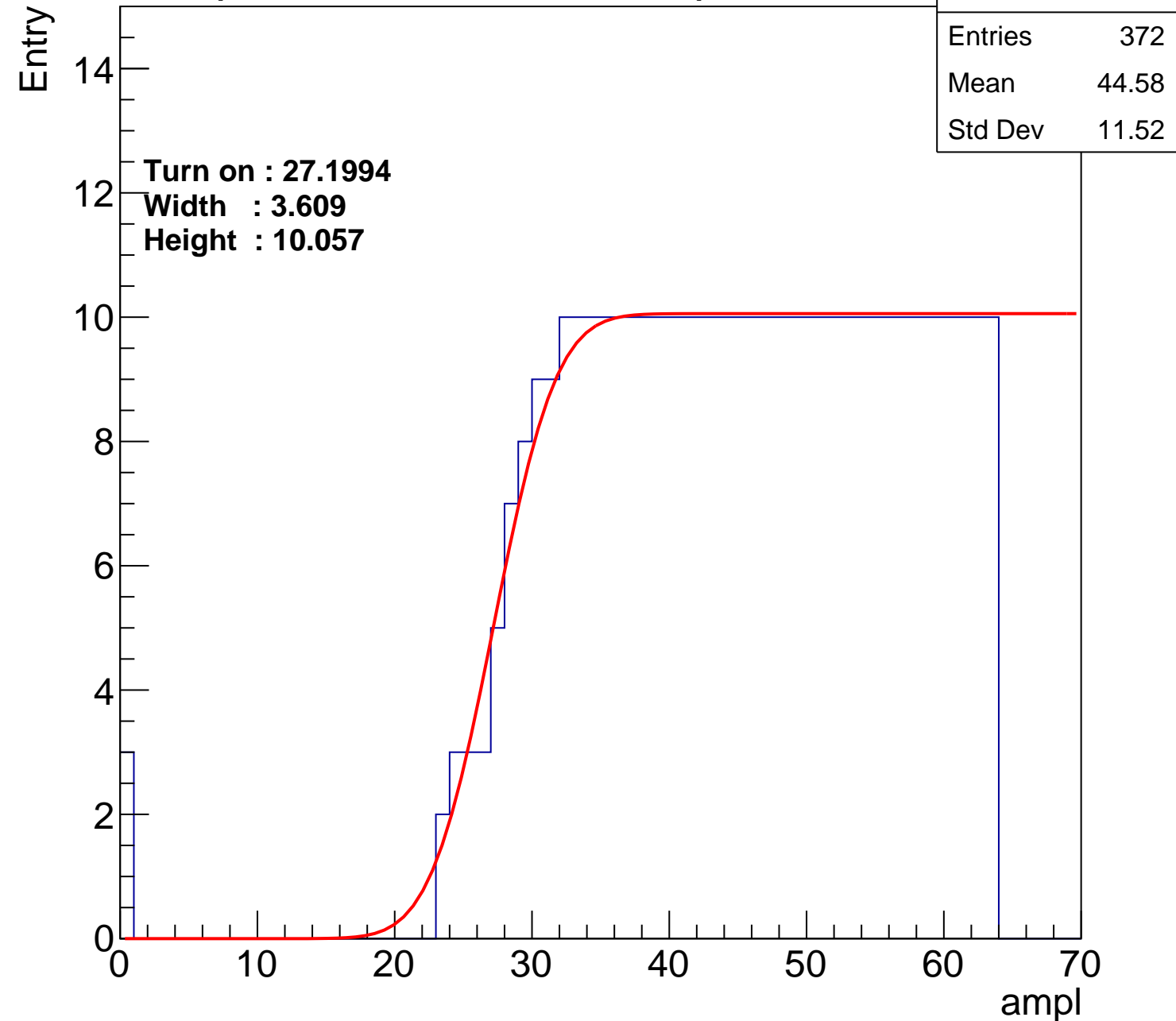
Width : 3.609

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.92
Std Dev	10.96

Turn on : 27.2153

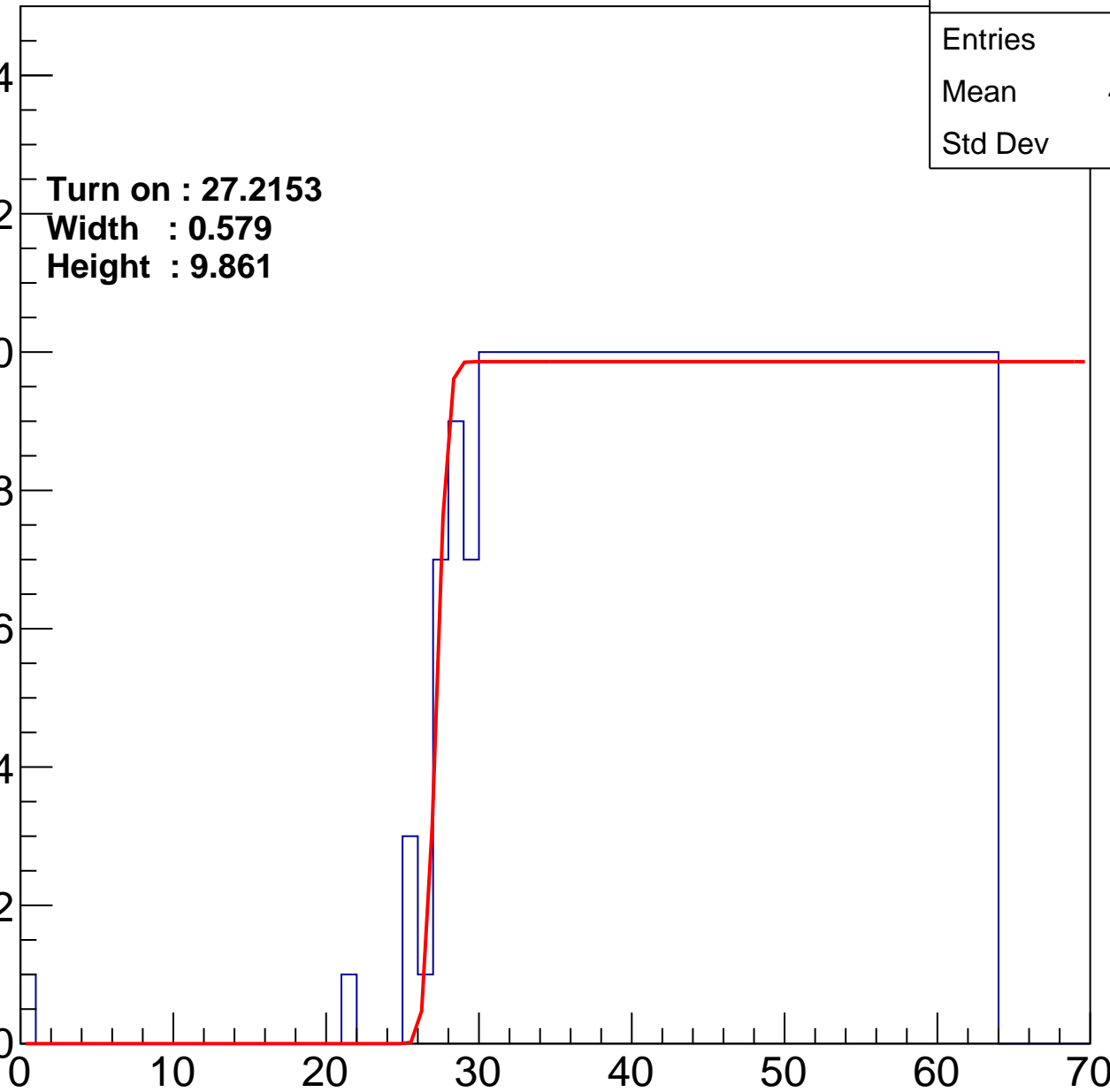
Width : 0.579

Height : 9.861

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	415
Mean	42.52
Std Dev	12.51

Turn on : 22.9721

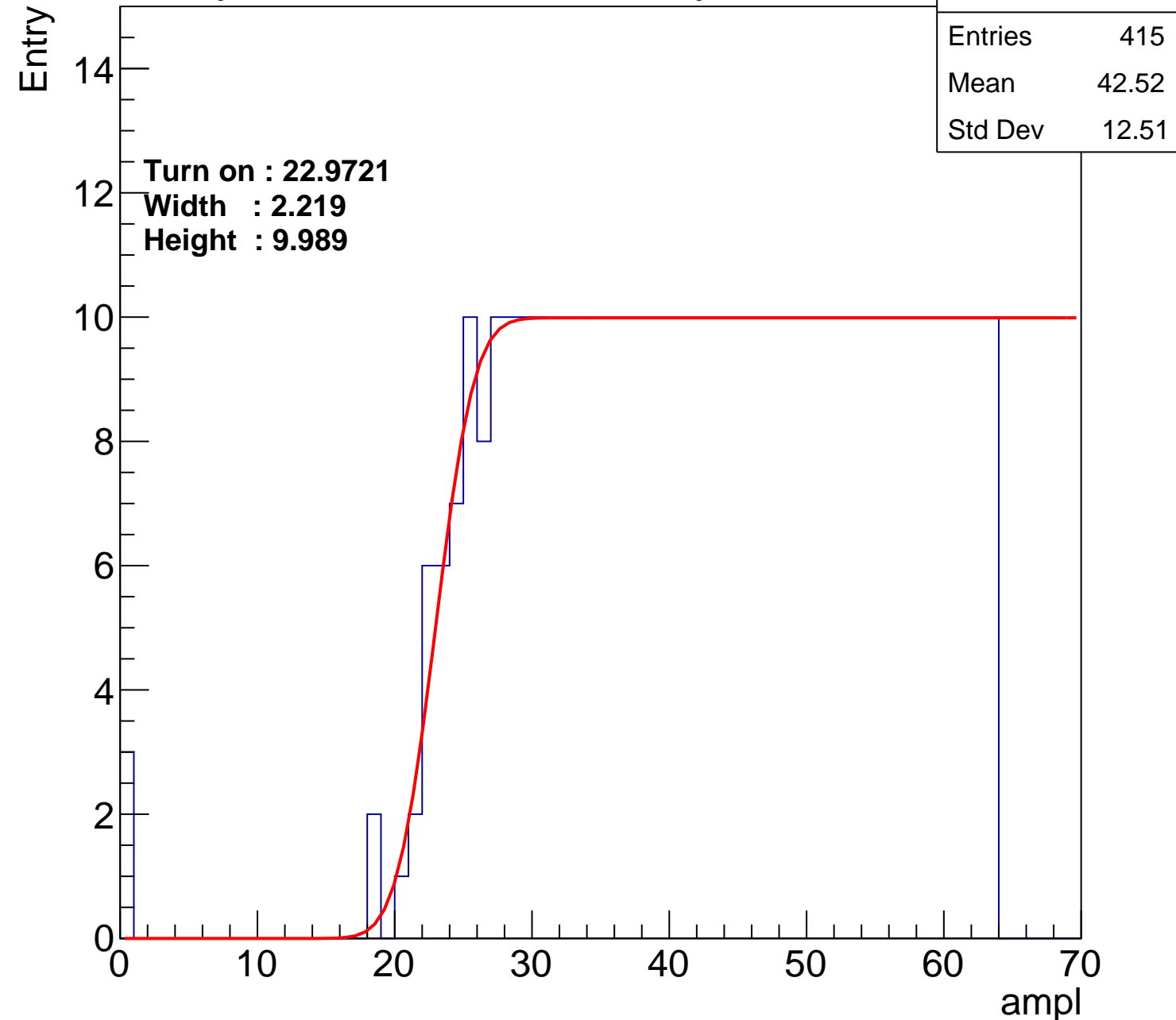
Width : 2.219

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch63

calib_packv5_042523_0143.root, FC#3, port B3

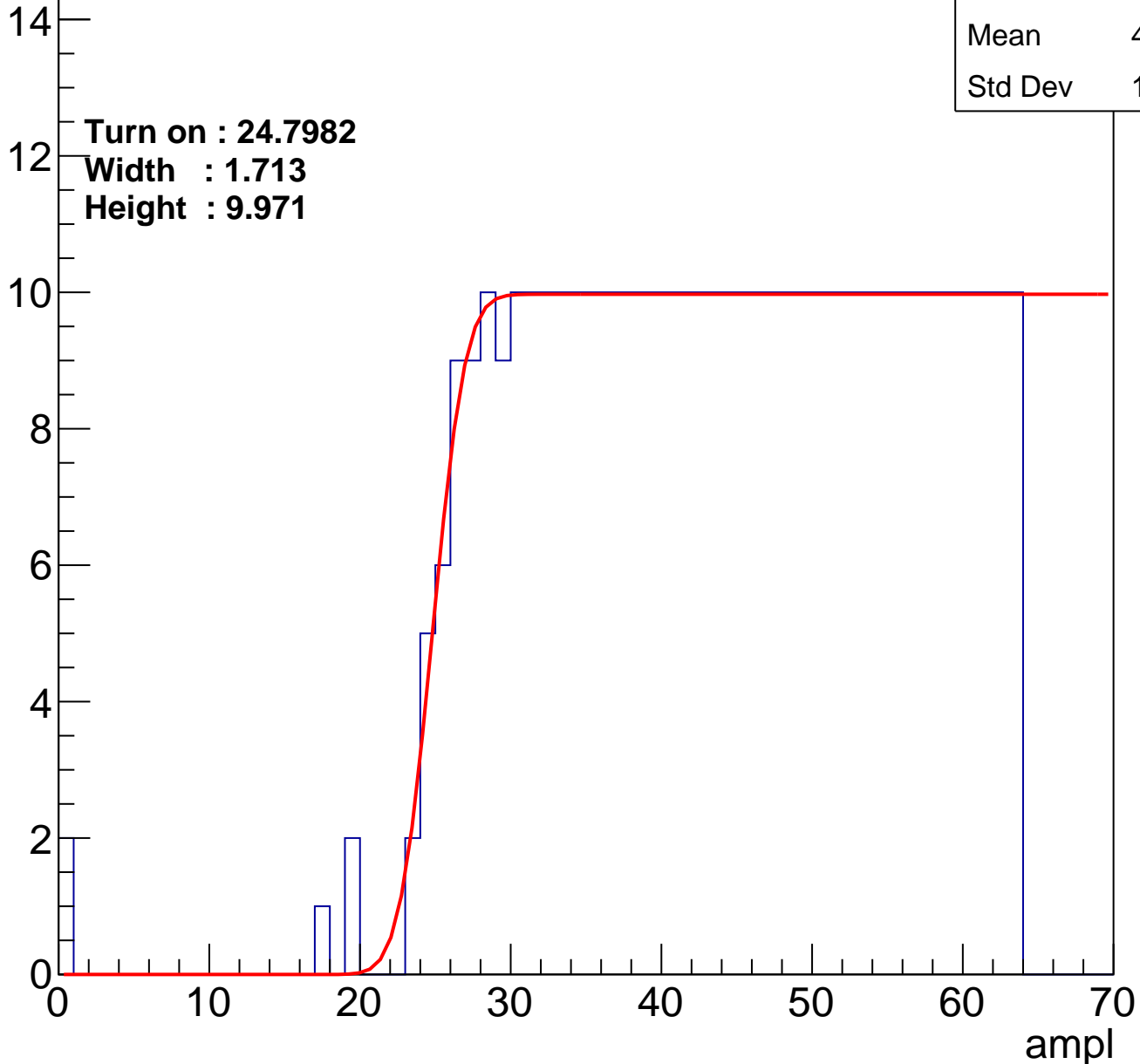
Entries	395
Mean	43.54
Std Dev	11.88

Turn on : 24.7982

Width : 1.713

Height : 9.971

Entry



B1L000S, U19-ch64

calib_packv5_042523_0143.root, FC#3, port B3

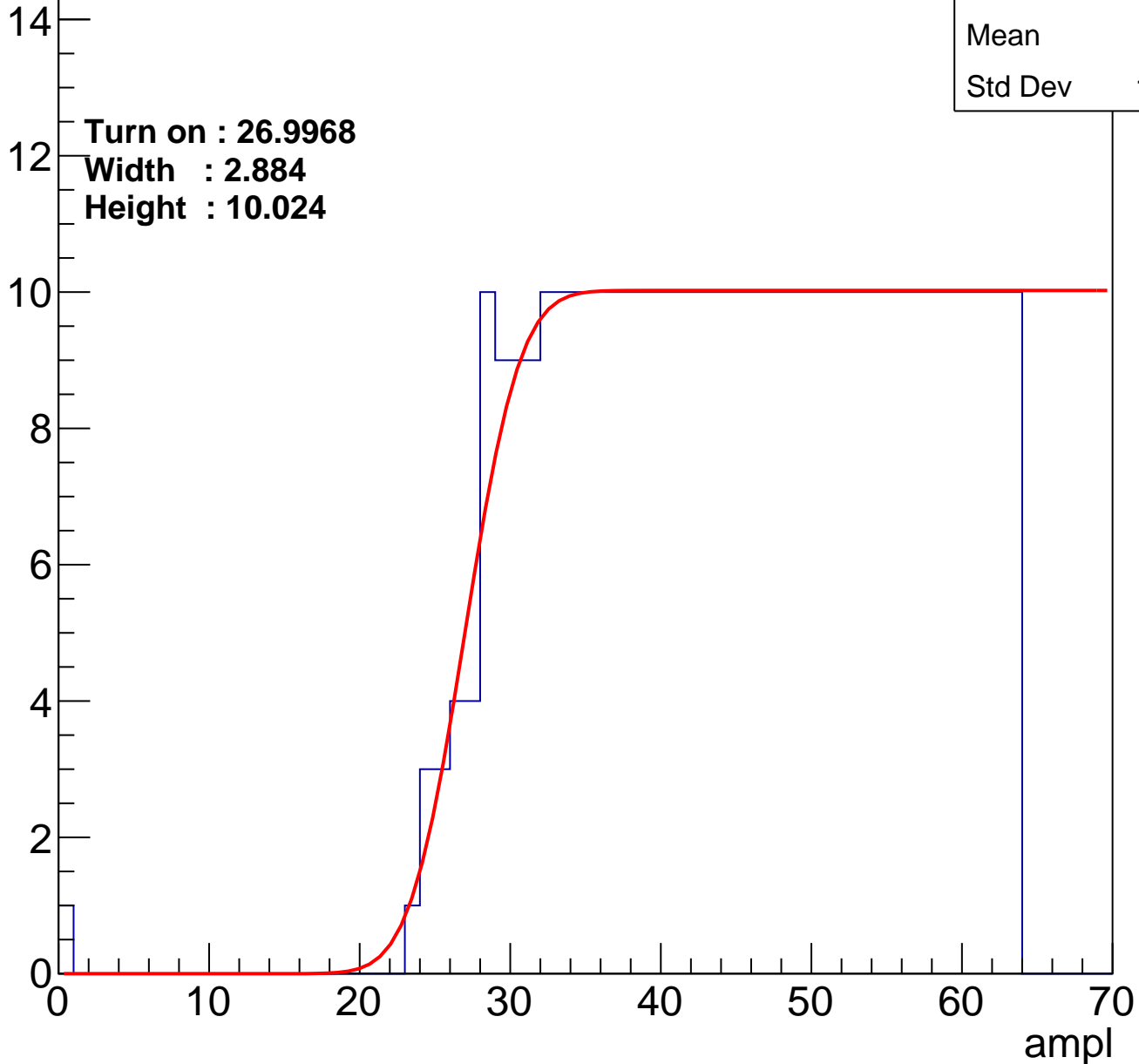
Entries	373
Mean	44.7
Std Dev	11.11

Turn on : 26.9968

Width : 2.884

Height : 10.024

Entry



B1L000S, U19-ch65

calib_packv5_042523_0143.root, FC#3, port B3

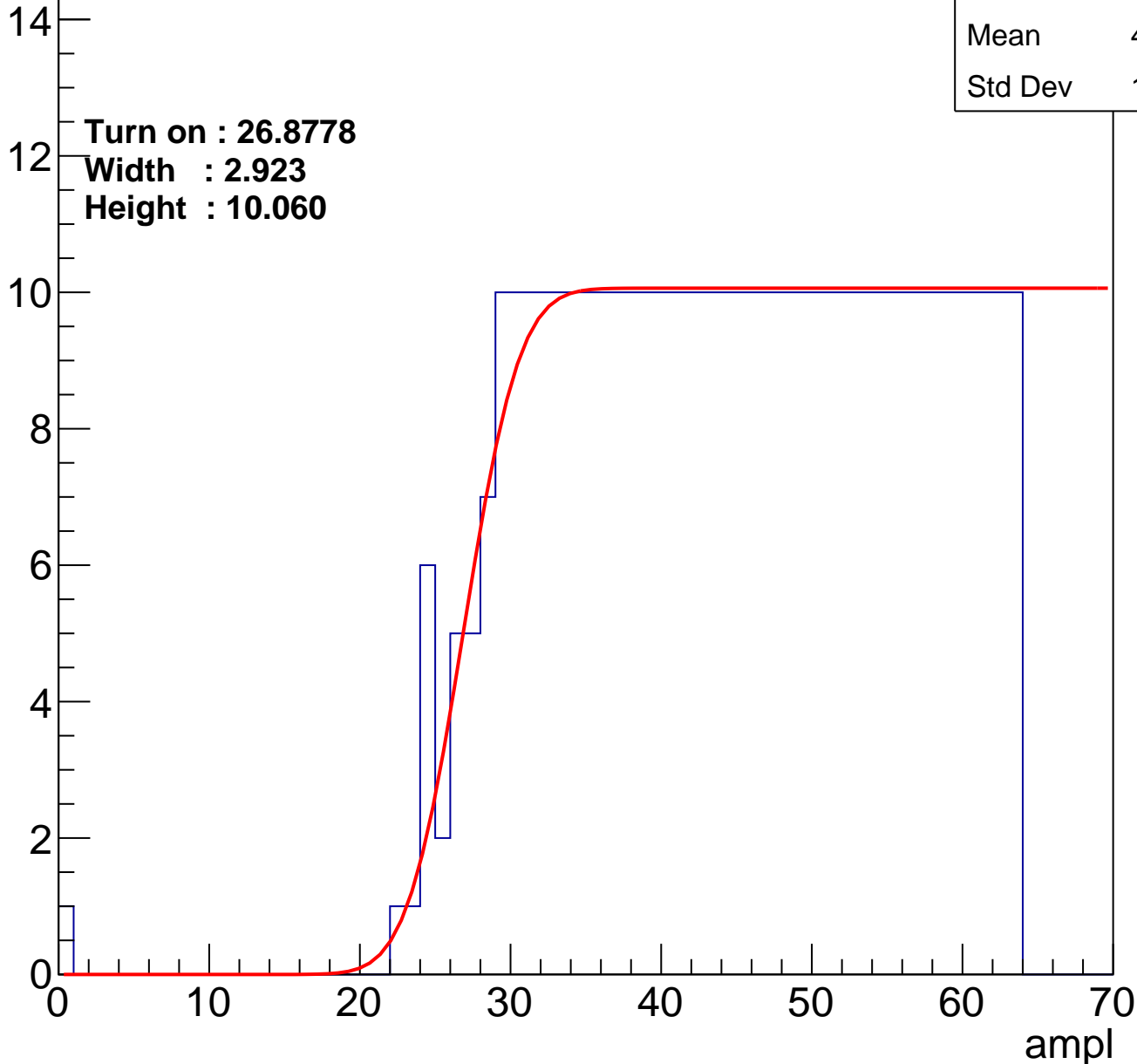
Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.8778

Width : 2.923

Height : 10.060

Entry



B1L000S, U19-ch66

calib_packv5_042523_0143.root, FC#3, port B3

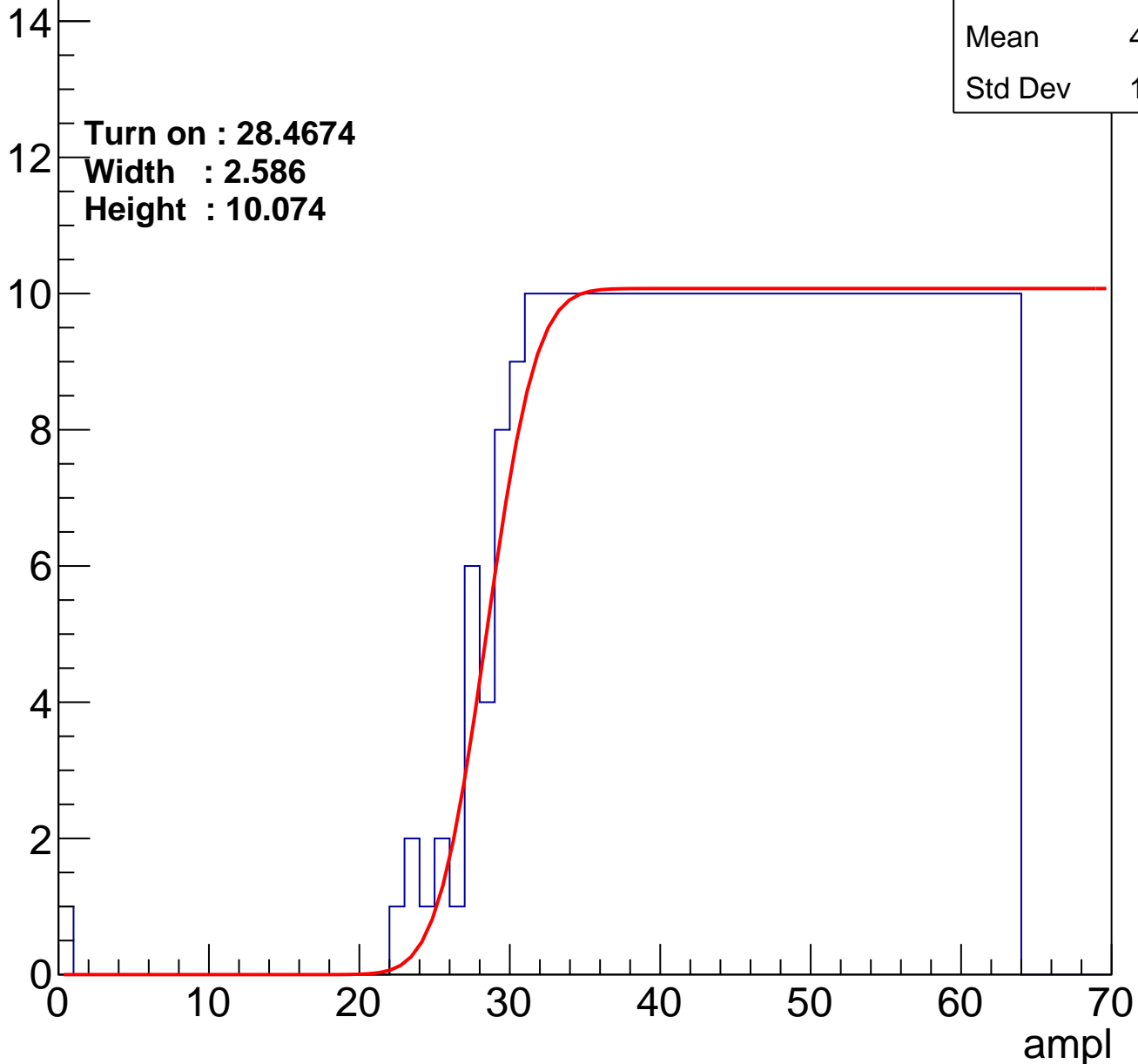
Entries	365
Mean	45.08
Std Dev	10.92

Turn on : 28.4674

Width : 2.586

Height : 10.074

Entry



B1L000S, U19-ch67

calib_packv5_042523_0143.root, FC#3, port B3

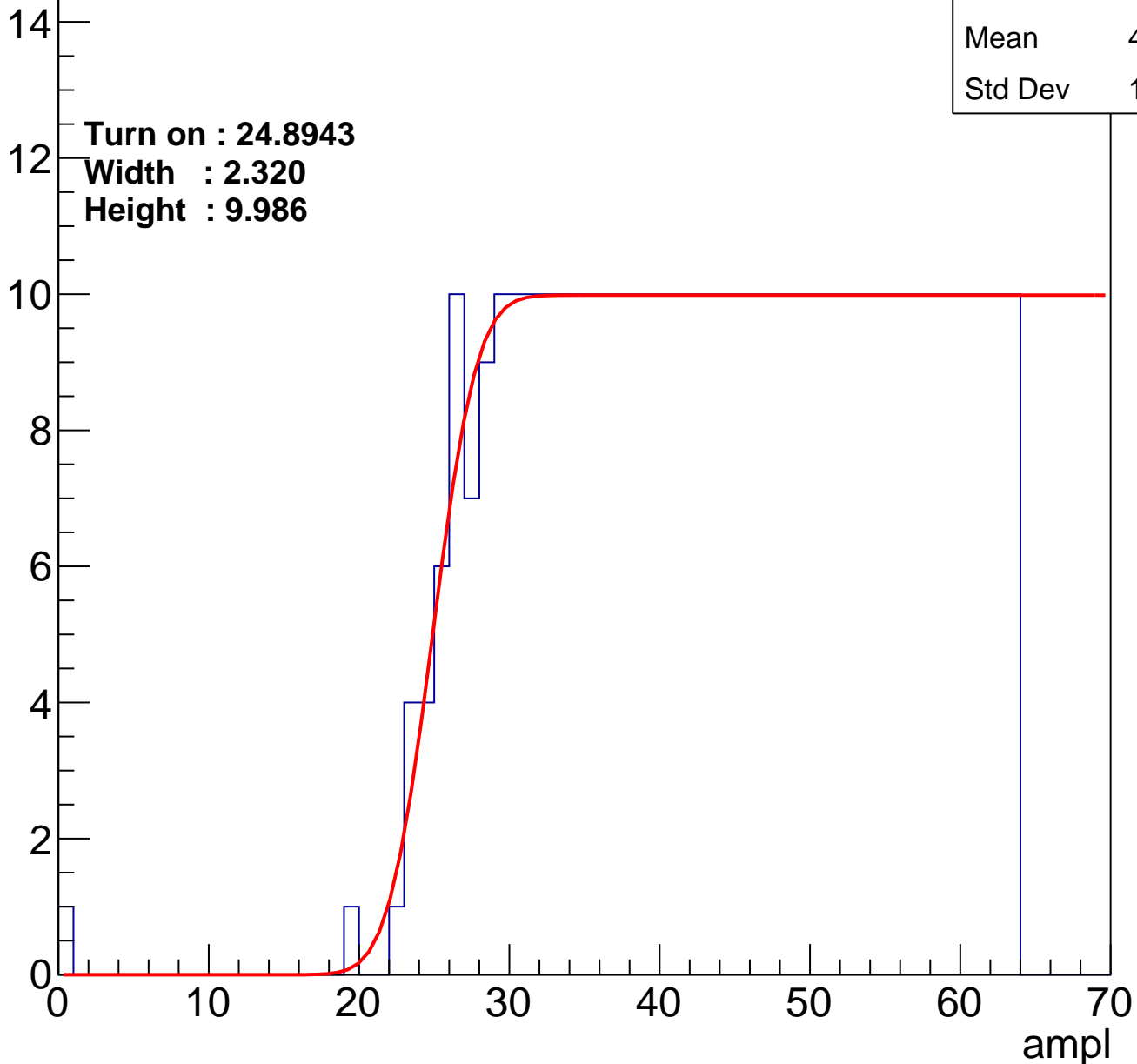
Entries	393
Mean	43.72
Std Dev	11.64

Turn on : 24.8943

Width : 2.320

Height : 9.986

Entry



B1L000S, U19-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.78
Std Dev	11.03

Turn on : 26.9317

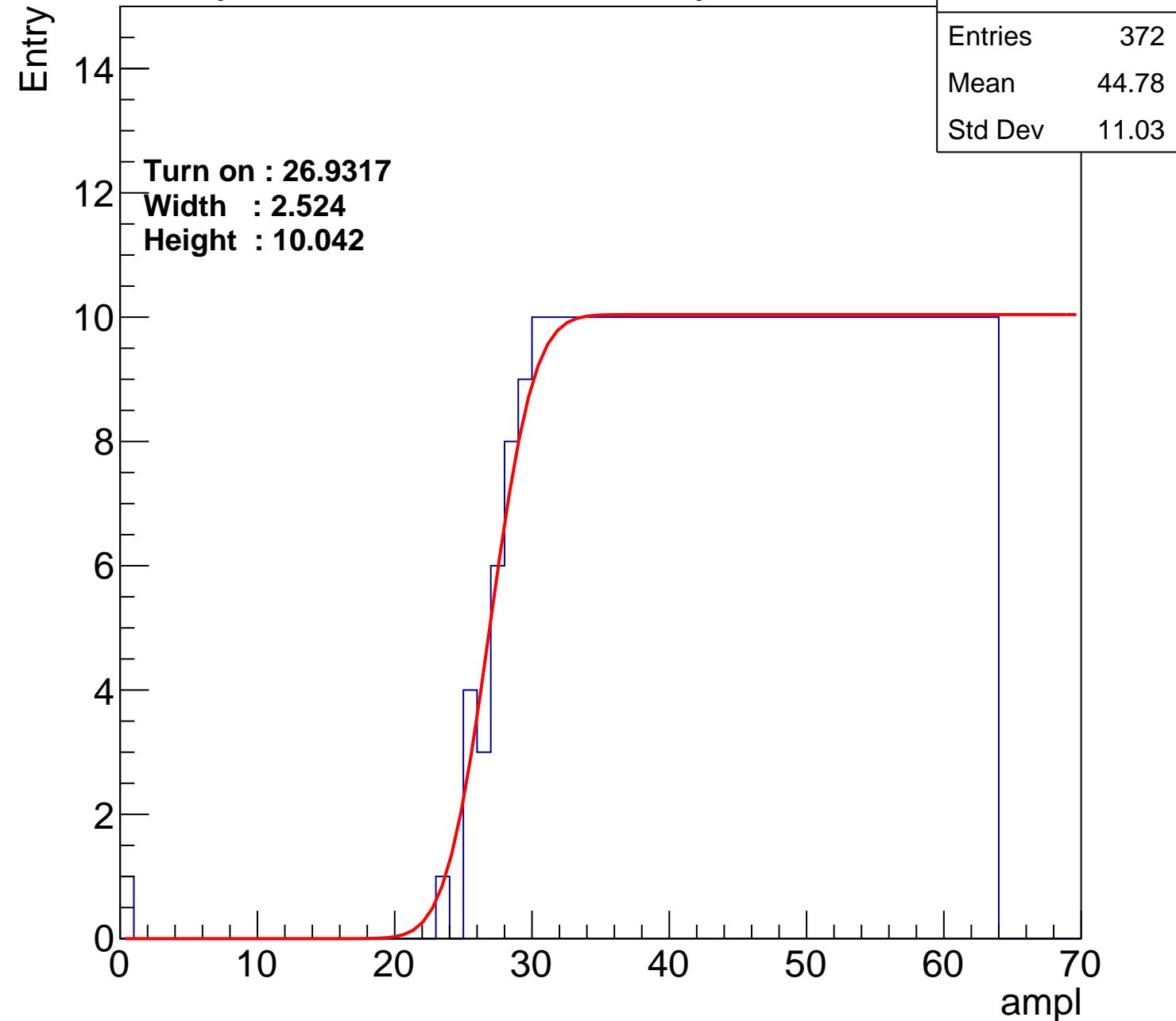
Width : 2.524

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 25.3083

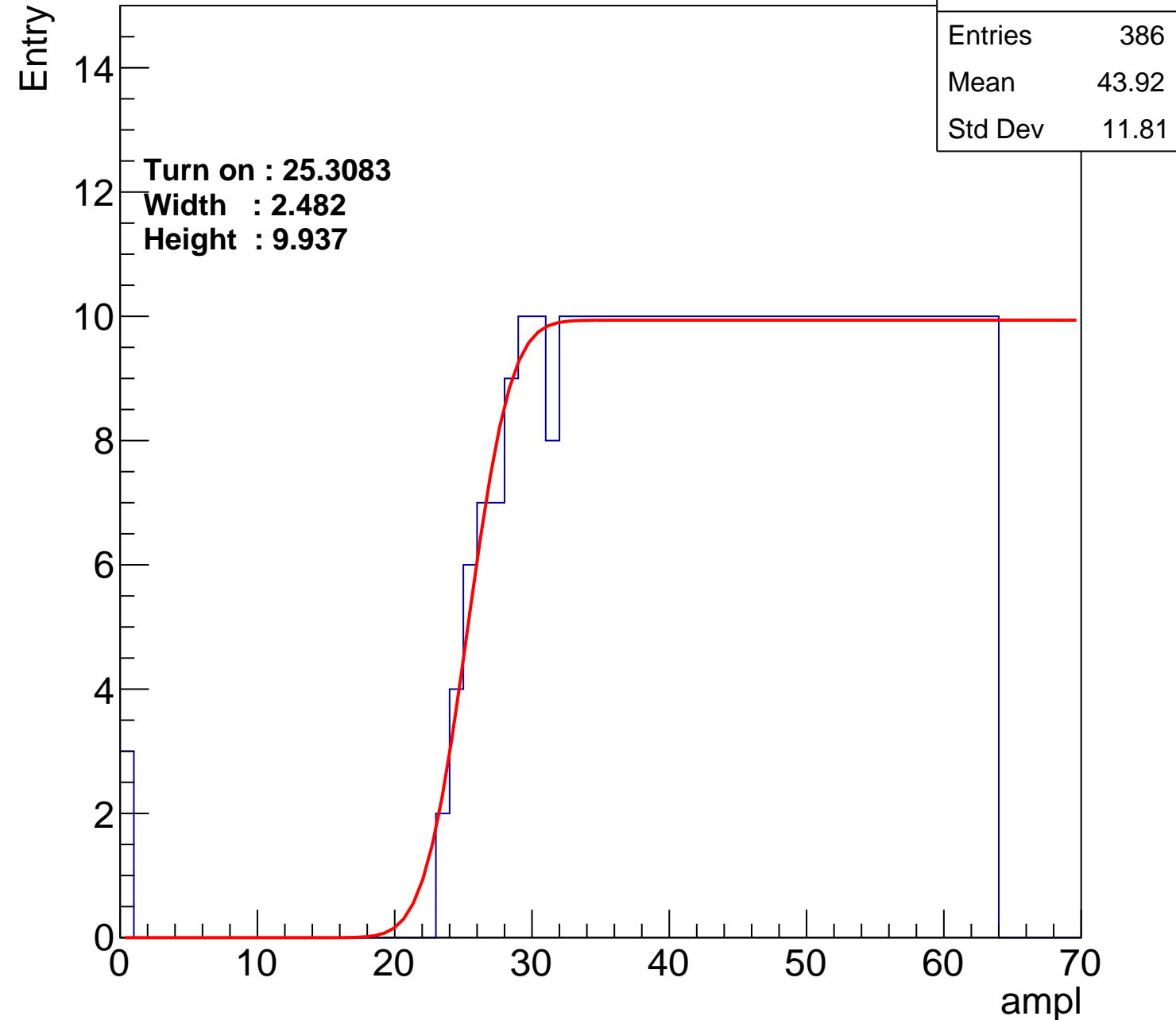
Width : 2.482

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch70

calib_packv5_042523_0143.root, FC#3, port B3

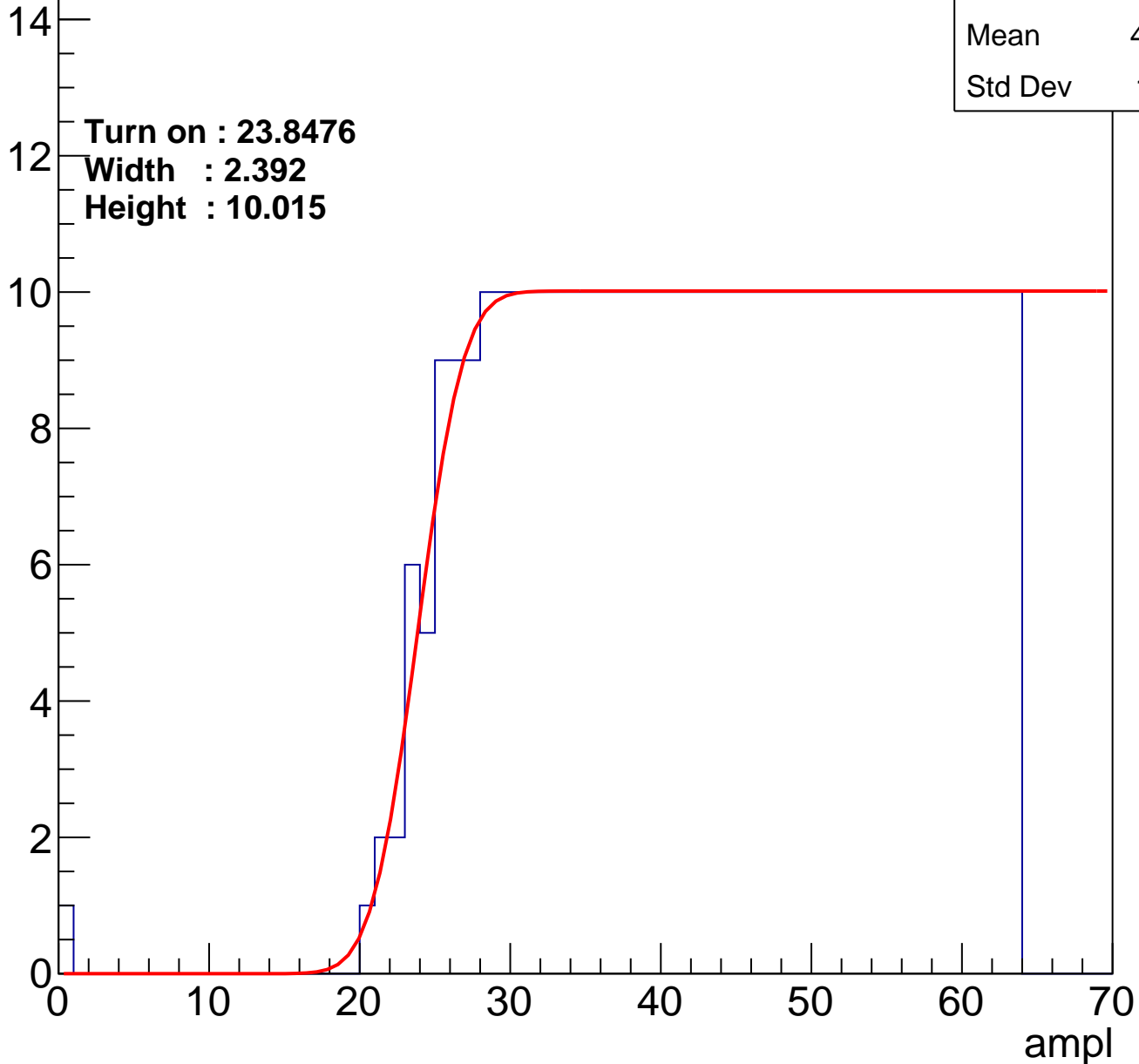
Entries	404
Mean	43.18
Std Dev	11.91

Turn on : 23.8476

Width : 2.392

Height : 10.015

Entry



B1L000S, U19-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.3547

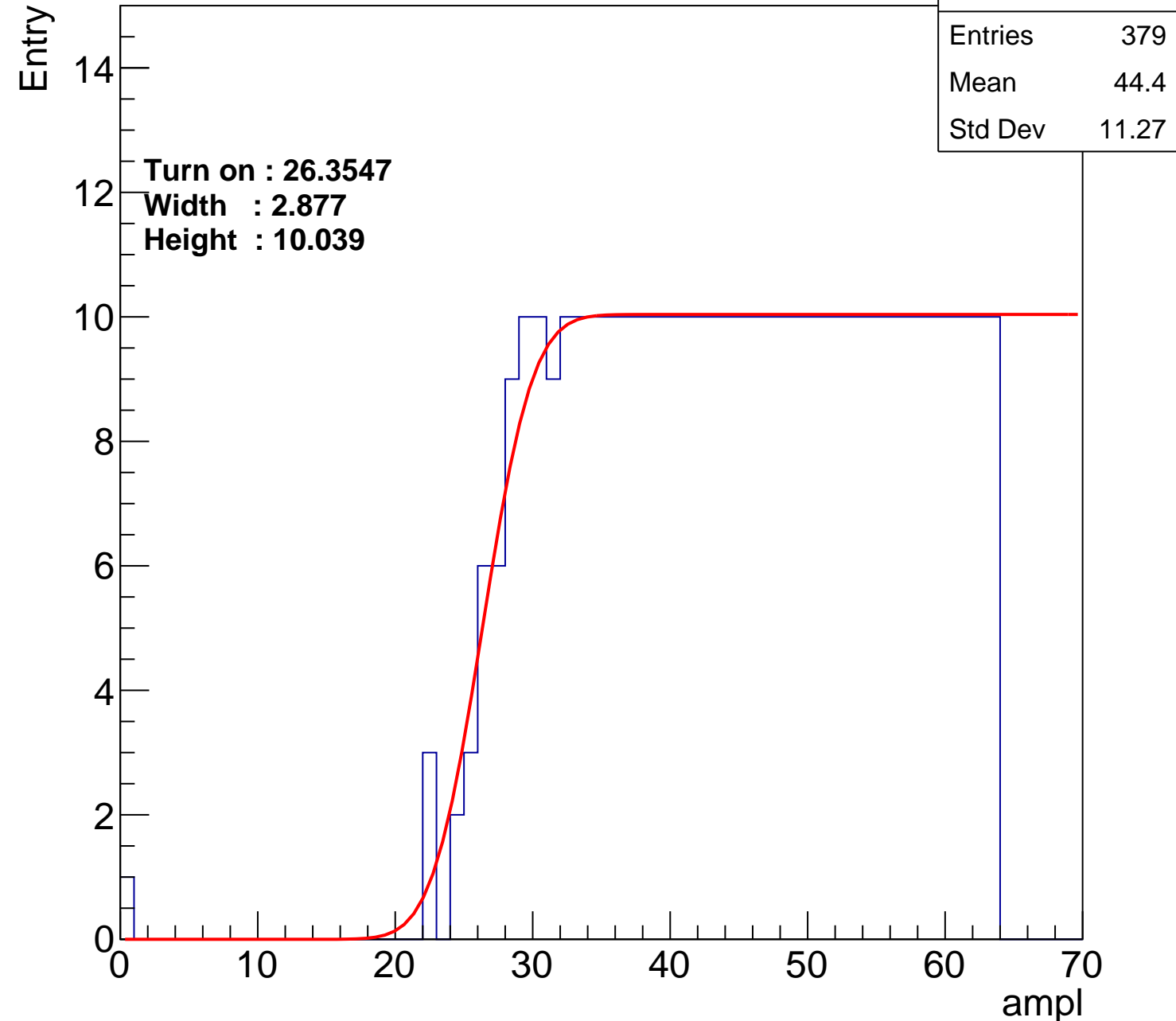
Width : 2.877

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 27.4608

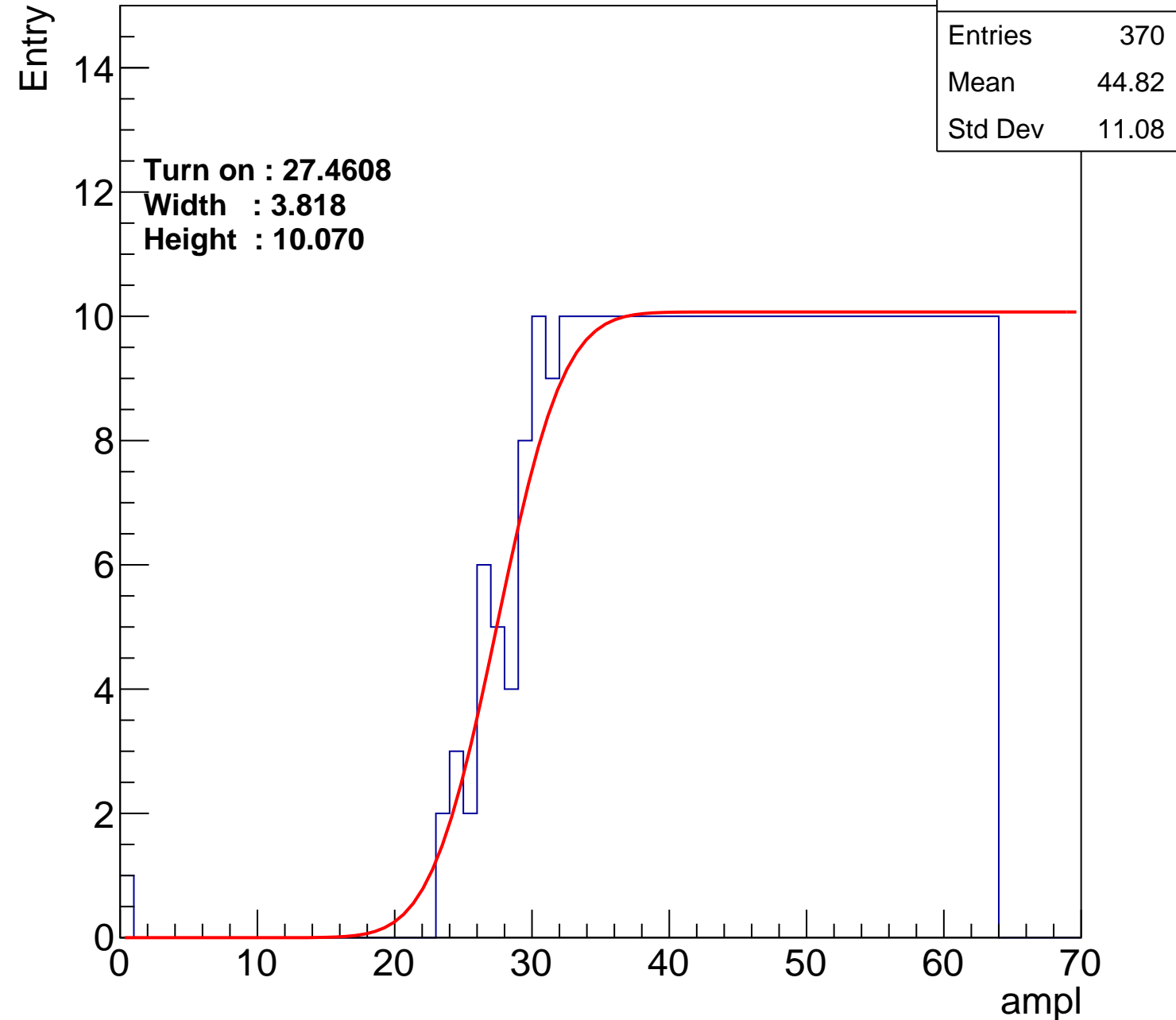
Width : 3.818

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.45
Std Dev	11.6

Turn on : 26.4207

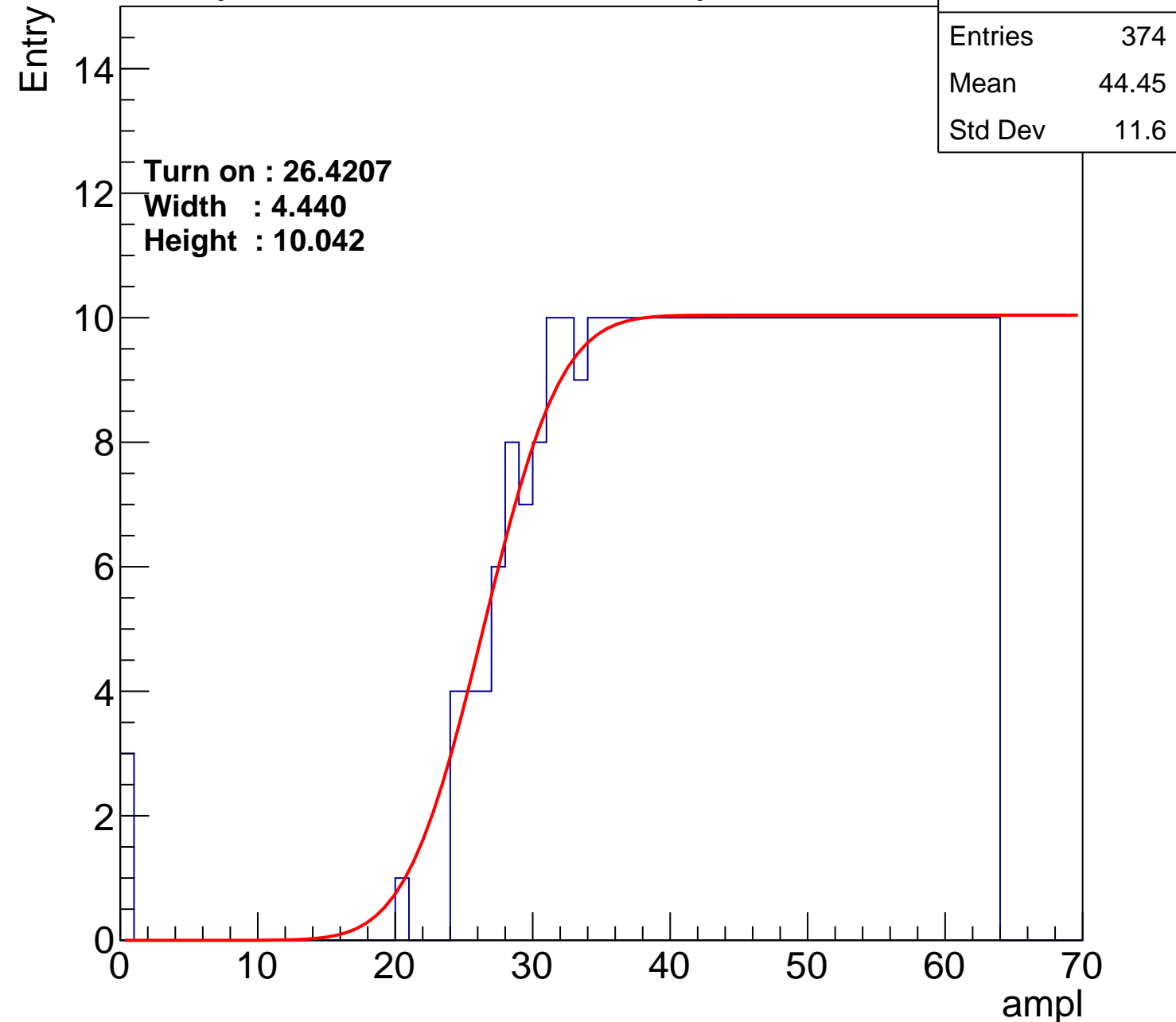
Width : 4.440

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch74

calib_packv5_042523_0143.root, FC#3, port B3

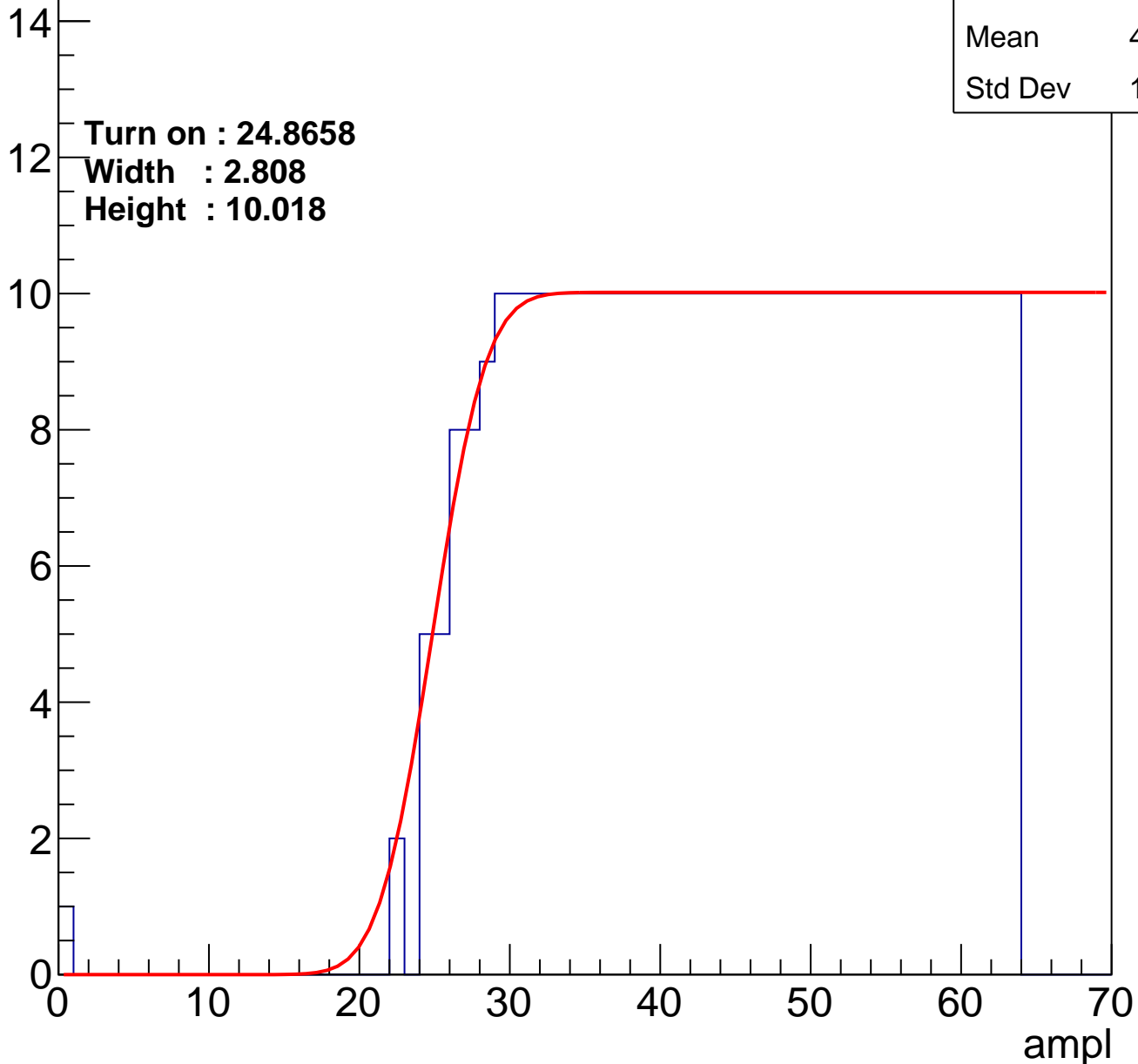
Entries	388
Mean	43.98
Std Dev	11.47

Turn on : 24.8658

Width : 2.808

Height : 10.018

Entry



B1L000S, U19-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.34
Std Dev	10.75

Turn on : 28.0404

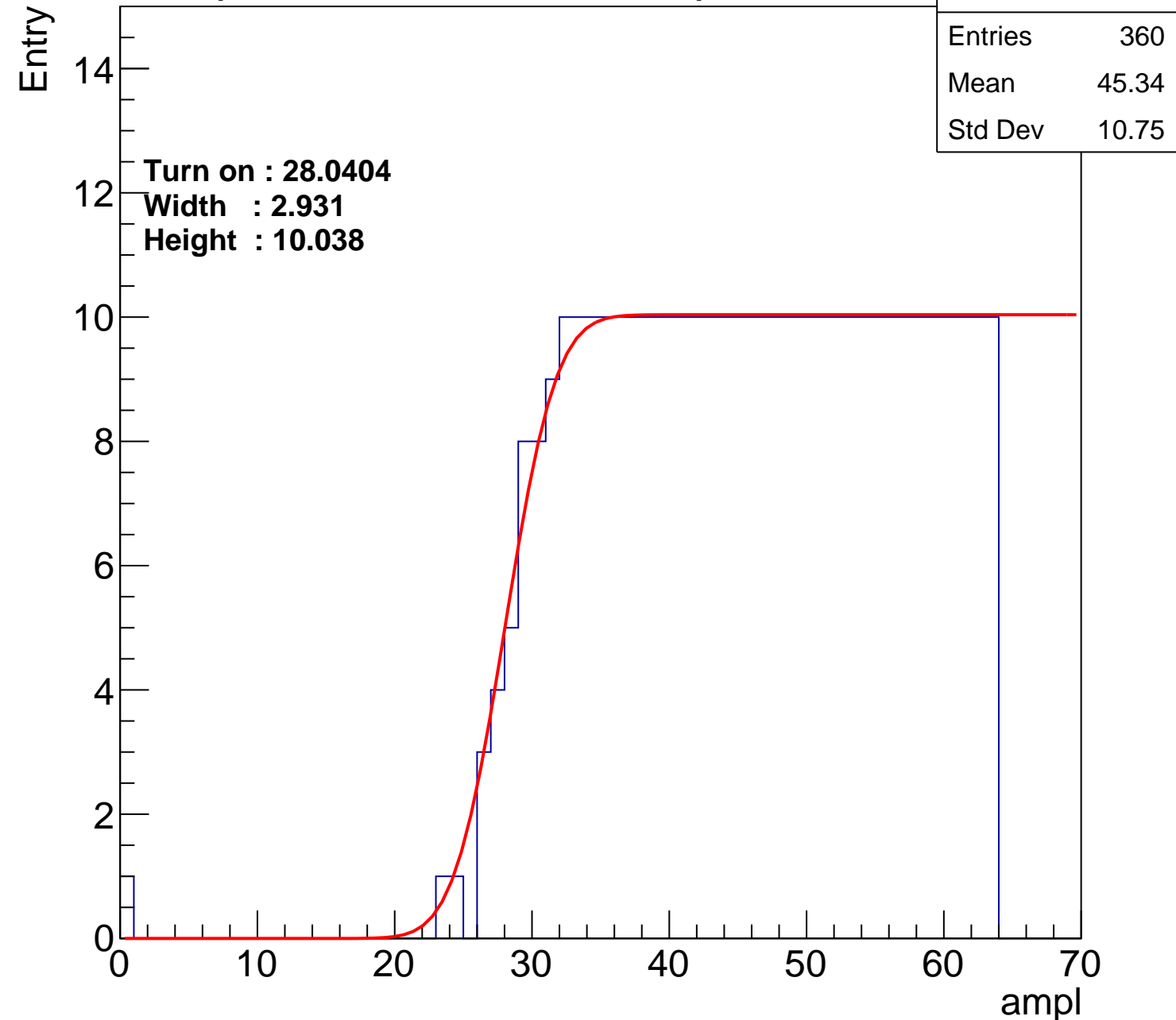
Width : 2.931

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch76

calib_packv5_042523_0143.root, FC#3, port B3

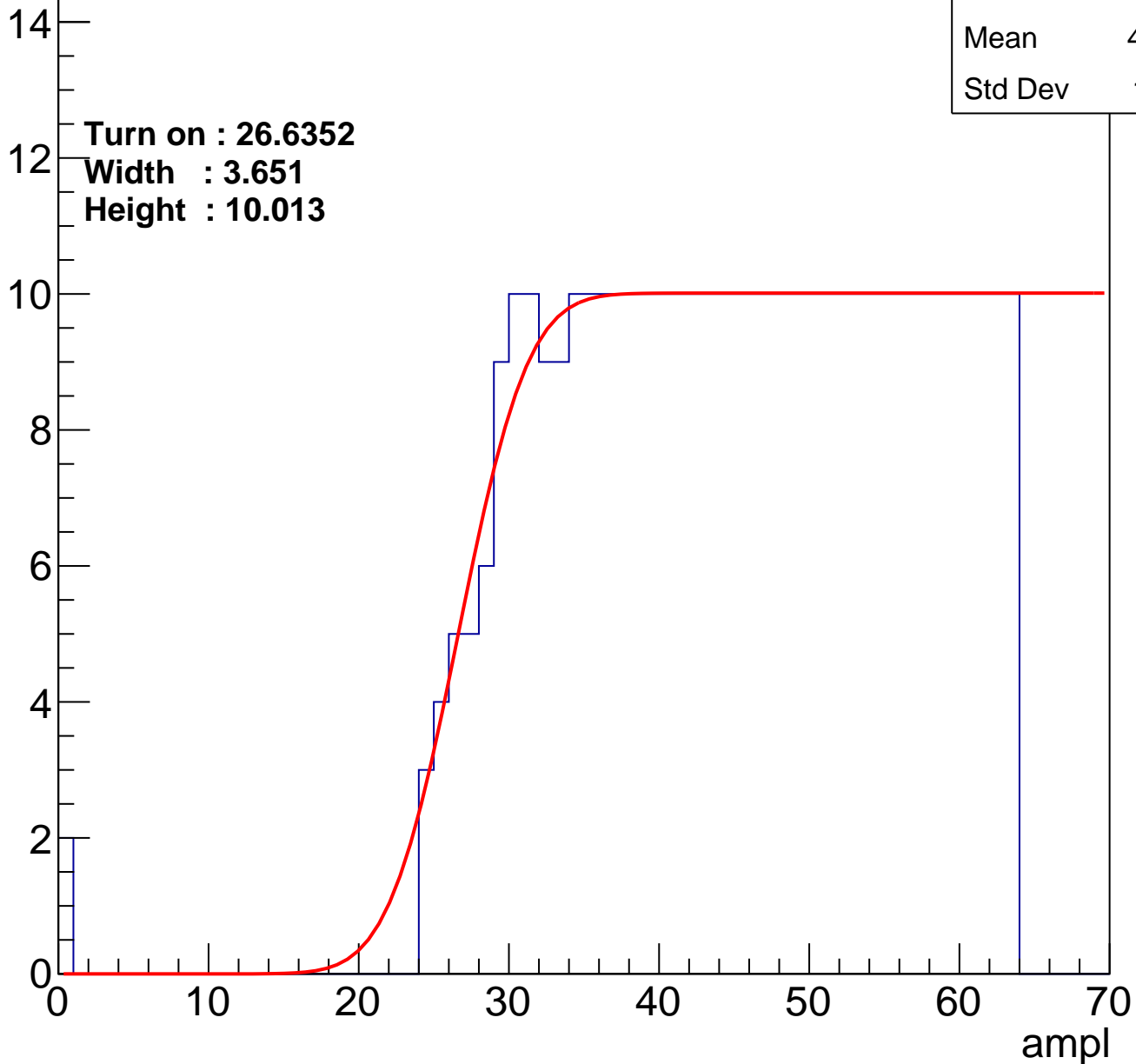
Entries	372
Mean	44.65
Std Dev	11.31

Turn on : 26.6352

Width : 3.651

Height : 10.013

Entry



B1L000S, U19-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.4491

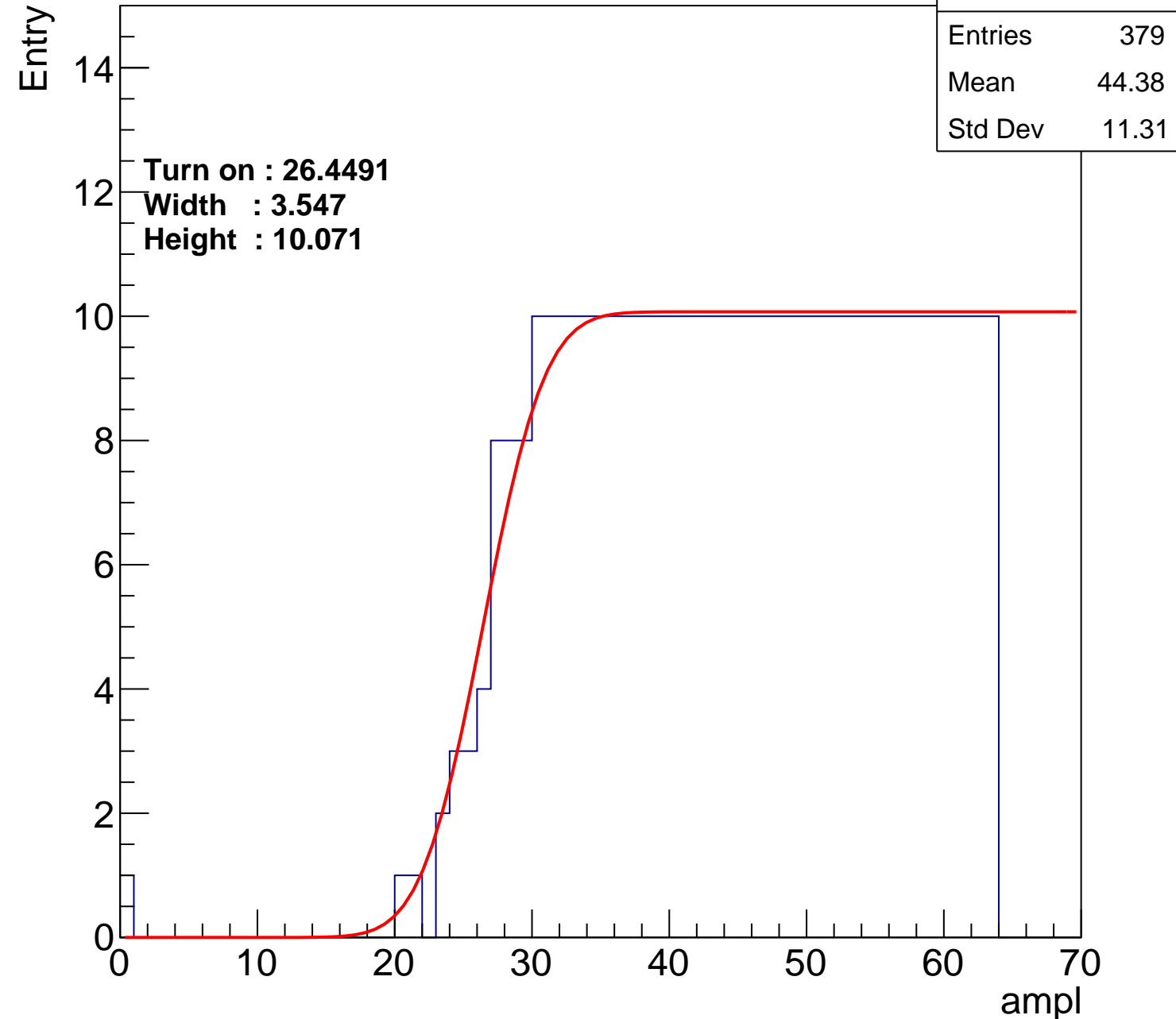
Width : 3.547

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch78

calib_packv5_042523_0143.root, FC#3, port B3

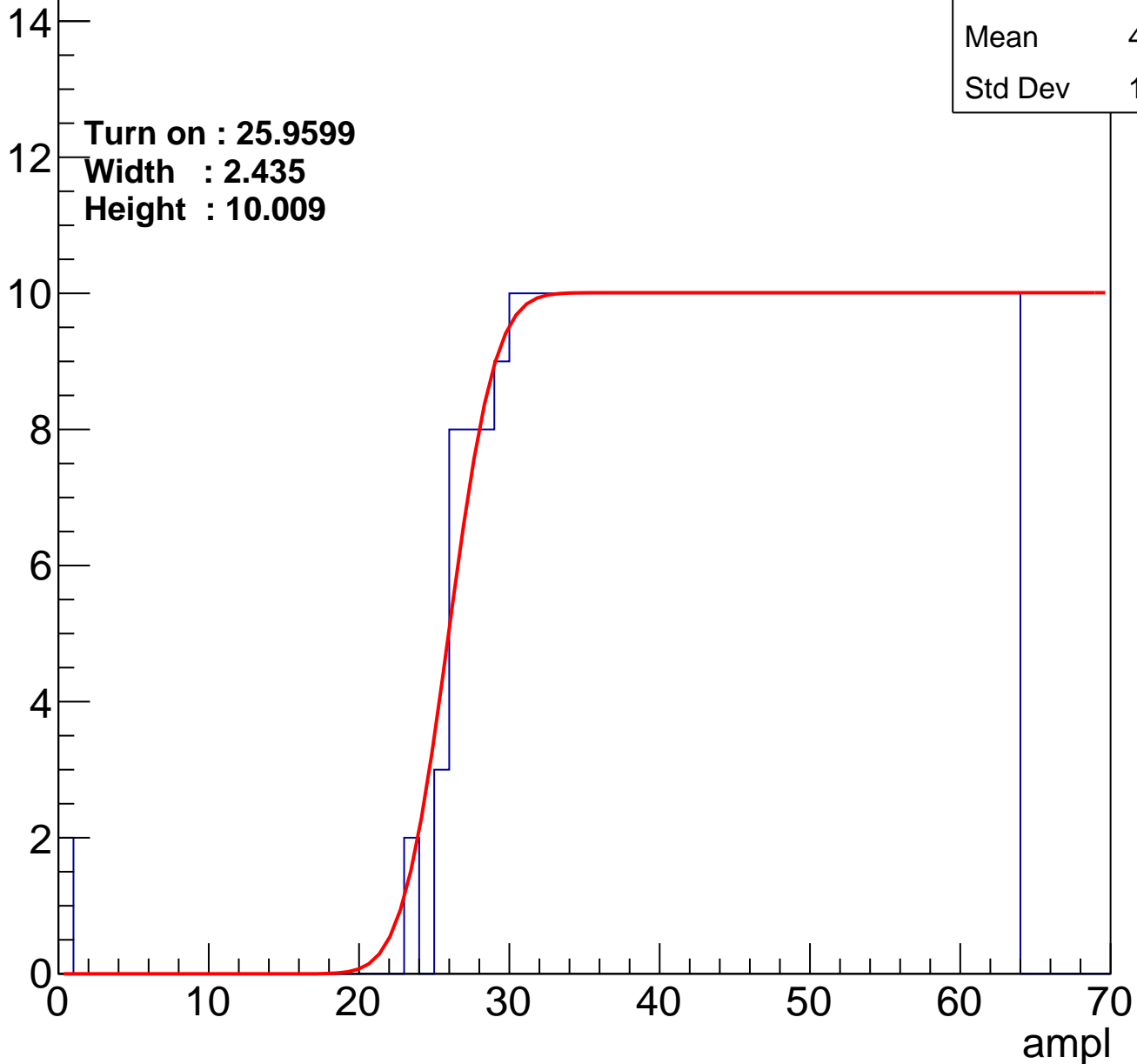
Entries	380
Mean	44.32
Std Dev	11.43

Turn on : 25.9599

Width : 2.435

Height : 10.009

Entry



B1L000S, U19-ch79

calib_packv5_042523_0143.root, FC#3, port B3

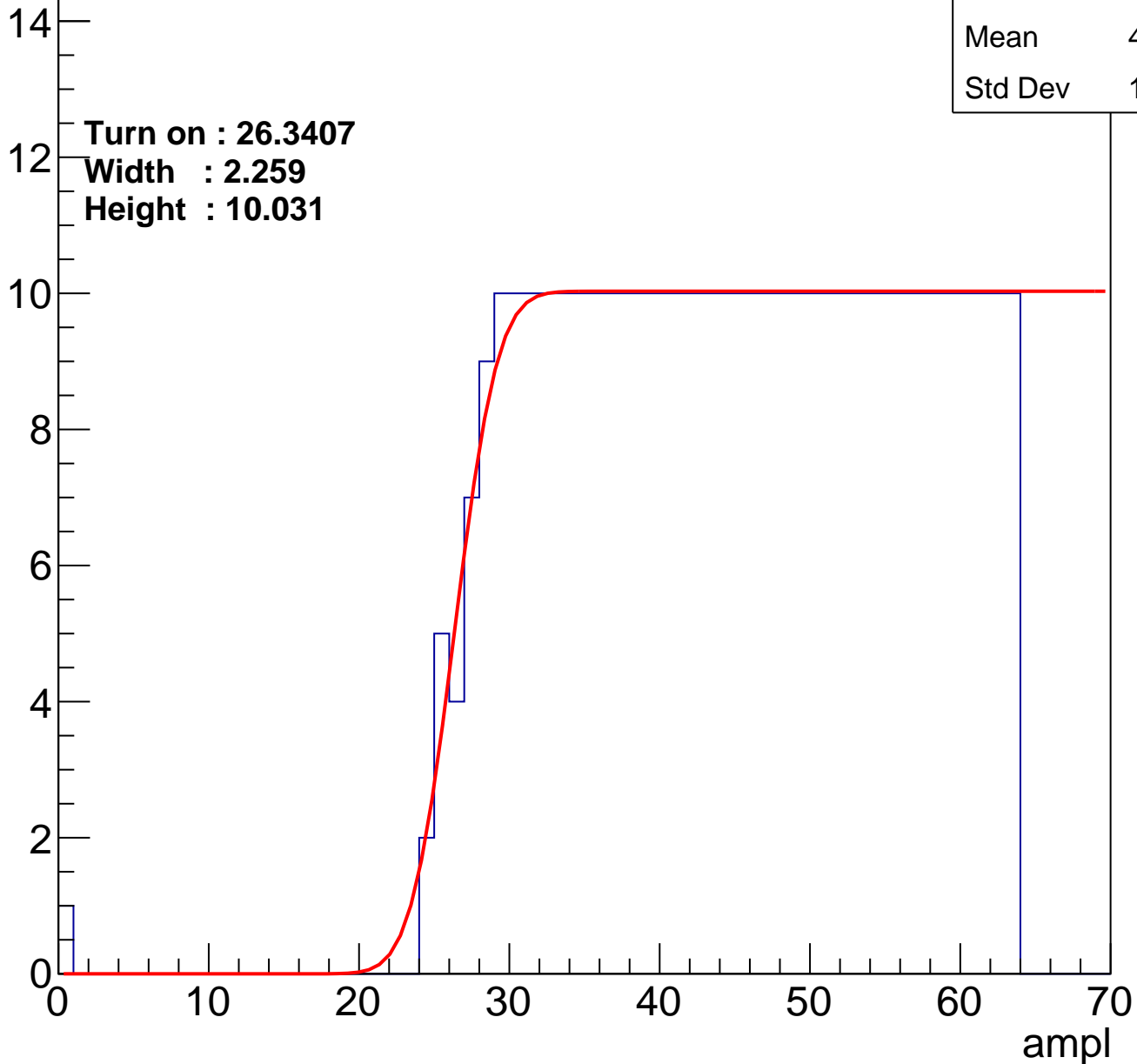
Entries	378
Mean	44.49
Std Dev	11.17

Turn on : 26.3407

Width : 2.259

Height : 10.031

Entry



B1L000S, U19-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.4314

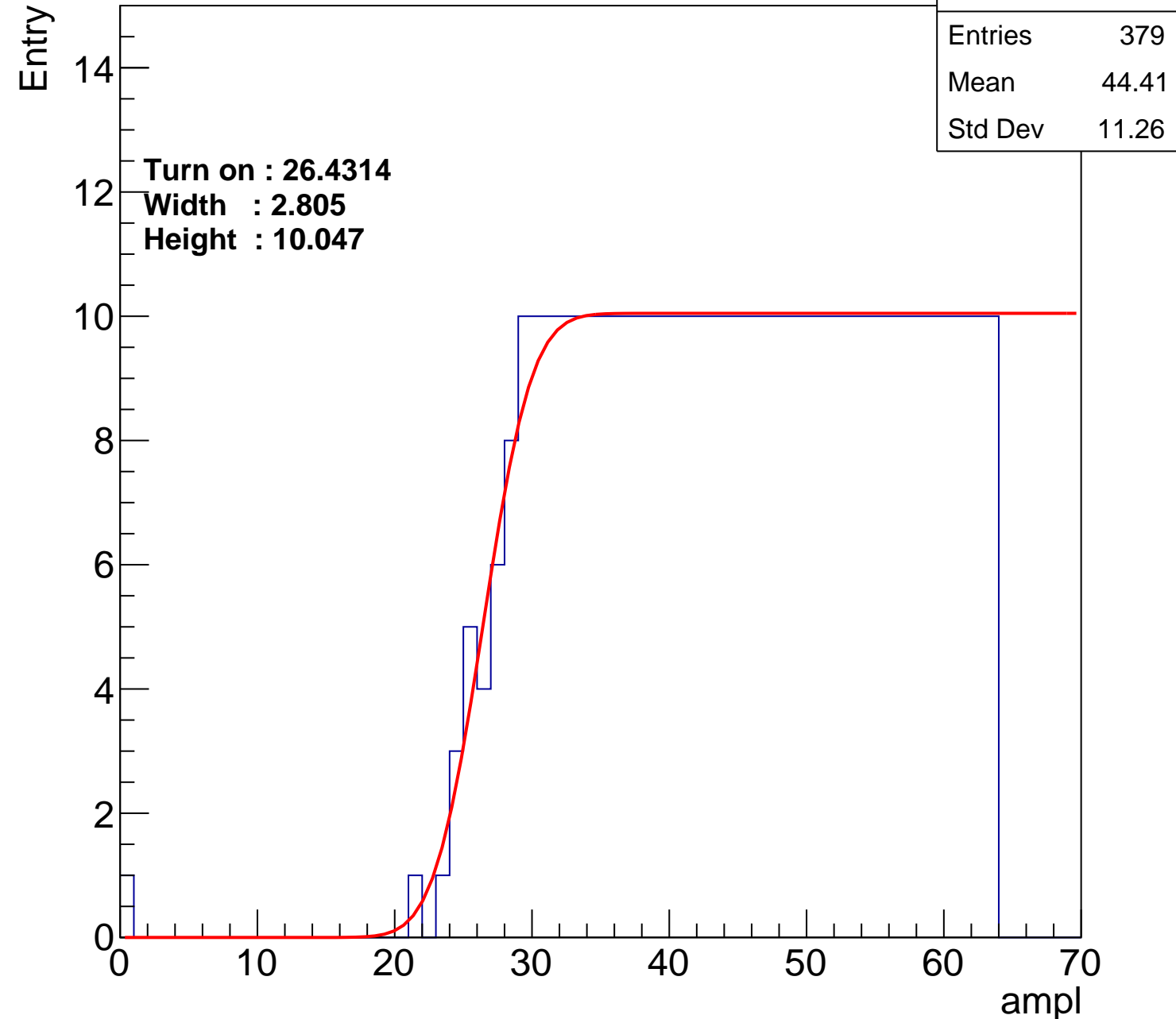
Width : 2.805

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch81

calib_packv5_042523_0143.root, FC#3, port B3

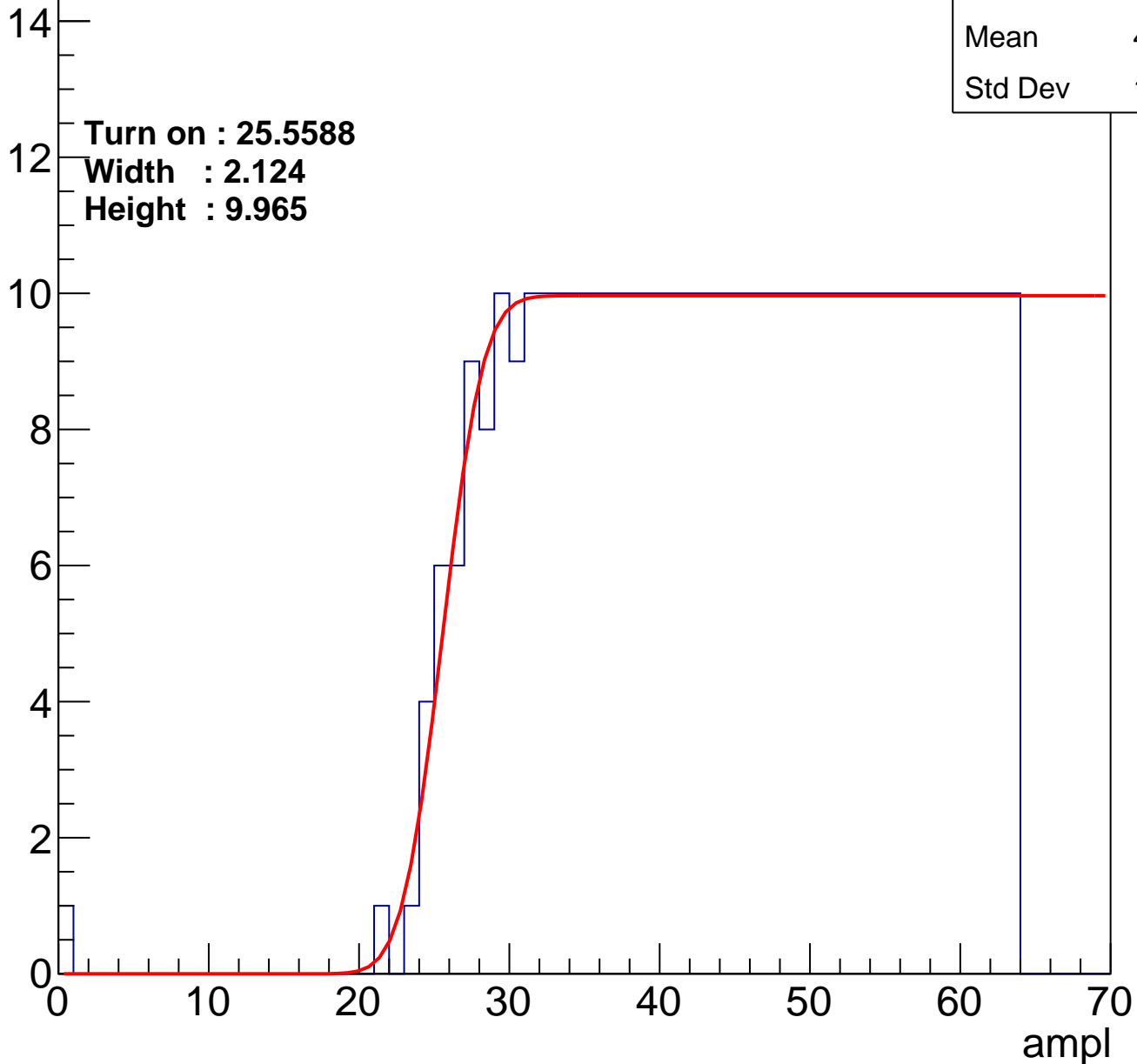
Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.5588

Width : 2.124

Height : 9.965

Entry



B1L000S, U19-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.64
Std Dev	11.29

Turn on : 27.3588

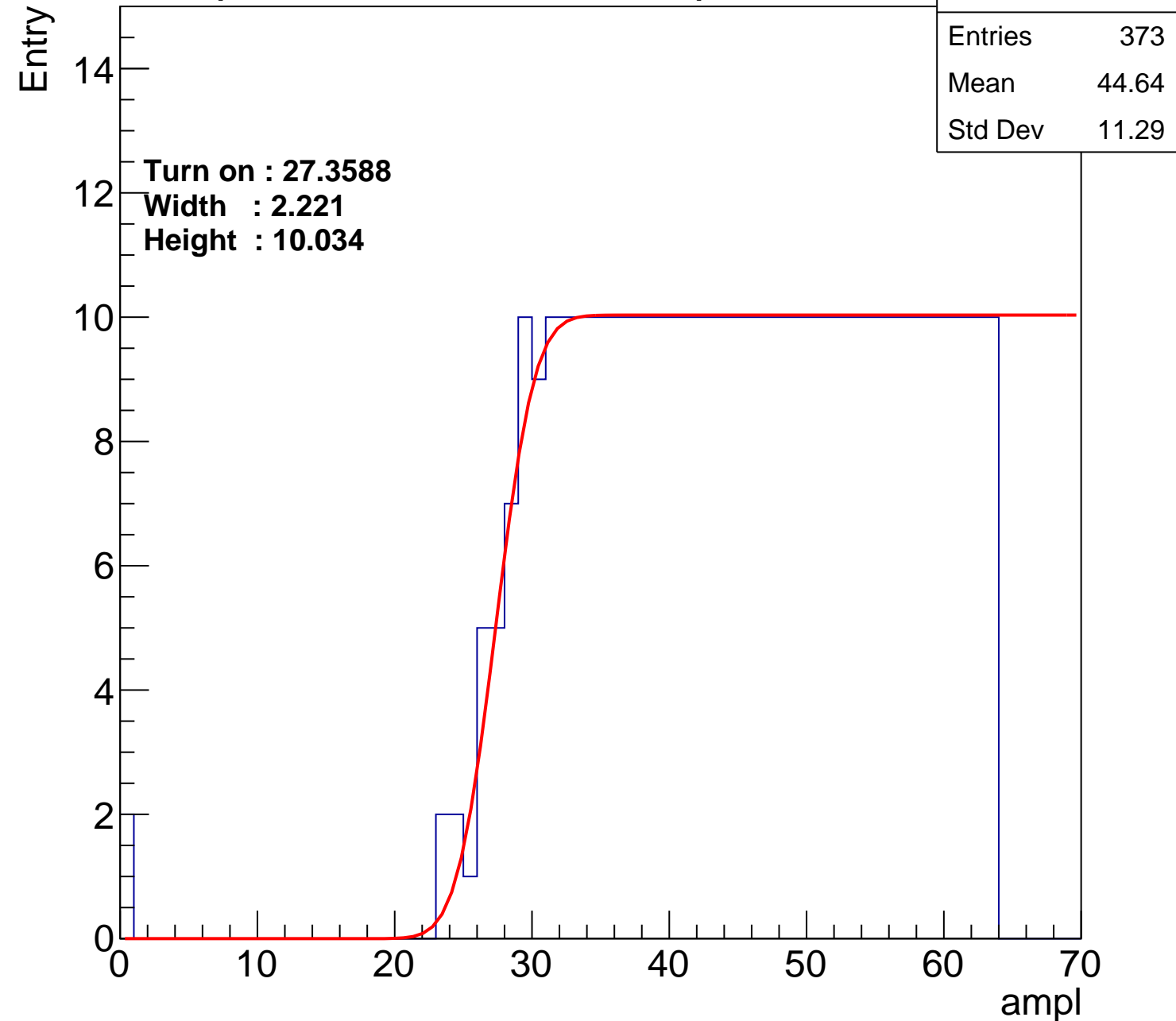
Width : 2.221

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.5335

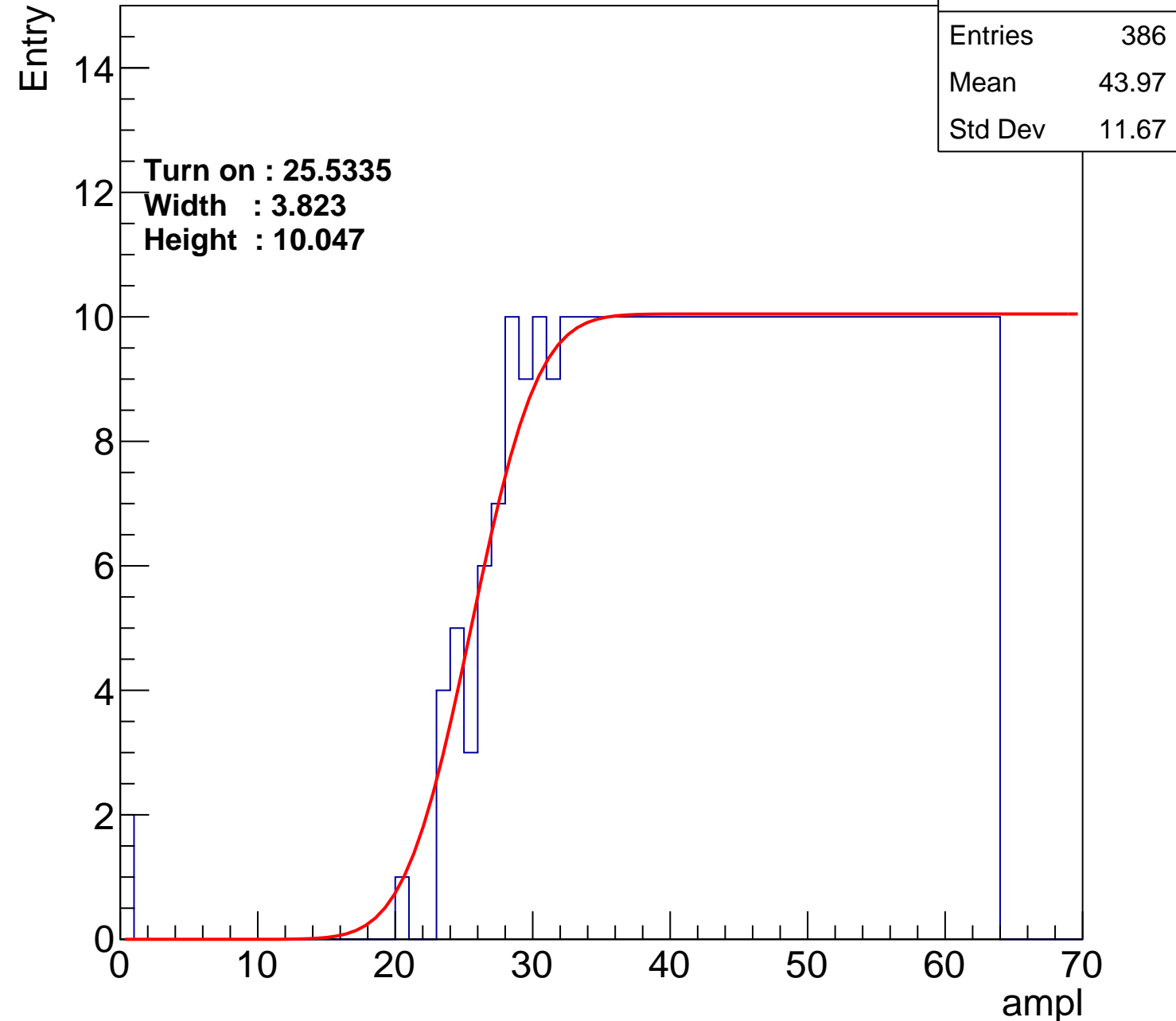
Width : 3.823

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 25.5103

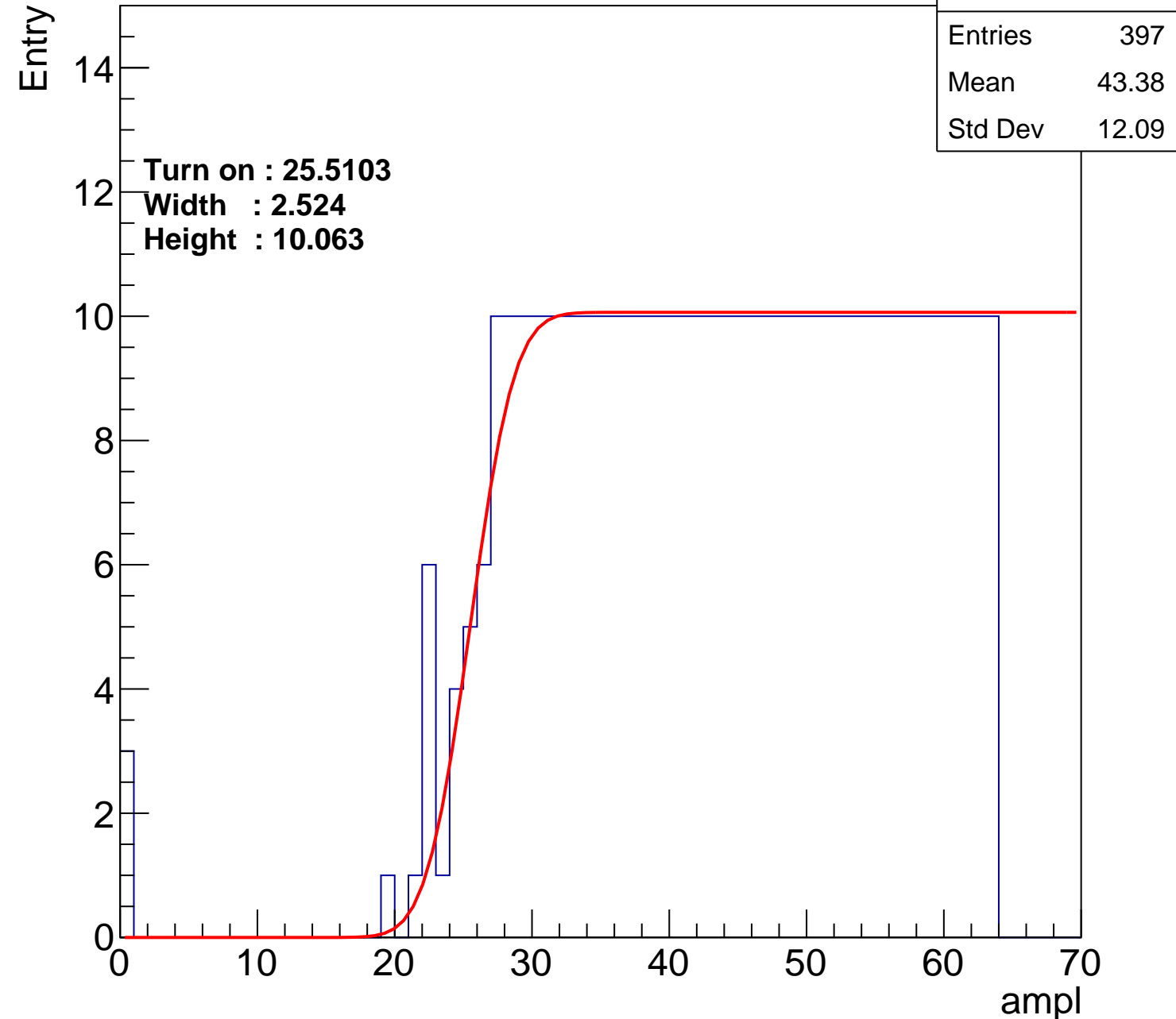
Width : 2.524

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.3
Std Dev	11.77

Mean	44.3
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Std Dev	11.77
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Width : 2.636

Height : 10.057



B1L000S, U19-ch86

calib_packv5_042523_0143.root, FC#3, port B3

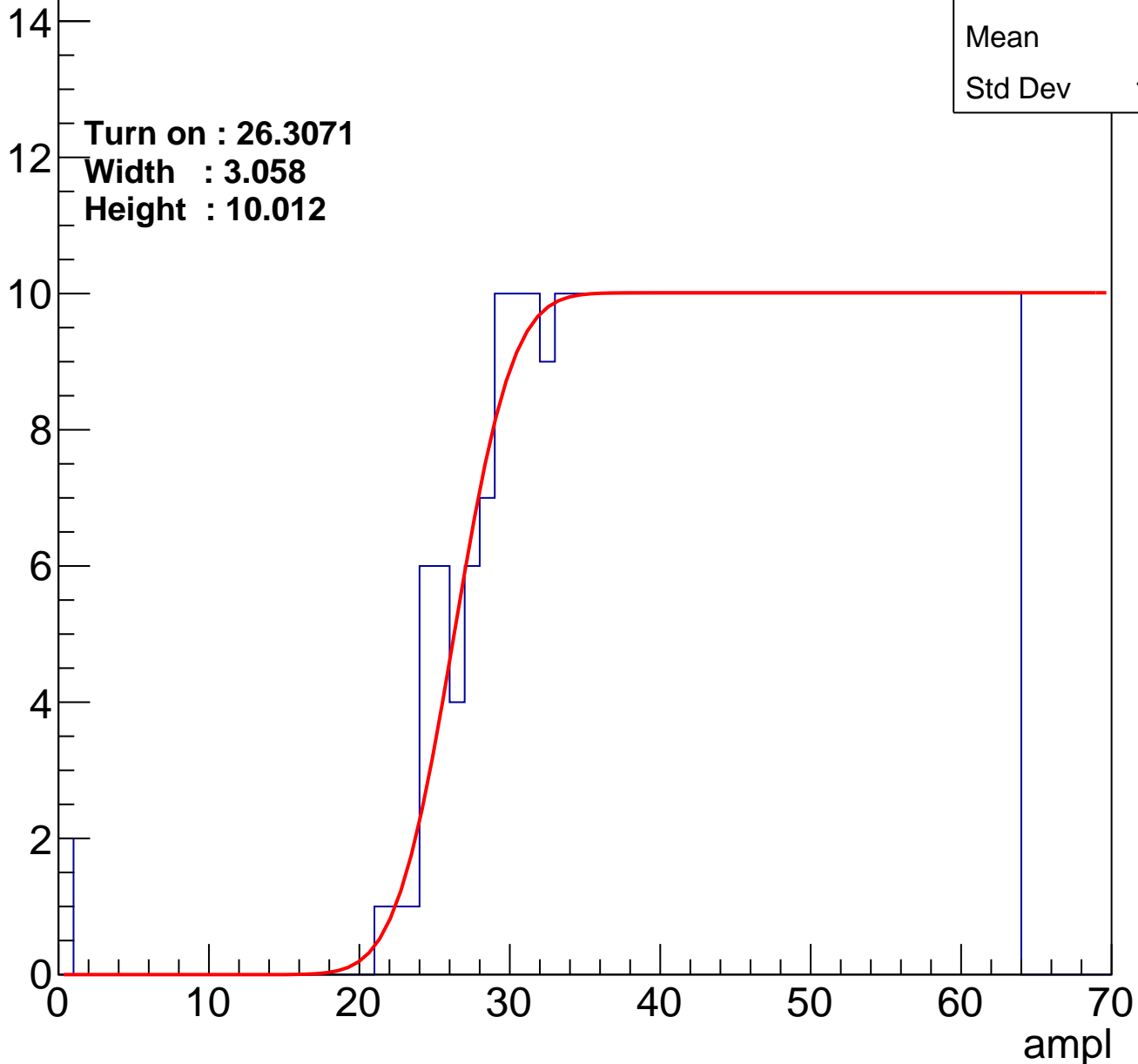
Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.3071

Width : 3.058

Height : 10.012

Entry



B1L000S, U19-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
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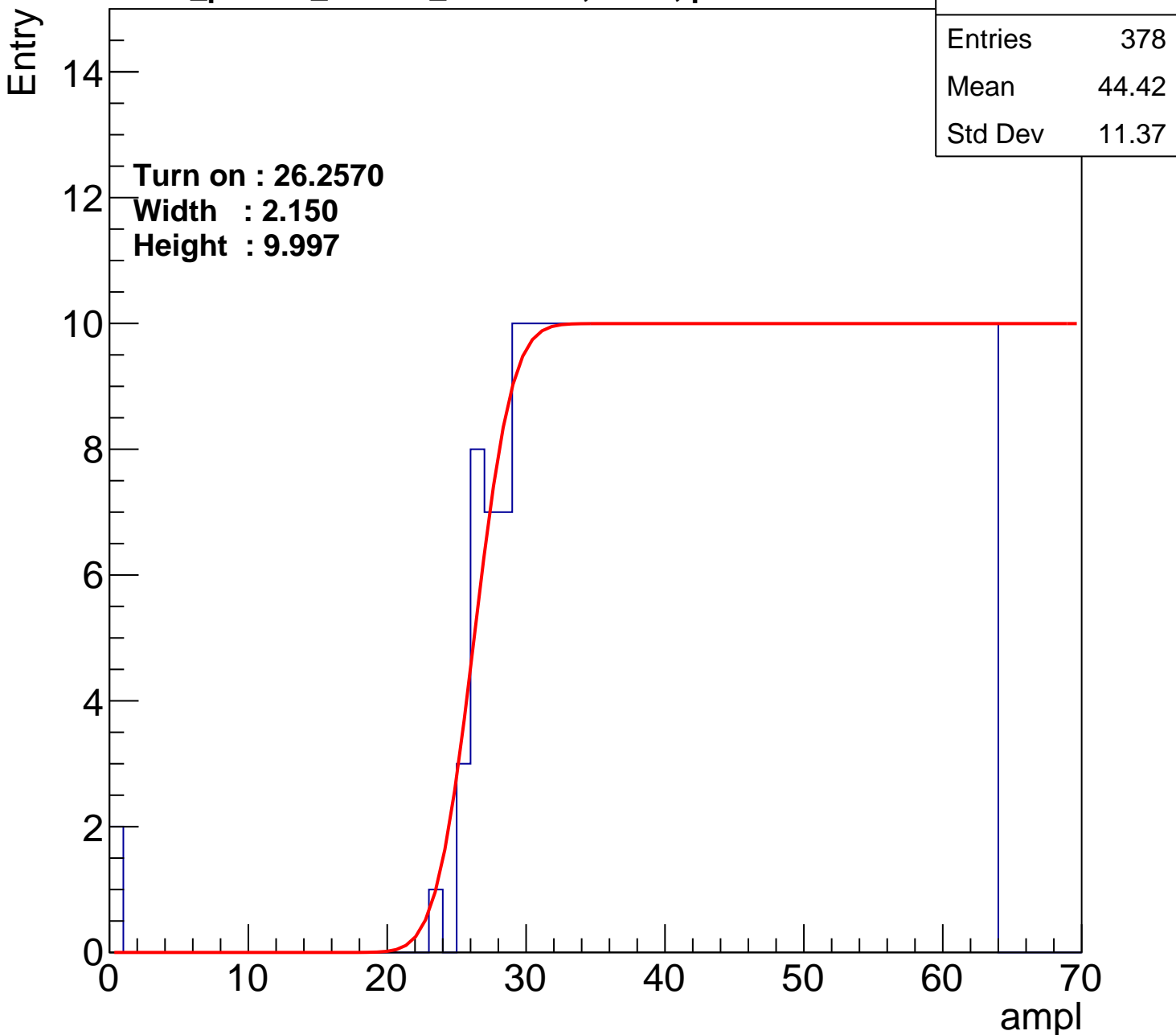
Mean	44.42
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Std Dev	11.37
---------	-------

Turn on : 26.2570

Width : 2.150

Height : 9.997



B1L000S, U19-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.86

Turn on : 25.3793

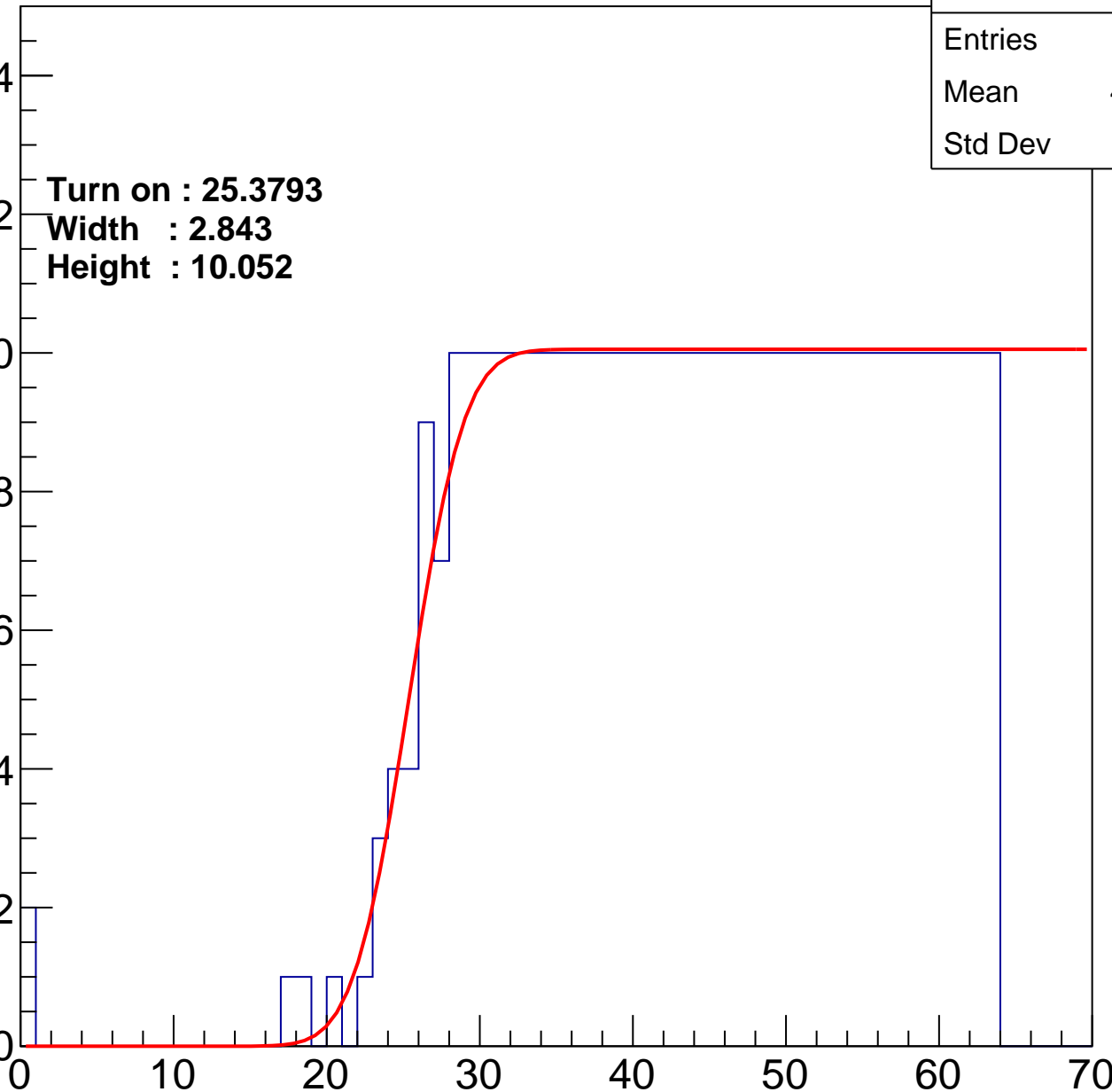
Width : 2.843

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch89

calib_packv5_042523_0143.root, FC#3, port B3

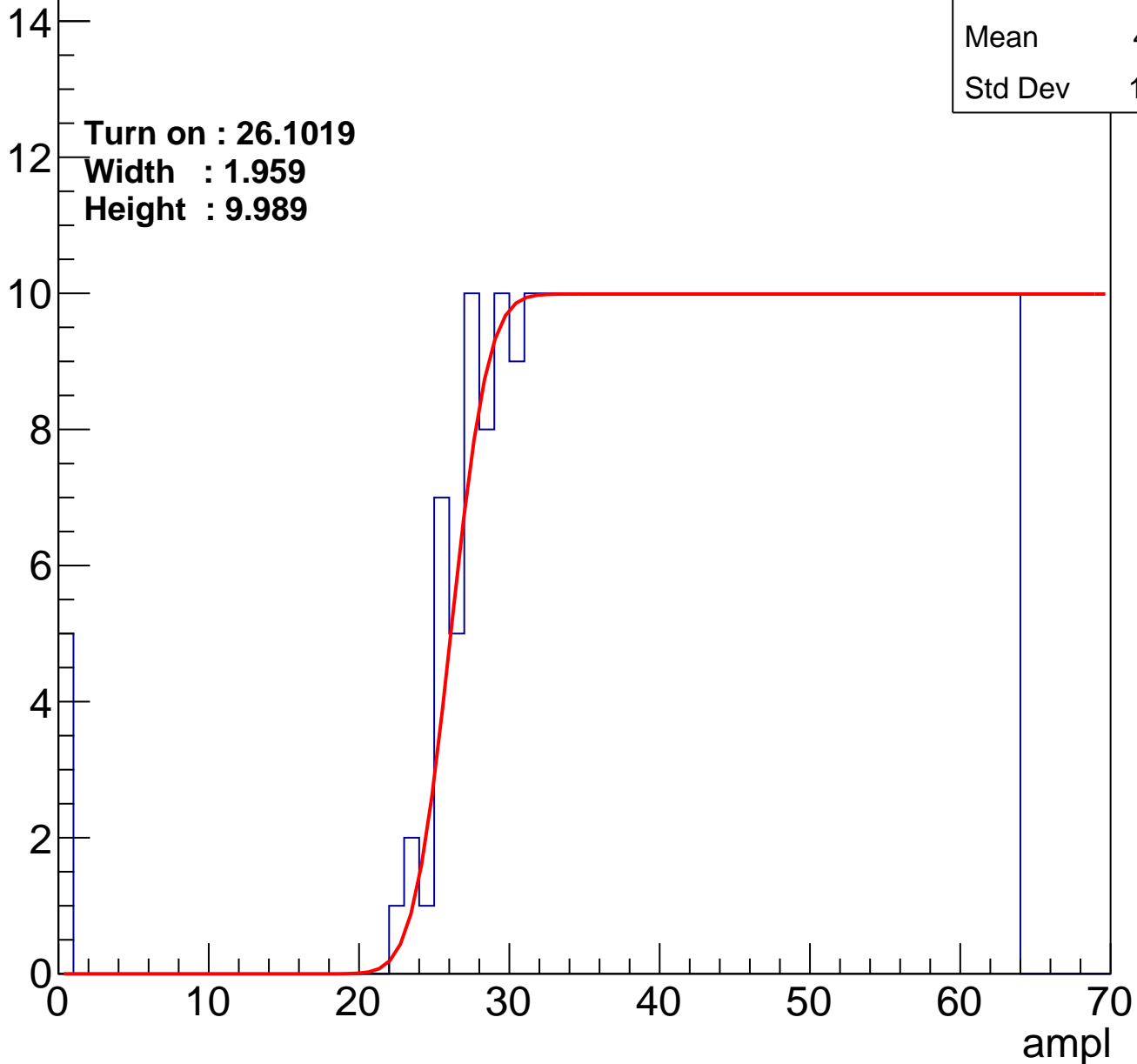
Entries	388
Mean	43.71
Std Dev	12.16

Turn on : 26.1019

Width : 1.959

Height : 9.989

Entry



B1L000S, U19-ch90

calib_packv5_042523_0143.root, FC#3, port B3

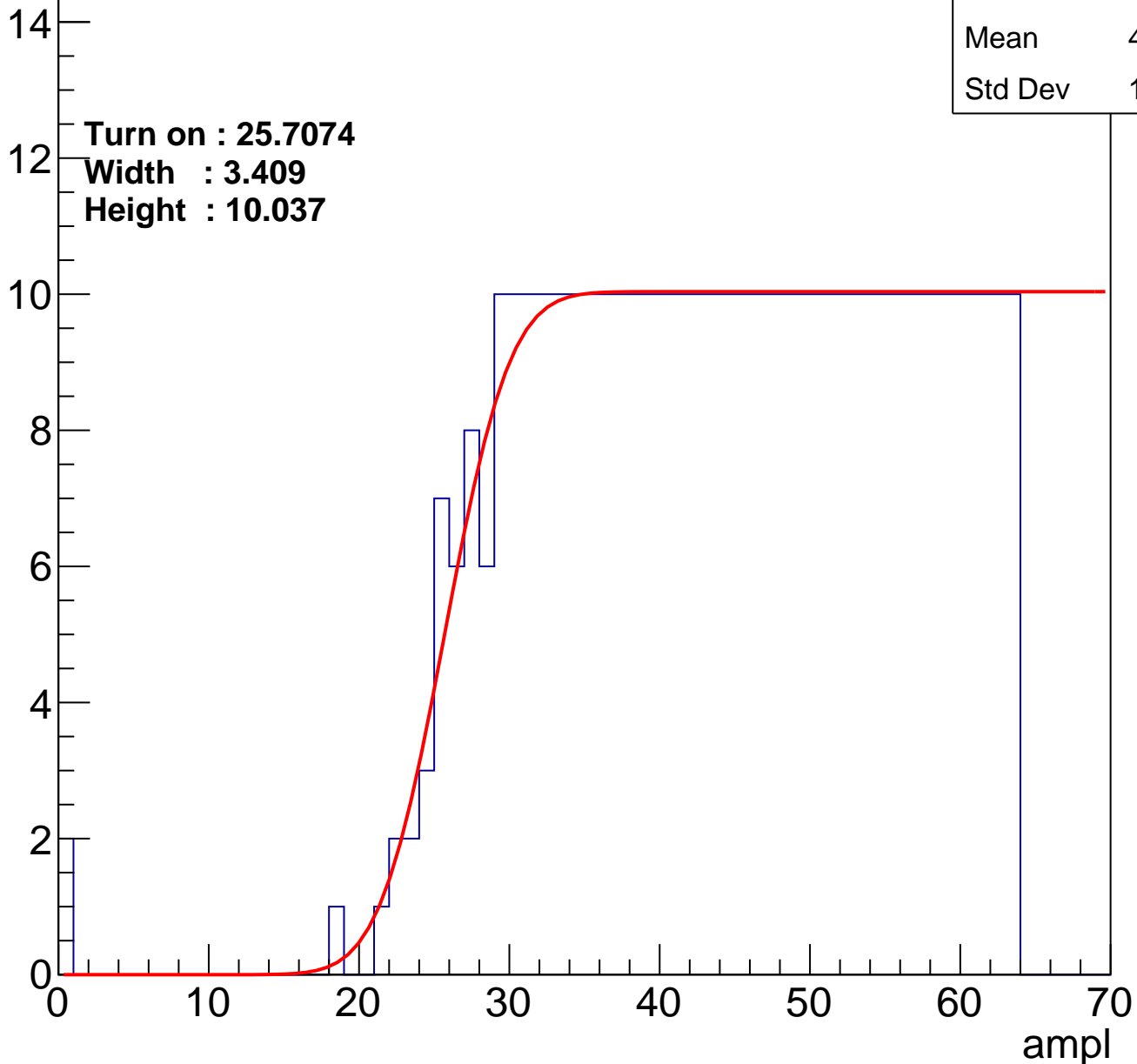
Entries	388
Mean	43.86
Std Dev	11.75

Turn on : 25.7074

Width : 3.409

Height : 10.037

Entry



B1L000S, U19-ch91

calib_packv5_042523_0143.root, FC#3, port B3

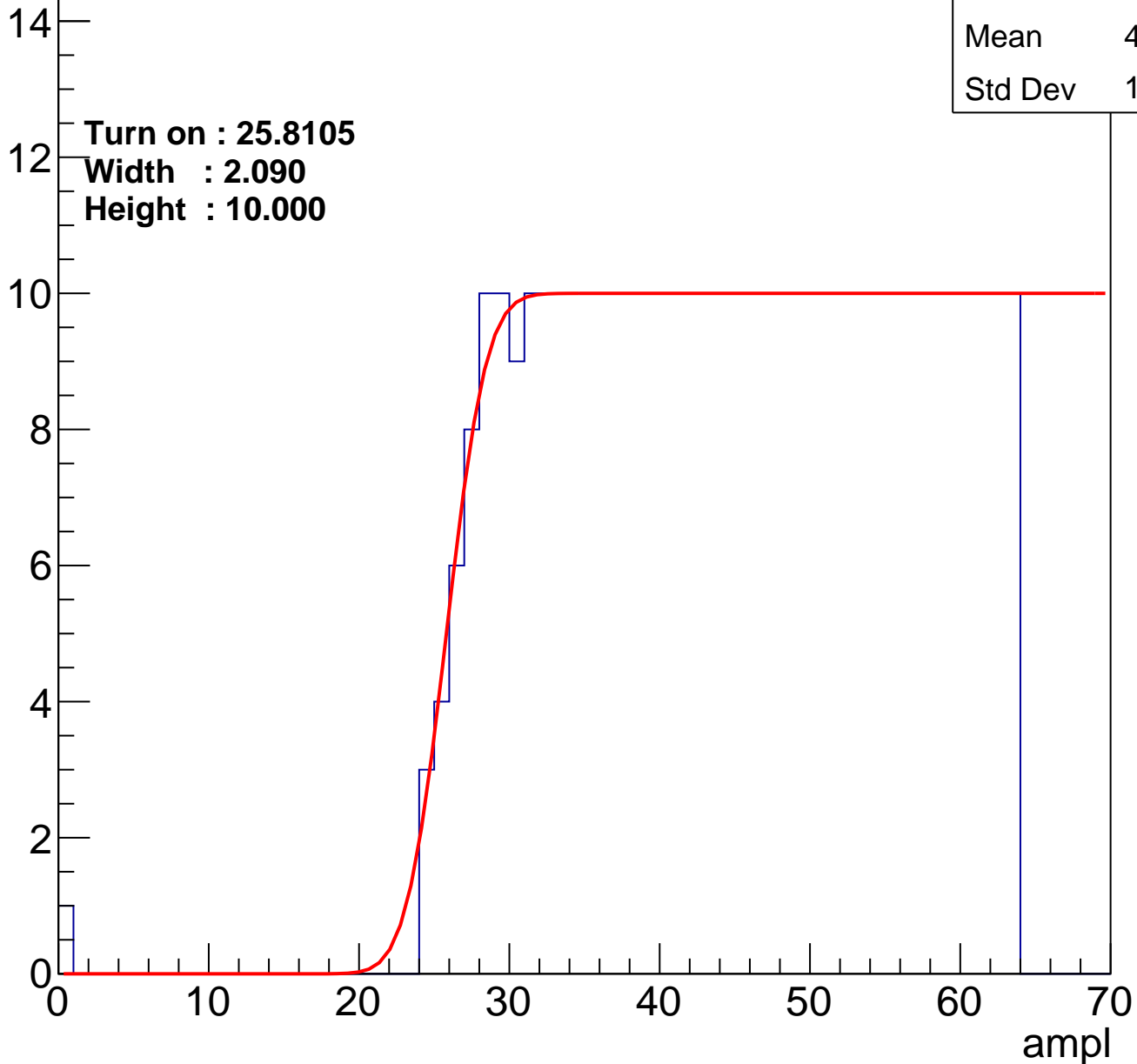
Entries	381
Mean	44.34
Std Dev	11.25

Turn on : 25.8105

Width : 2.090

Height : 10.000

Entry



B1L000S, U19-ch92

calib_packv5_042523_0143.root, FC#3, port B3

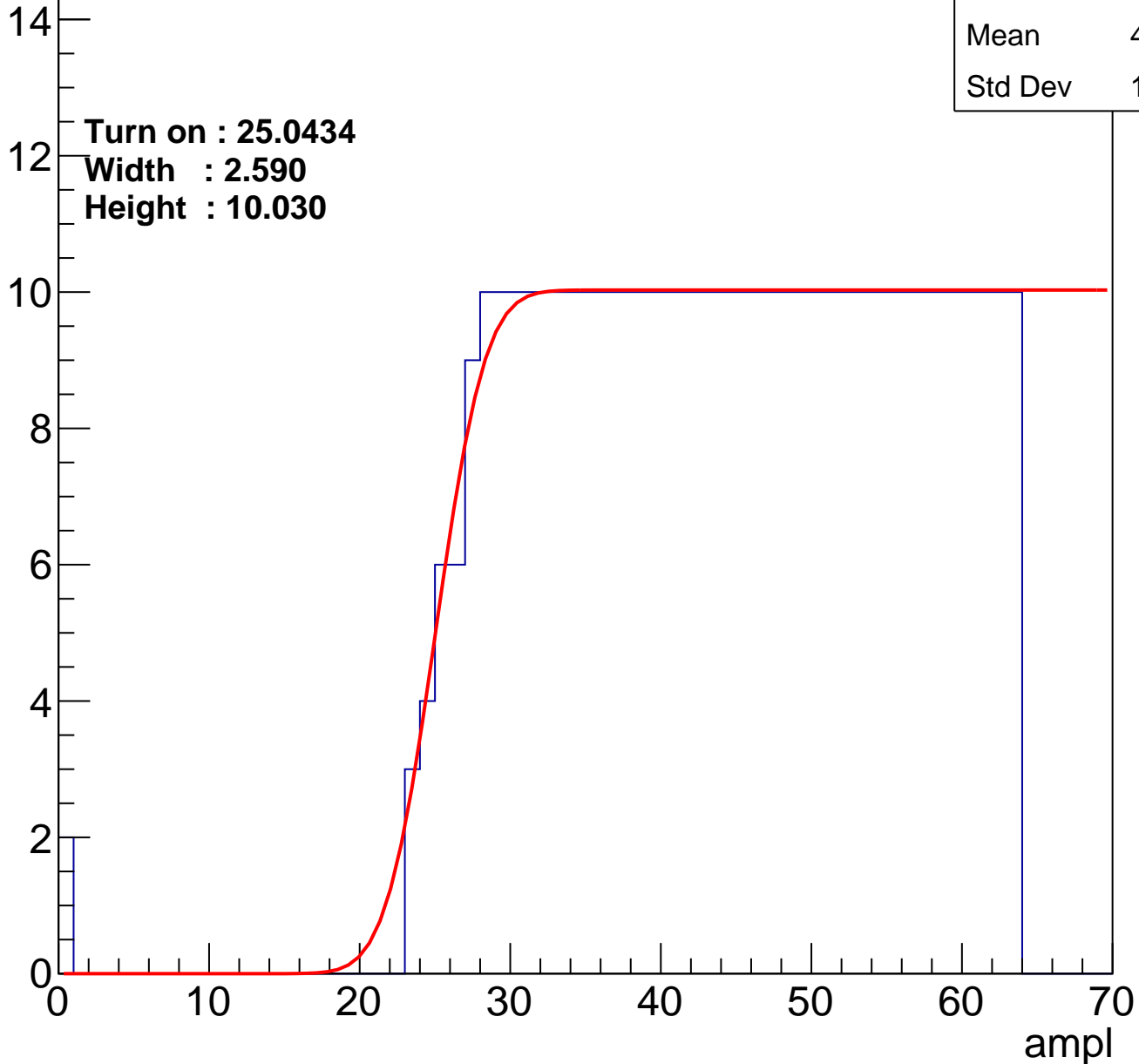
Entries	390
Mean	43.83
Std Dev	11.67

Turn on : 25.0434

Width : 2.590

Height : 10.030

Entry



B1L000S, U19-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.09
Std Dev	11.16

Turn on : 28.3699

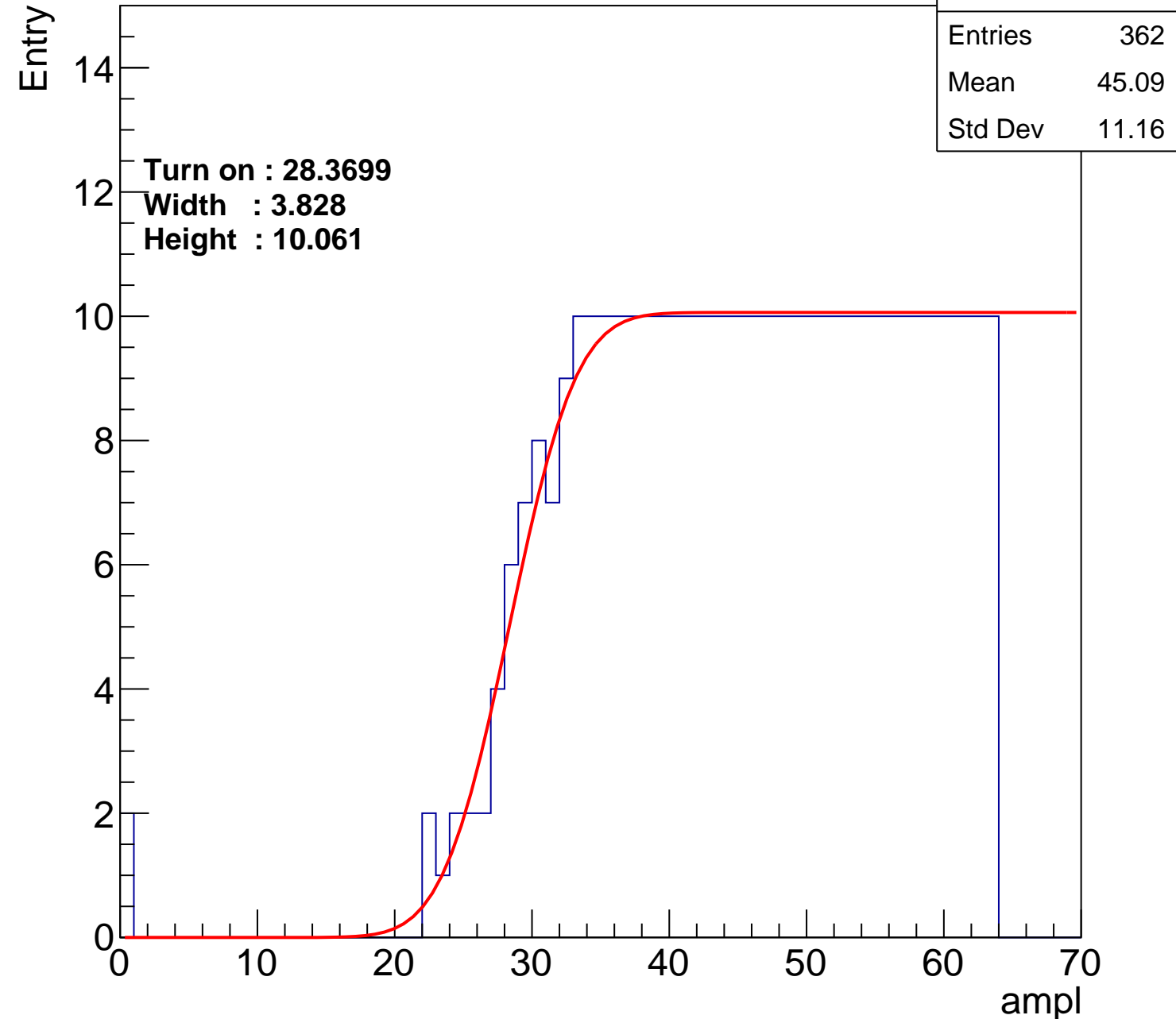
Width : 3.828

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.14
Std Dev	11.22

Turn on : 28.4063

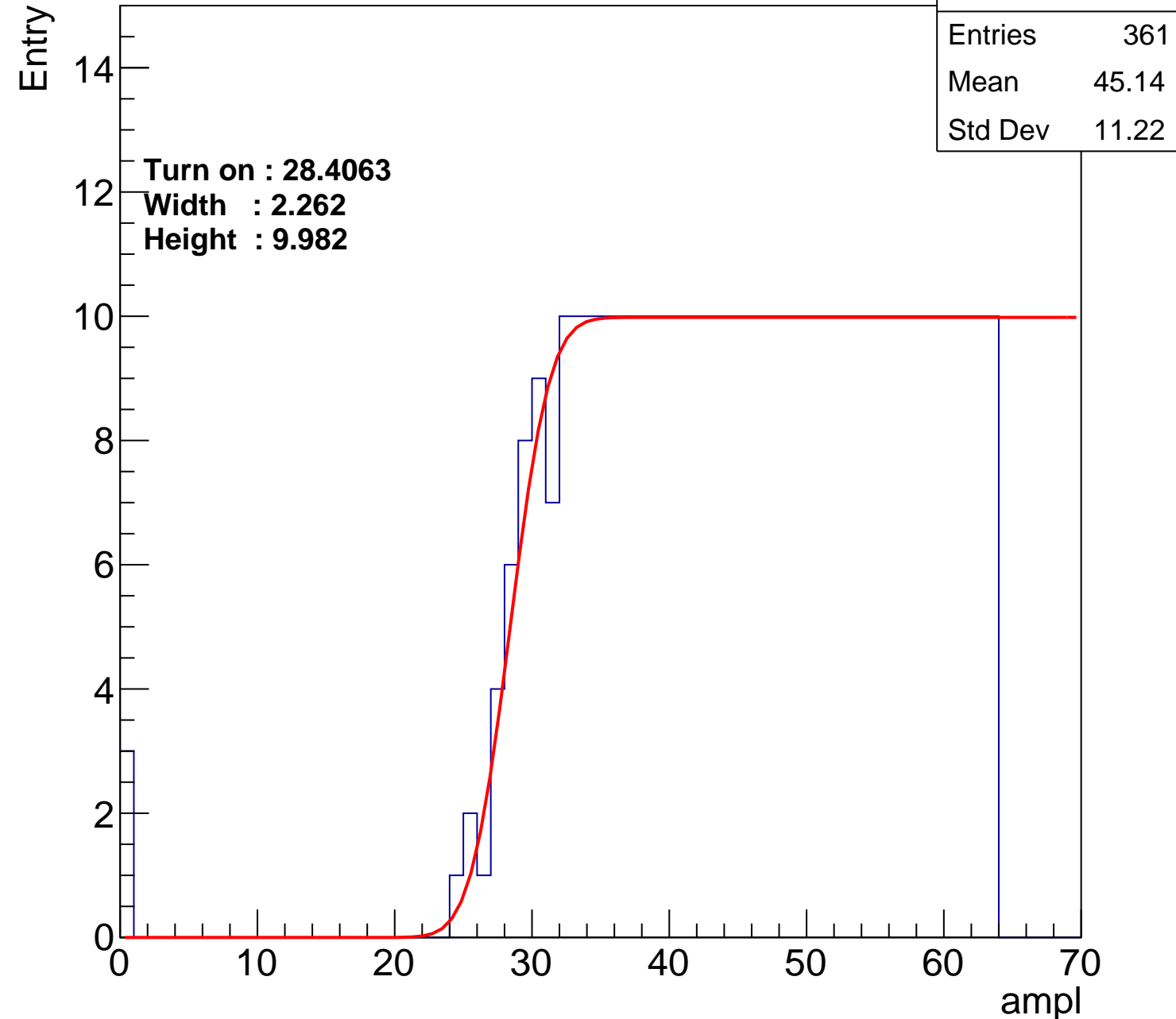
Width : 2.262

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch95

calib_packv5_042523_0143.root, FC#3, port B3

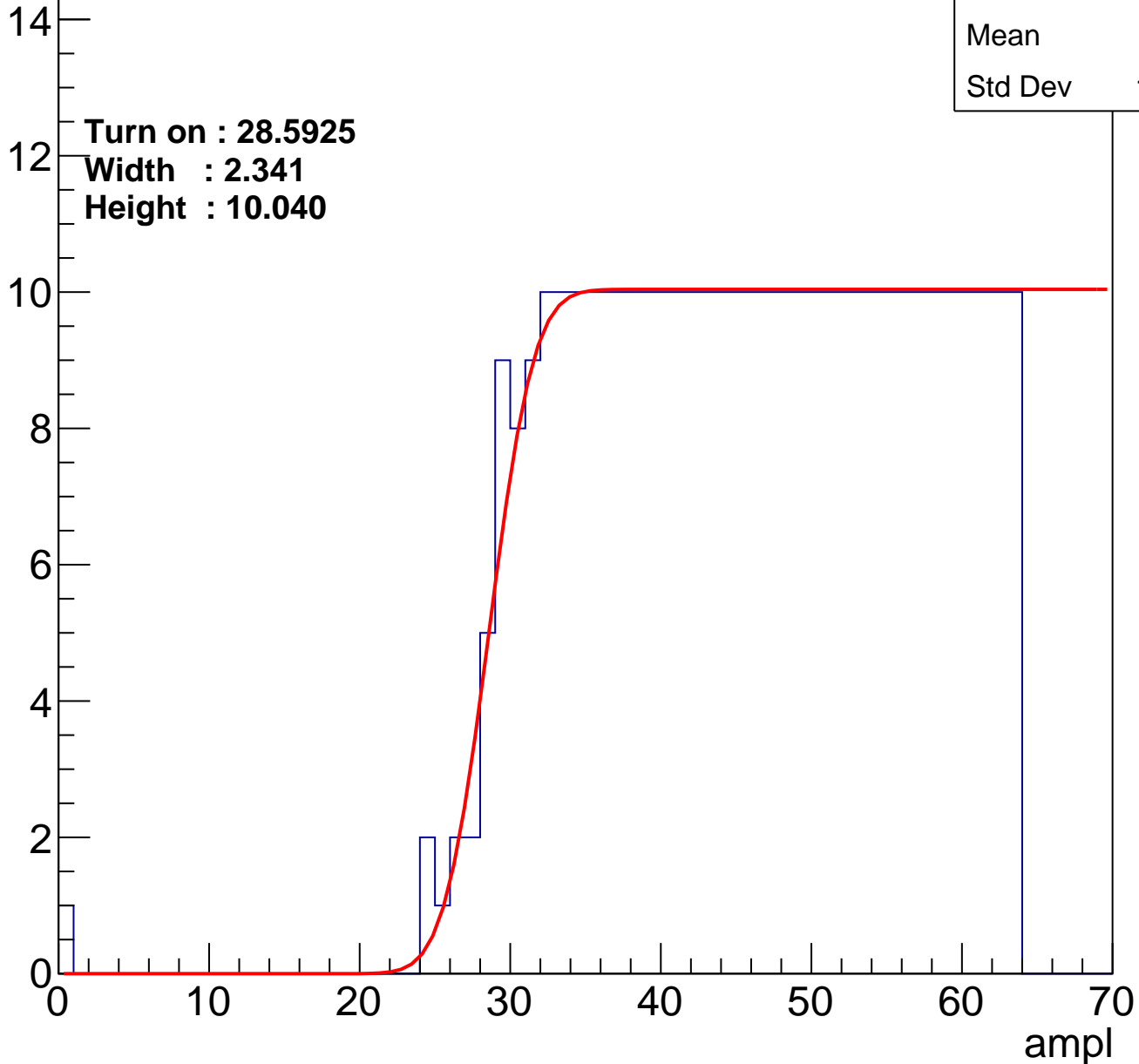
Entries	359
Mean	45.4
Std Dev	10.71

Turn on : 28.5925

Width : 2.341

Height : 10.040

Entry



B1L000S, U19-ch96

calib_packv5_042523_0143.root, FC#3, port B3

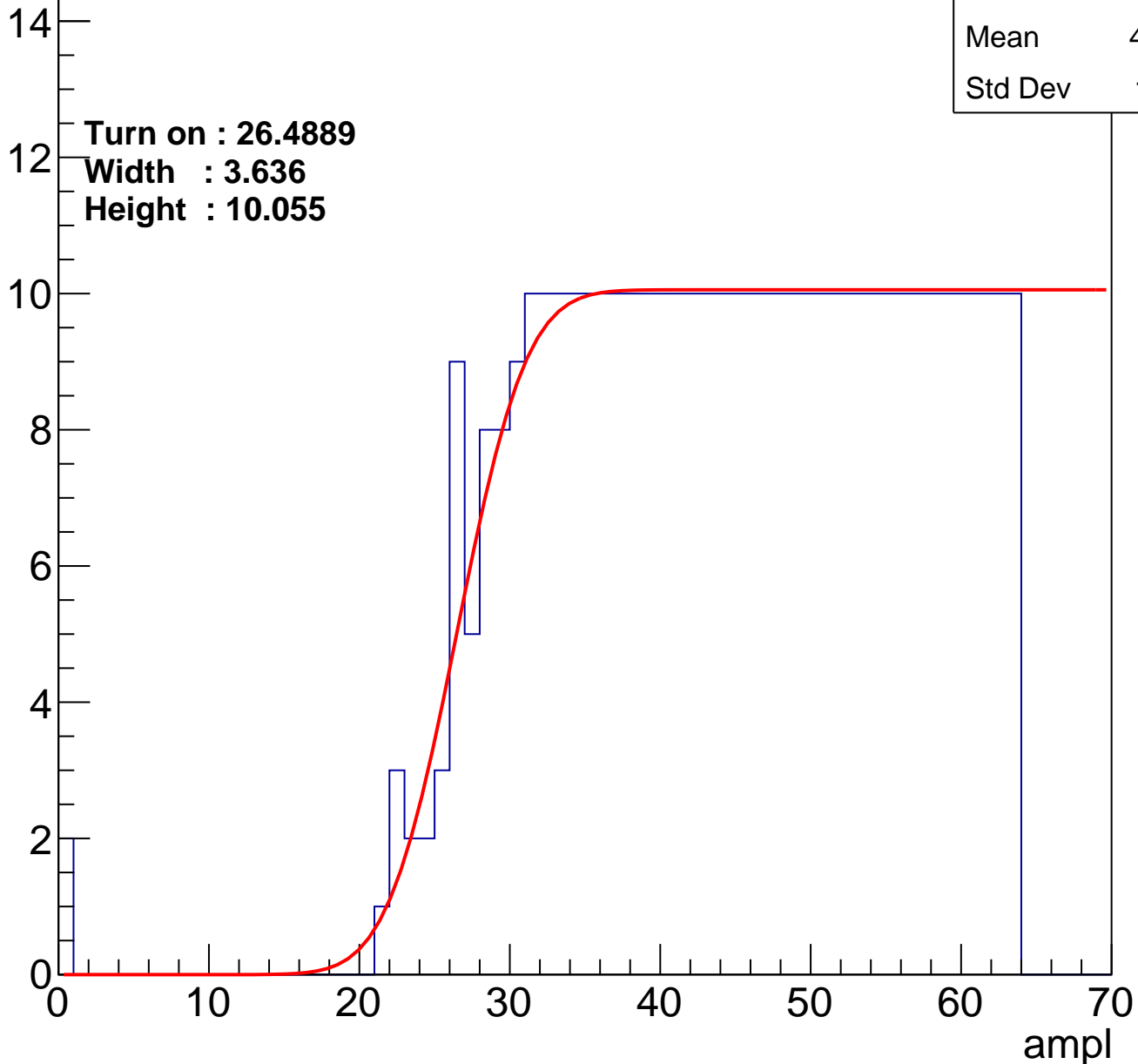
Entries	382
Mean	44.14
Std Dev	11.61

Turn on : 26.4889

Width : 3.636

Height : 10.055

Entry



B1L000S, U19-ch97

calib_packv5_042523_0143.root, FC#3, port B3

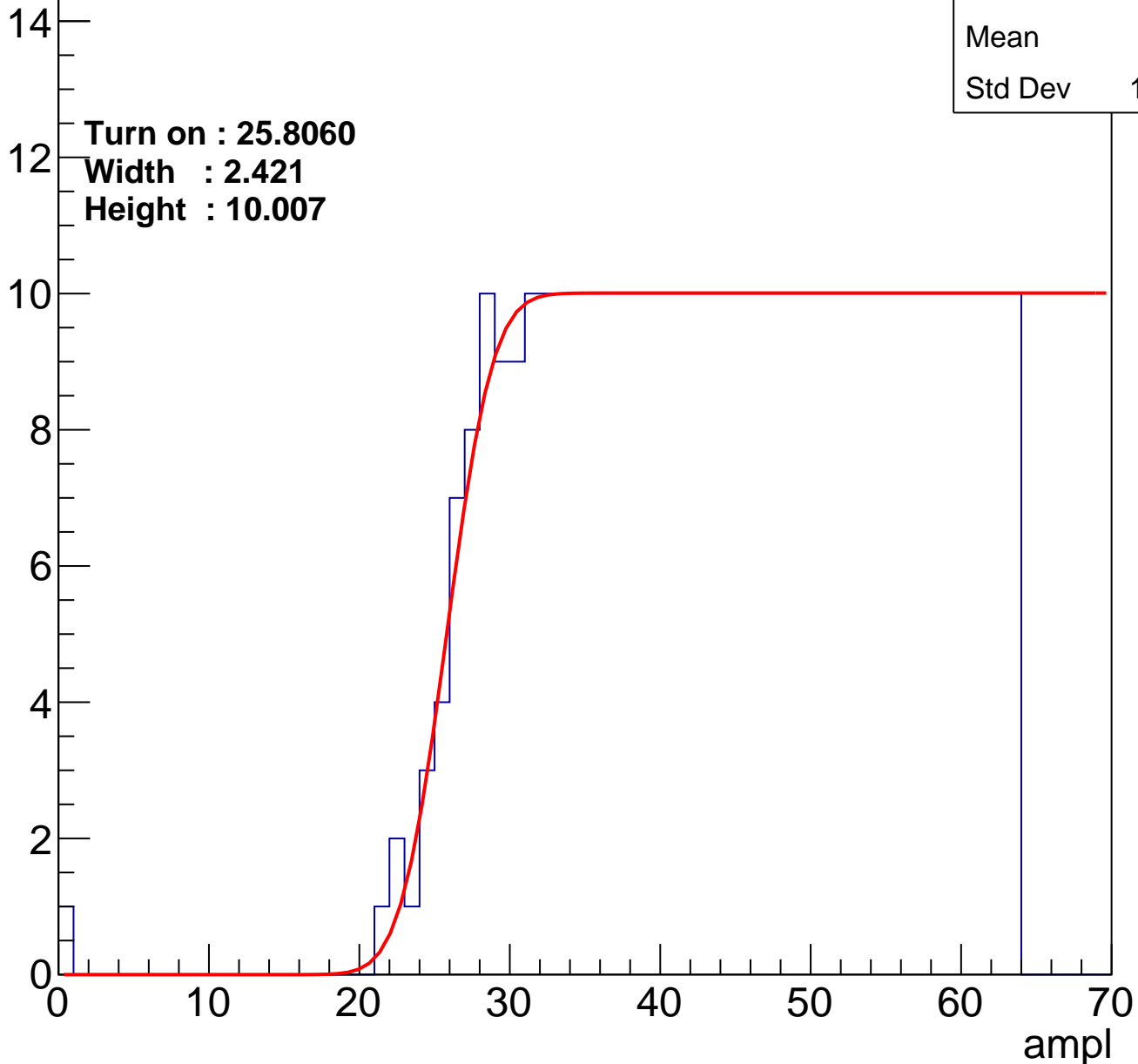
Entries	385
Mean	44.1
Std Dev	11.43

Turn on : 25.8060

Width : 2.421

Height : 10.007

Entry



B1L000S, U19-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 26.7857

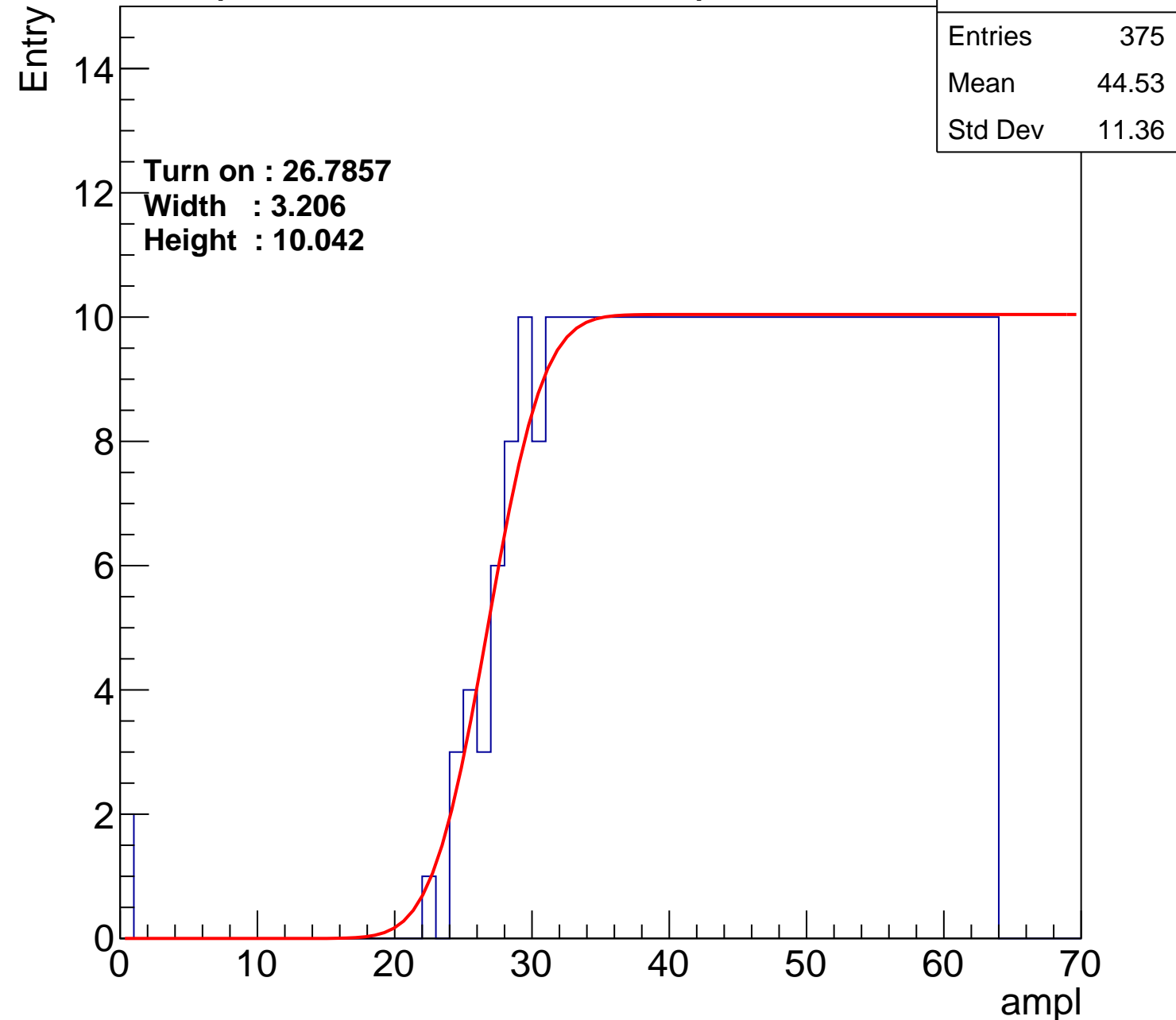
Width : 3.206

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch99

calib_packv5_042523_0143.root, FC#3, port B3

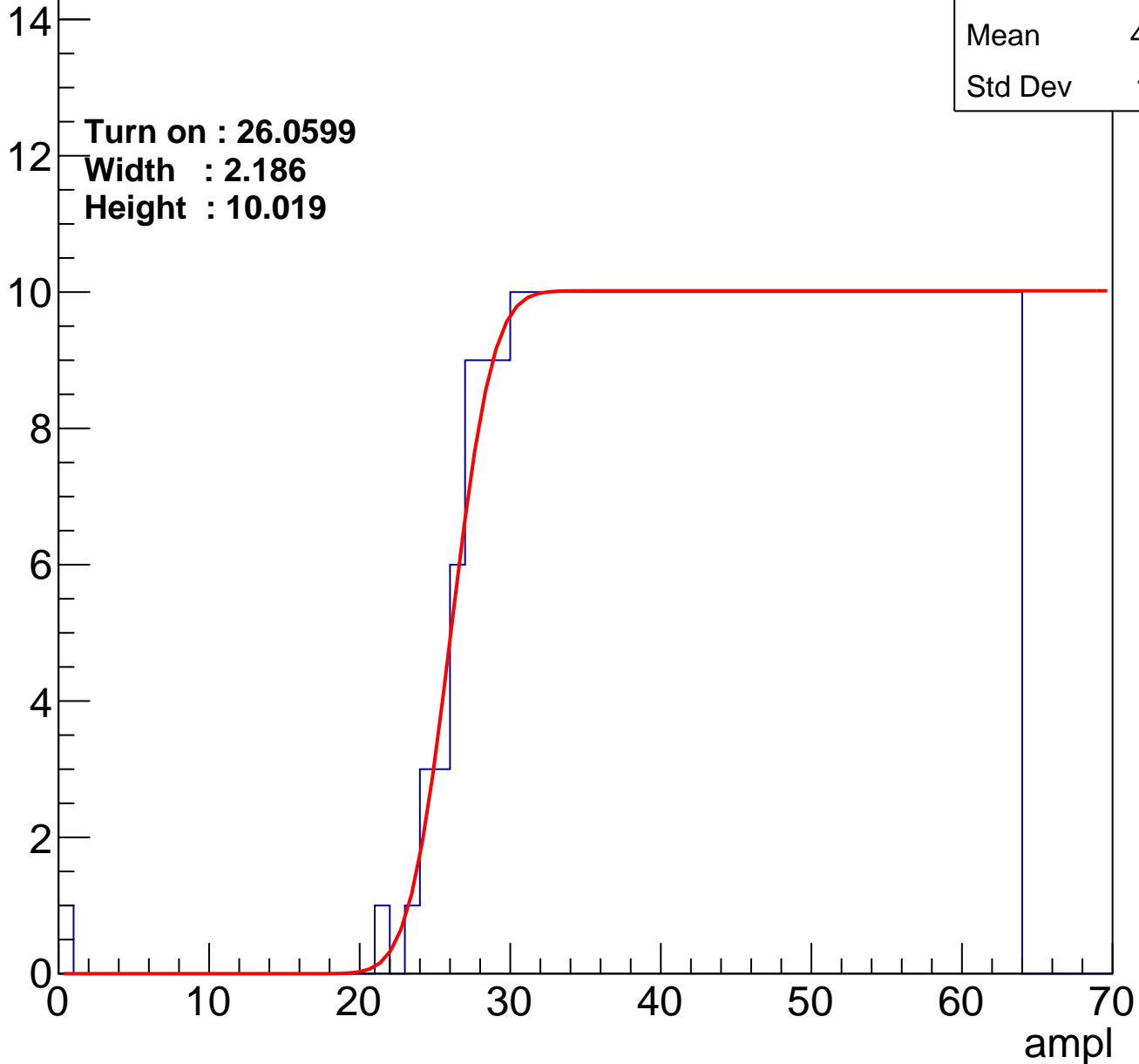
Entries	382
Mean	44.27
Std Dev	11.31

Turn on : 26.0599

Width : 2.186

Height : 10.019

Entry



B1L000S, U19-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
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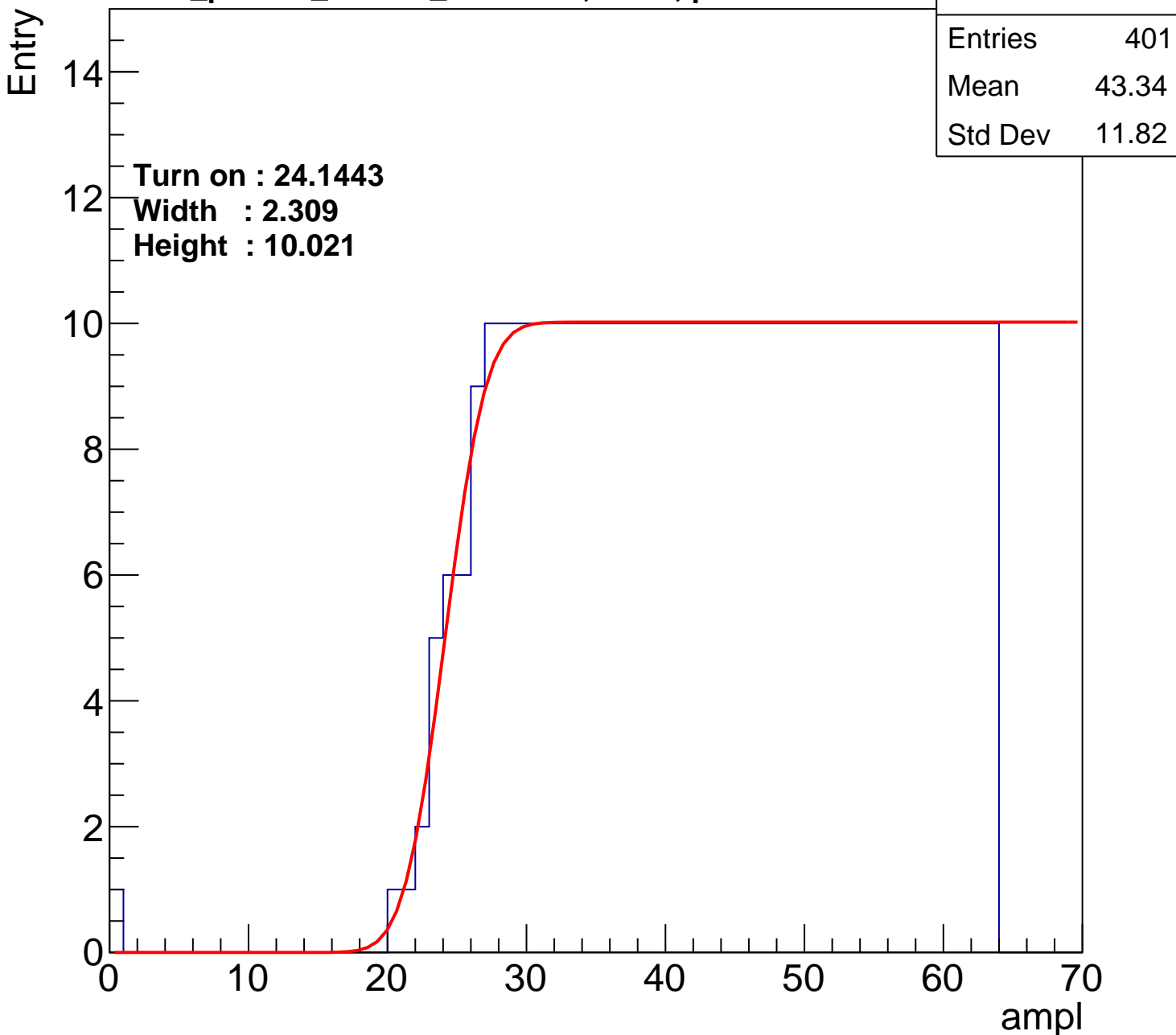
Mean	43.34
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Std Dev	11.82
---------	-------

Turn on : 24.1443

Width : 2.309

Height : 10.021



B1L000S, U19-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.94
Std Dev	11.03

Turn on : 27.8402

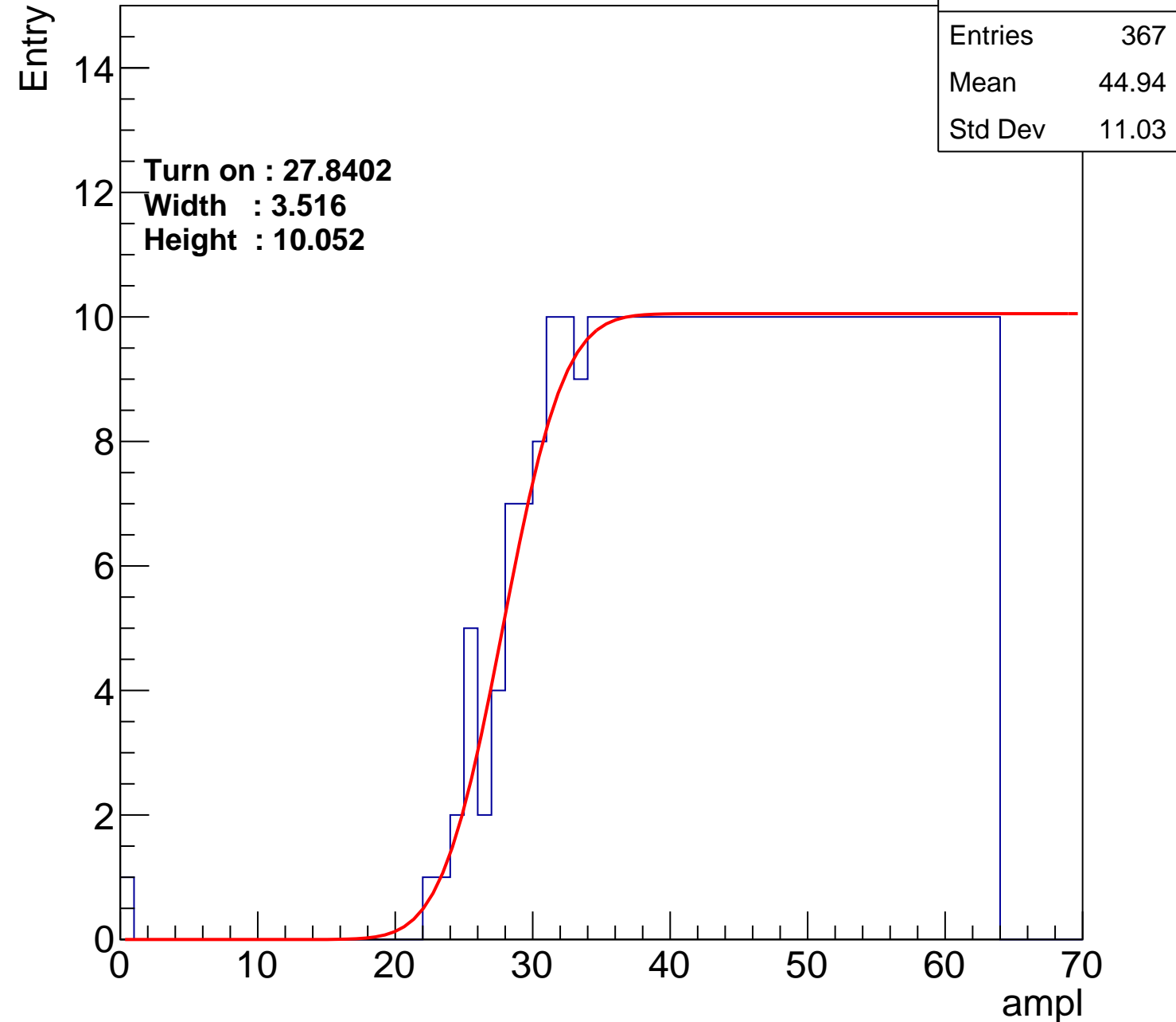
Width : 3.516

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.0956

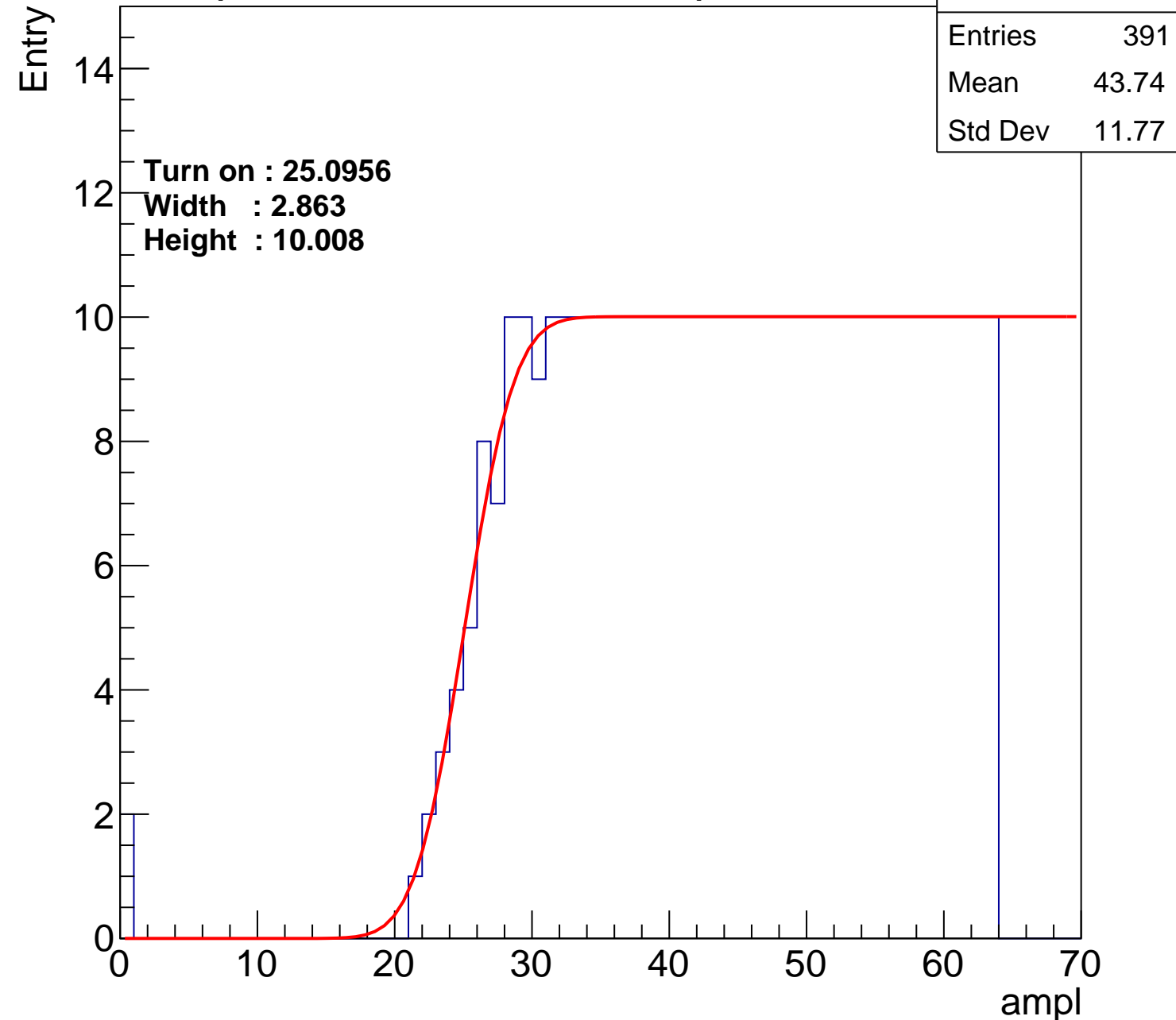
Width : 2.863

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.6225

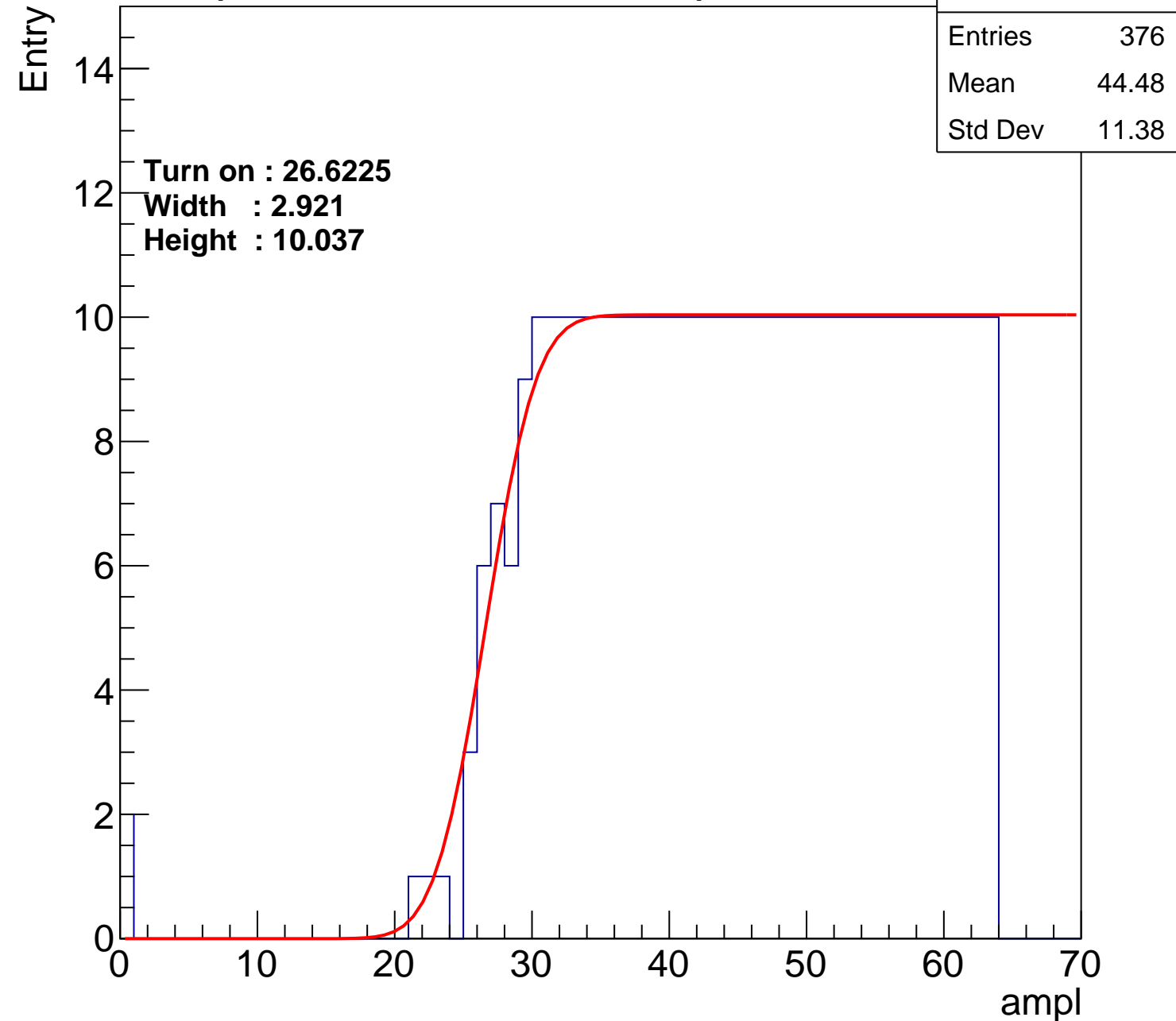
Width : 2.921

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.61
Std Dev	11.3

Turn on : 26.9094

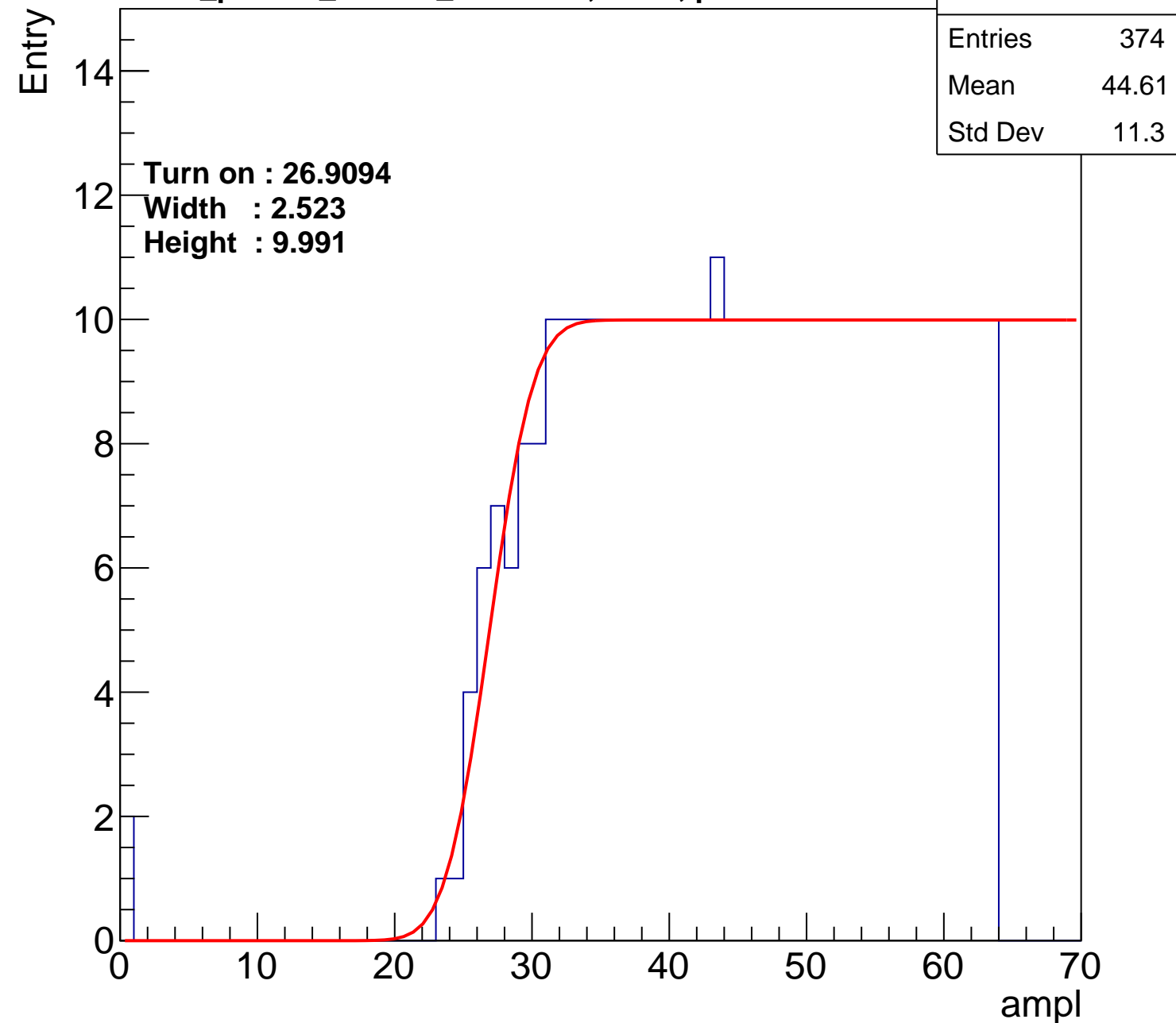
Width : 2.523

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch105

calib_packv5_042523_0143.root, FC#3, port B3

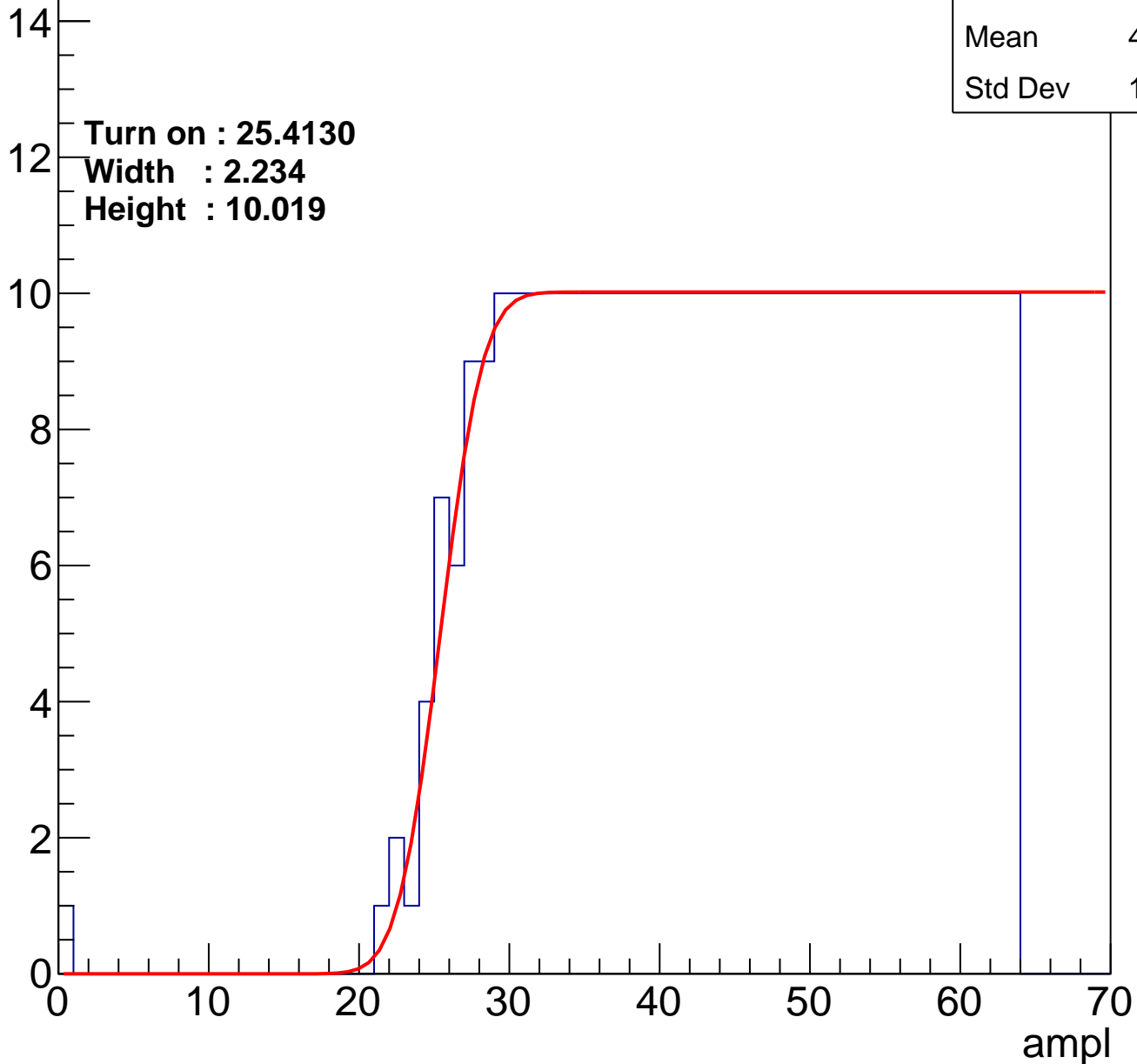
Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.4130

Width : 2.234

Height : 10.019

Entry



B1L000S, U19-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.0617

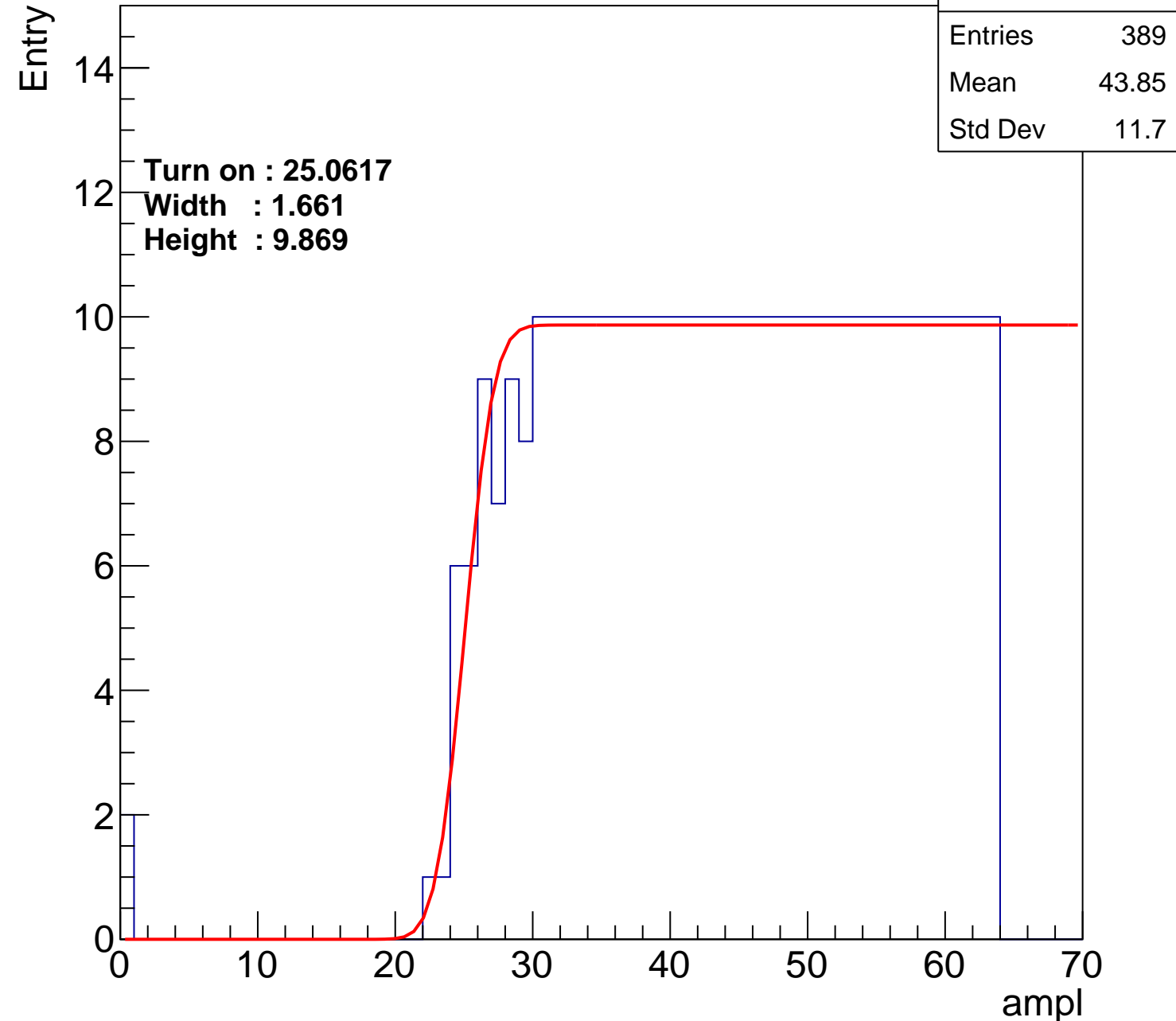
Width : 1.661

Height : 9.869

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.6082

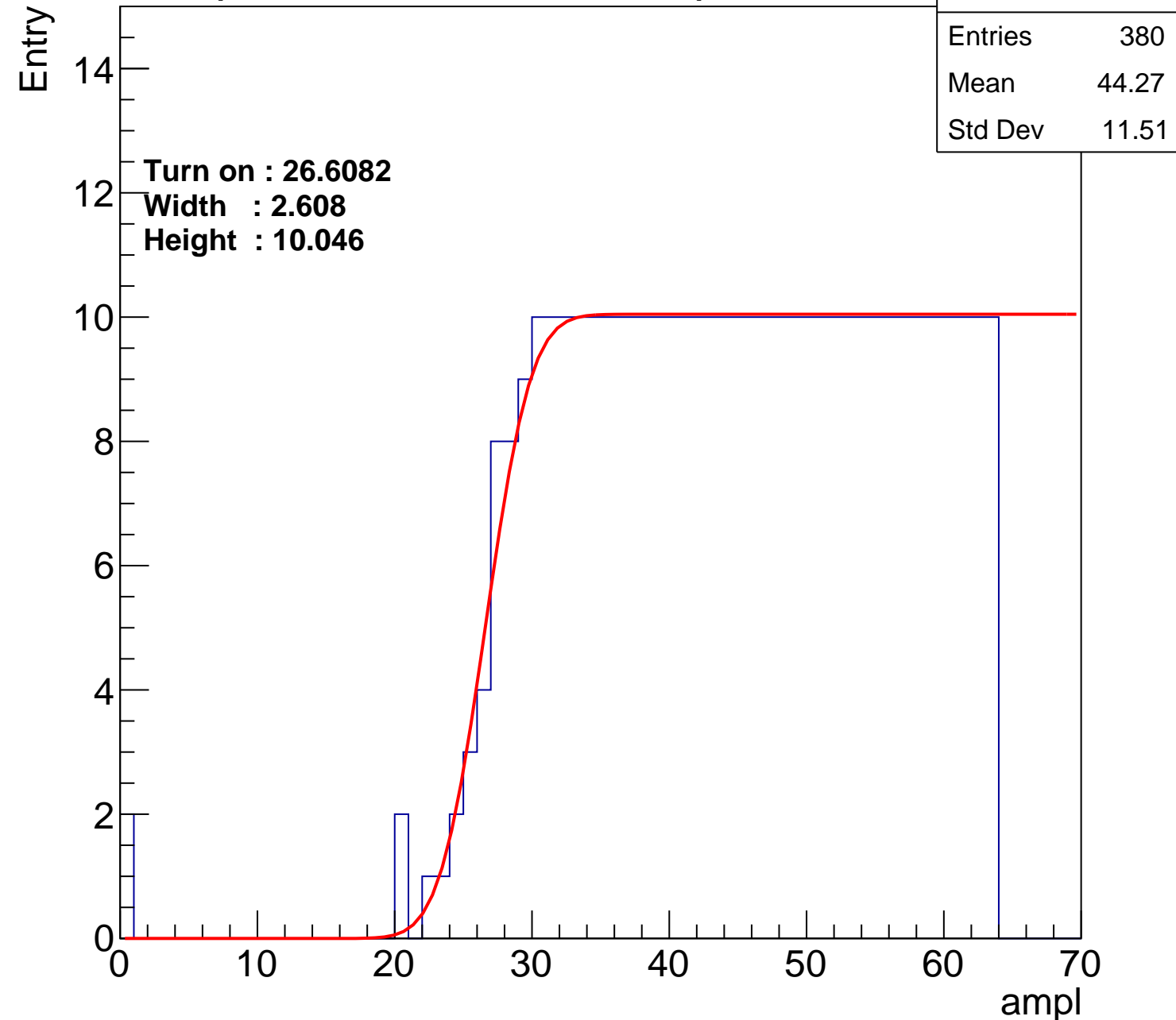
Width : 2.608

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.14
Std Dev	11.86

Turn on : 26.5144

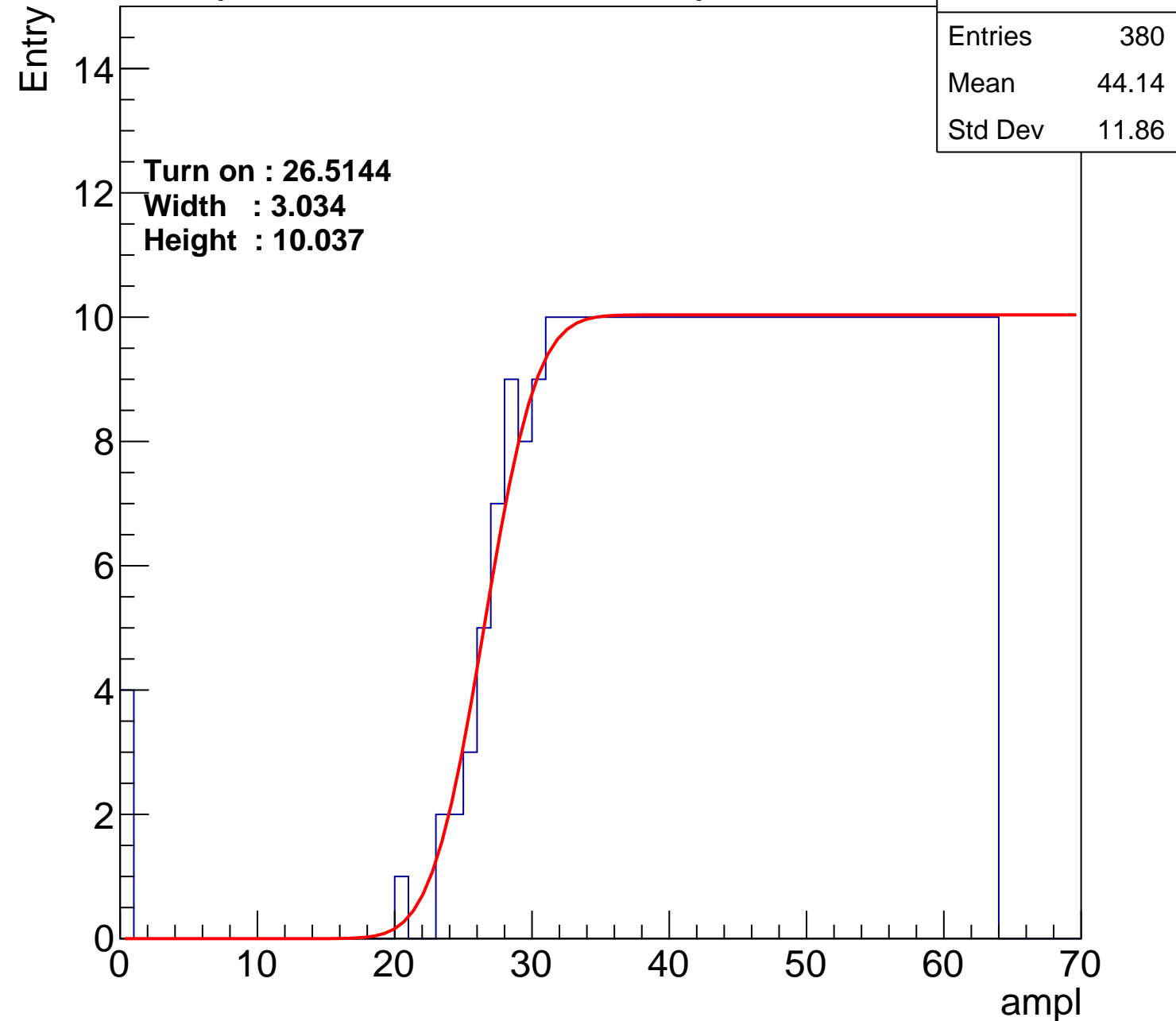
Width : 3.034

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.52
Std Dev	11.98

Turn on : 24.9165

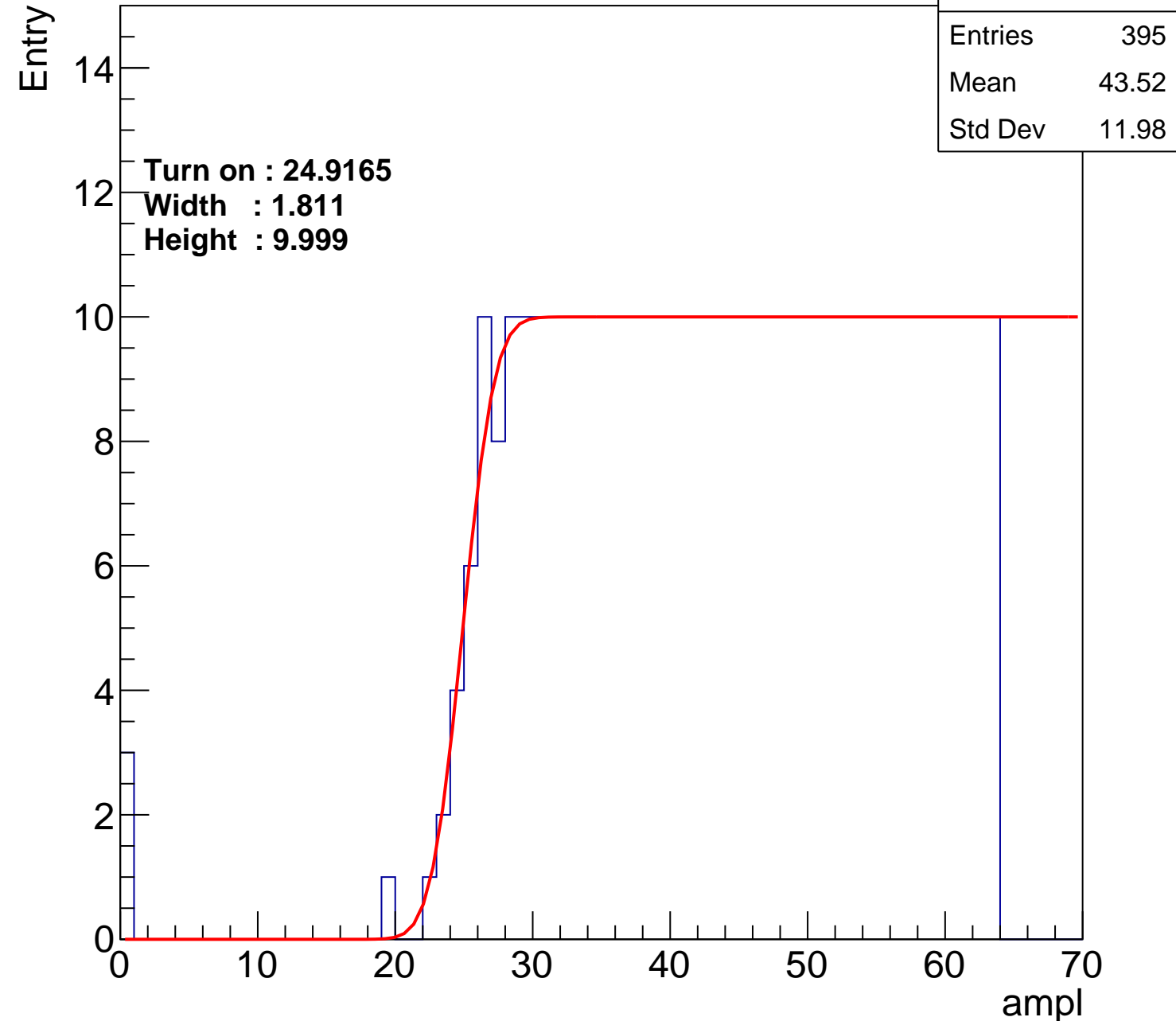
Width : 1.811

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.81
Std Dev	11.59

Turn on : 25.1030

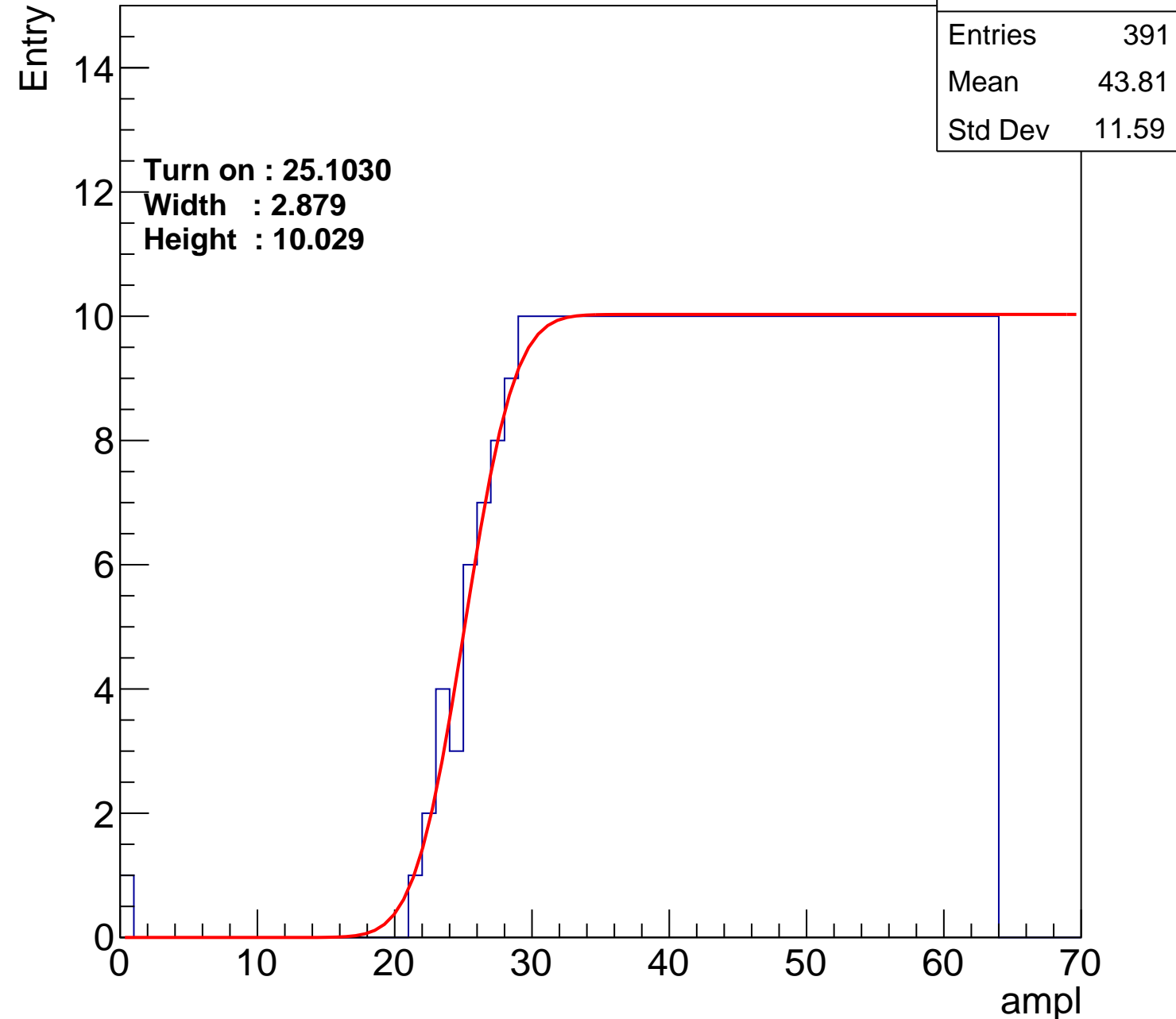
Width : 2.879

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.23
Std Dev	12.55

Turn on : 25.2424

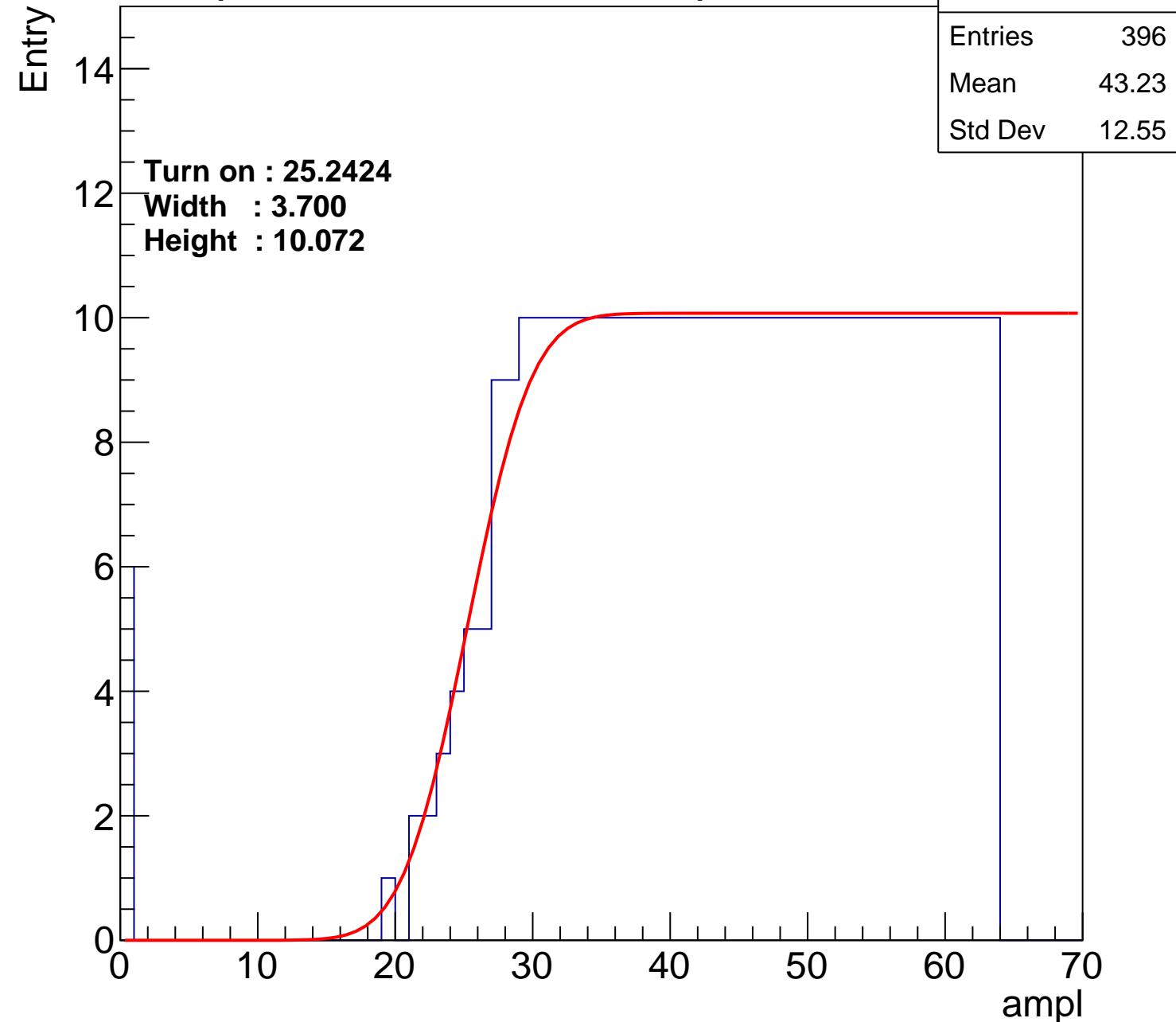
Width : 3.700

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.82
Std Dev	11.57

Turn on : 25.5136

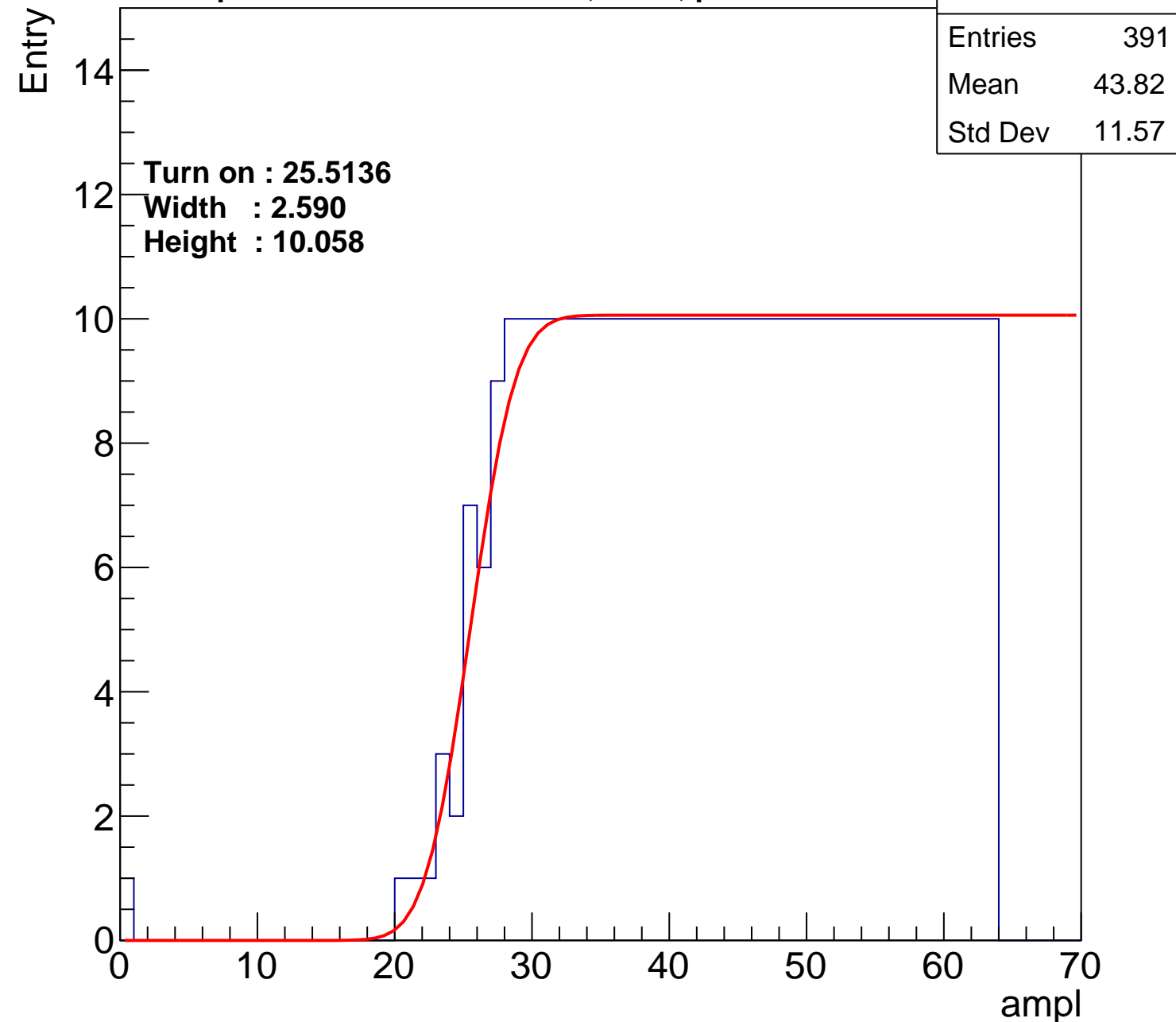
Width : 2.590

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.76
Std Dev	11.54

Turn on : 27.8018

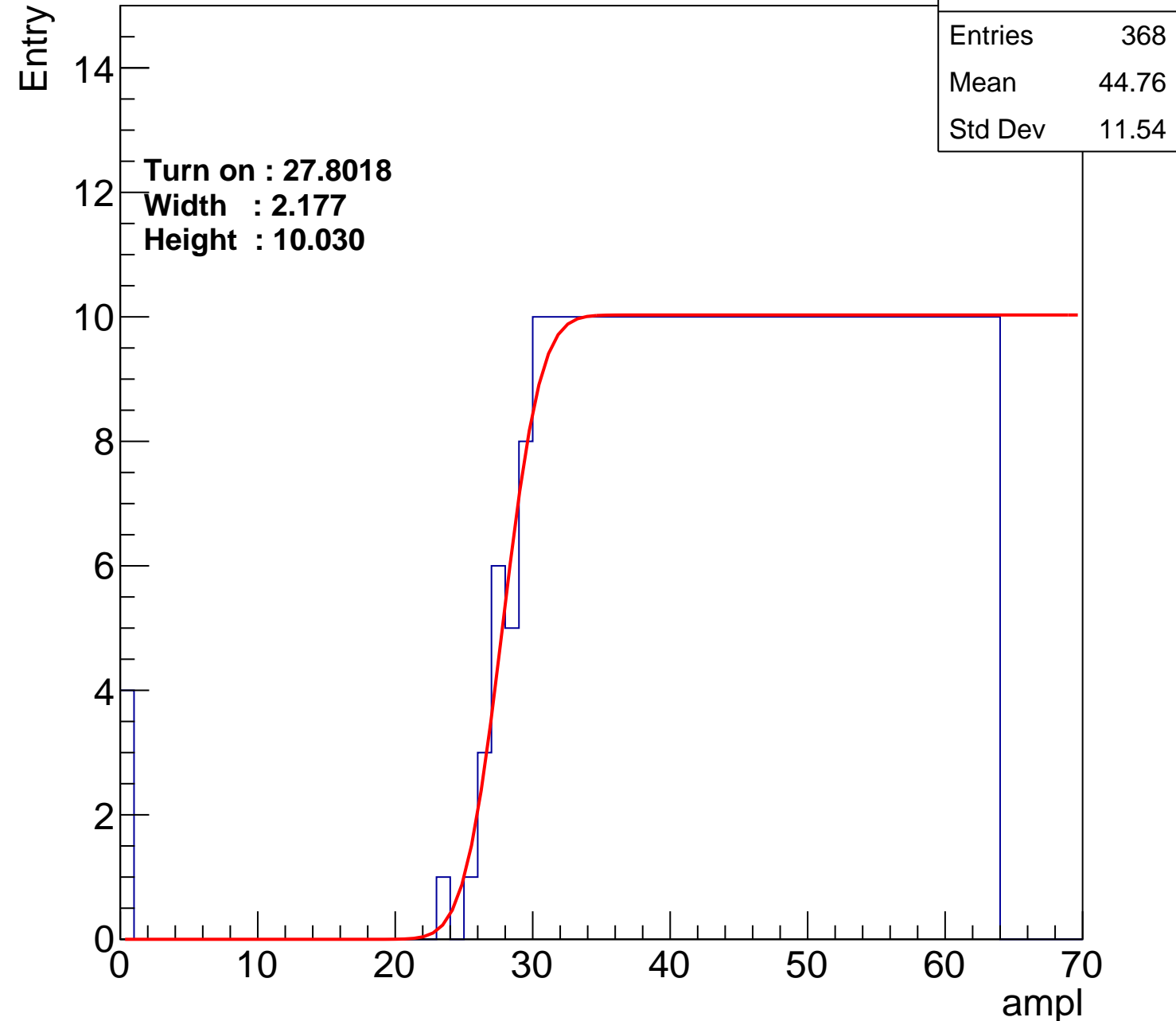
Width : 2.177

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.18
Std Dev	11.06

Turn on : 28.4603

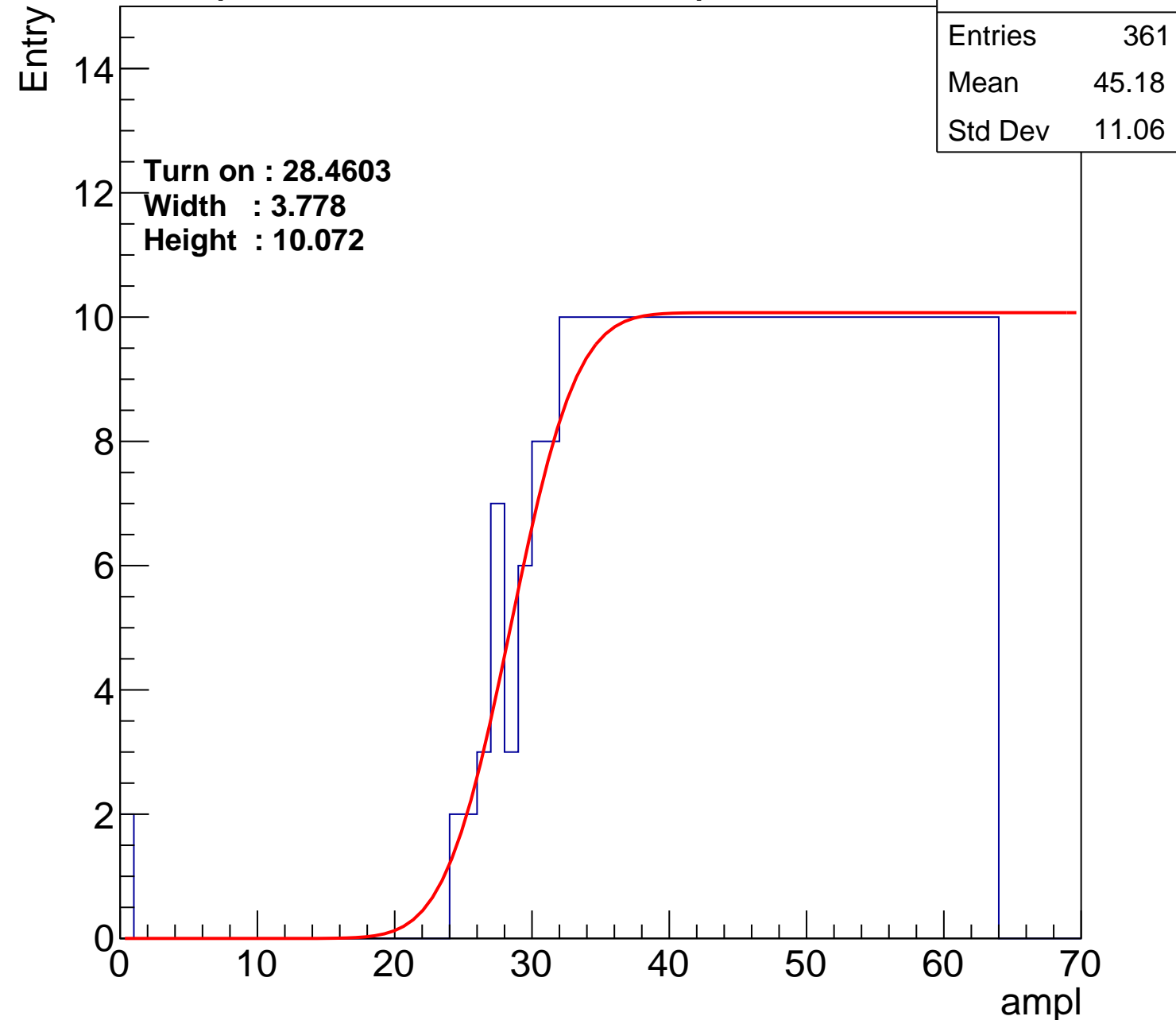
Width : 3.778

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch115

calib_packv5_042523_0143.root, FC#3, port B3

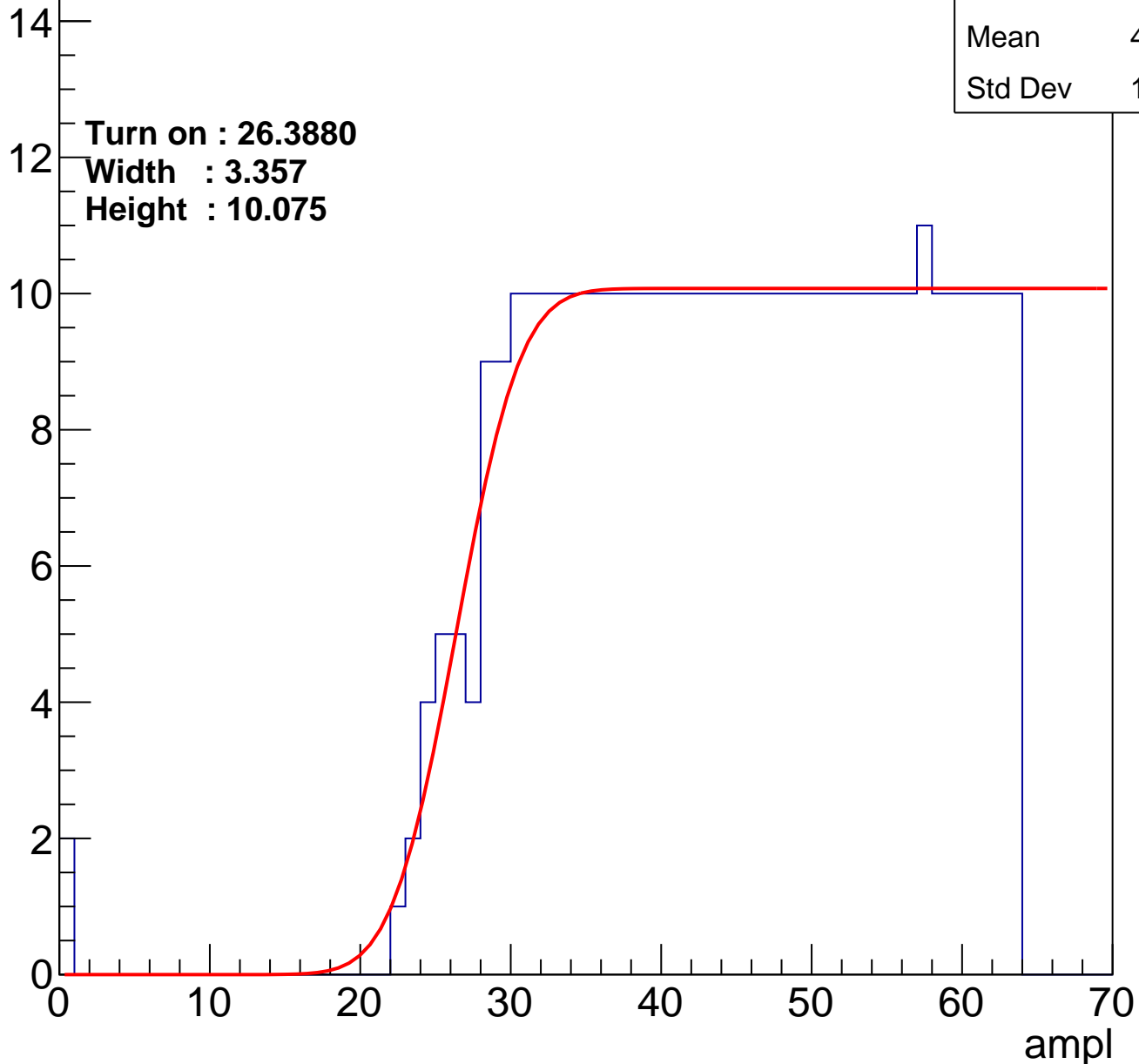
Entries	382
Mean	44.26
Std Dev	11.53

Turn on : 26.3880

Width : 3.357

Height : 10.075

Entry



B1L000S, U19-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 24.2447

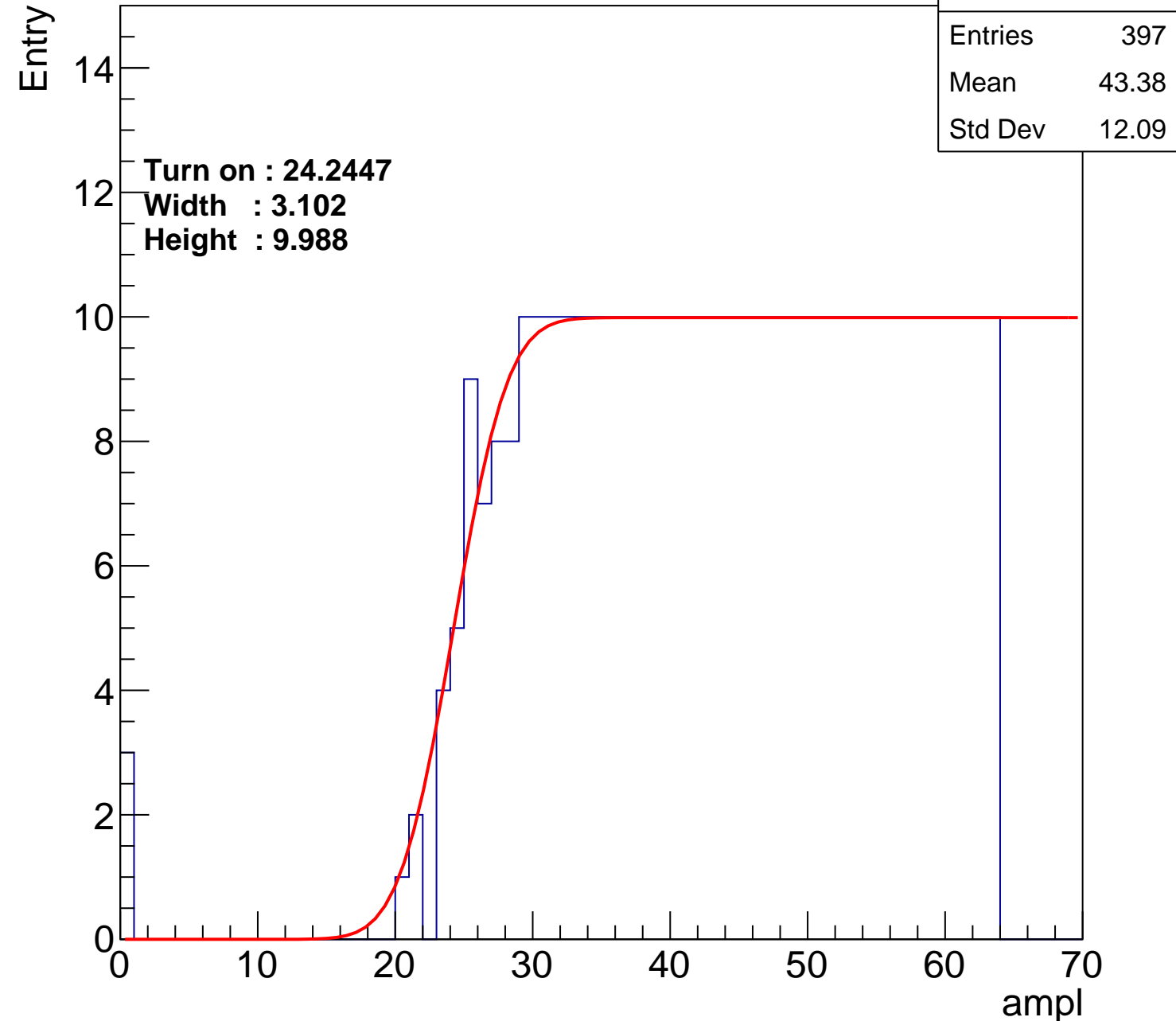
Width : 3.102

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 25.6396

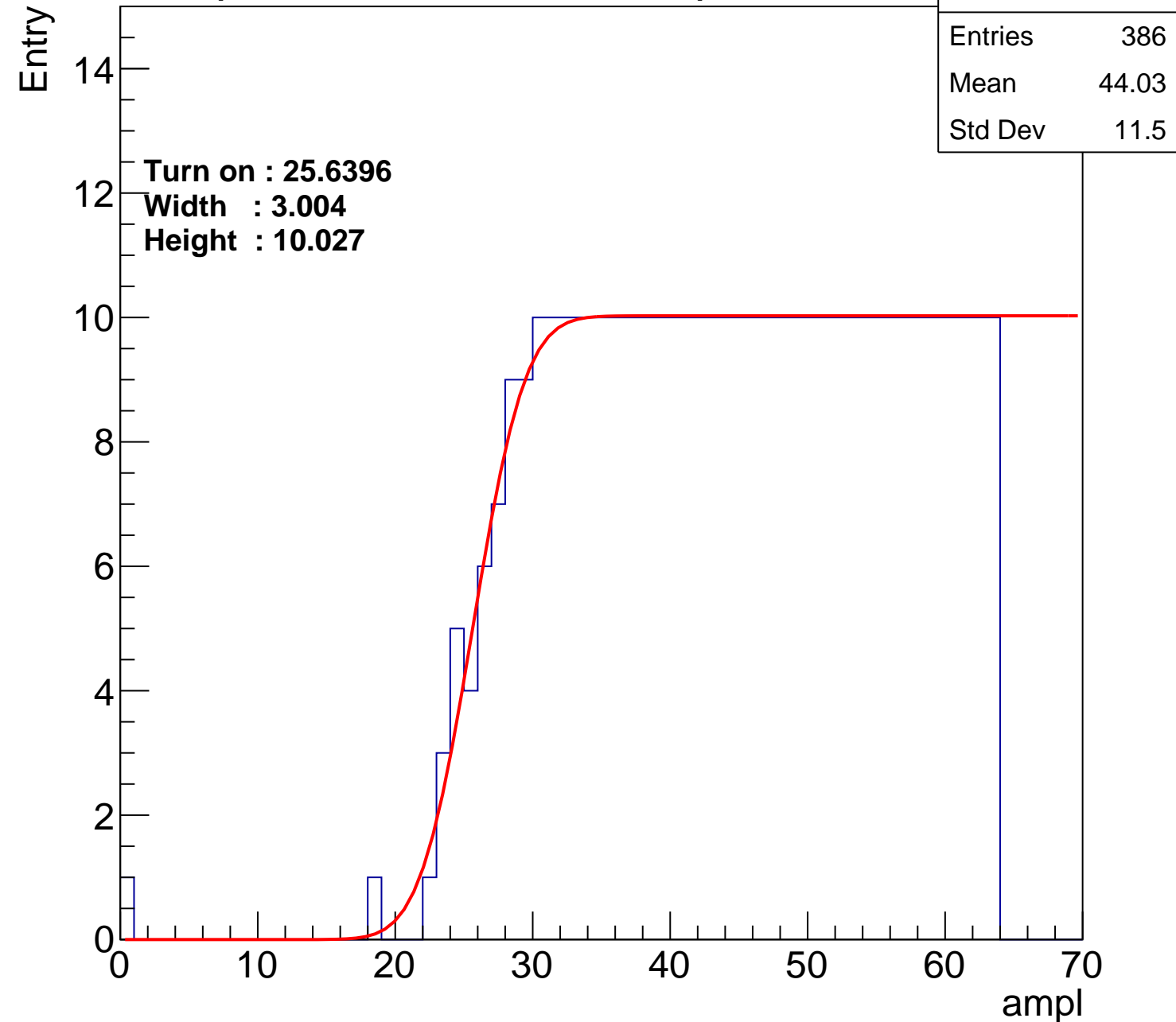
Width : 3.004

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch118

calib_packv5_042523_0143.root, FC#3, port B3

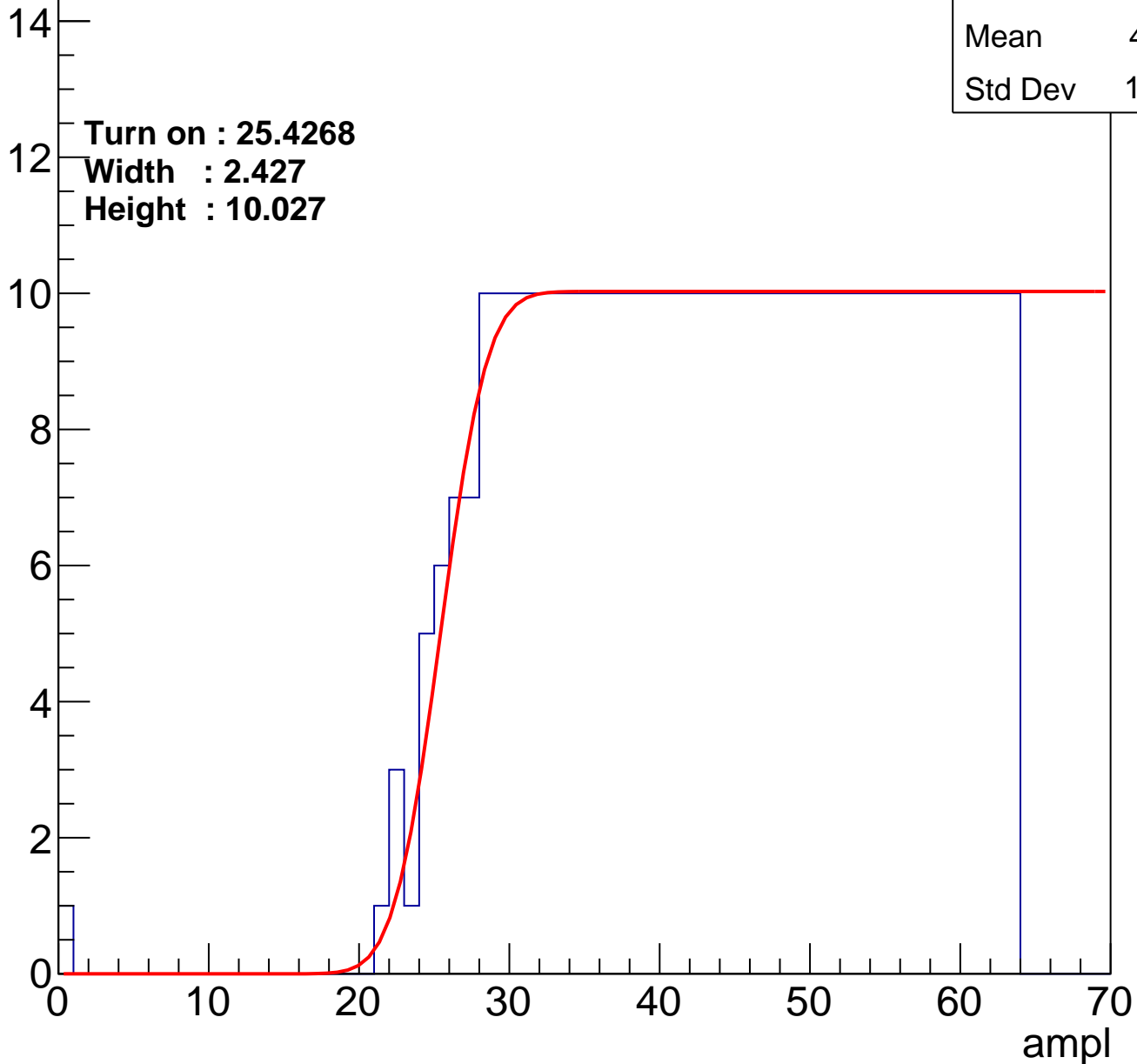
Entries	391
Mean	43.81
Std Dev	11.58

Turn on : 25.4268

Width : 2.427

Height : 10.027

Entry



B1L000S, U19-ch119

calib_packv5_042523_0143.root, FC#3, port B3

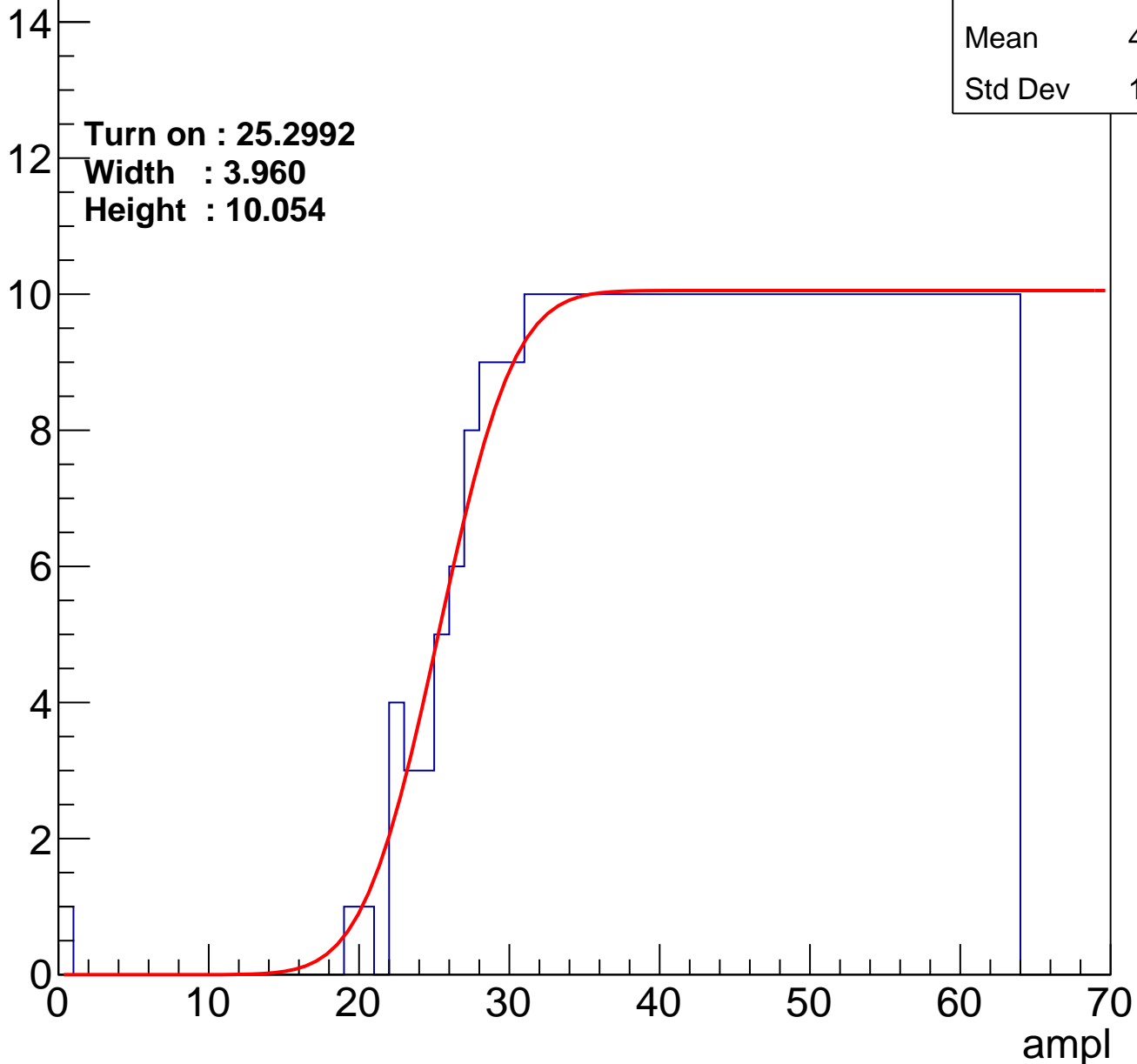
Entries	389
Mean	43.85
Std Dev	11.63

Turn on : 25.2992

Width : 3.960

Height : 10.054

Entry



B1L000S, U19-ch120

calib_packv5_042523_0143.root, FC#3, port B3

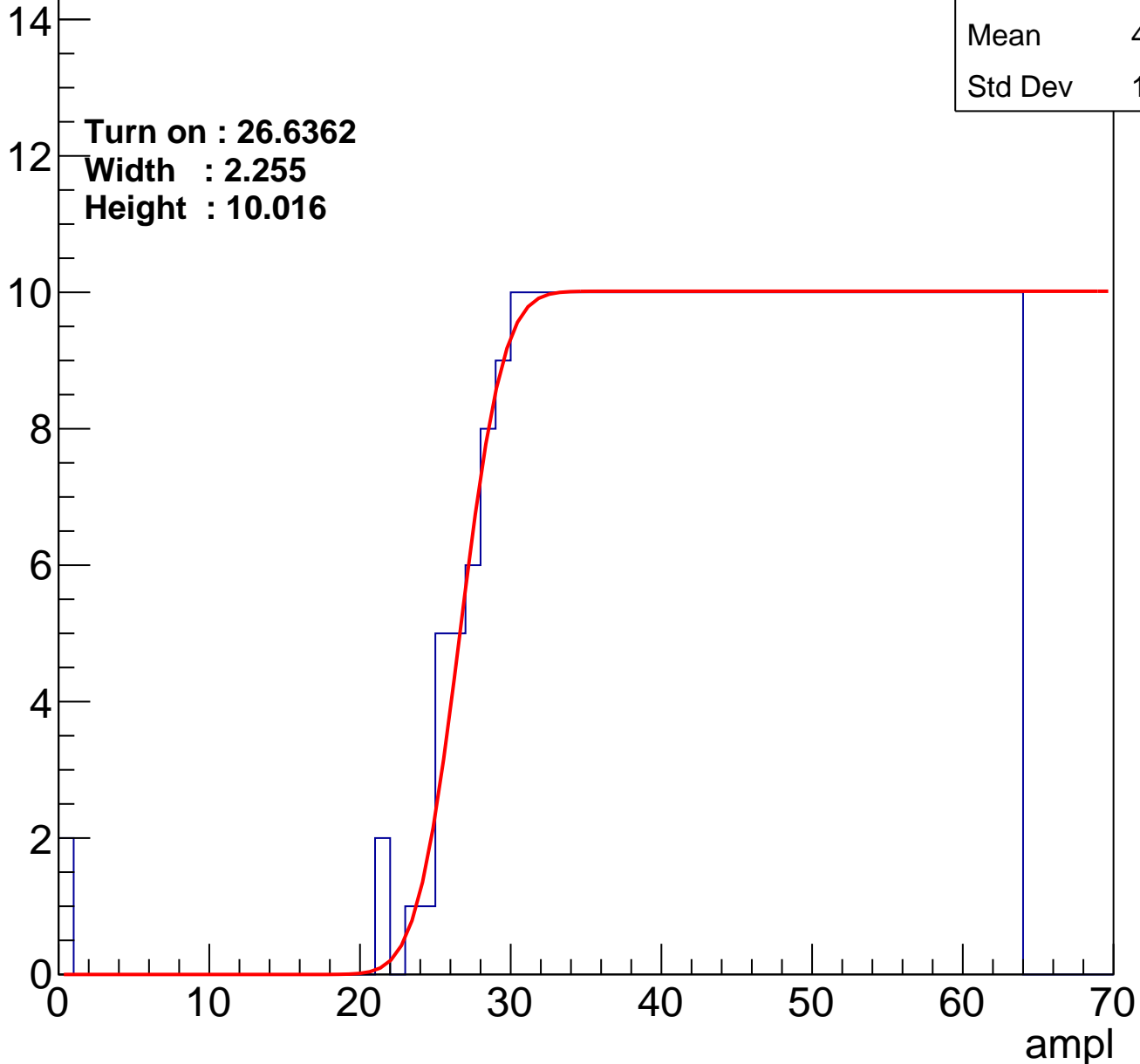
Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.6362

Width : 2.255

Height : 10.016

Entry



B1L000S, U19-ch121

calib_packv5_042523_0143.root, FC#3, port B3

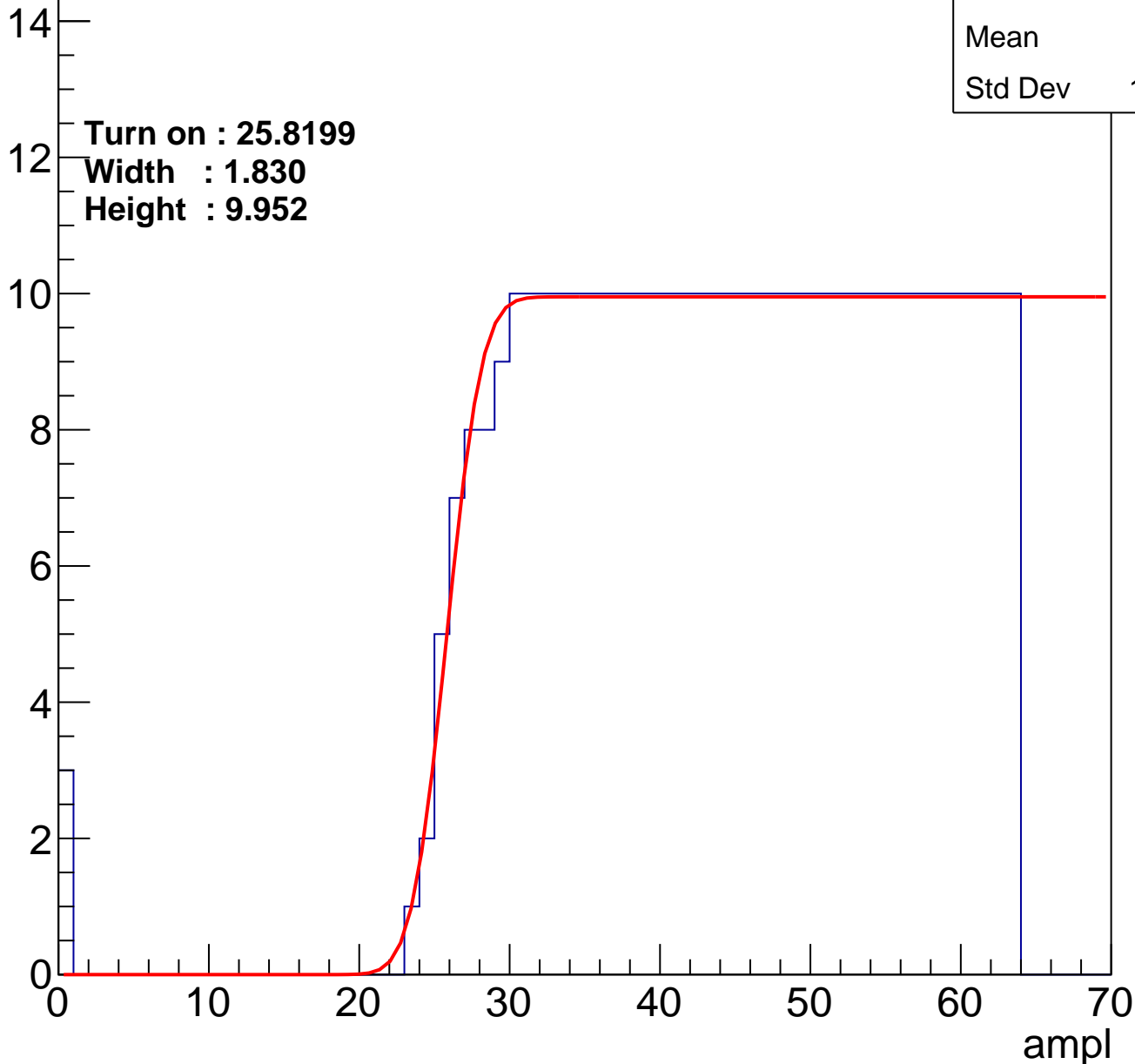
Entries	383
Mean	44.1
Std Dev	11.69

Turn on : 25.8199

Width : 1.830

Height : 9.952

Entry



B1L000S, U19-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.87
Std Dev	11.67

Turn on : 25.3997

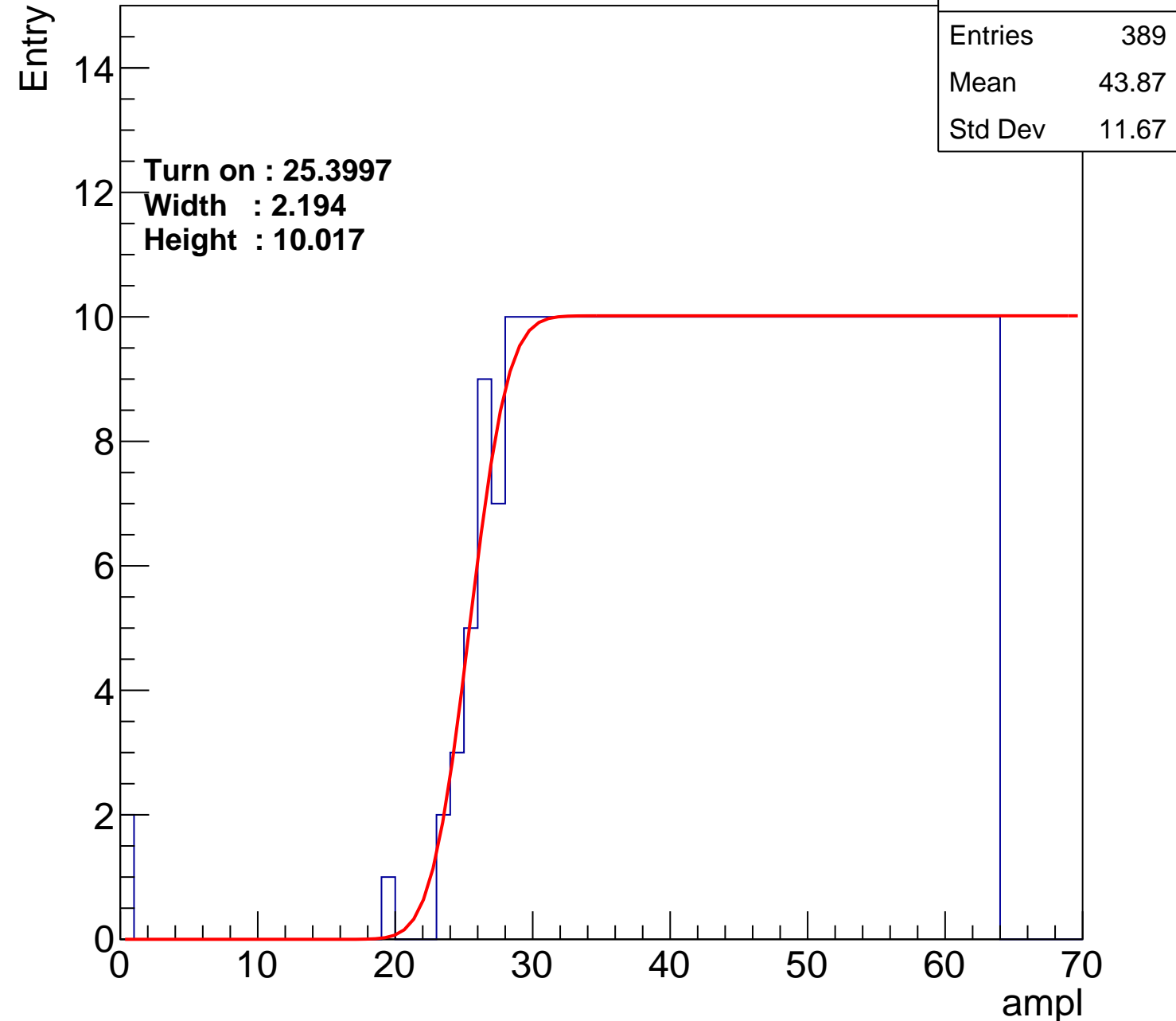
Width : 2.194

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.7470

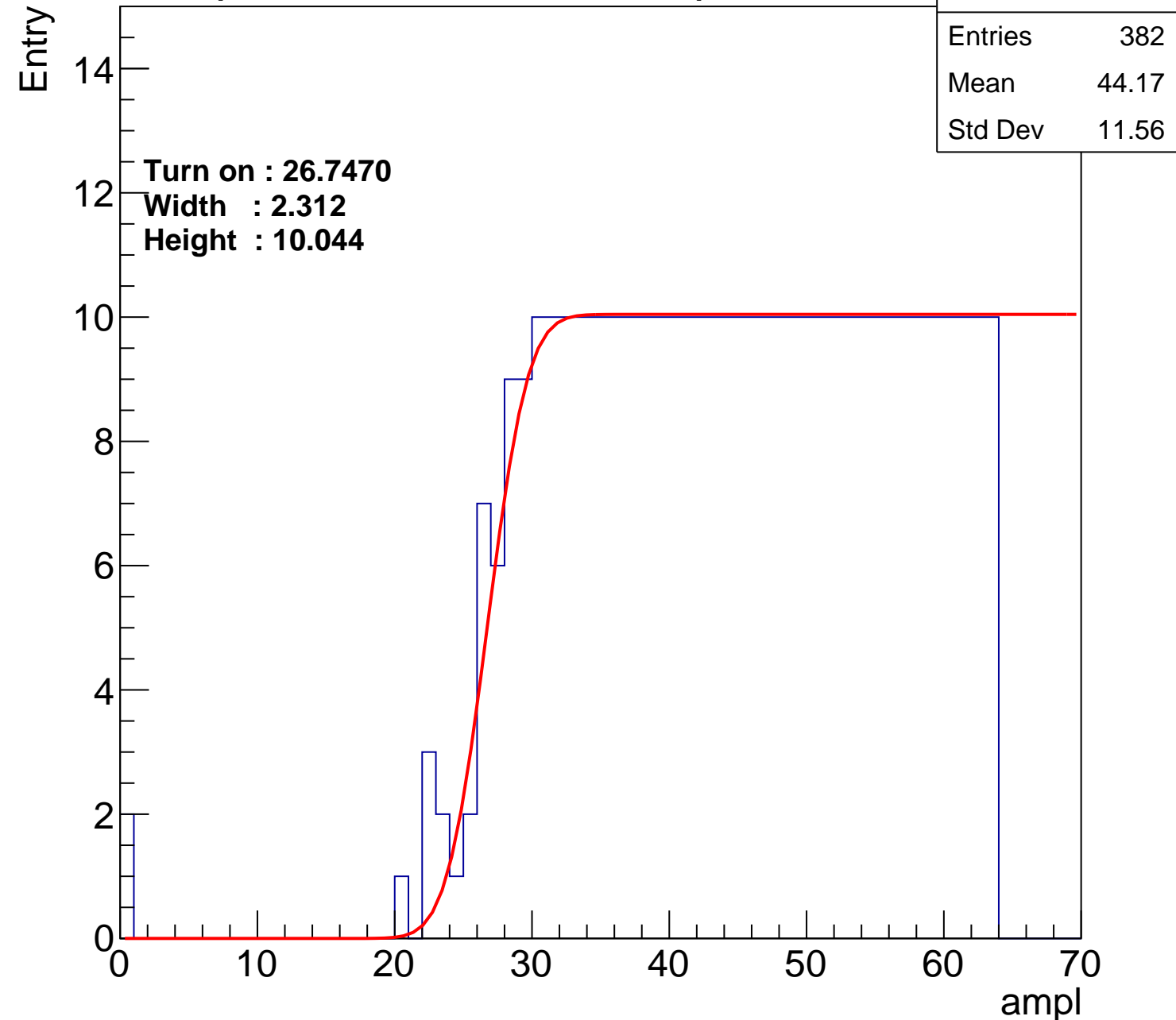
Width : 2.312

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch124

calib_packv5_042523_0143.root, FC#3, port B3

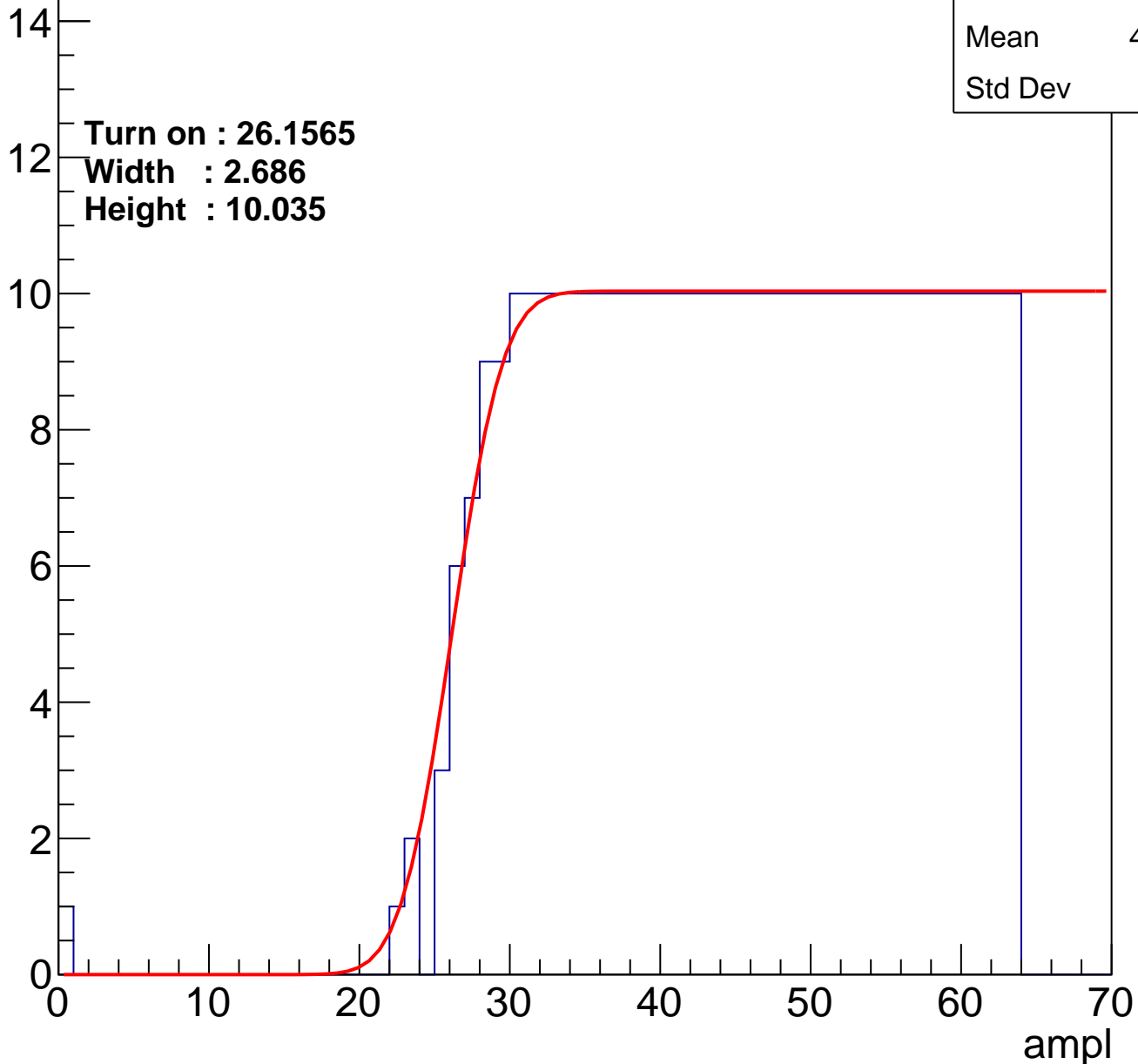
Entries	378
Mean	44.47
Std Dev	11.2

Turn on : 26.1565

Width : 2.686

Height : 10.035

Entry



B1L000S, U19-ch125

calib_packv5_042523_0143.root, FC#3, port B3

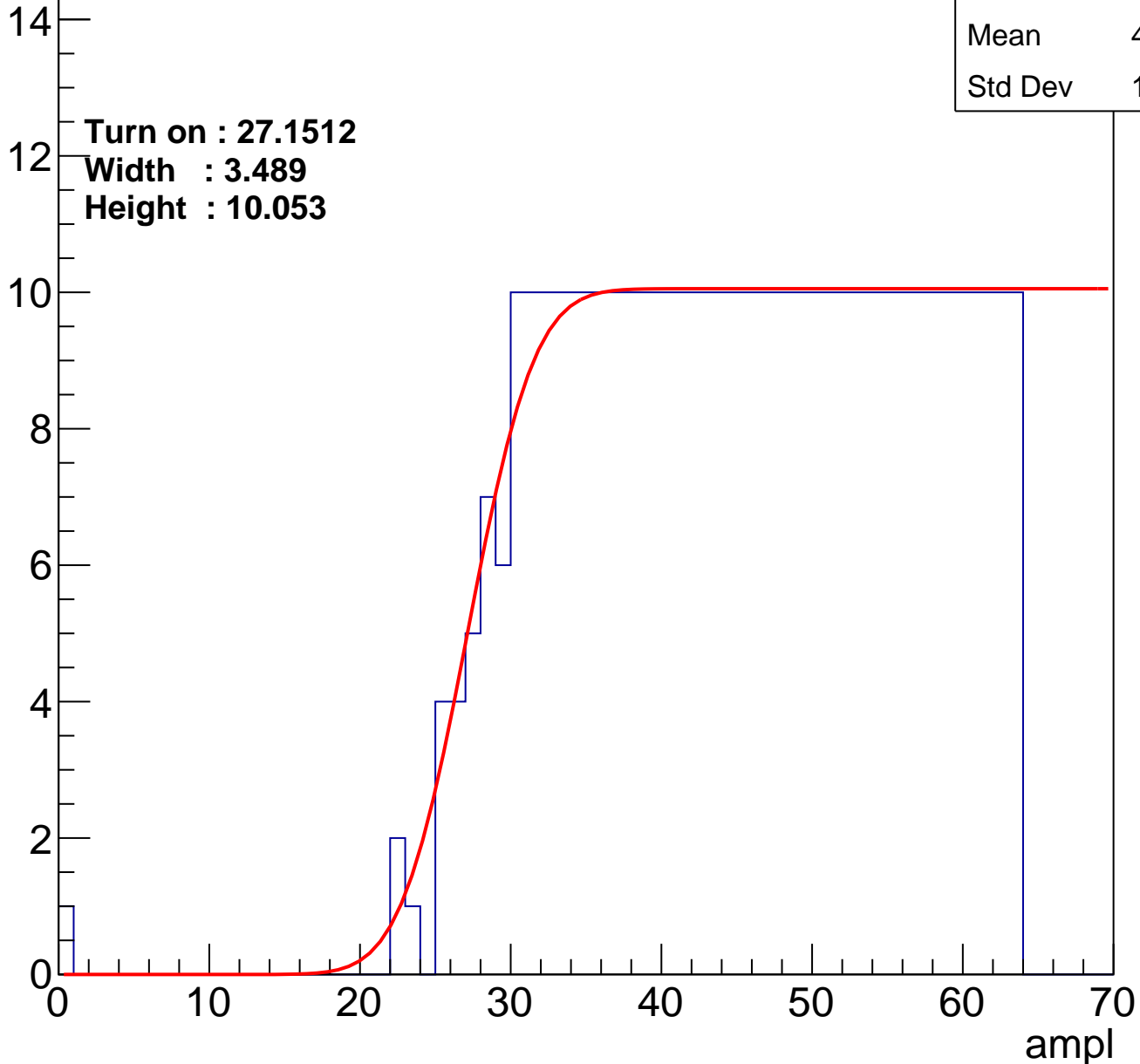
Entries	370
Mean	44.83
Std Dev	11.06

Turn on : 27.1512

Width : 3.489

Height : 10.053

Entry



B1L000S, U19-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.41
Std Dev	11.93

Turn on : 24.5181

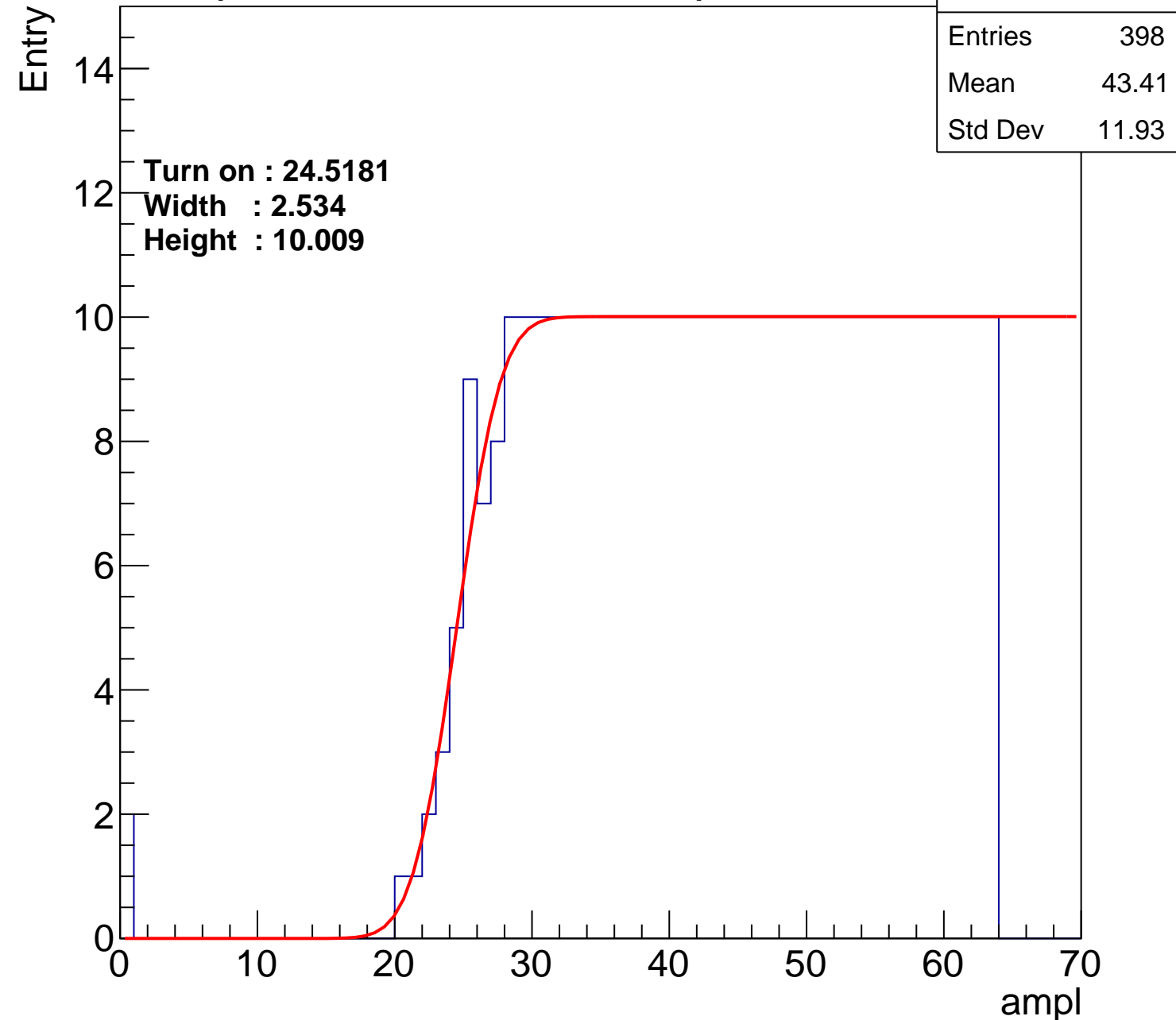
Width : 2.534

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.1084

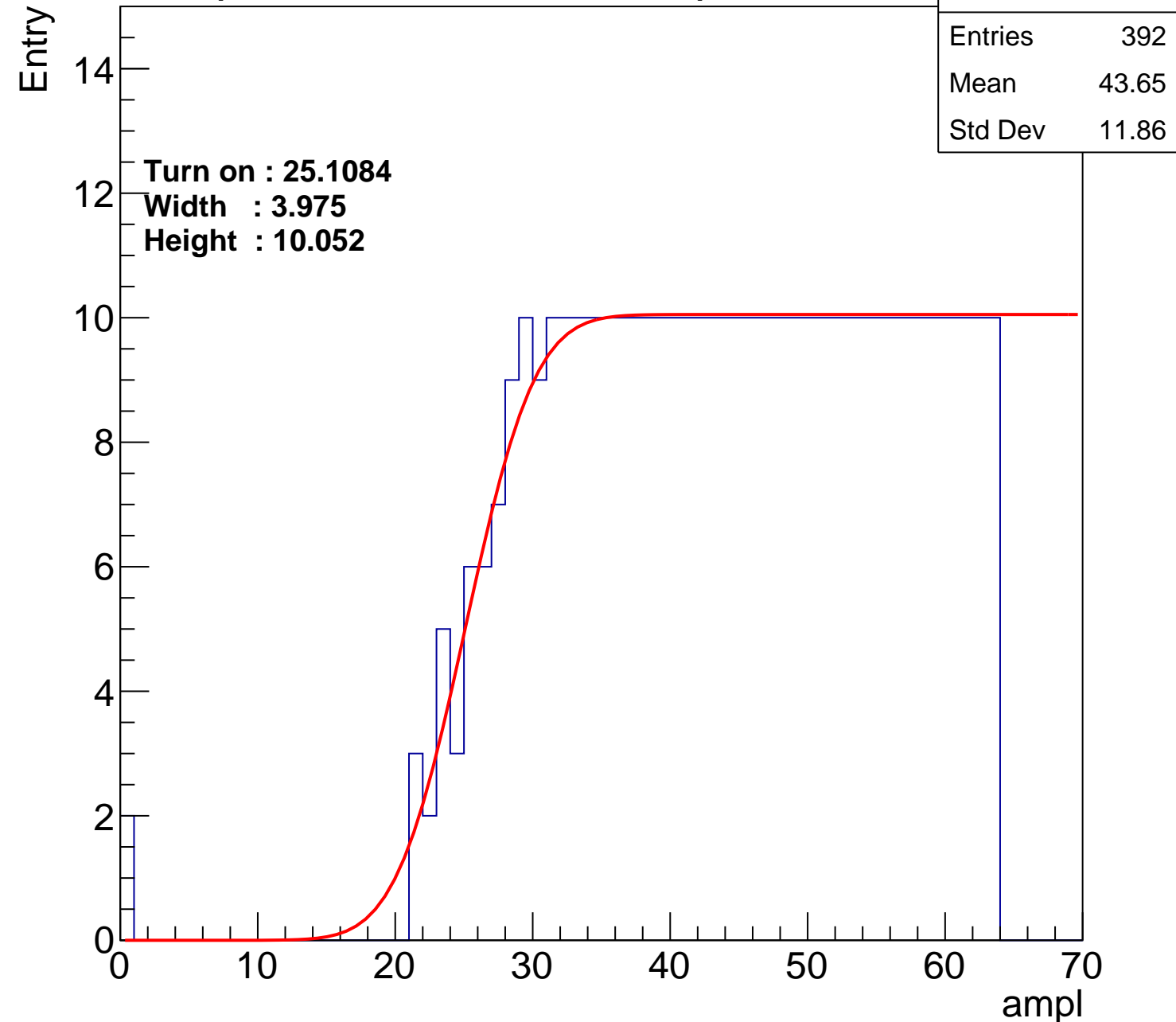
Width : 3.975

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U19-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.65
Std Dev	11.86

Turn on : 25.1084

Width : 3.975

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

