

B1L102S, U6-ch0, adc0

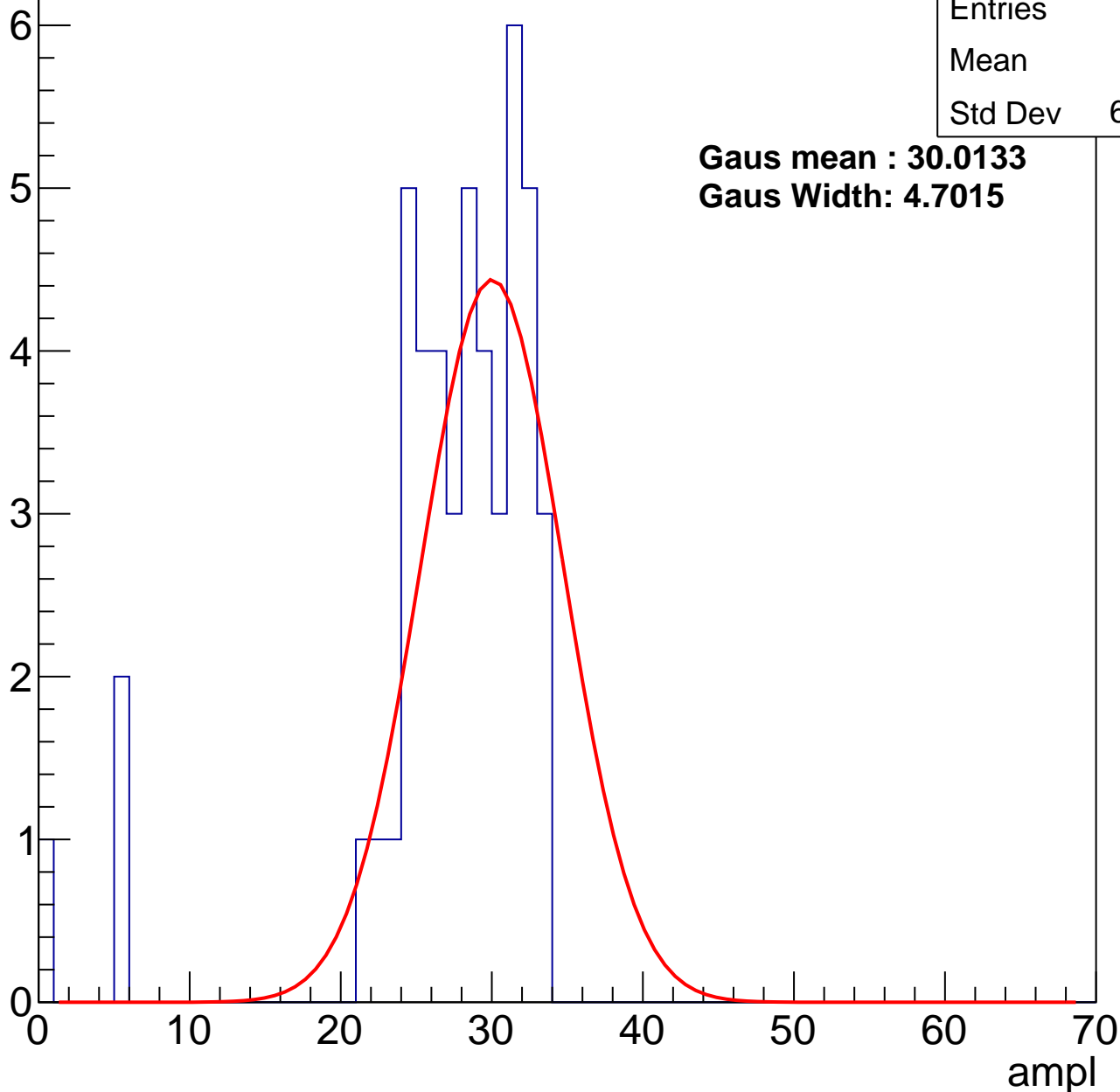
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	26.5
Std Dev	6.776

Gaus mean : 30.0133

Gaus Width: 4.7015



B1L102S, U6-ch0, adc1

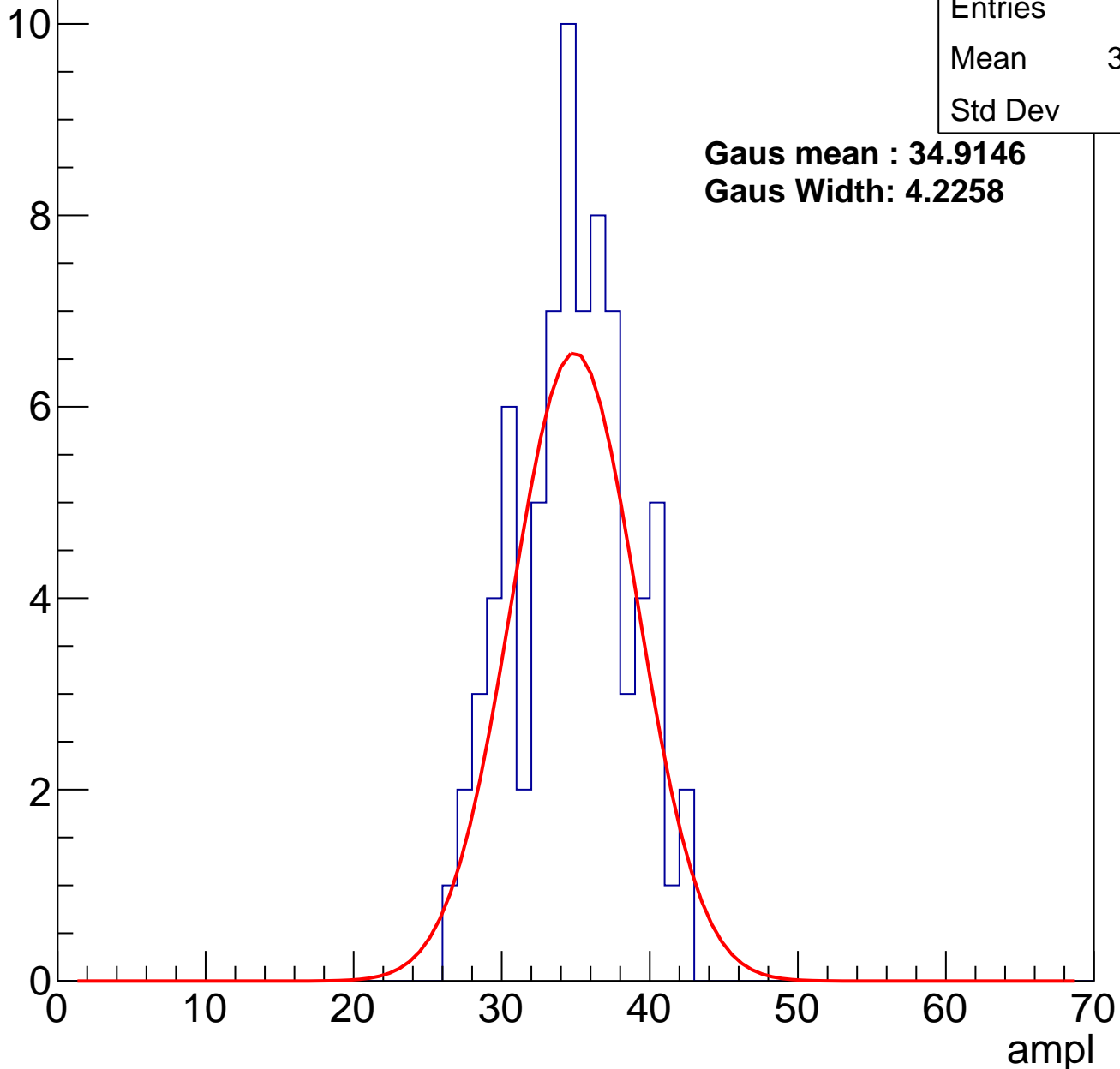
calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	34.29
Std Dev	3.8

Gaus mean : 34.9146

Gaus Width: 4.2258

Entry



B1L102S, U6-ch0, adc2

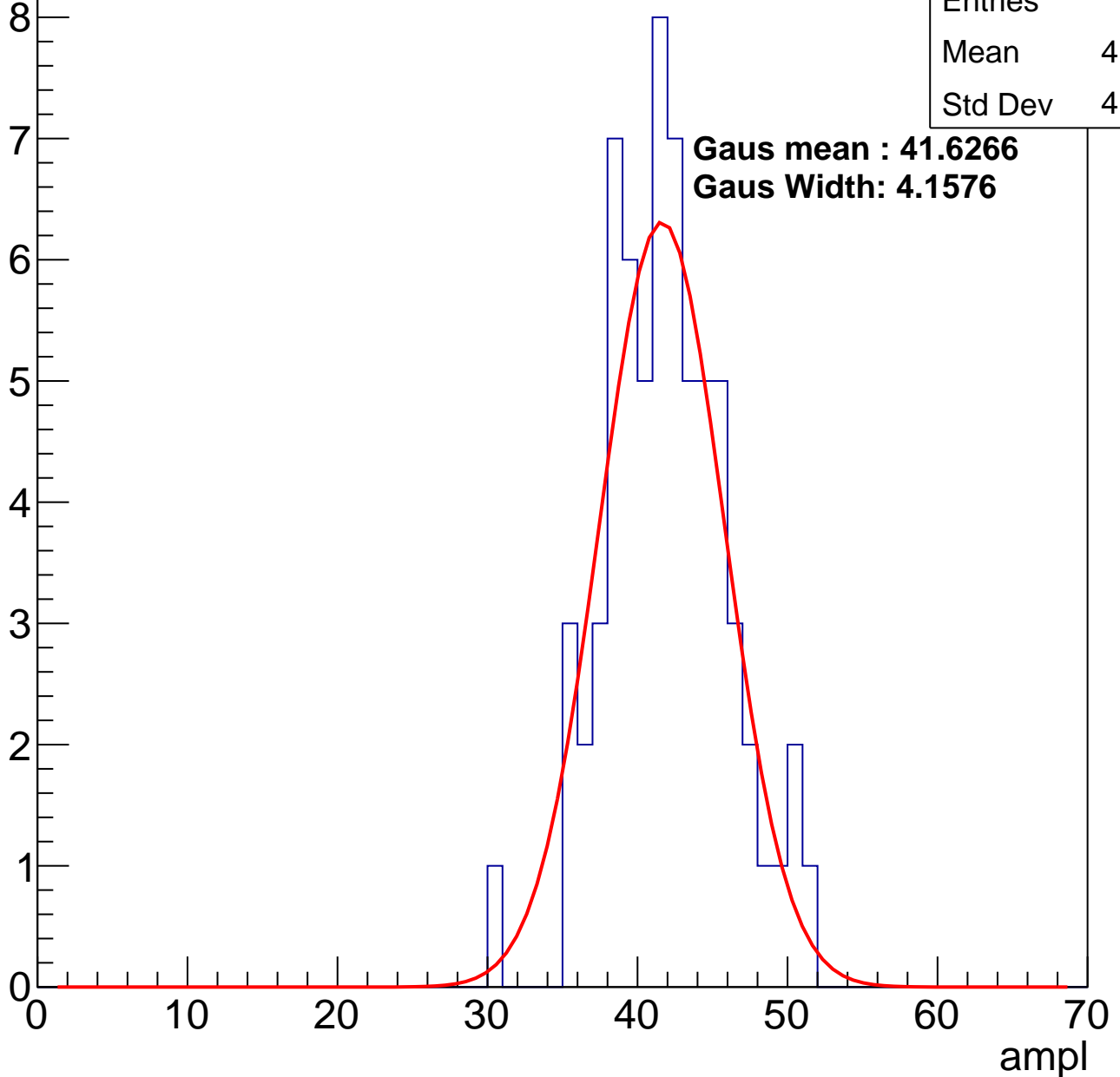
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.49
Std Dev	4.009

Gaus mean : 41.6266

Gaus Width: 4.1576

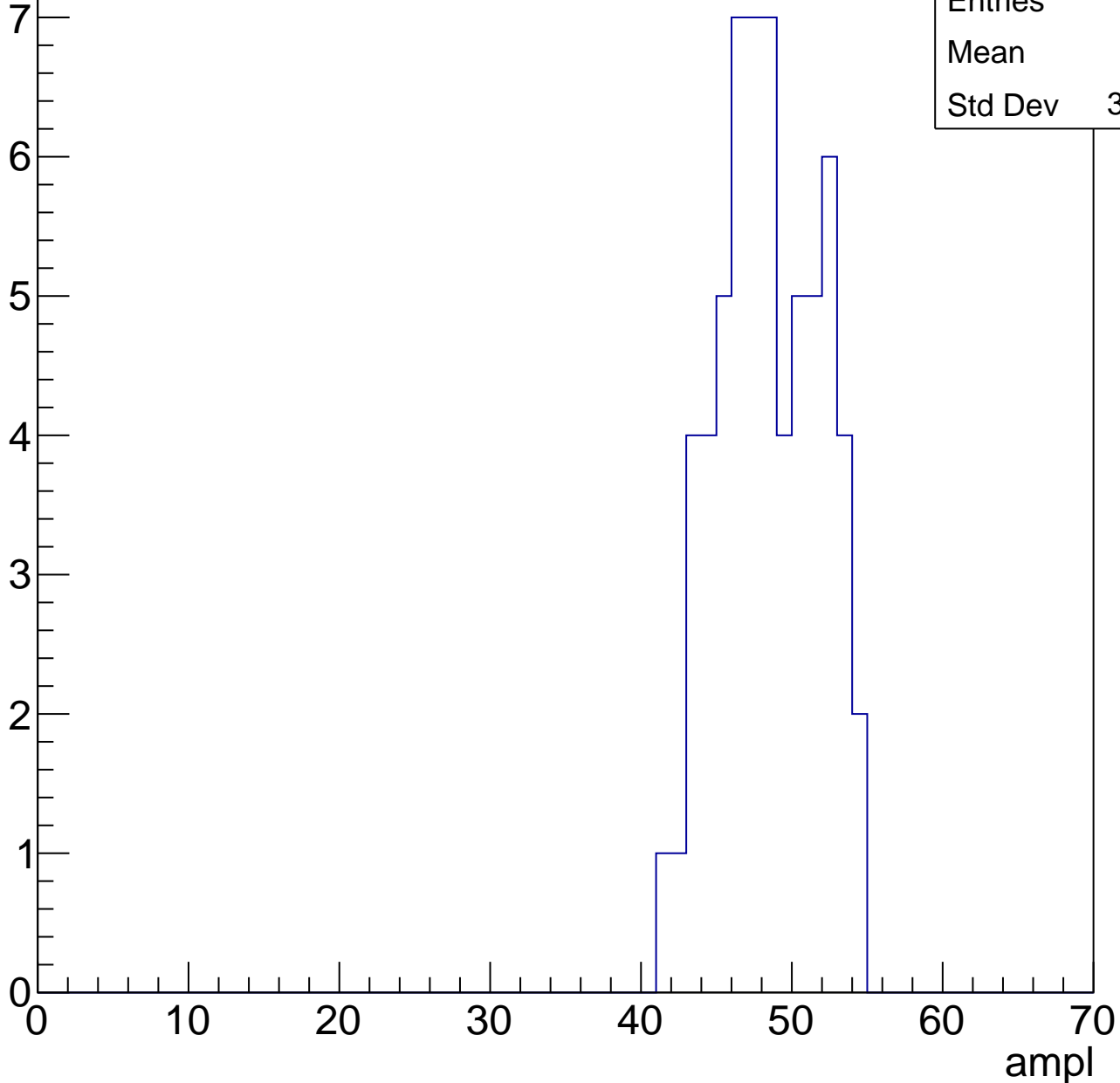


B1L102S, U6-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48
Std Dev	3.277

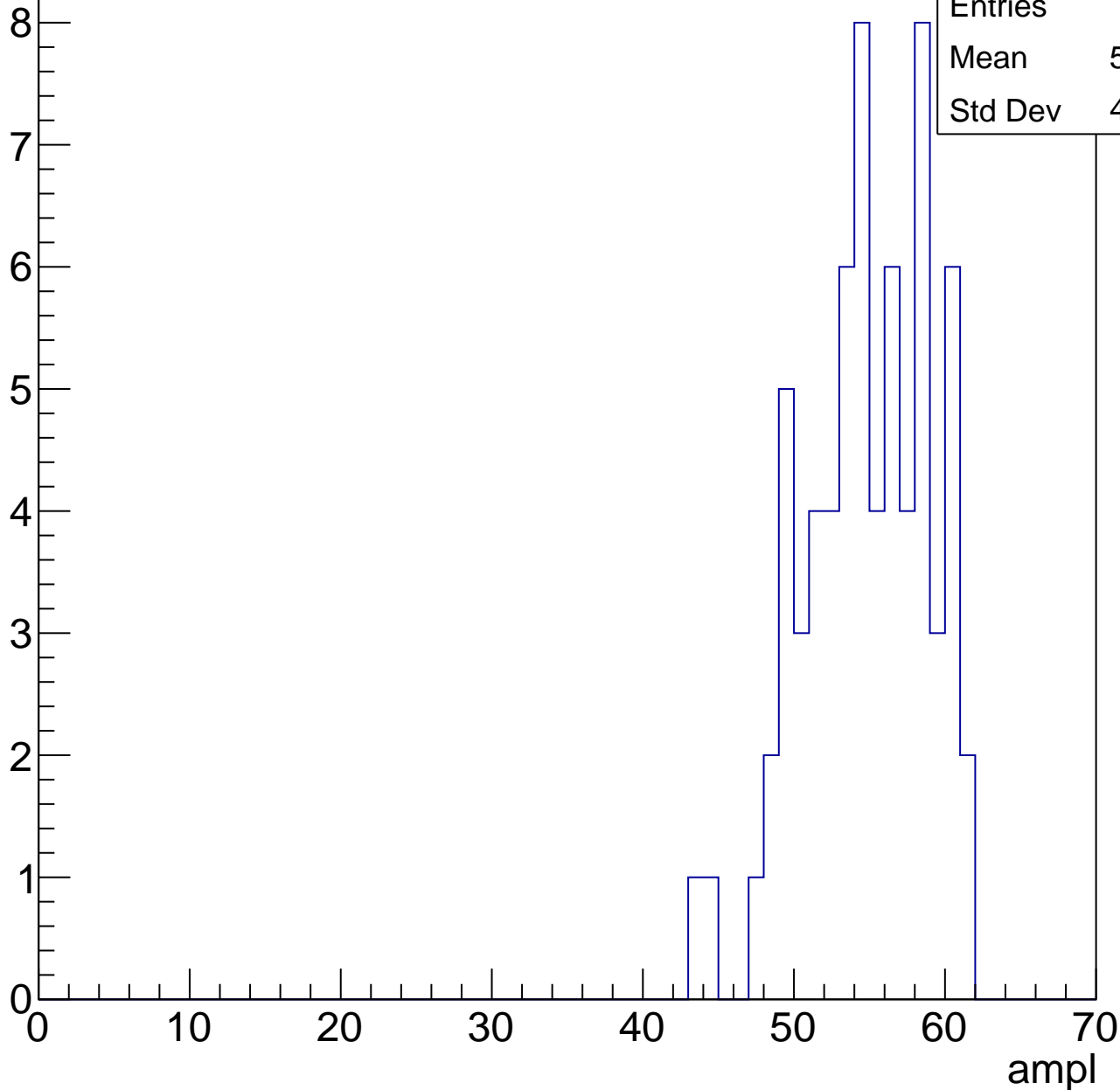


B1L102S, U6-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.32
Std Dev	4.107

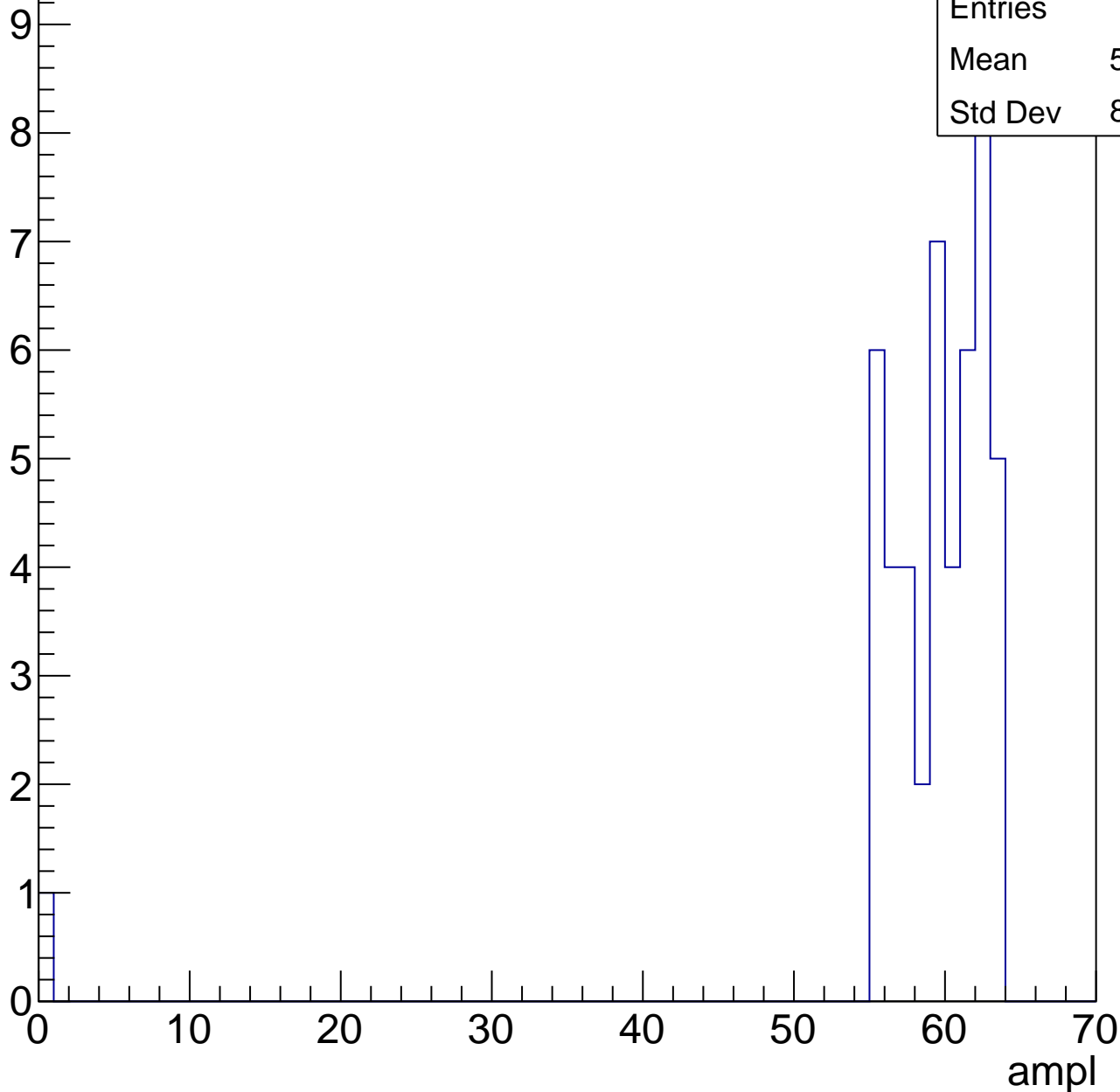


B1L102S, U6-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.12
Std Dev	8.878

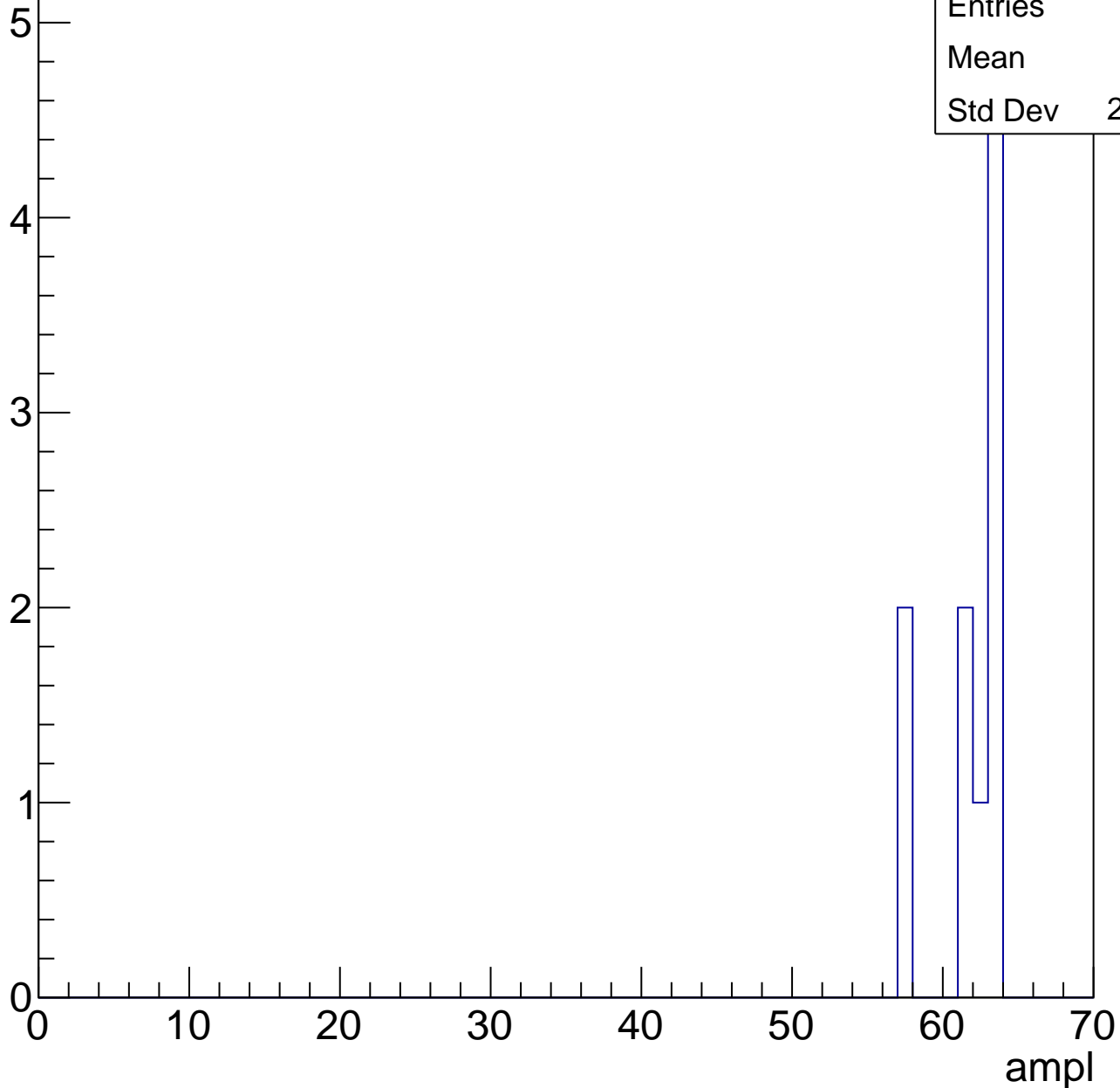


B1L102S, U6-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

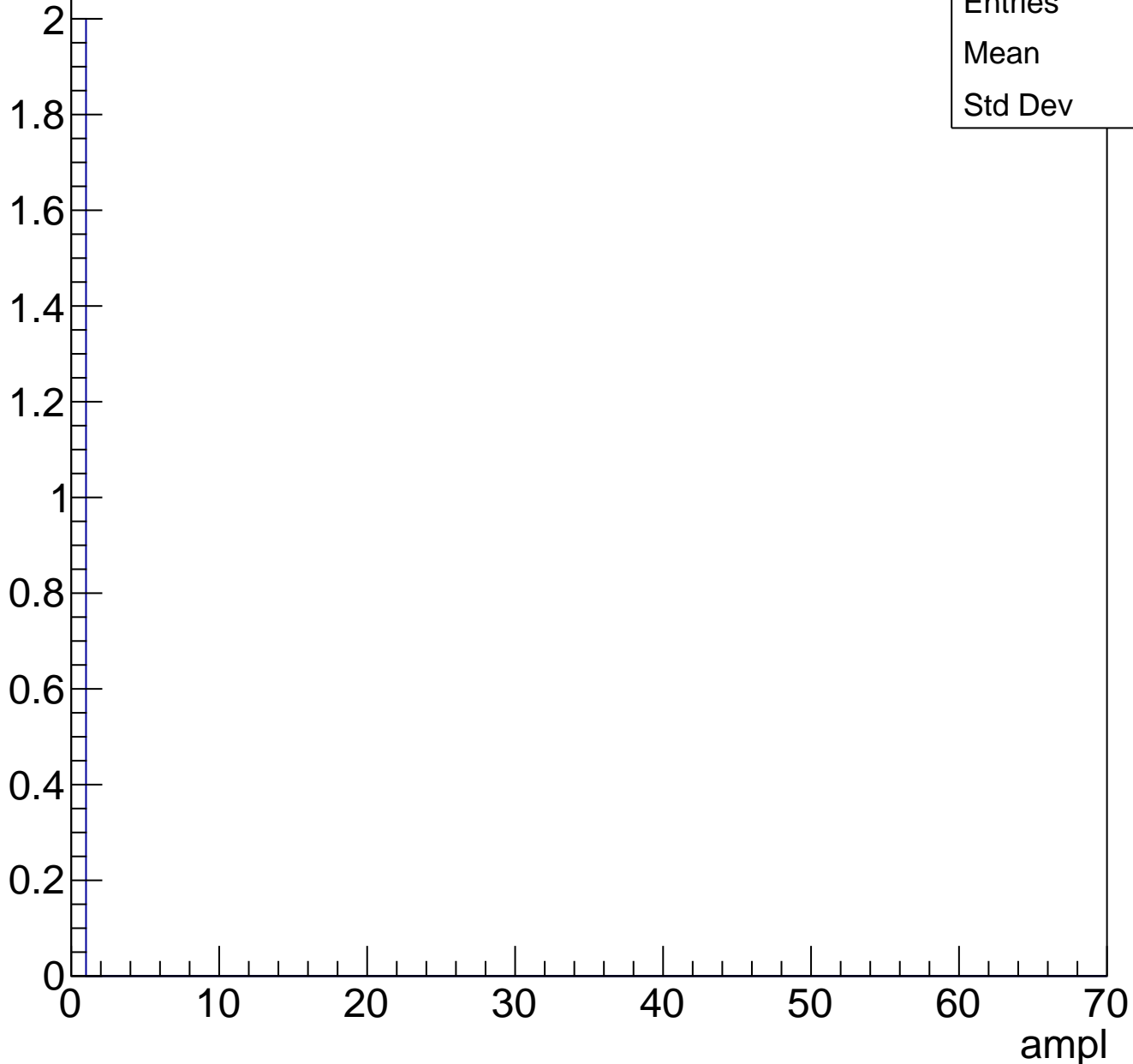
Entries	10
Mean	61.3
Std Dev	2.283



B1L102S, U6-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch1, adc0

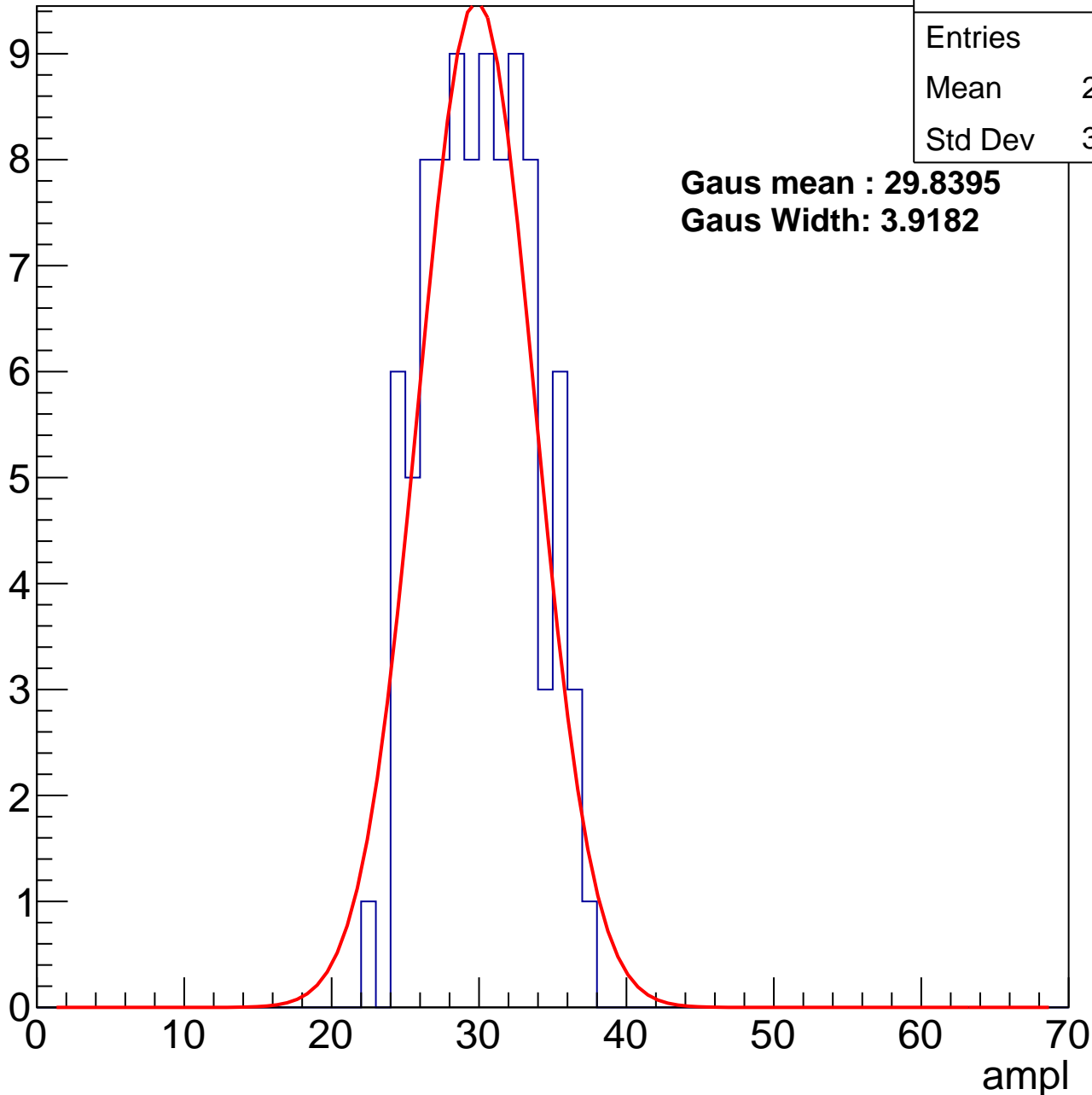
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	92
Mean	29.63
Std Dev	3.469

Gaus mean : 29.8395

Gaus Width: 3.9182



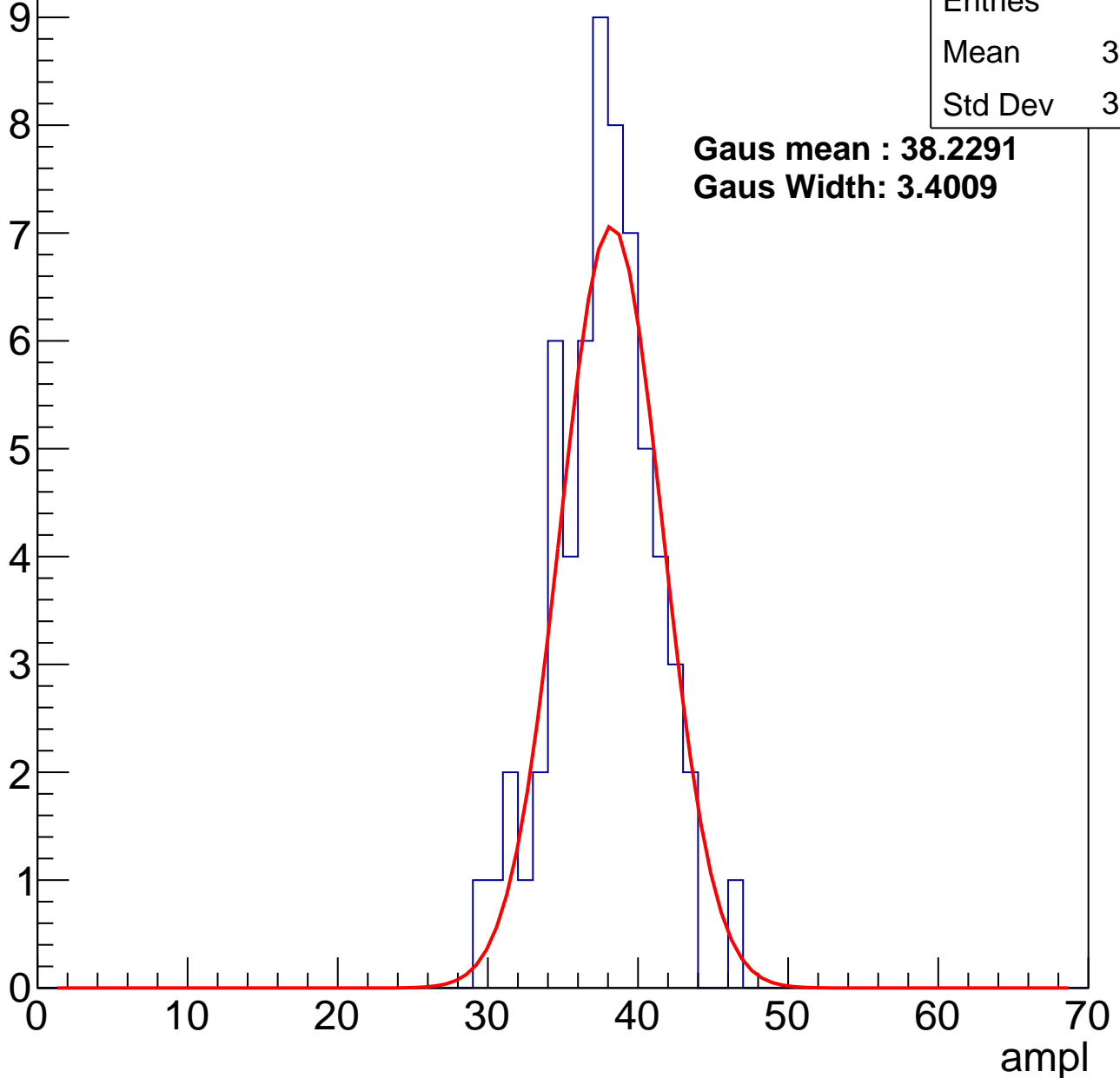
B1L102S, U6-ch1, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.27
Std Dev	3.327

Gaus mean : 38.2291
Gaus Width: 3.4009



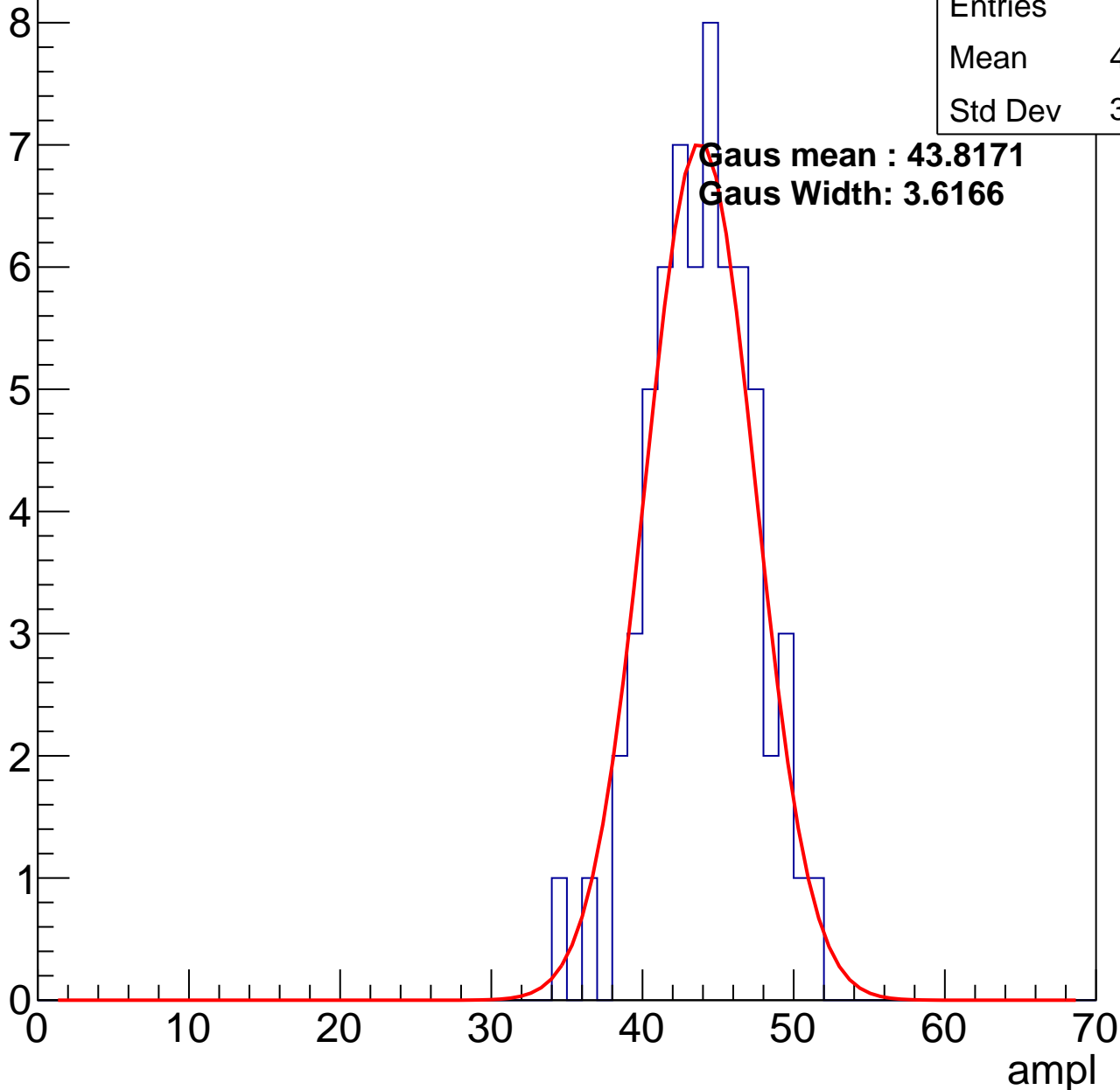
B1L102S, U6-ch1, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.46
Std Dev	3.399

Gaus mean : 43.8171
Gaus Width: 3.6166

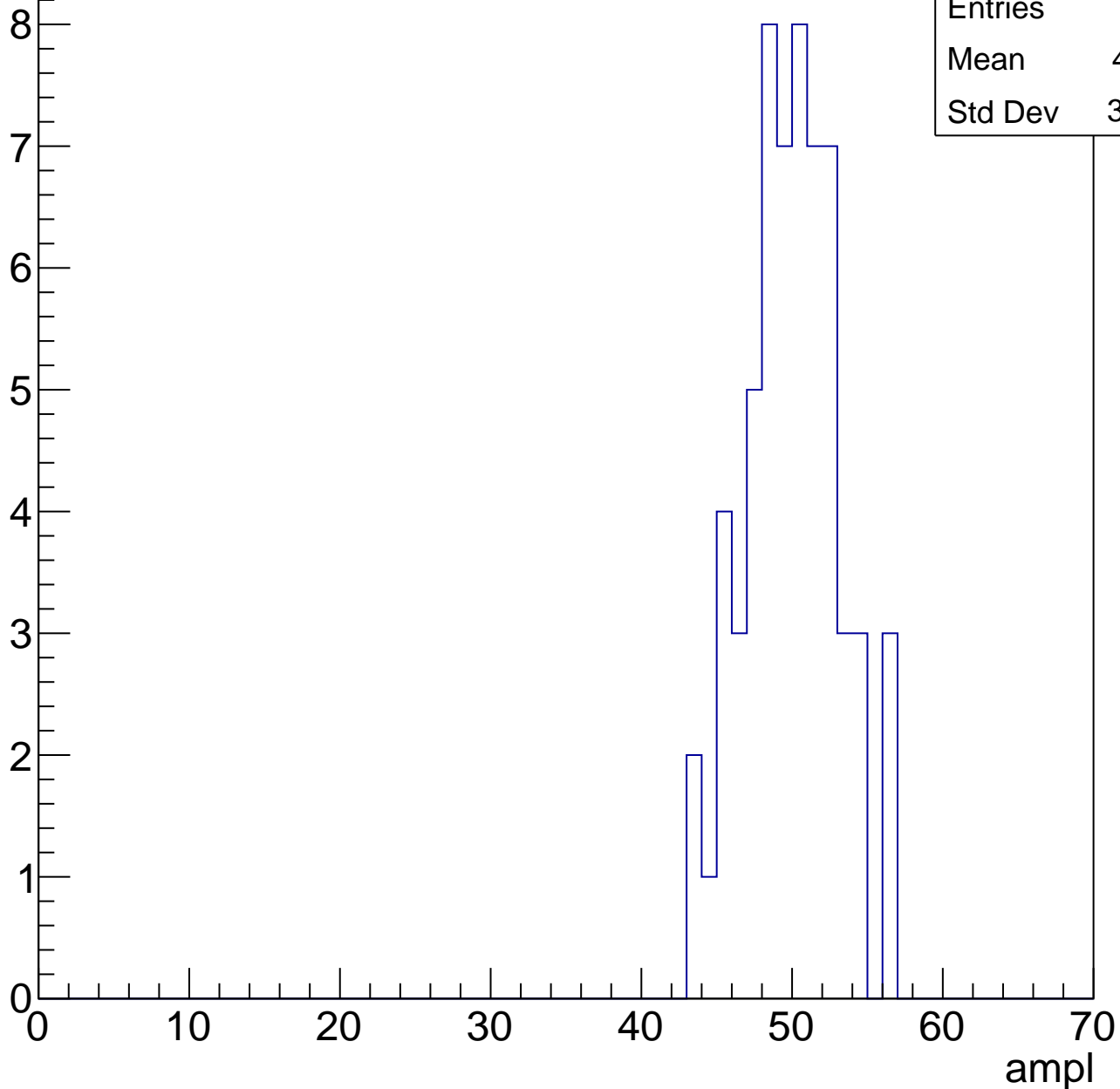


B1L102S, U6-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.51
Std Dev	3.055

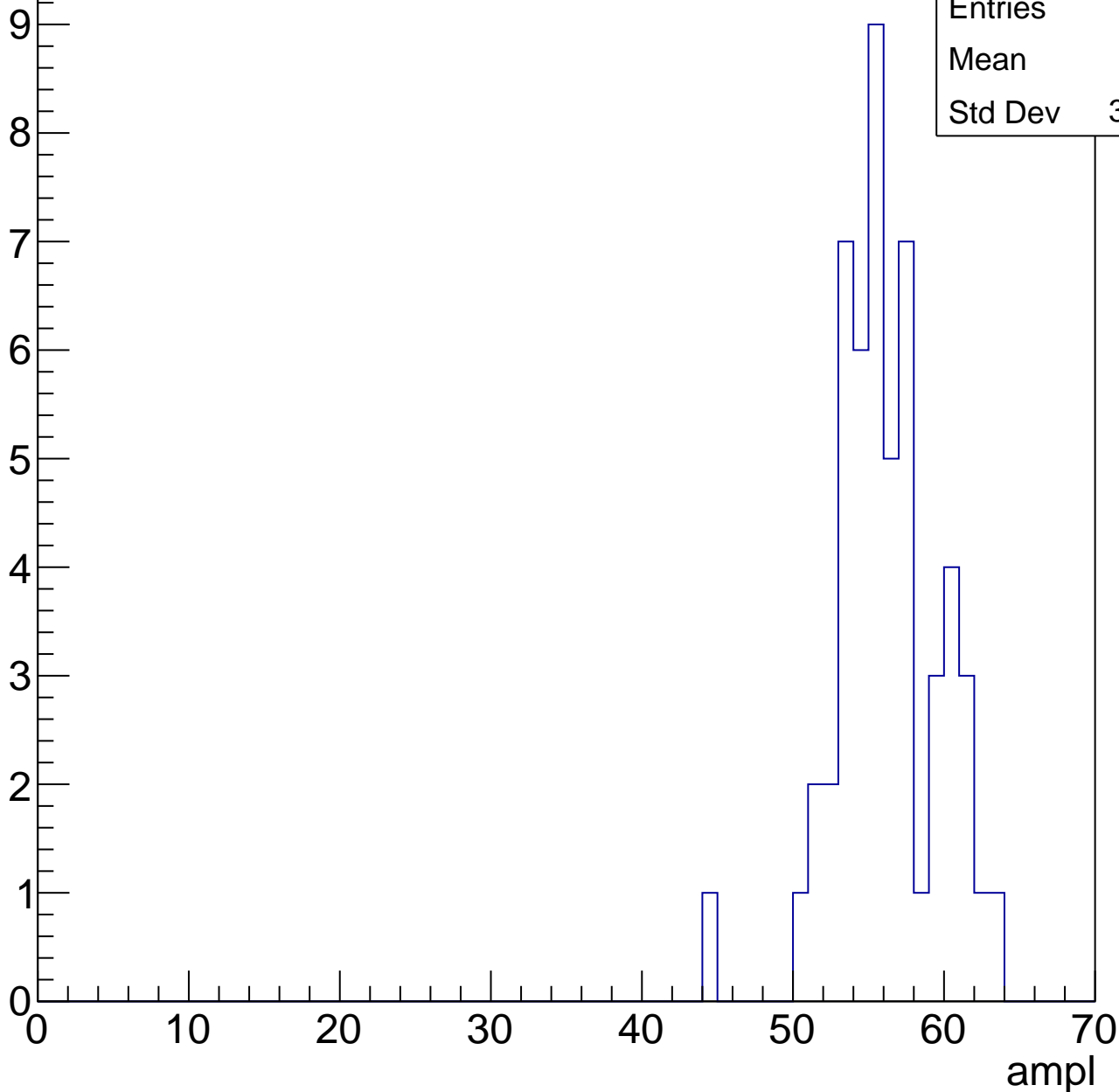


B1L102S, U6-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

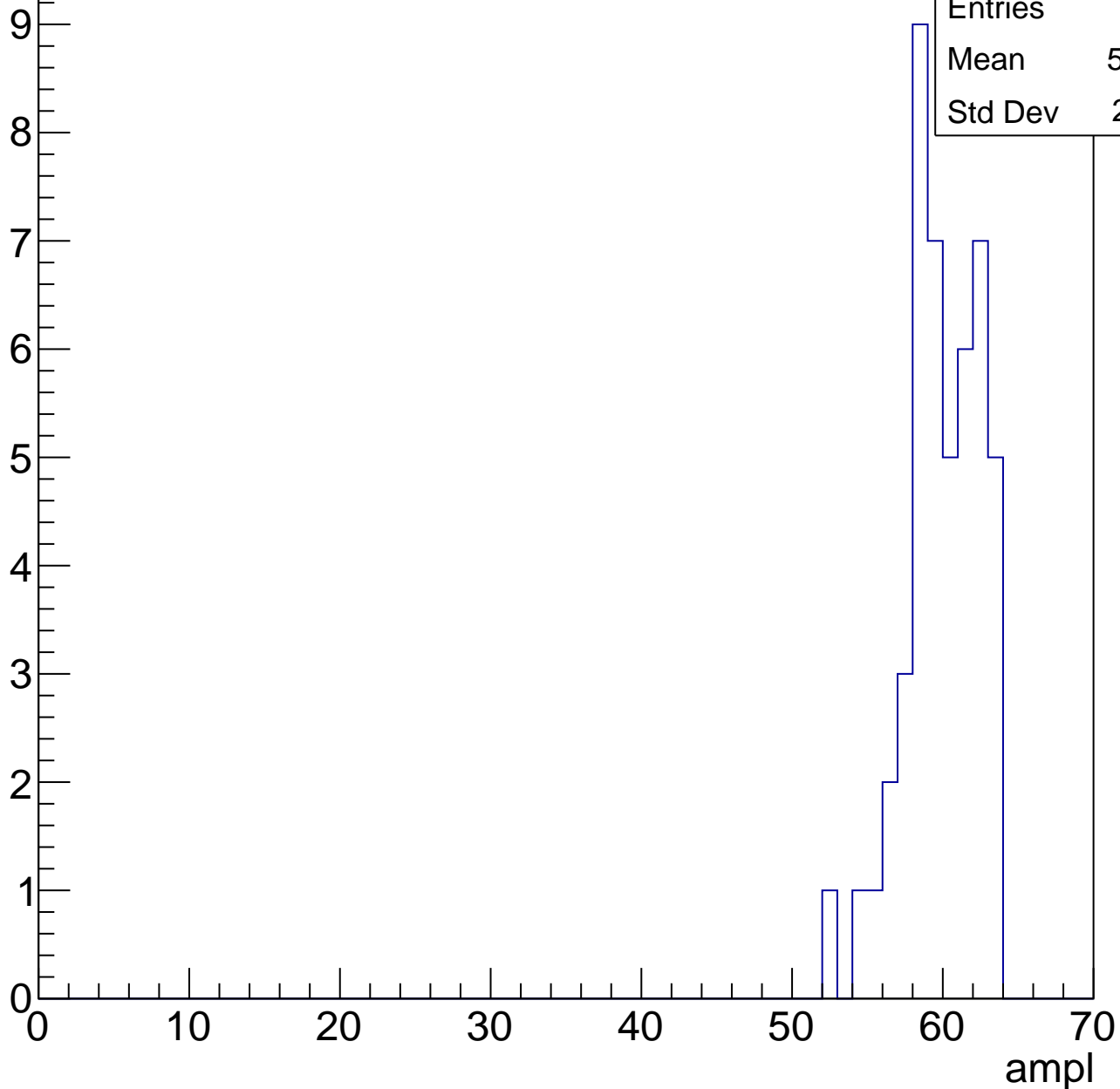
Entries	53
Mean	55.7
Std Dev	3.407



B1L102S, U6-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

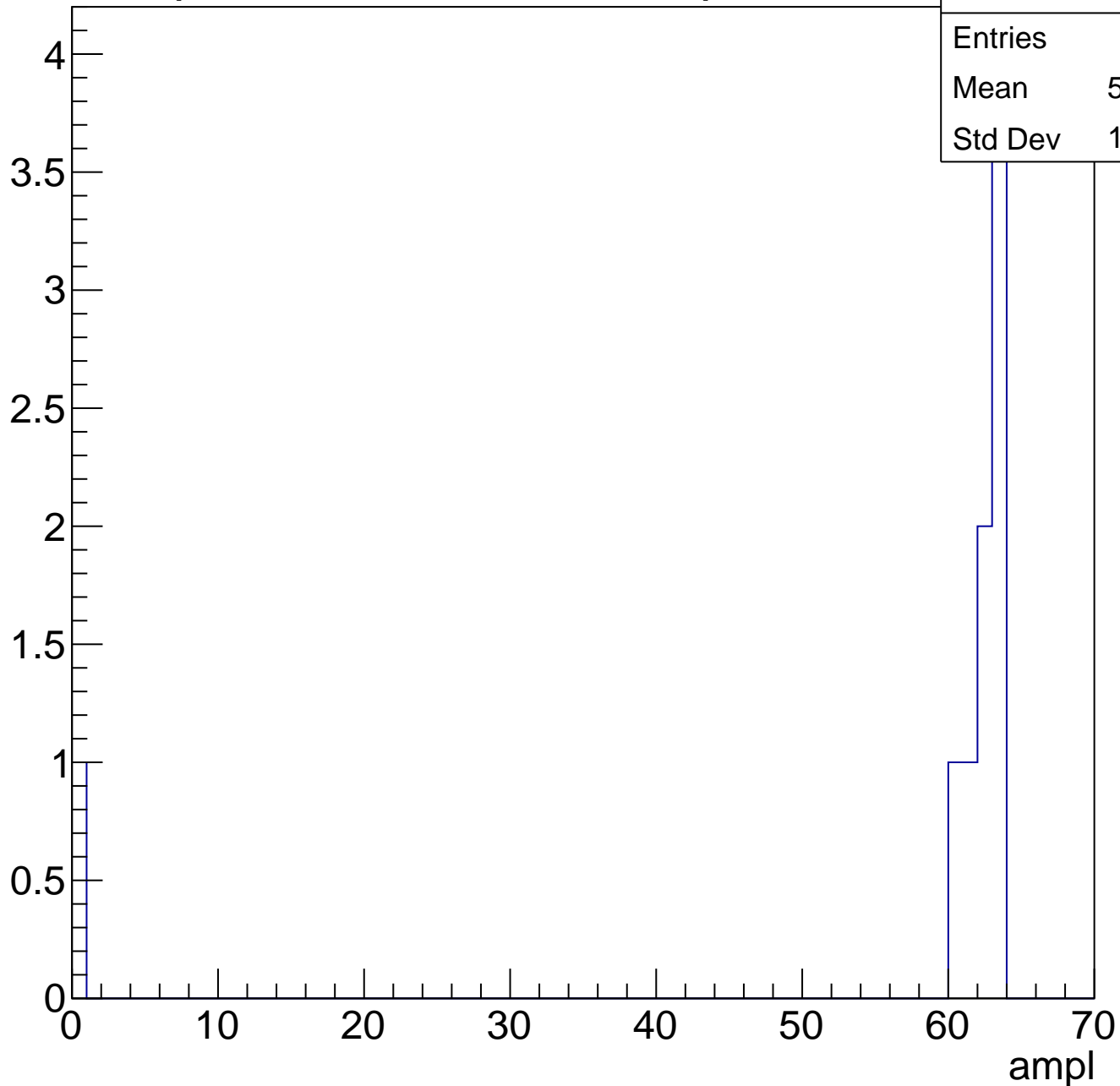
Entry



B1L102S, U6-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

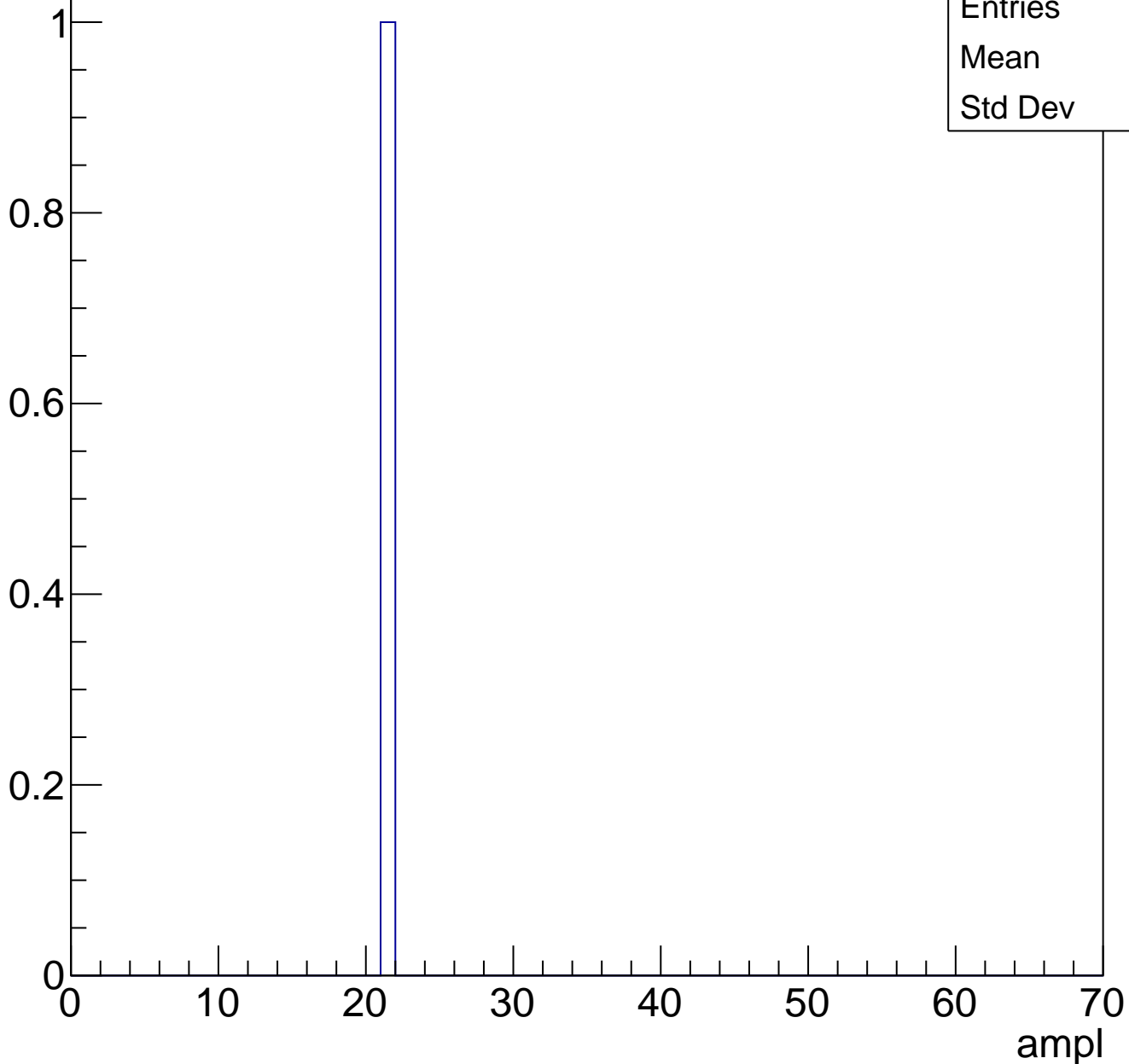
Entry



B1L102S, U6-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch2, adc0

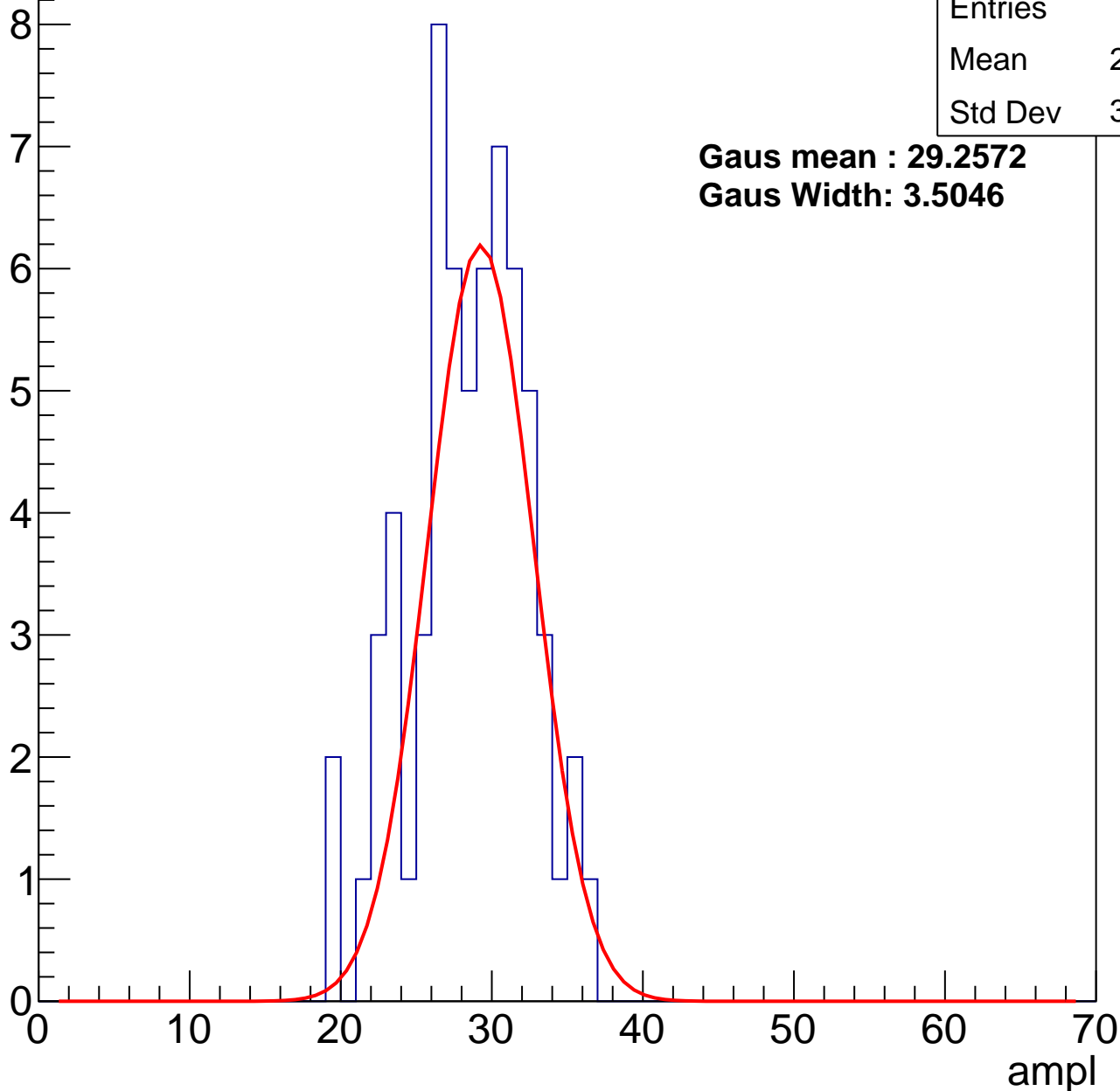
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.05
Std Dev	3.834

Gaus mean : 29.2572

Gaus Width: 3.5046



B1L102S, U6-ch2, adc1

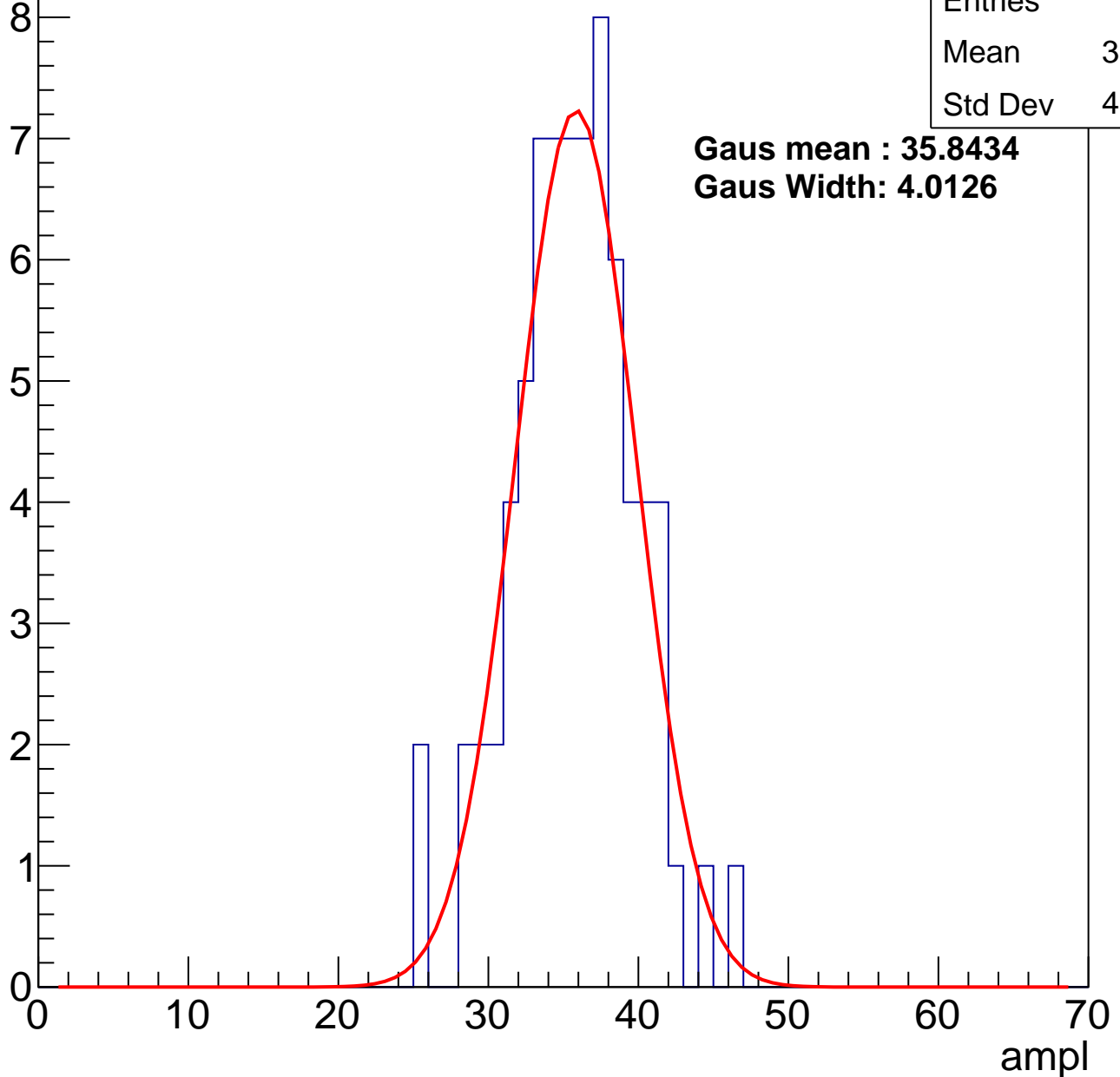
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.27
Std Dev	4.045

Gaus mean : 35.8434

Gaus Width: 4.0126



B1L102S, U6-ch2, adc2

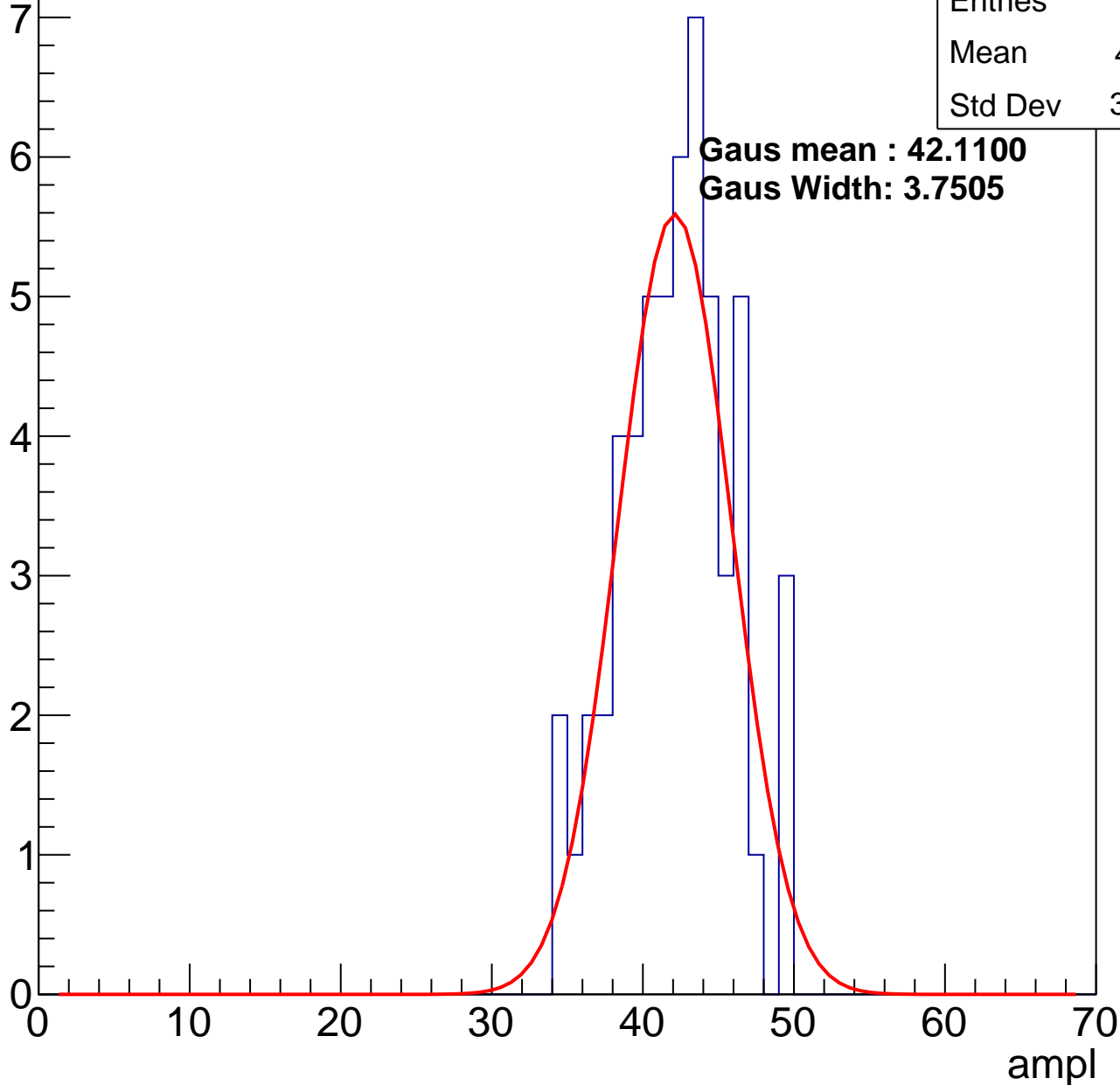
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.71
Std Dev	3.632

Gaus mean : 42.1100

Gaus Width: 3.7505

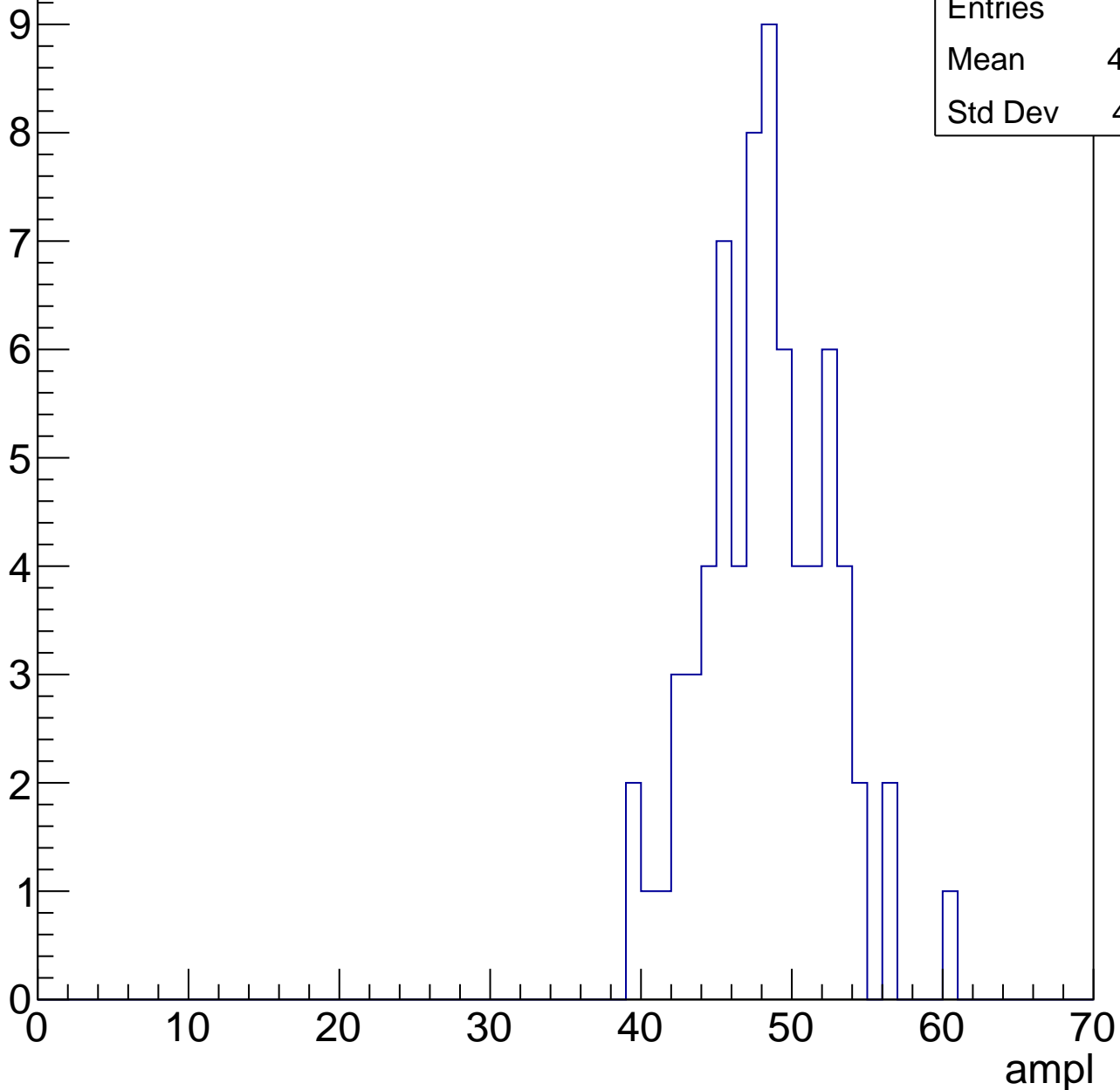


B1L102S, U6-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

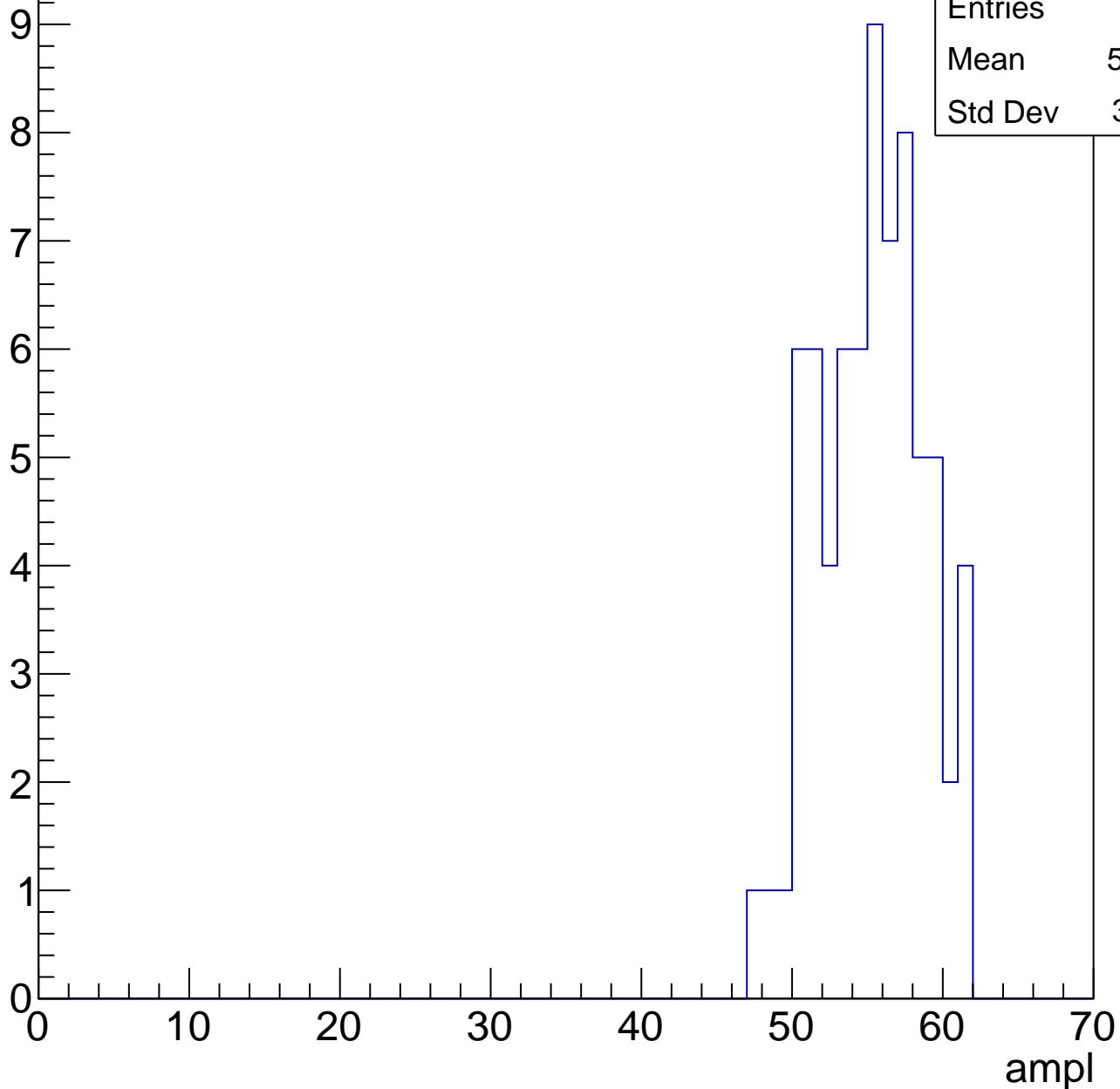
Entries	71
Mean	47.87
Std Dev	4.121



B1L102S, U6-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

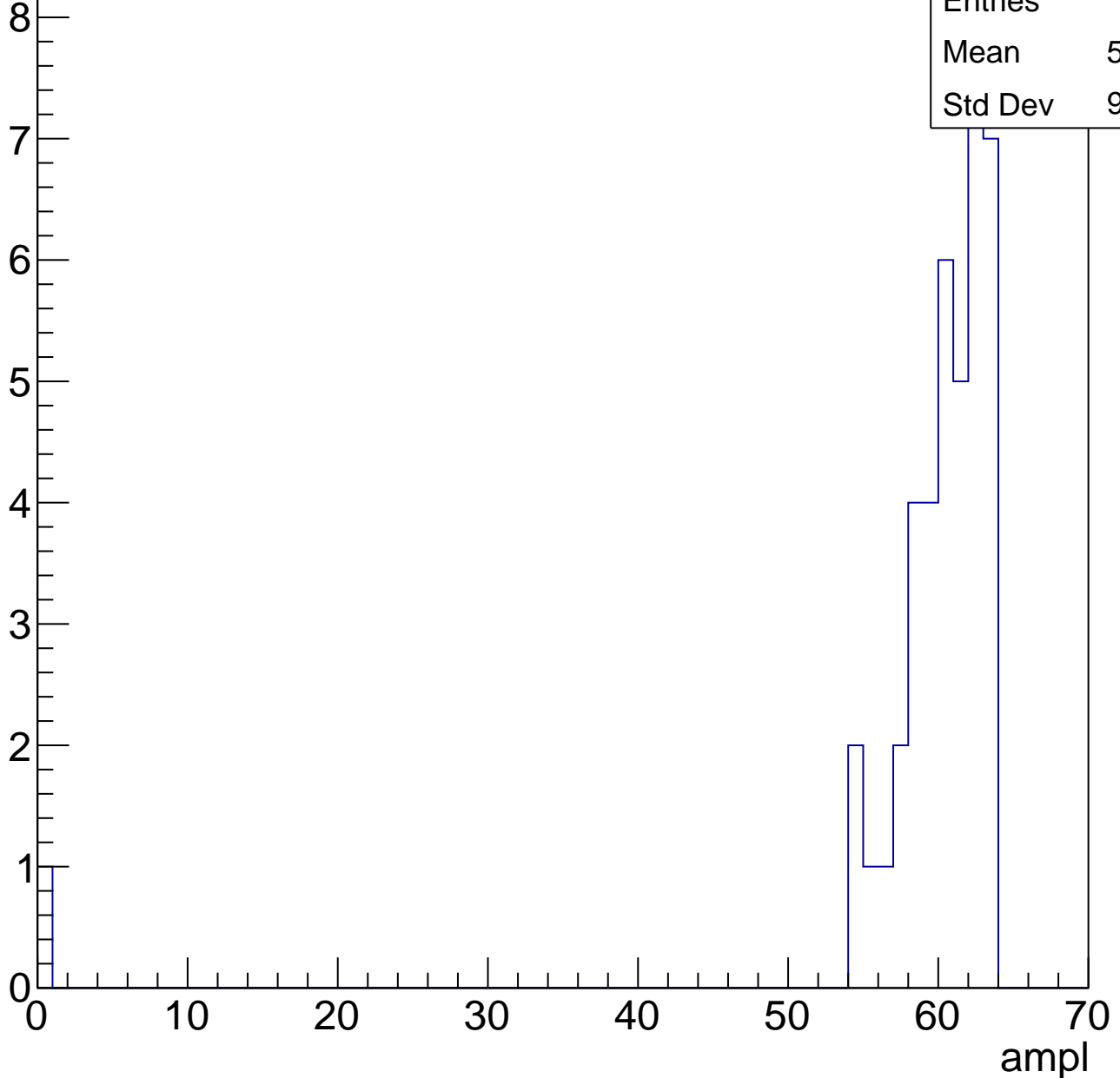


B1L102S, U6-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.61
Std Dev	9.591



B1L102S, U6-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

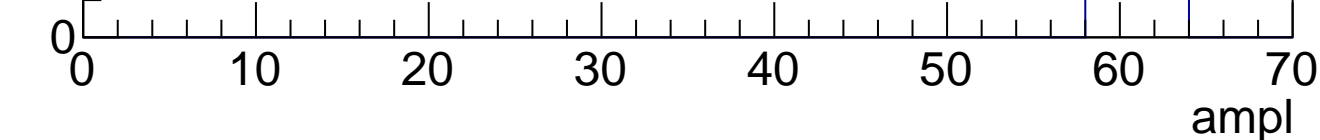
9

Mean

61.22

Std Dev

1.75



B1L102S, U6-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch3, adc0

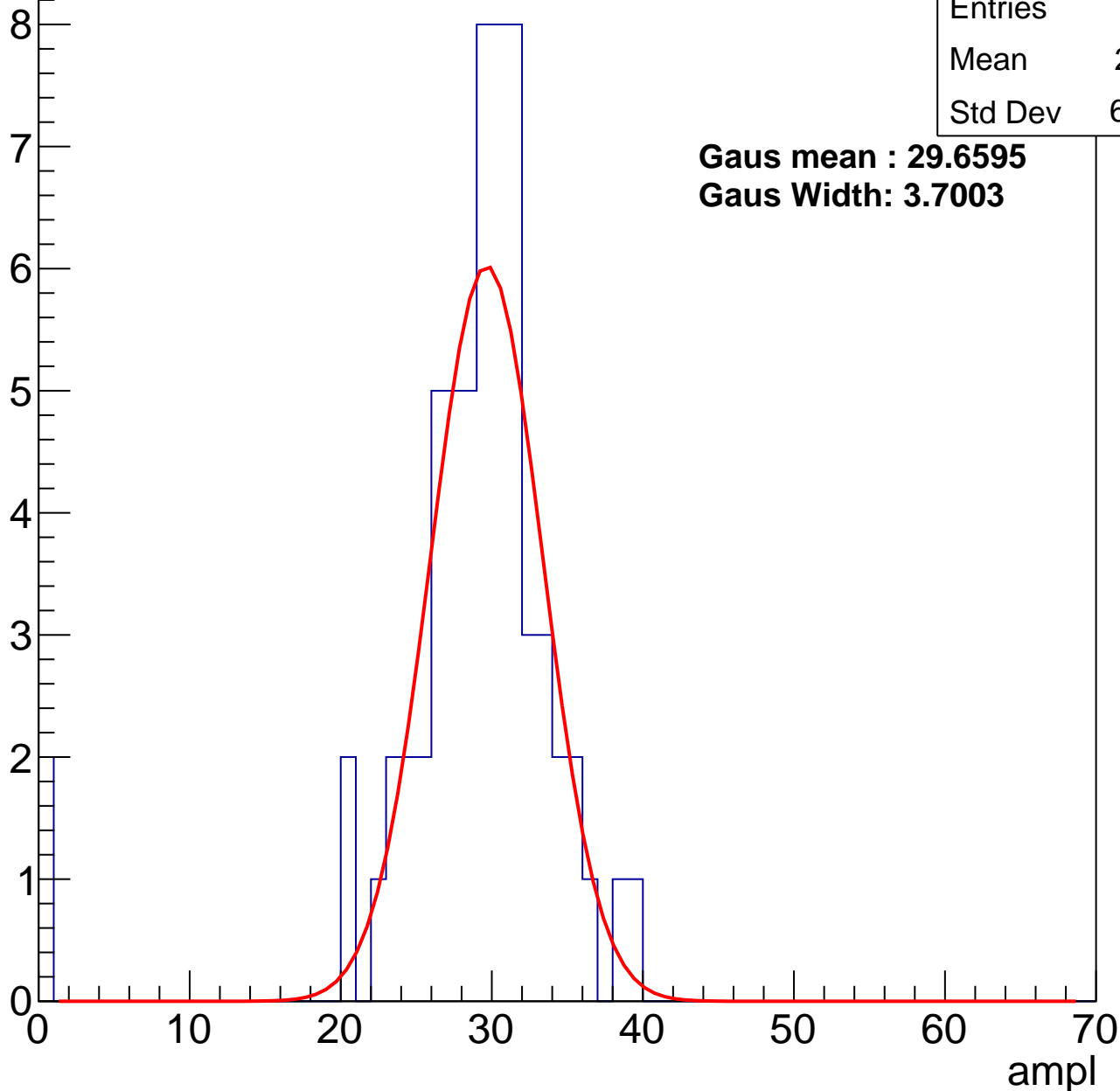
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.21
Std Dev	6.355

Gaus mean : 29.6595

Gaus Width: 3.7003



B1L102S, U6-ch3, adc1

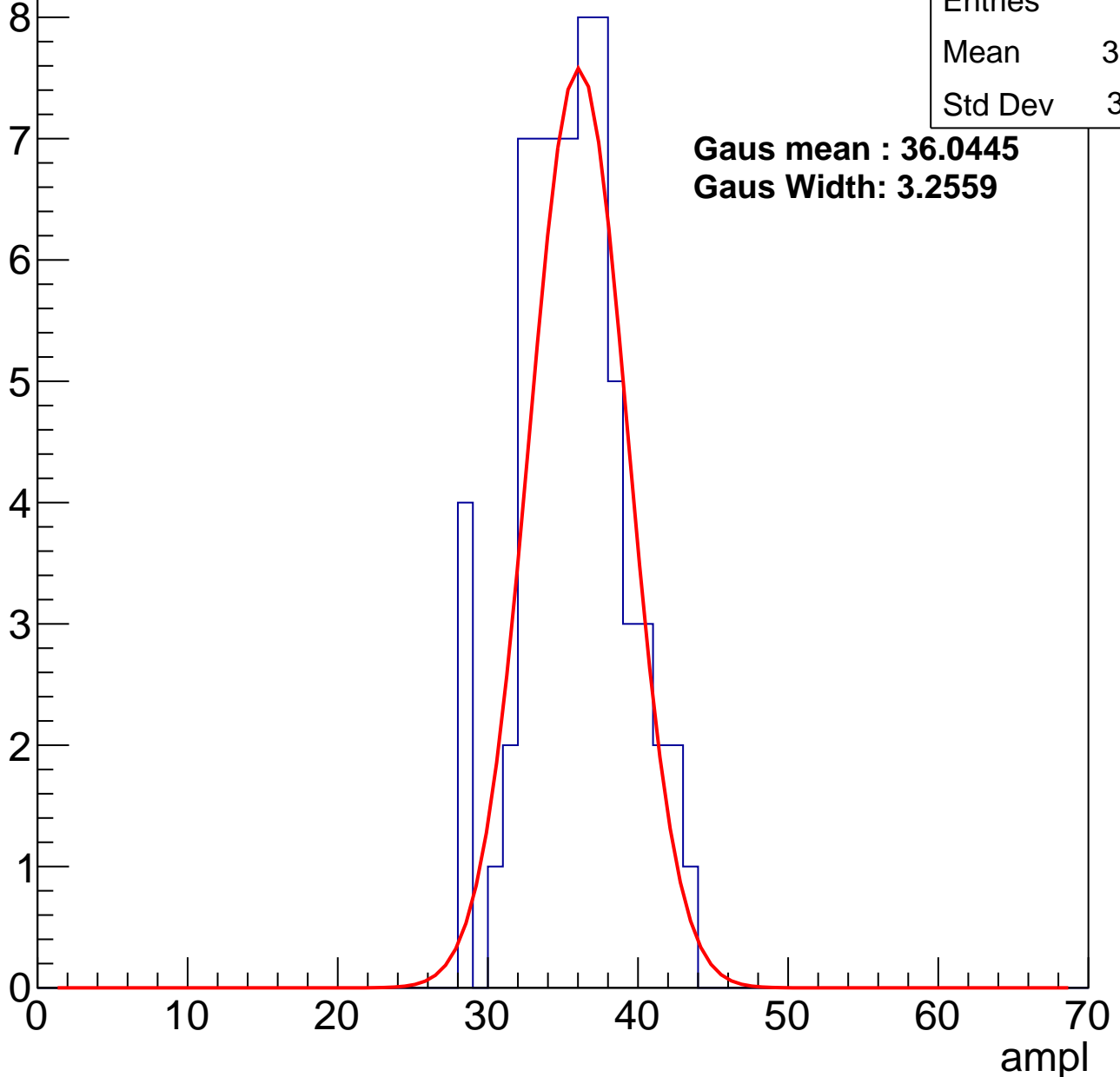
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.25
Std Dev	3.431

Gaus mean : 36.0445

Gaus Width: 3.2559



B1L102S, U6-ch3, adc2

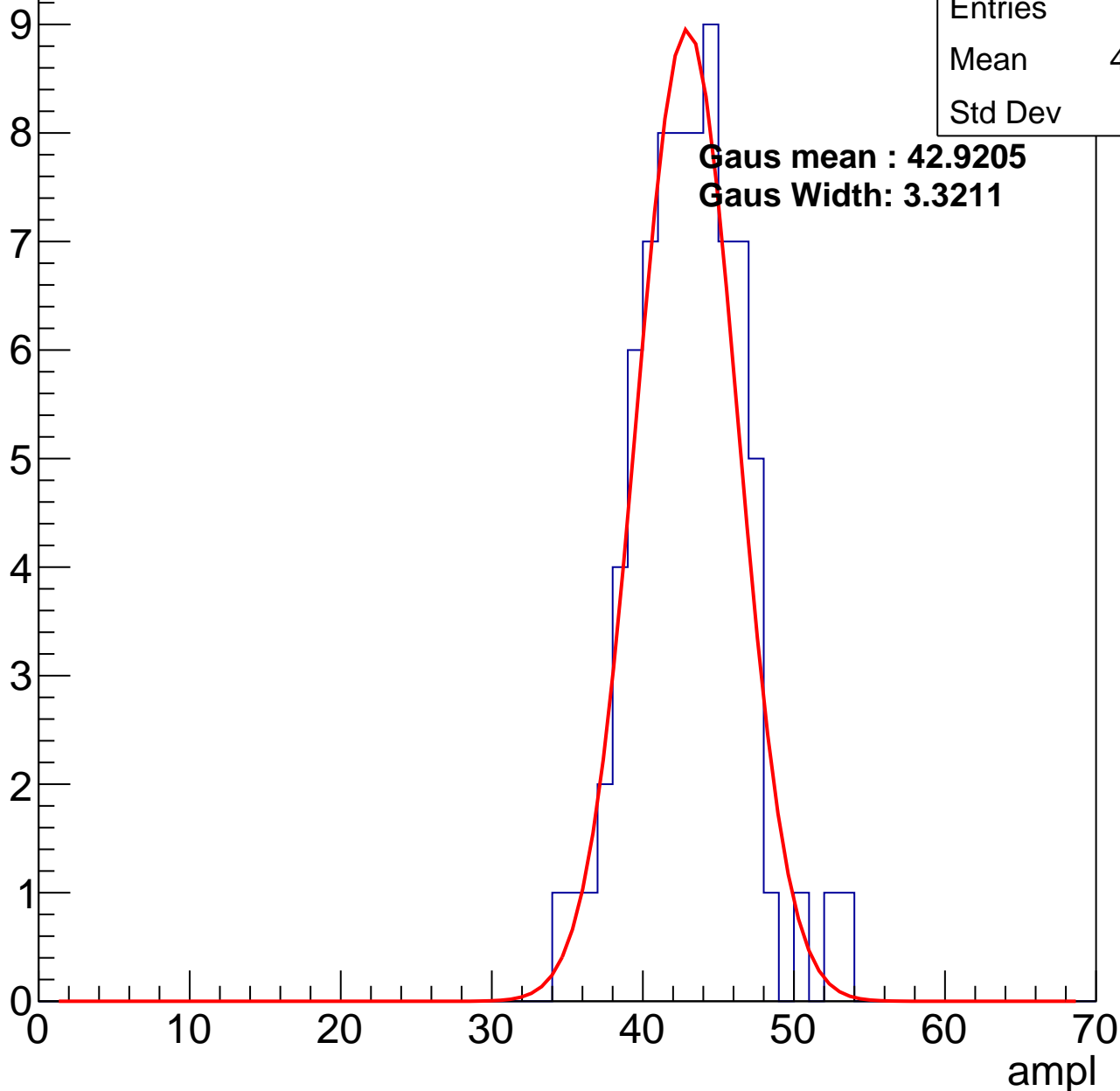
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.62
Std Dev	3.56

Gaus mean : 42.9205

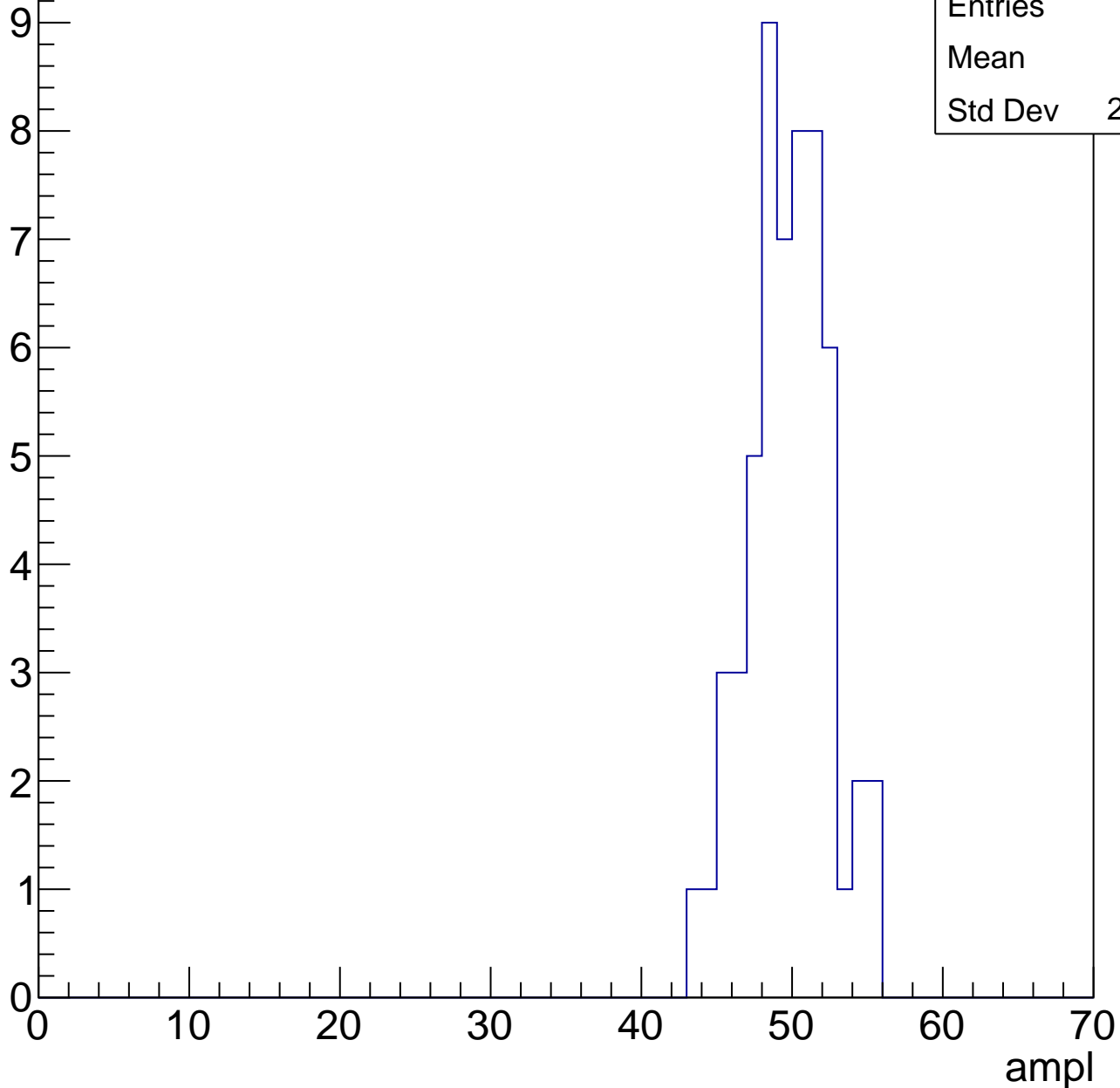
Gaus Width: 3.3211



B1L102S, U6-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



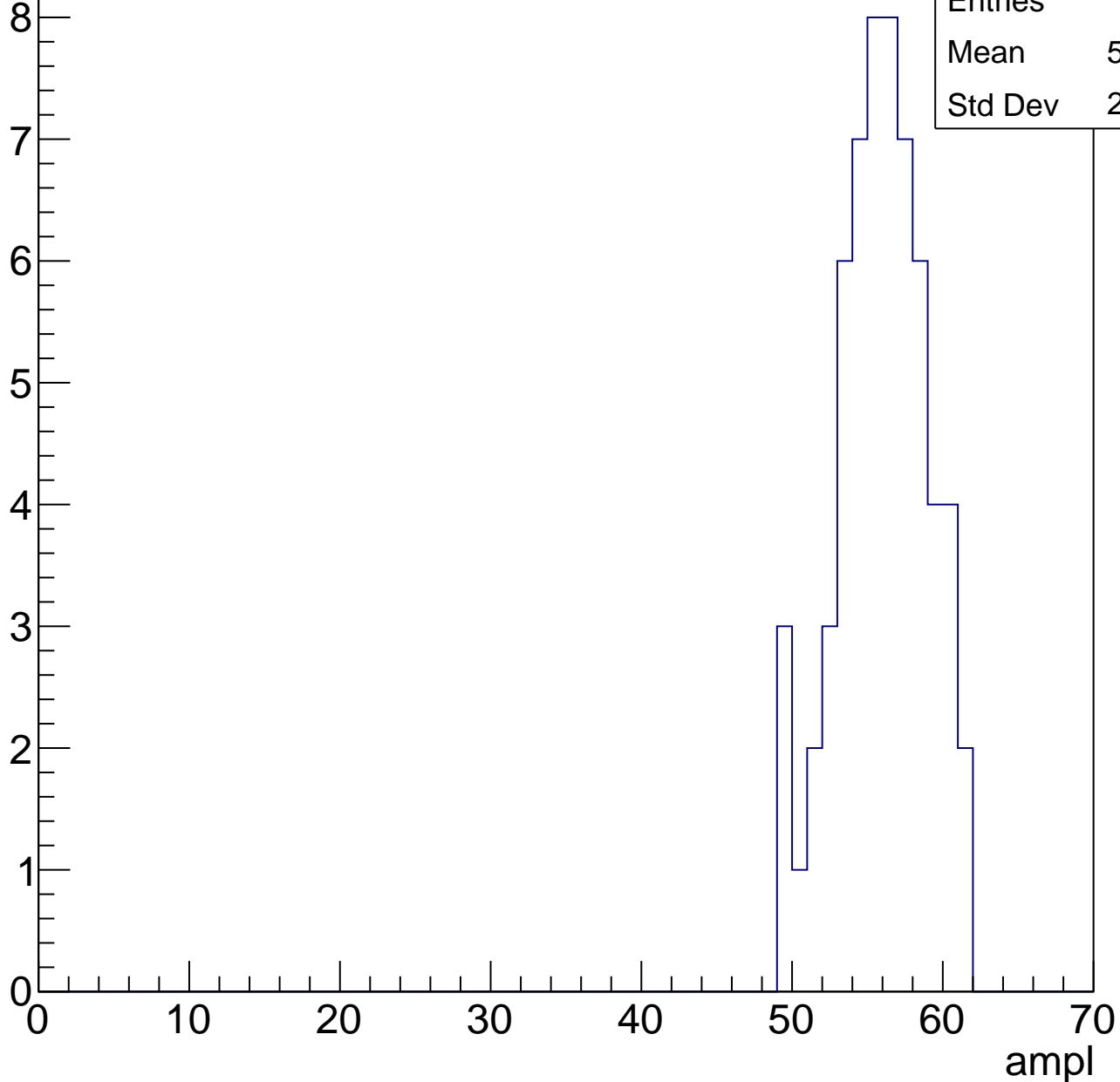
Entries	56
Mean	49.3
Std Dev	2.645

B1L102S, U6-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.48
Std Dev	2.962



B1L102S, U6-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entries	51
Mean	58.94
Std Dev	8.739

Entry

10

8

6

4

2

0

0

10

20

30

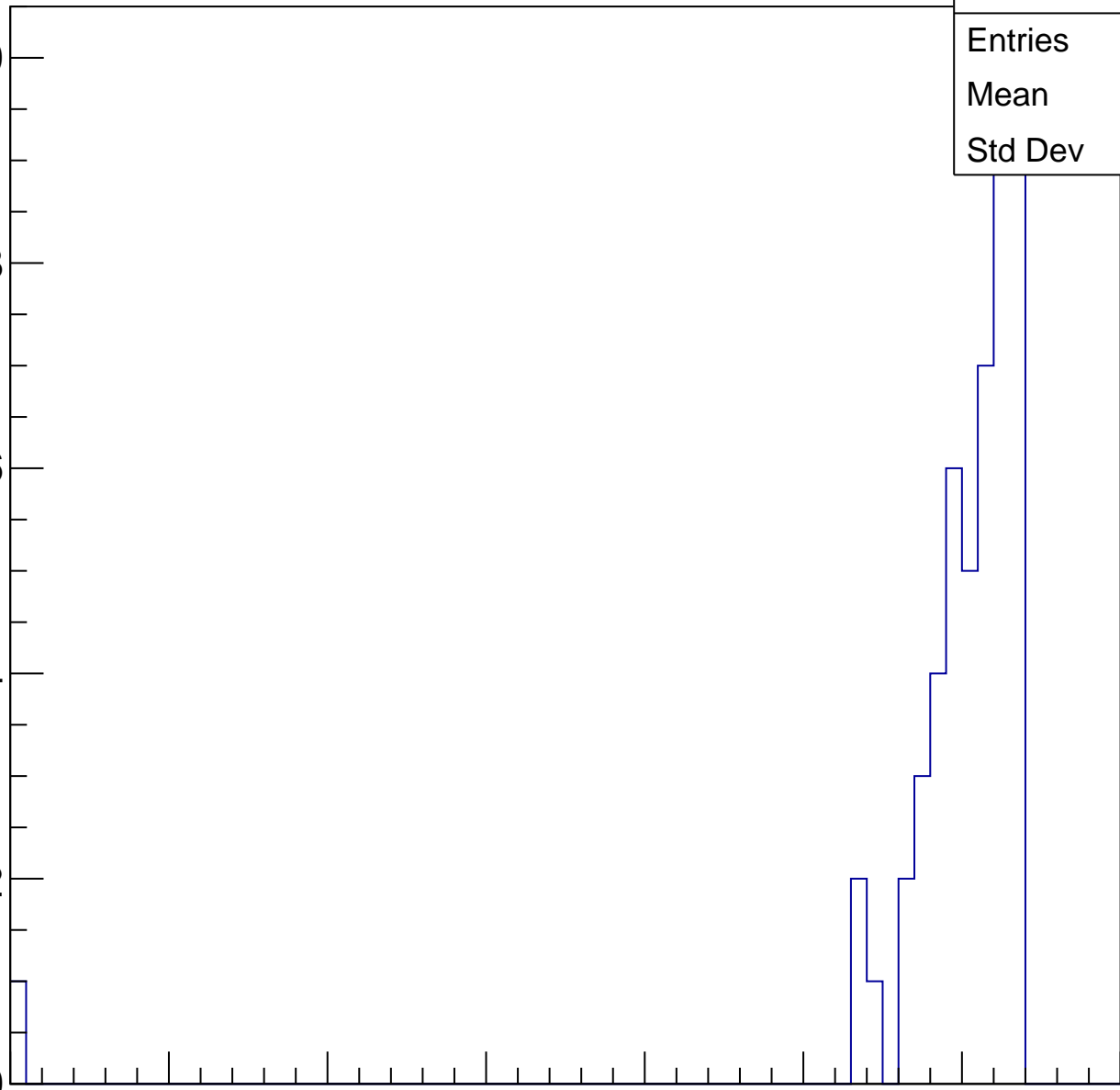
40

50

ampl

60

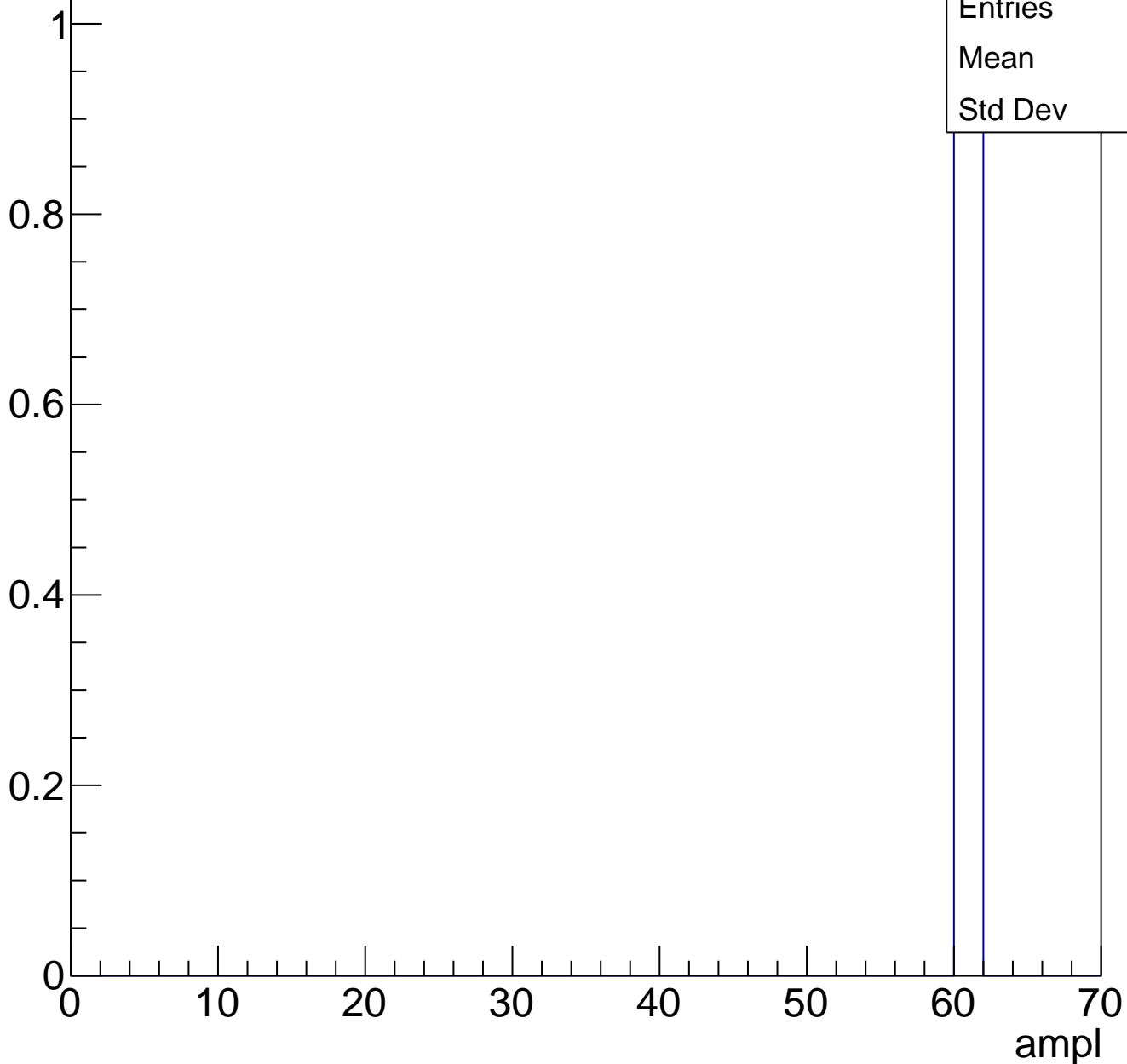
70



B1L102S, U6-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch4, adc0

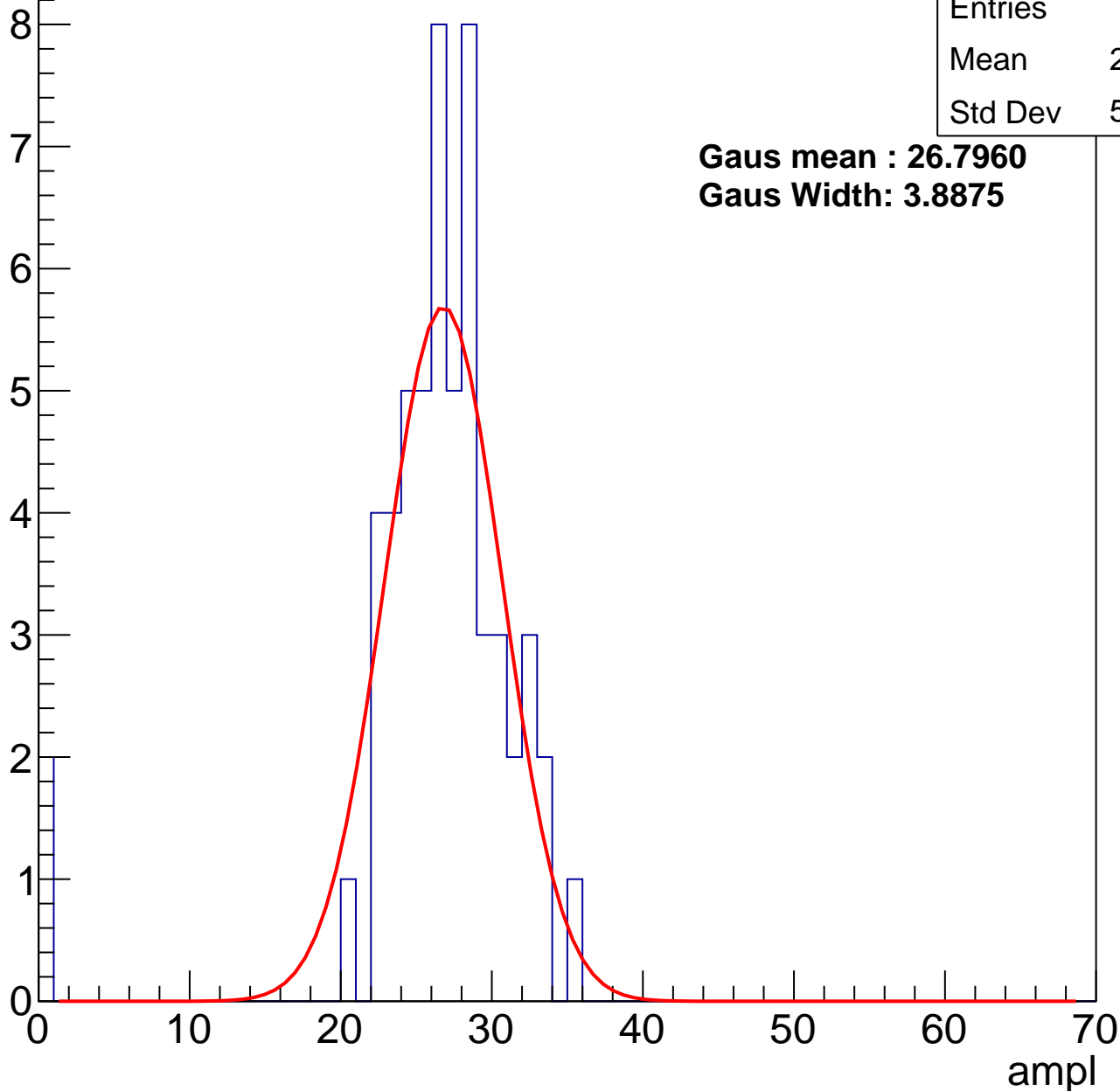
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	25.86
Std Dev	5.917

Gaus mean : 26.7960

Gaus Width: 3.8875



B1L102S, U6-ch4, adc1

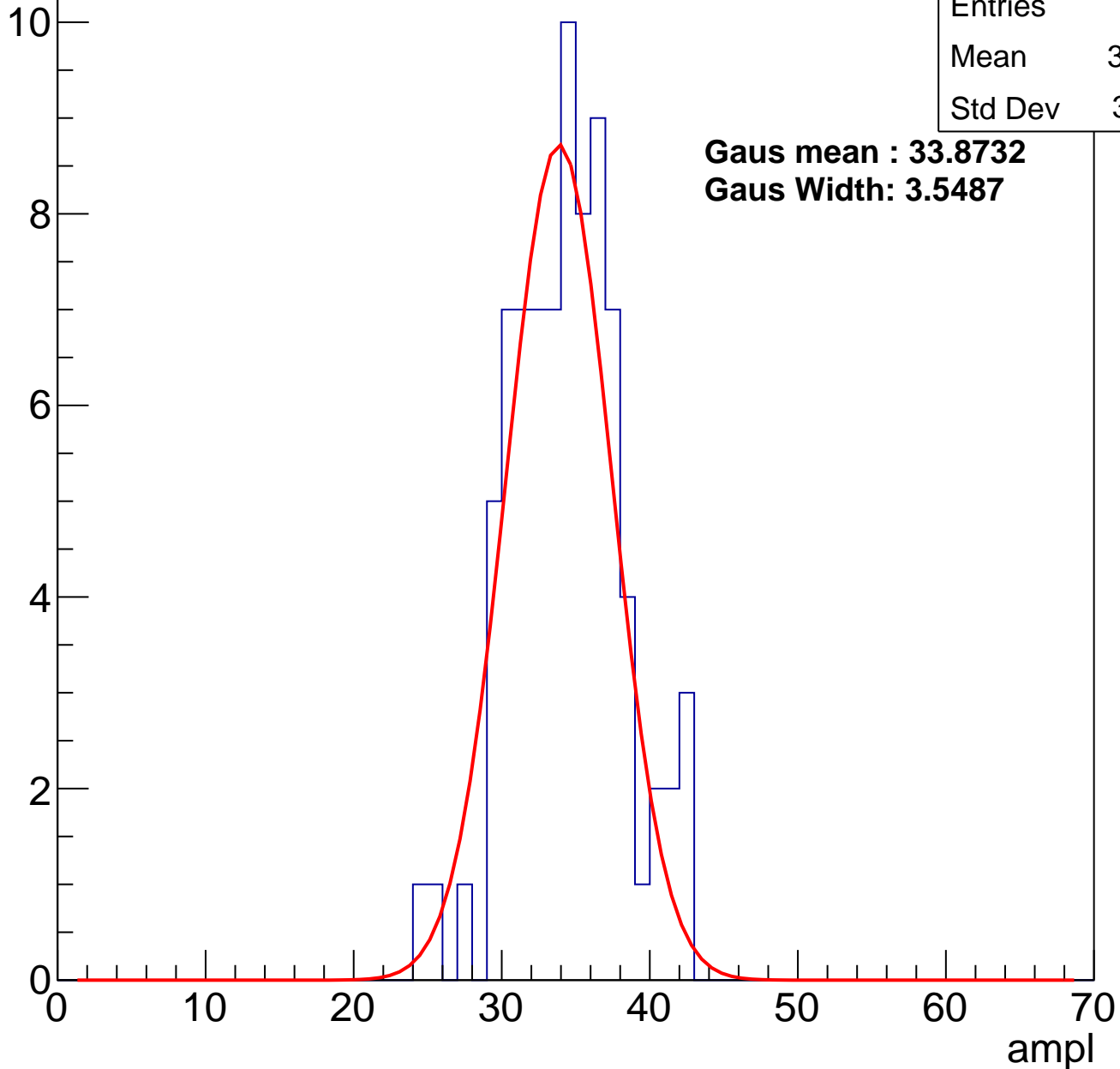
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	33.96
Std Dev	3.701

Gaus mean : 33.8732

Gaus Width: 3.5487

Entry



B1L102S, U6-ch4, adc2

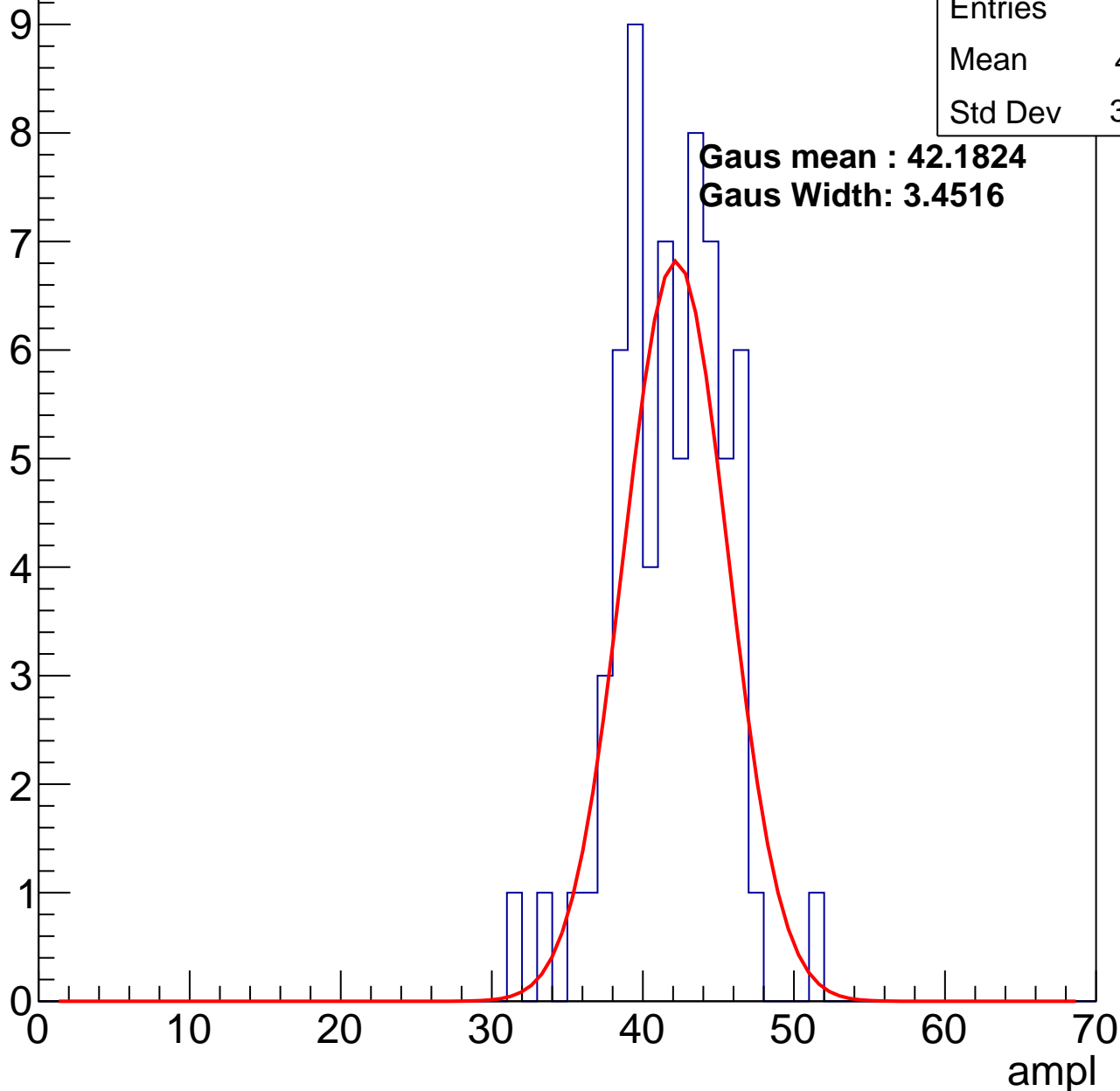
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.41
Std Dev	3.529

Gaus mean : 42.1824

Gaus Width: 3.4516

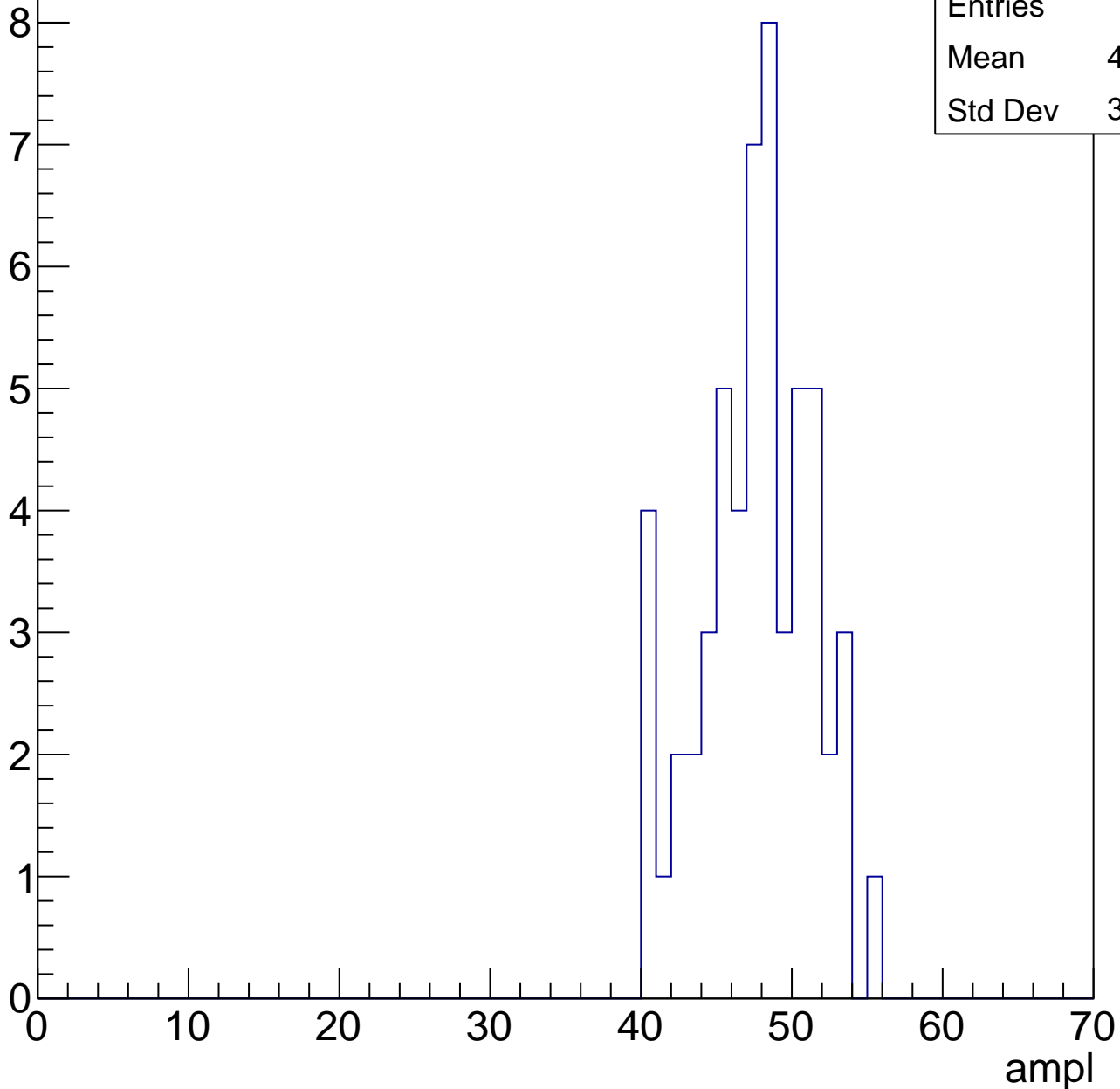


B1L102S, U6-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

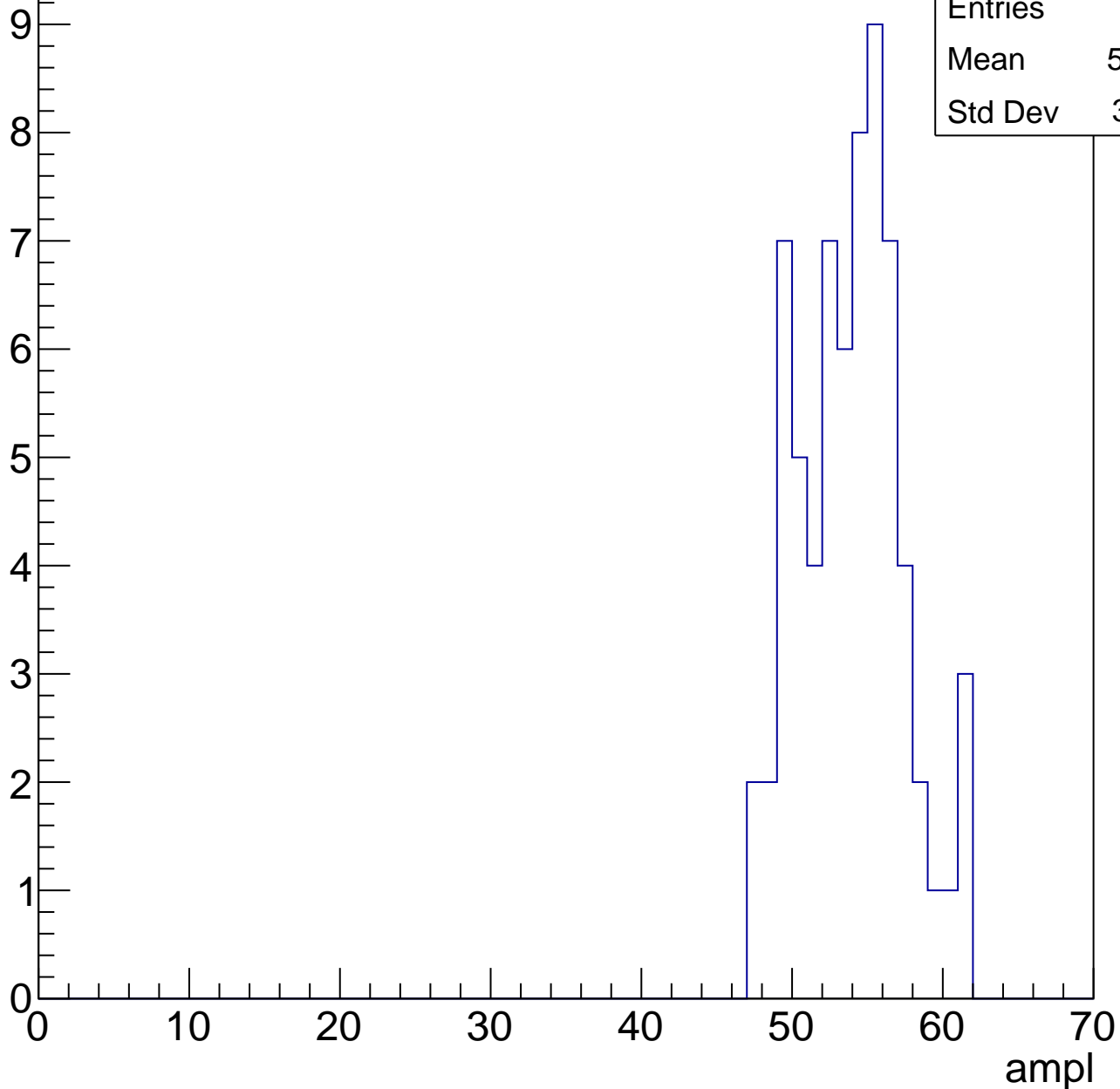
Entries	55
Mean	47.18
Std Dev	3.644



B1L102S, U6-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



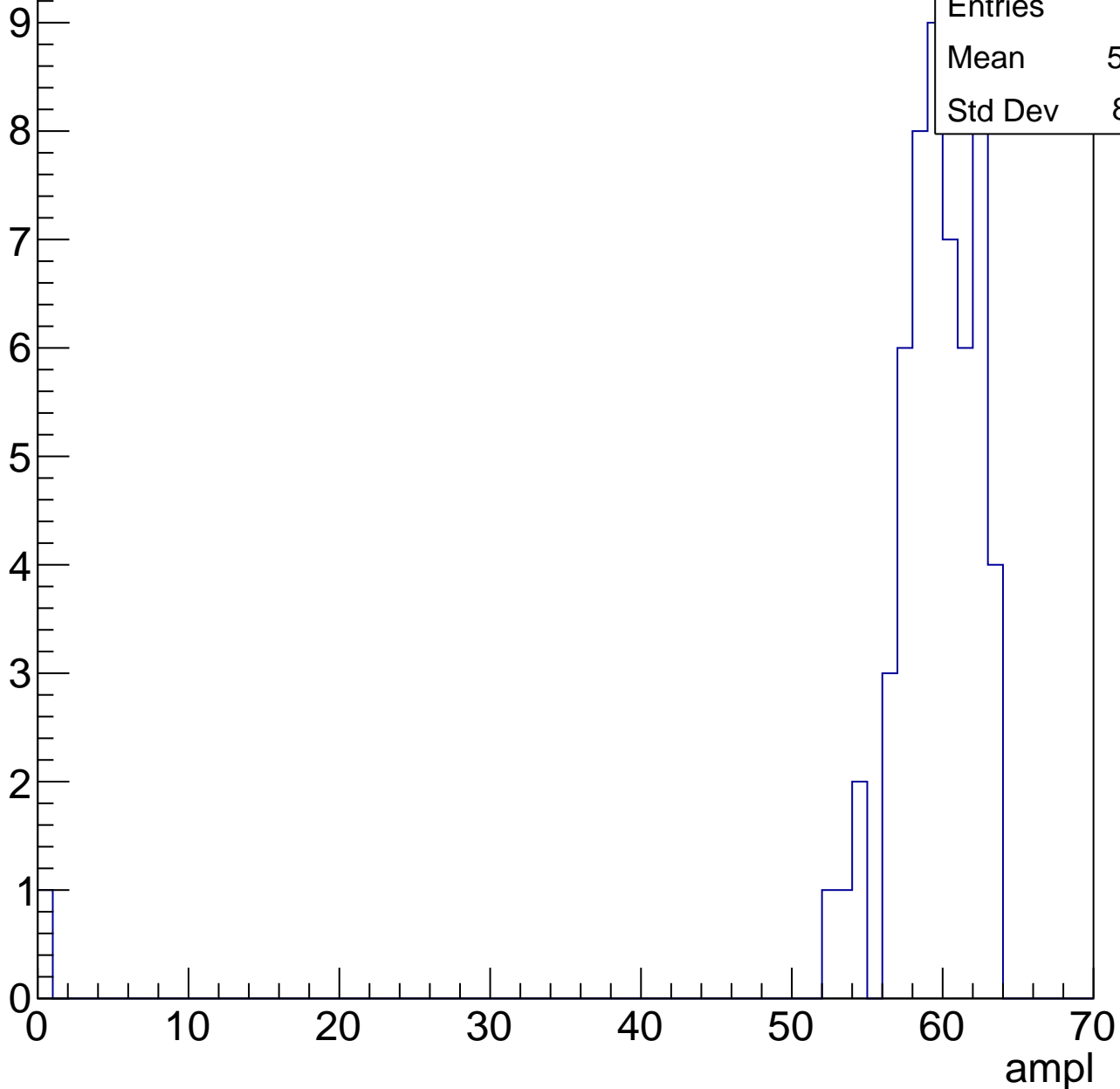
Entries	68
Mean	53.44
Std Dev	3.401

B1L102S, U6-ch4, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

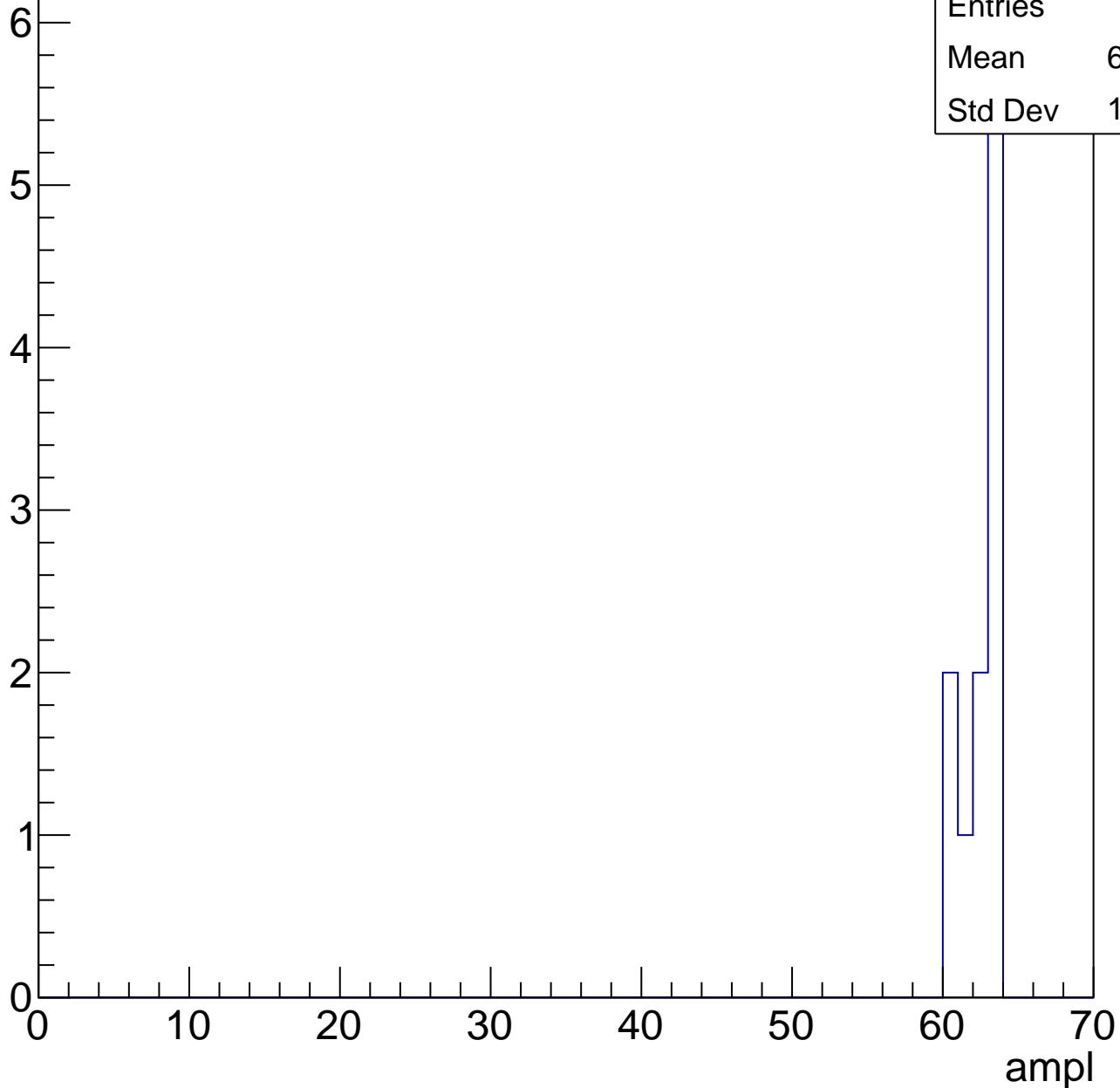
Entries	56
Mean	58.07
Std Dev	8.231



B1L102S, U6-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch5, adc0

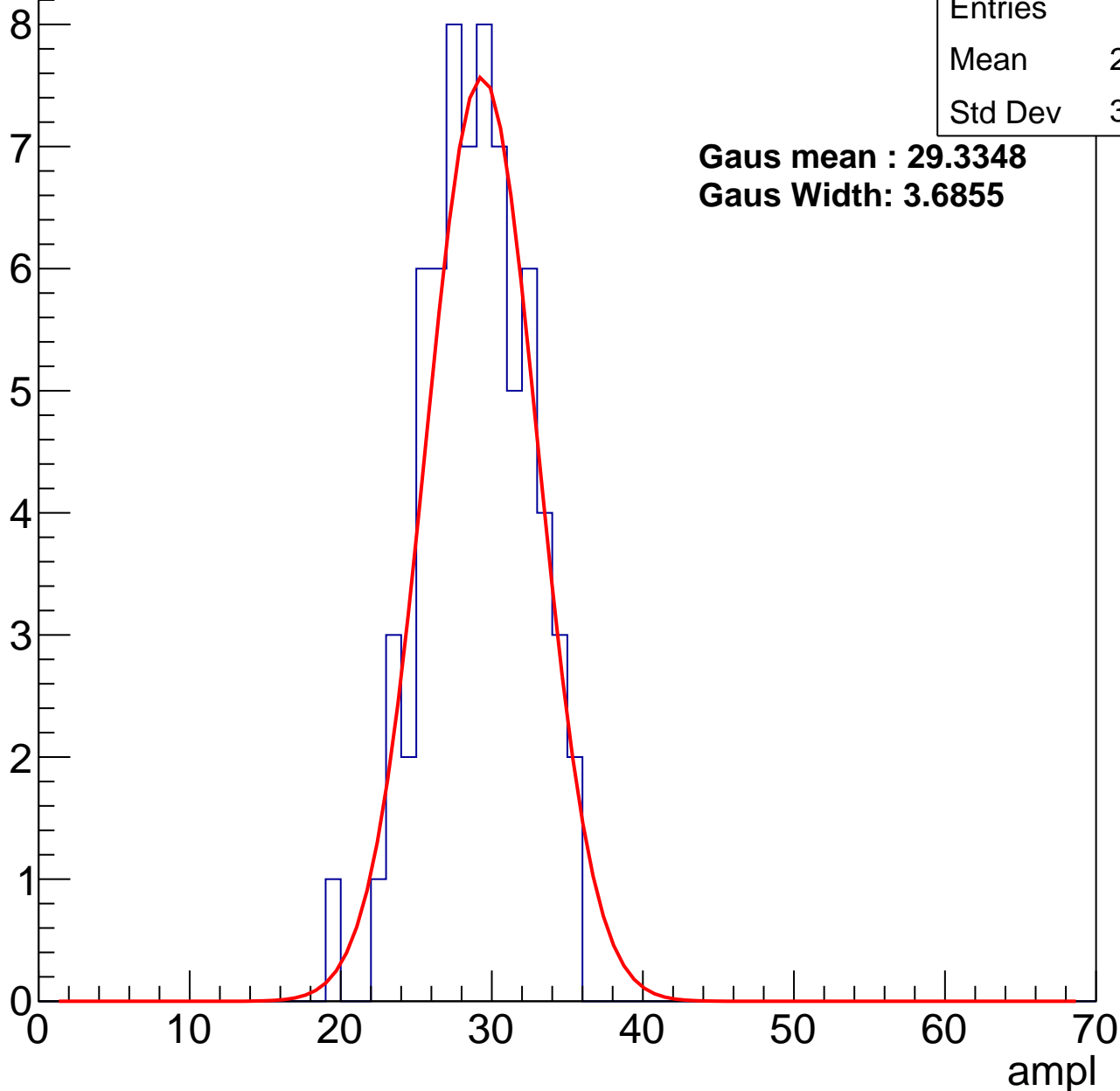
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.54
Std Dev	3.347

Gaus mean : 29.3348

Gaus Width: 3.6855



B1L102S, U6-ch5, adc1

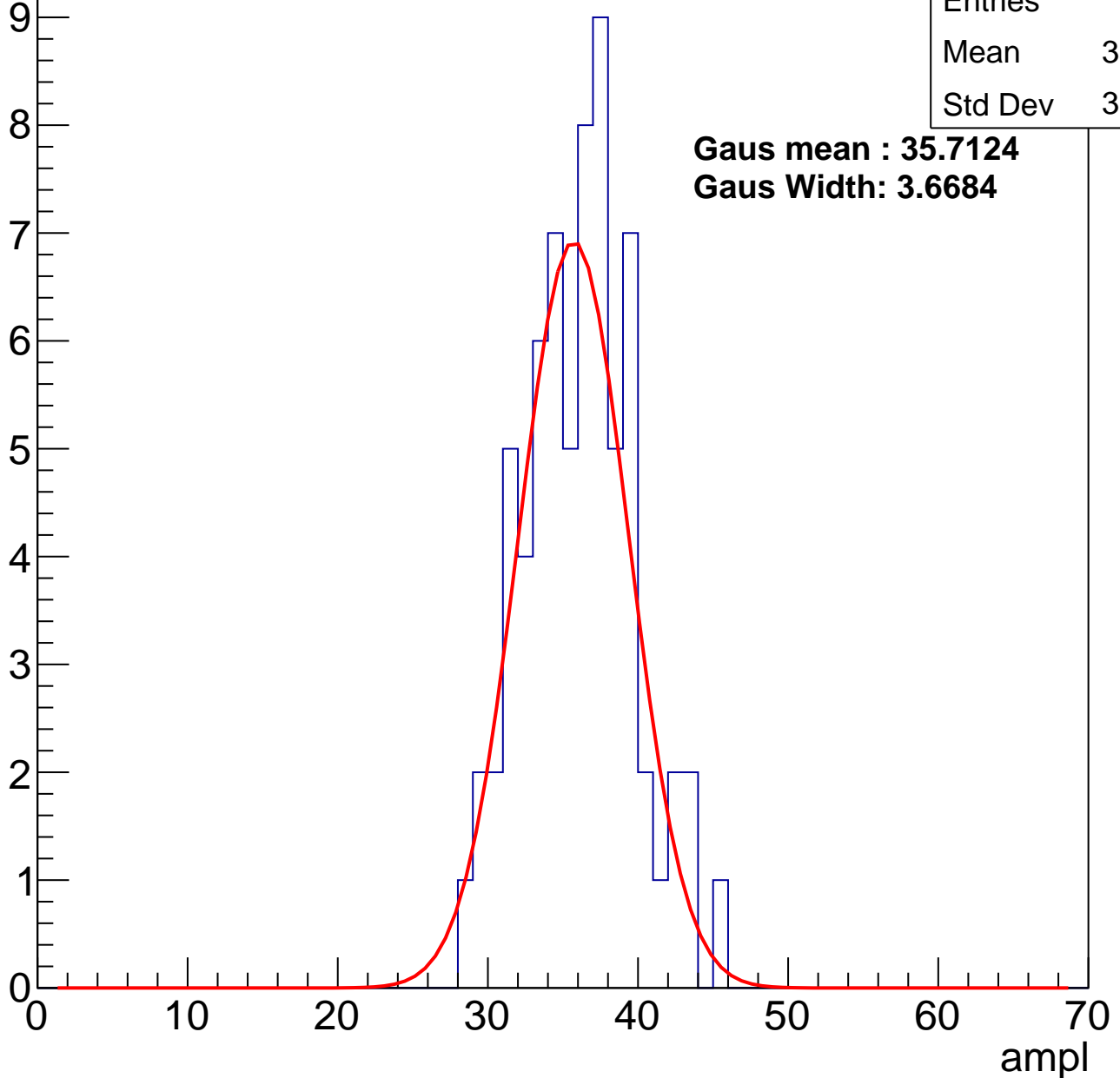
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.65
Std Dev	3.615

Gaus mean : 35.7124

Gaus Width: 3.6684



B1L102S, U6-ch5, adc2

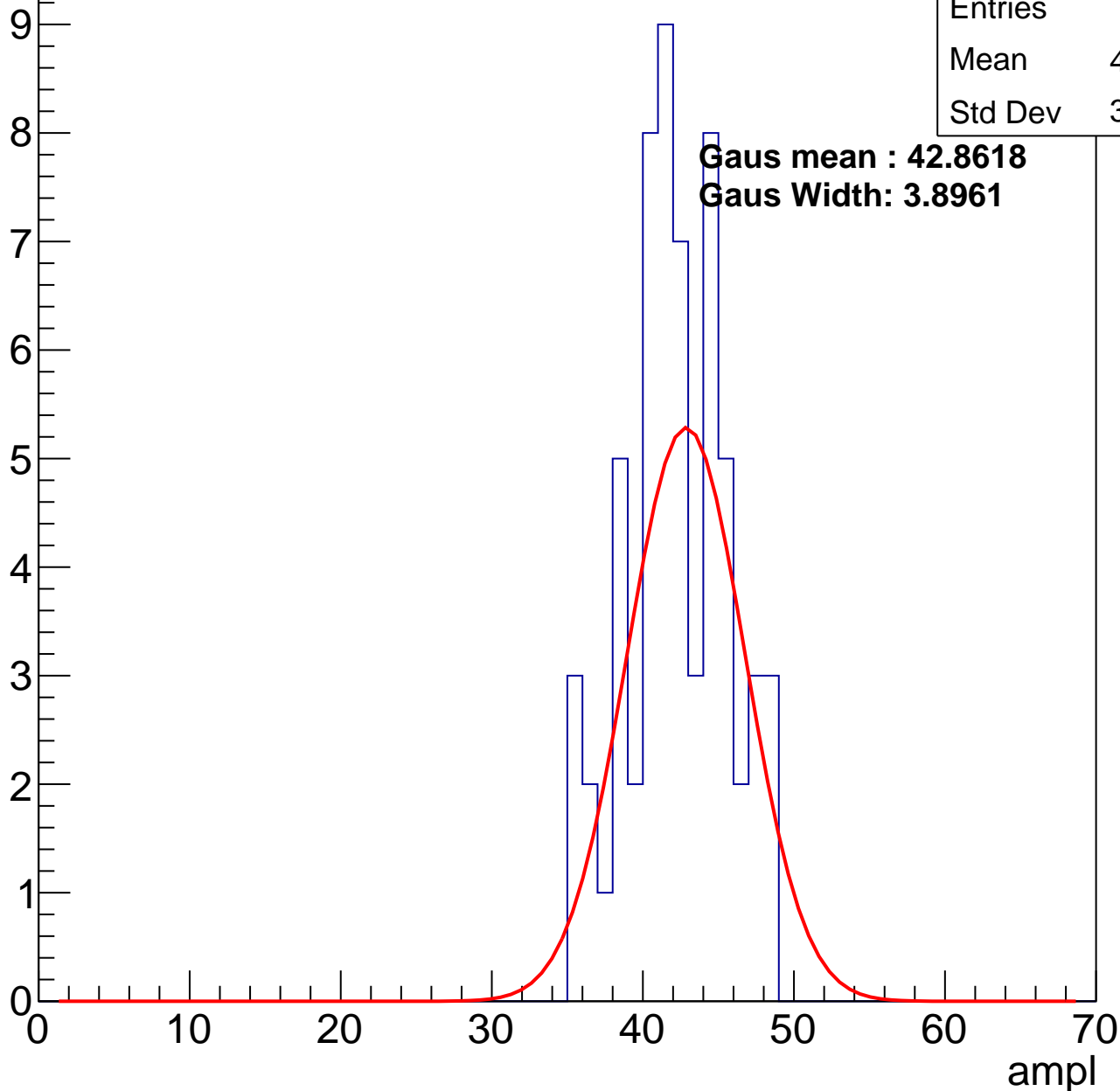
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.77
Std Dev	3.326

Gaus mean : 42.8618

Gaus Width: 3.8961

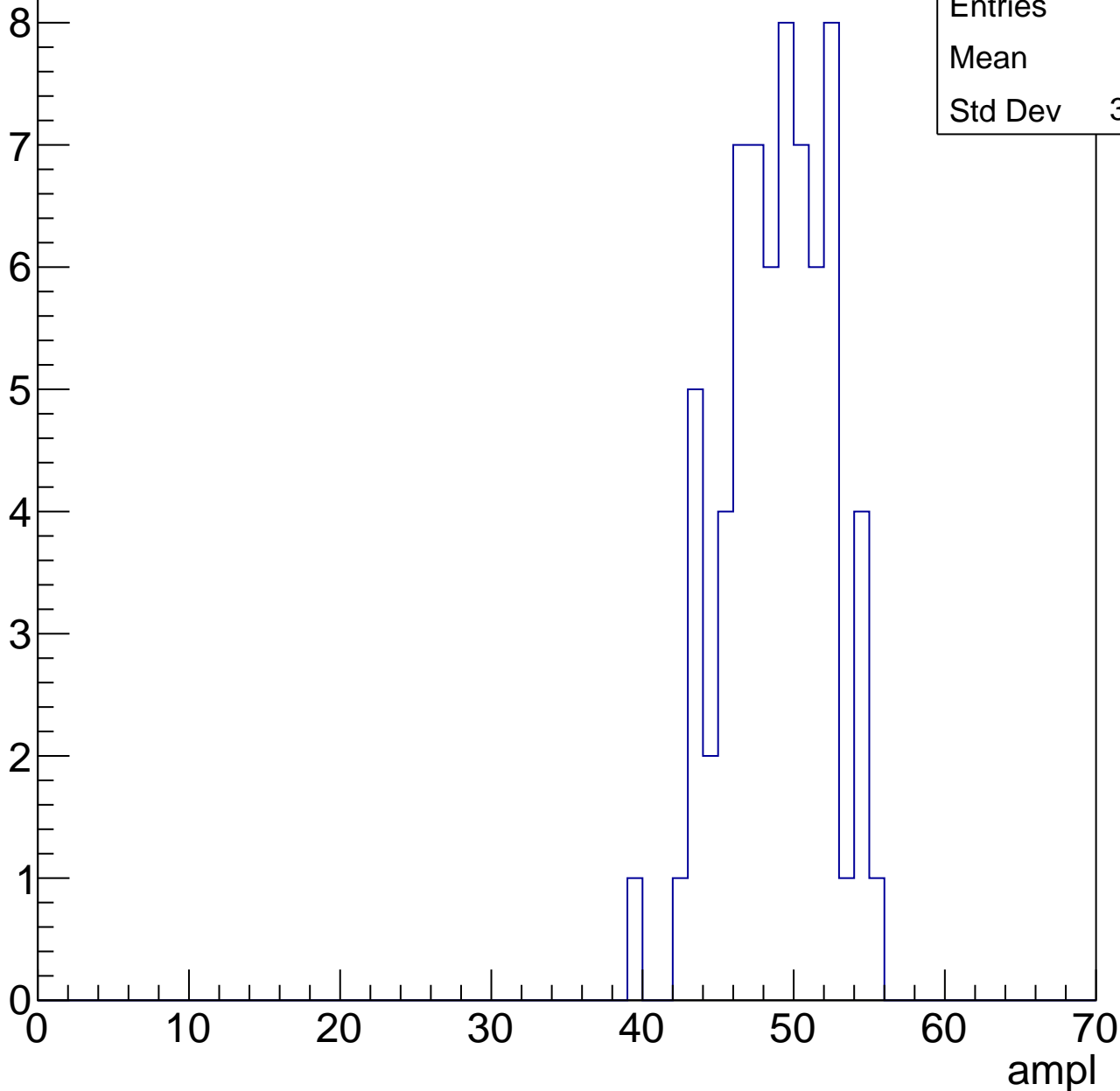


B1L102S, U6-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.4
Std Dev	3.366

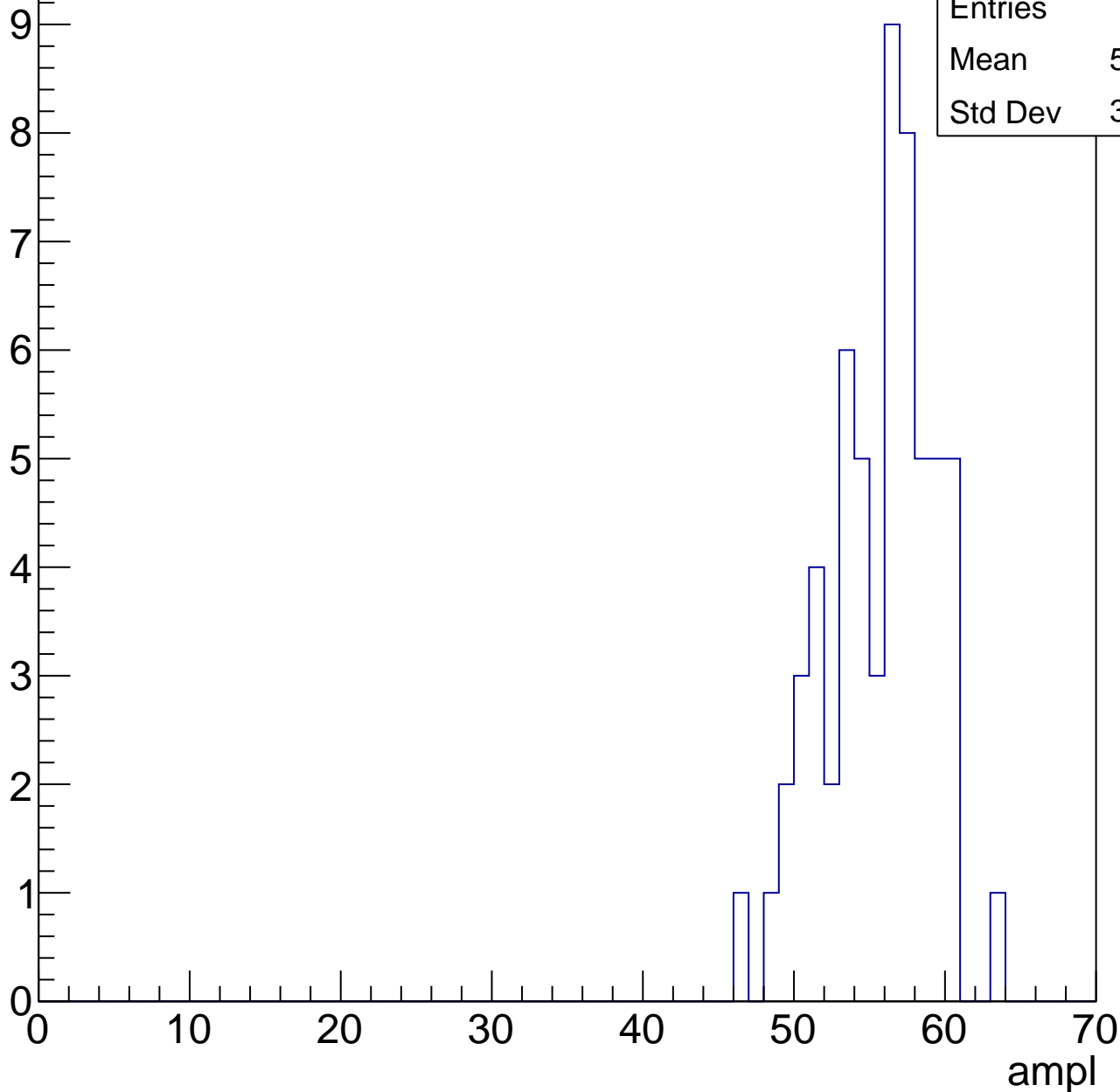


B1L102S, U6-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.18
Std Dev	3.514

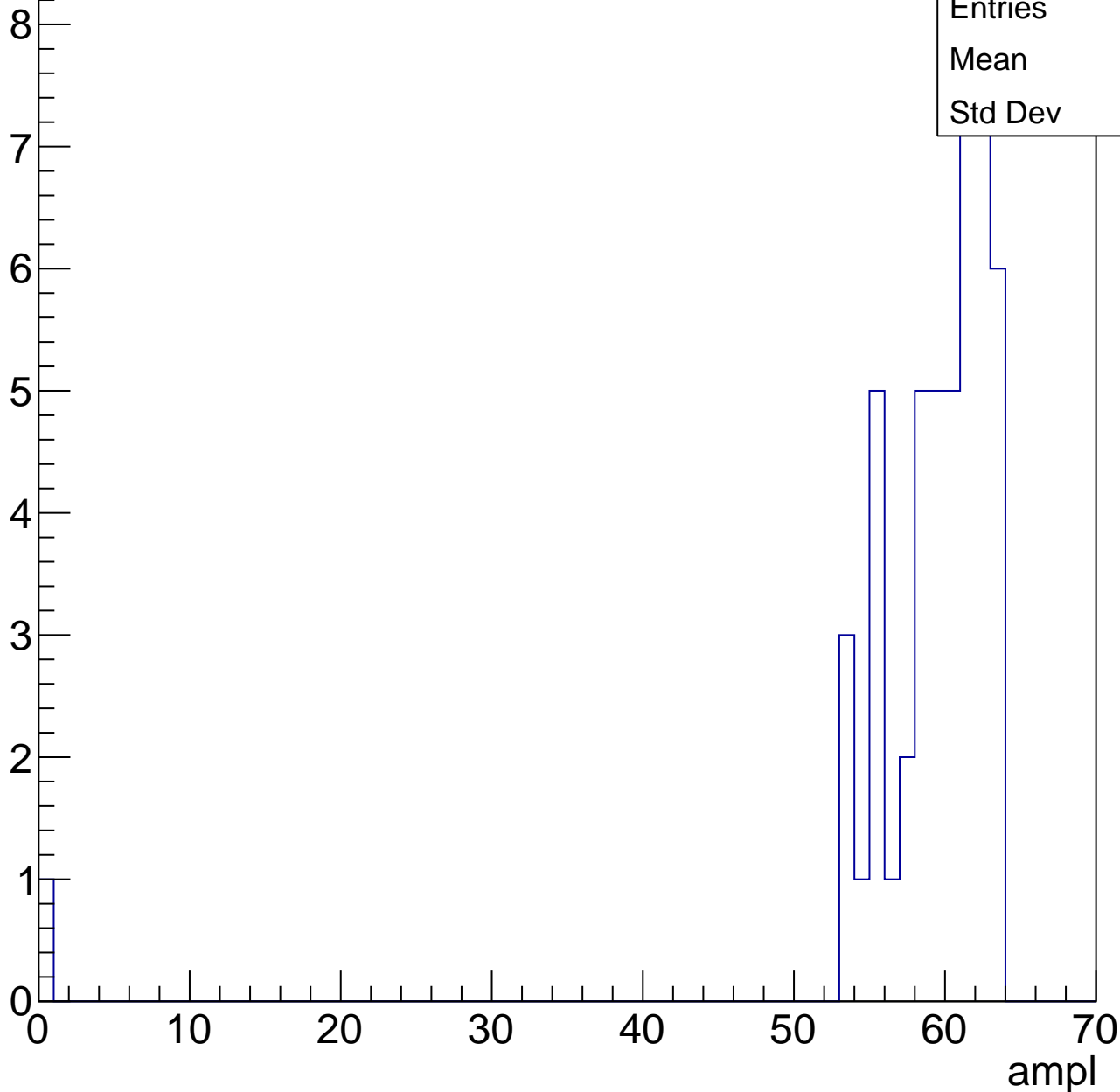


B1L102S, U6-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

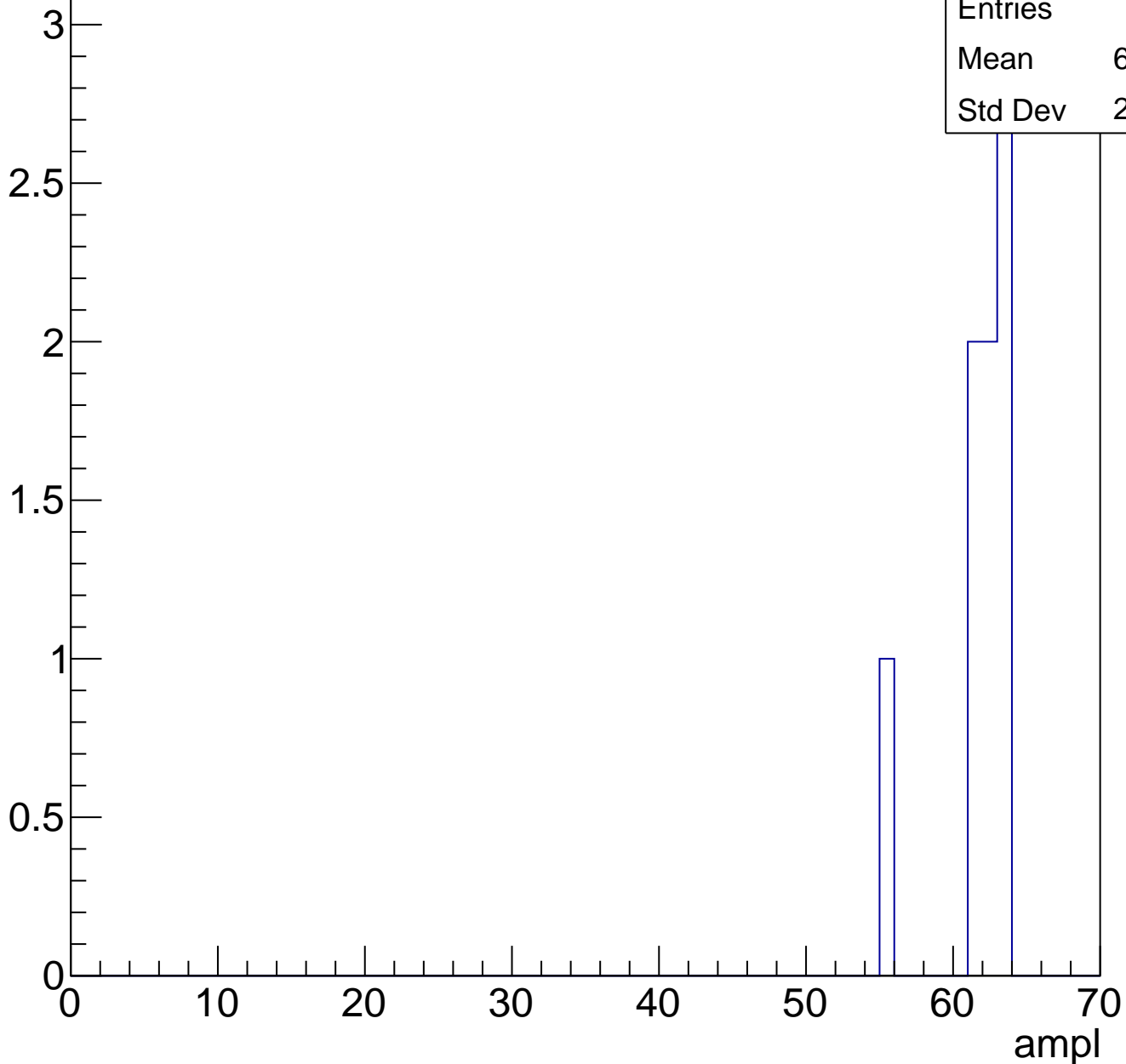
Entries	50
Mean	58.1
Std Dev	8.81



B1L102S, U6-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch6, adc0

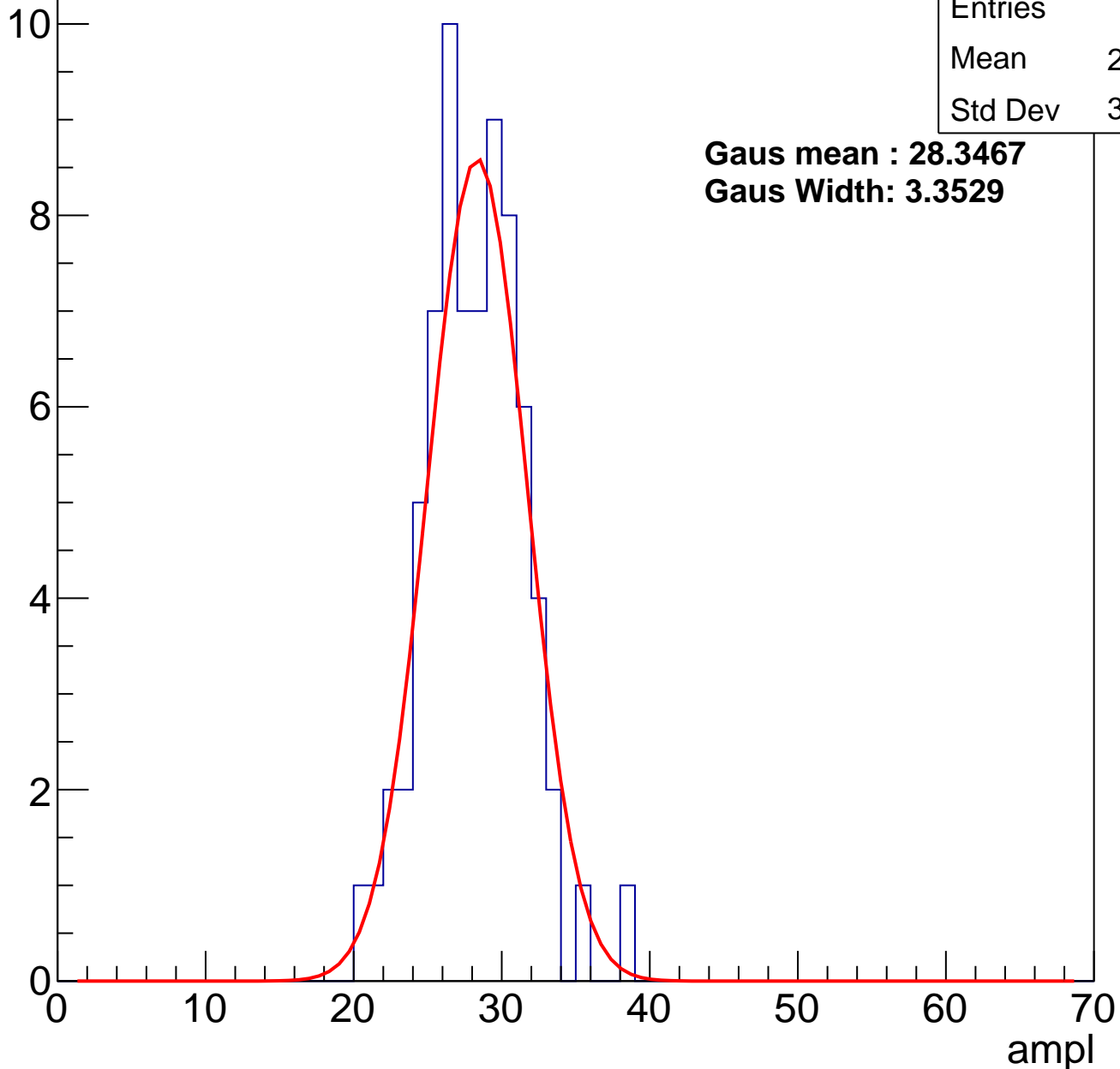
calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	27.74
Std Dev	3.265

Gaus mean : 28.3467

Gaus Width: 3.3529

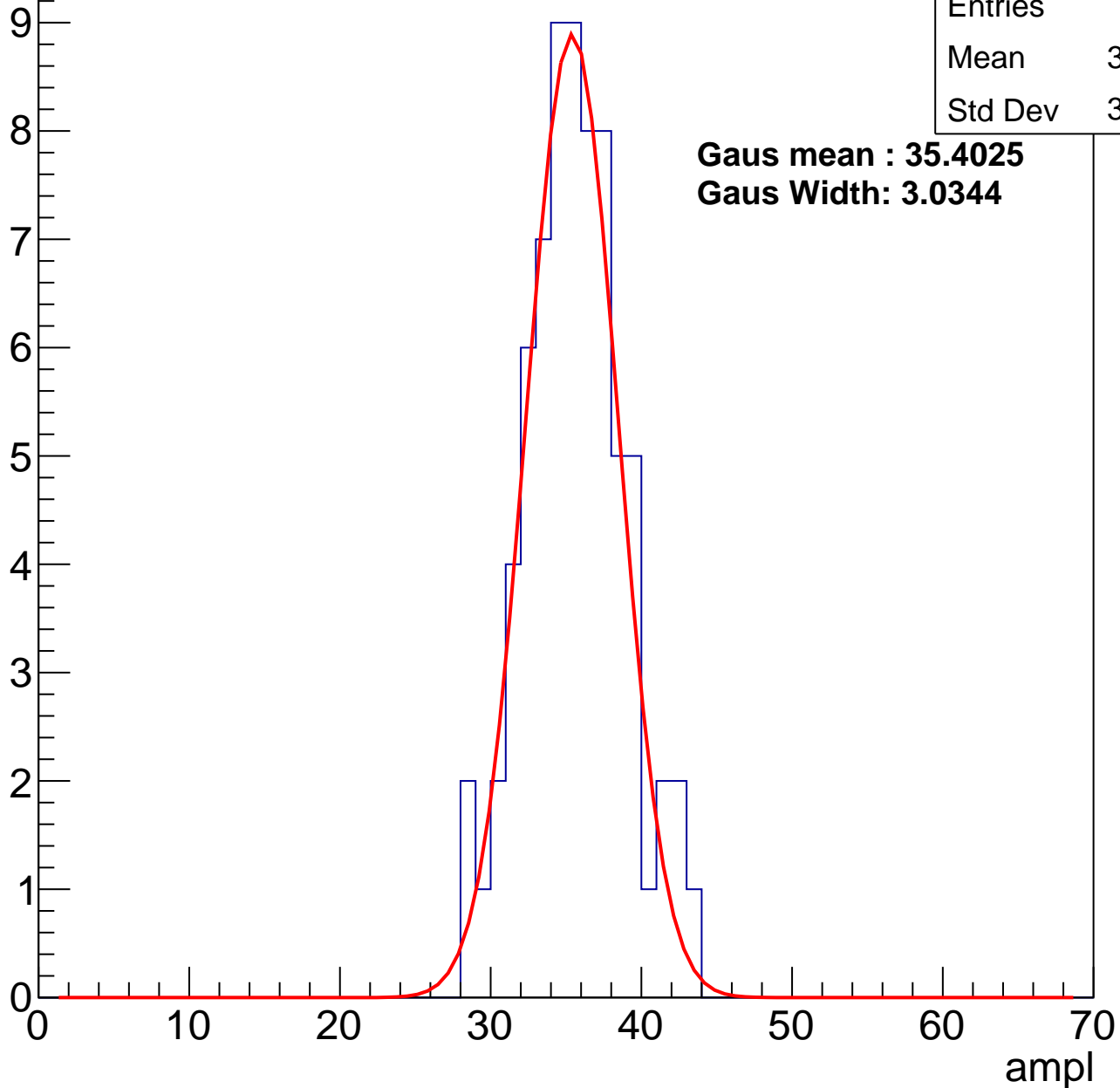
Entry



B1L102S, U6-ch6, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



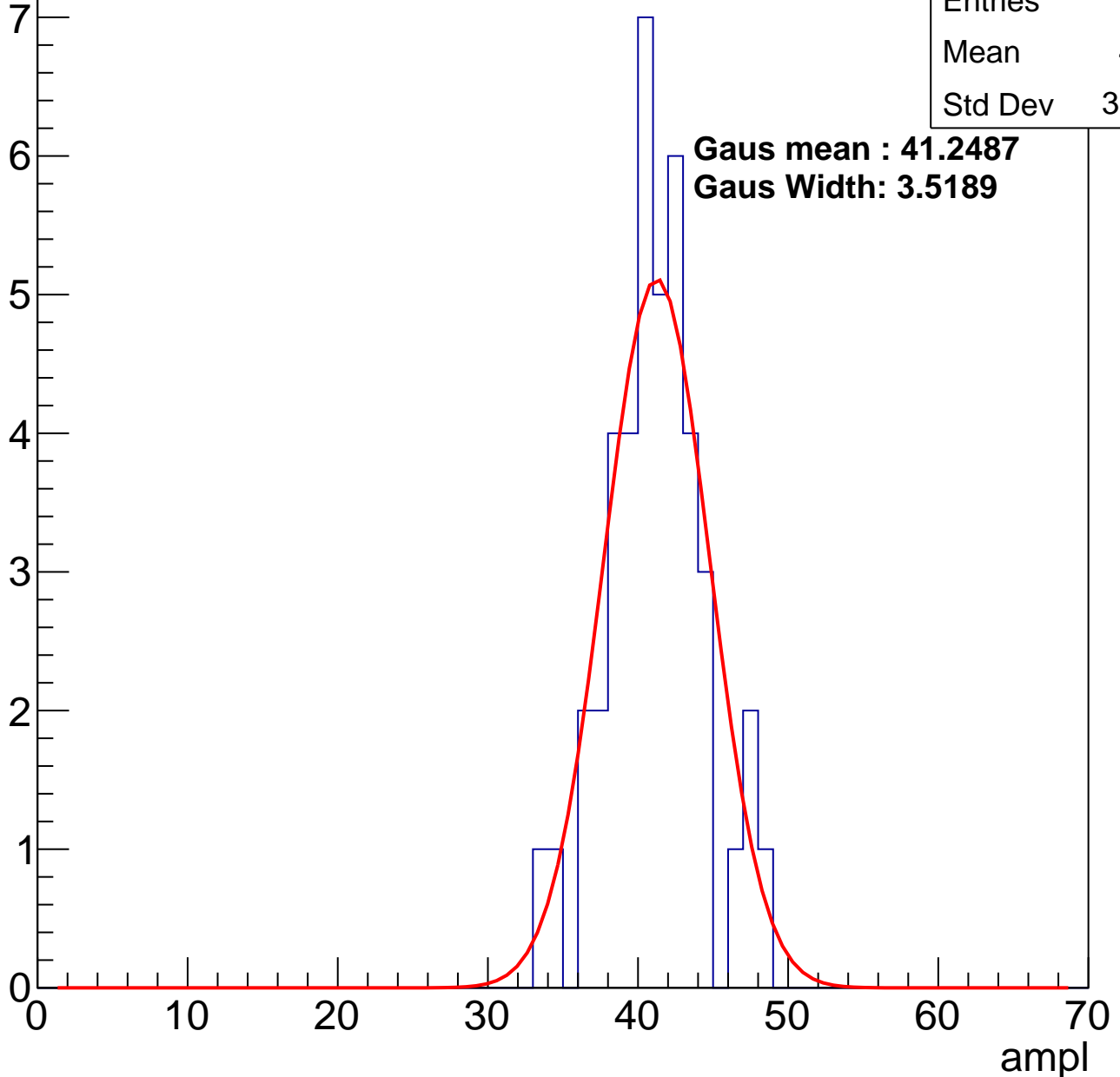
B1L102S, U6-ch6, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	40.7
Std Dev	3.217

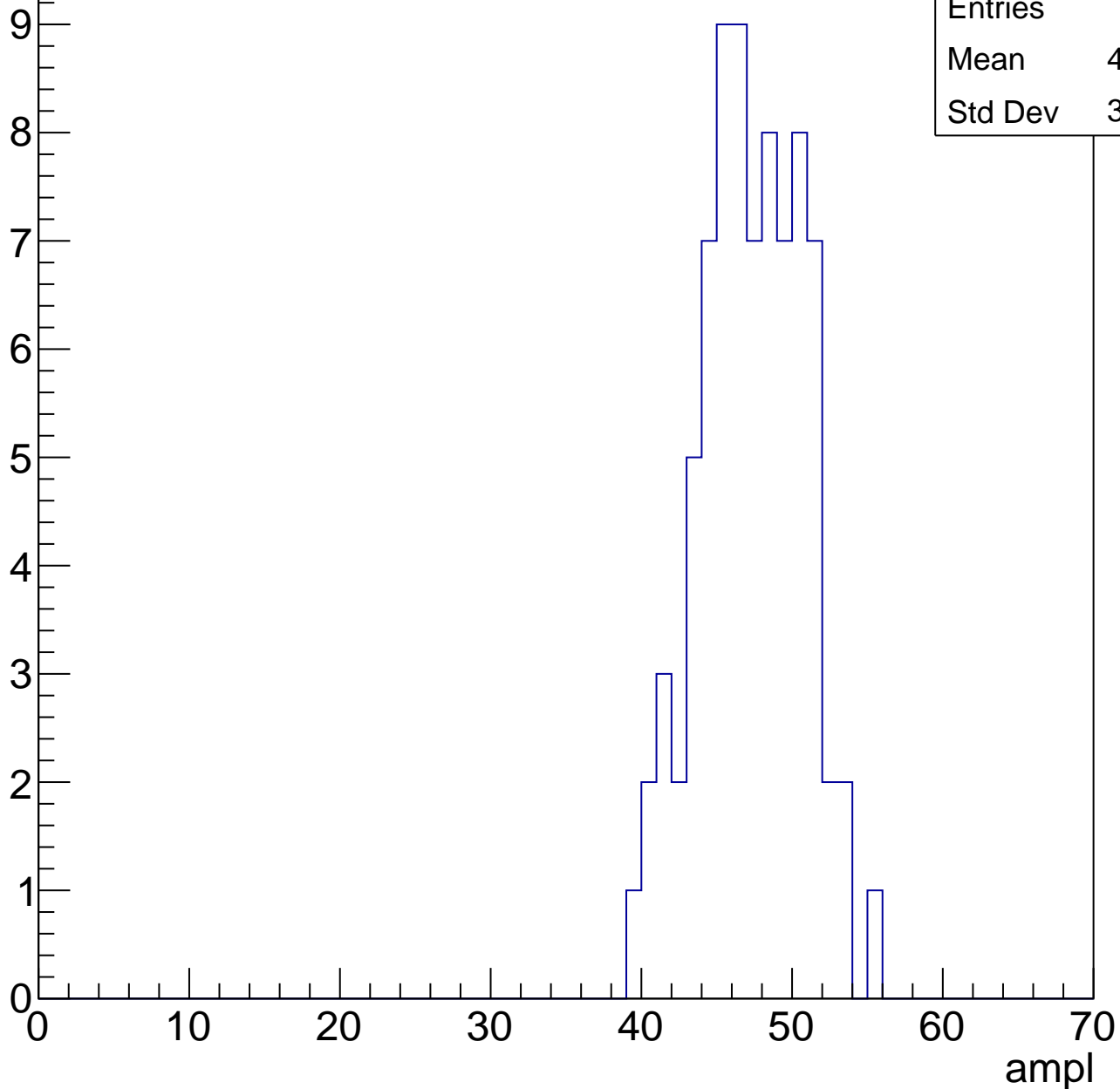
Gaus mean : 41.2487
Gaus Width: 3.5189



B1L102S, U6-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



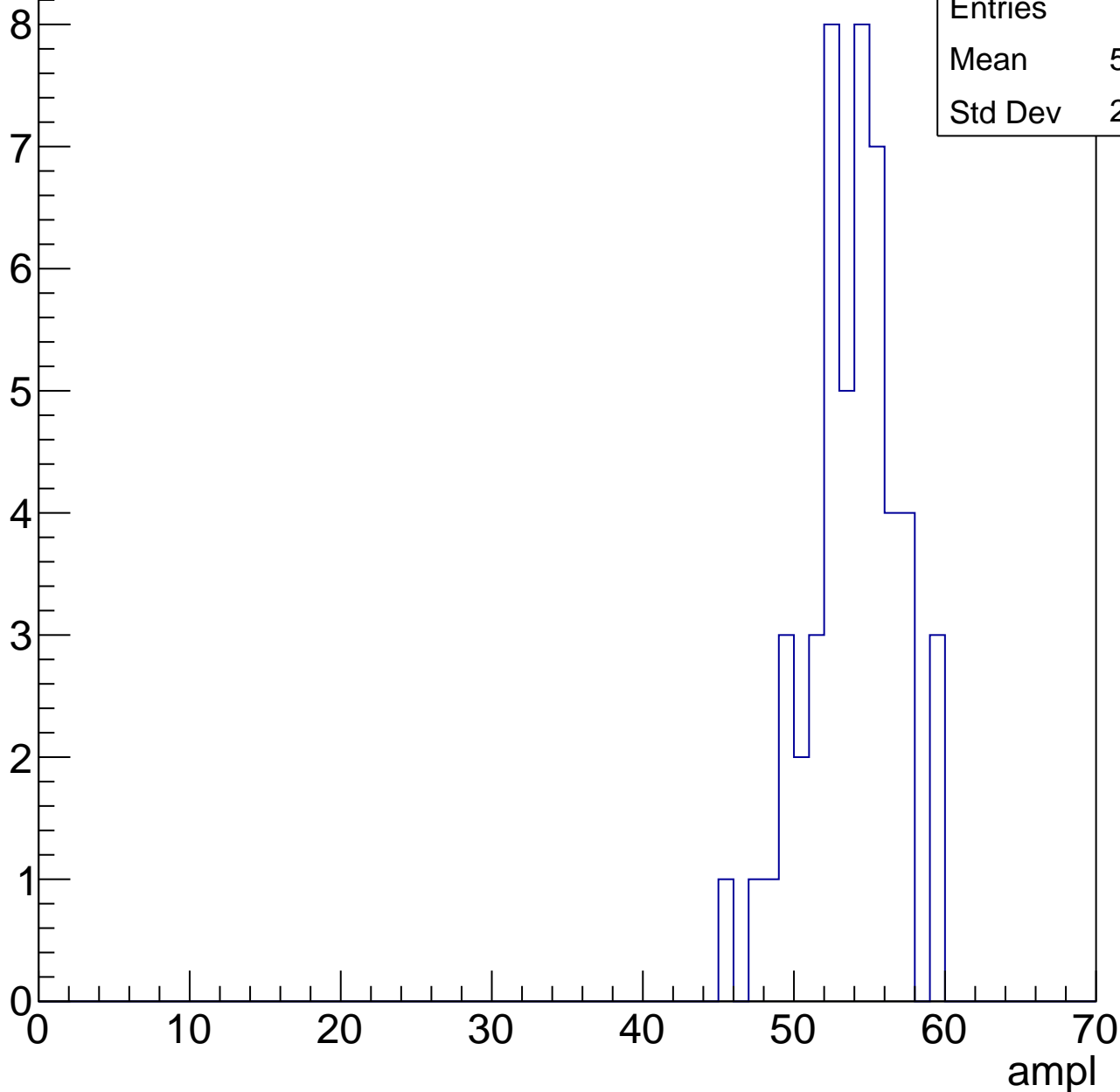
Entries	80
Mean	46.83
Std Dev	3.376

B1L102S, U6-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	53.34
Std Dev	2.997



B1L102S, U6-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

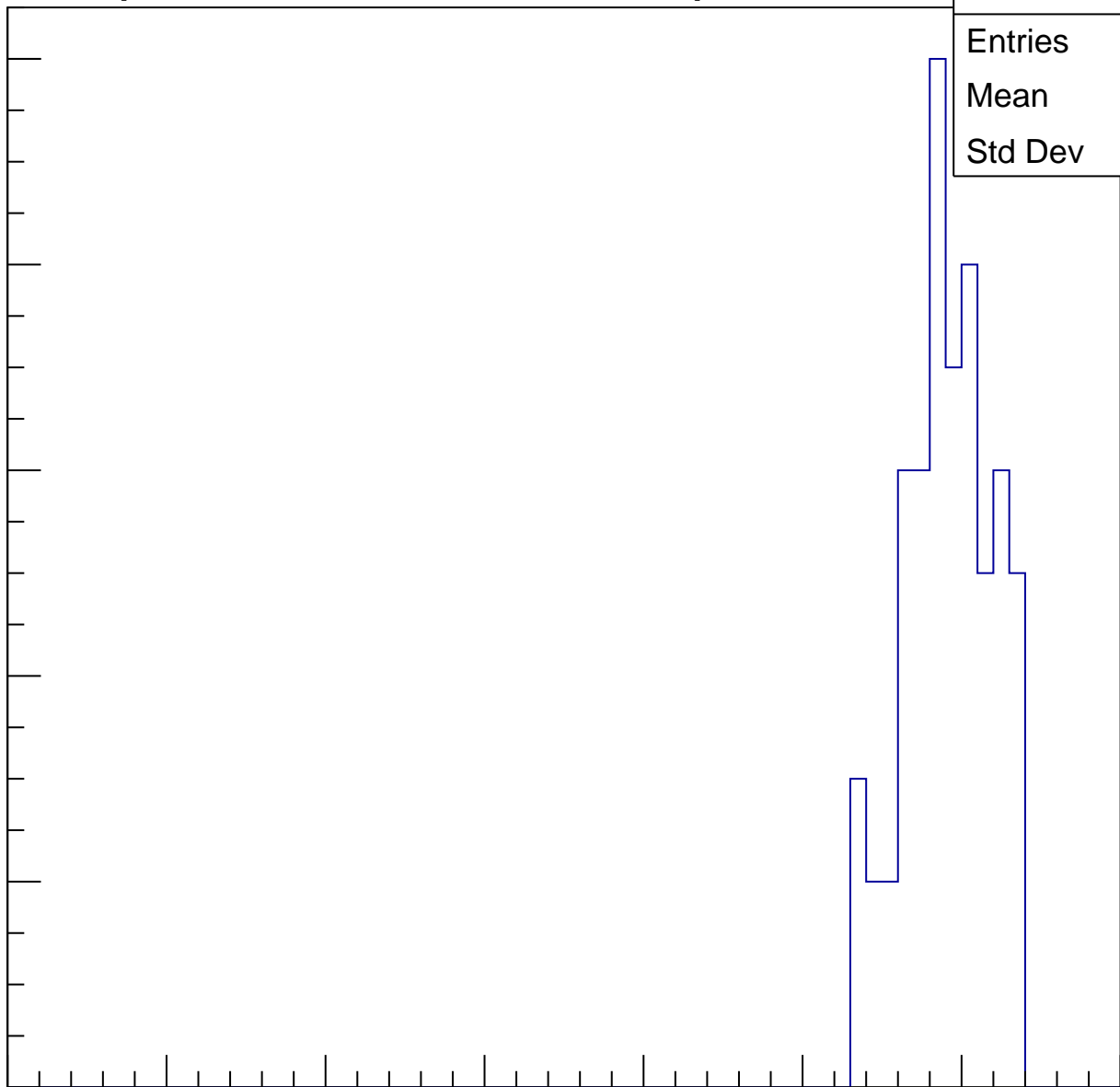
Entries	60
Mean	58.67
Std Dev	2.687

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

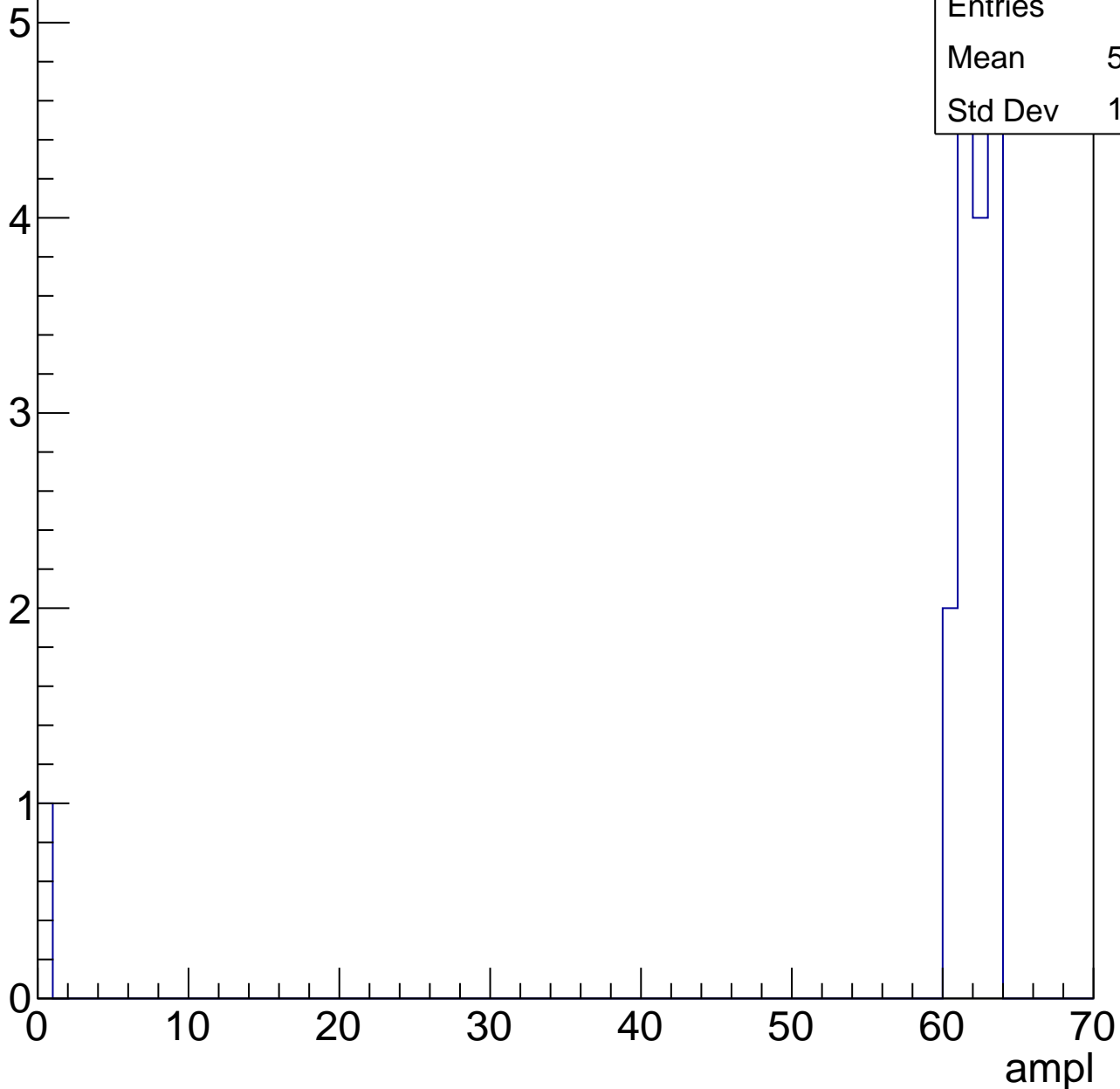


B1L102S, U6-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

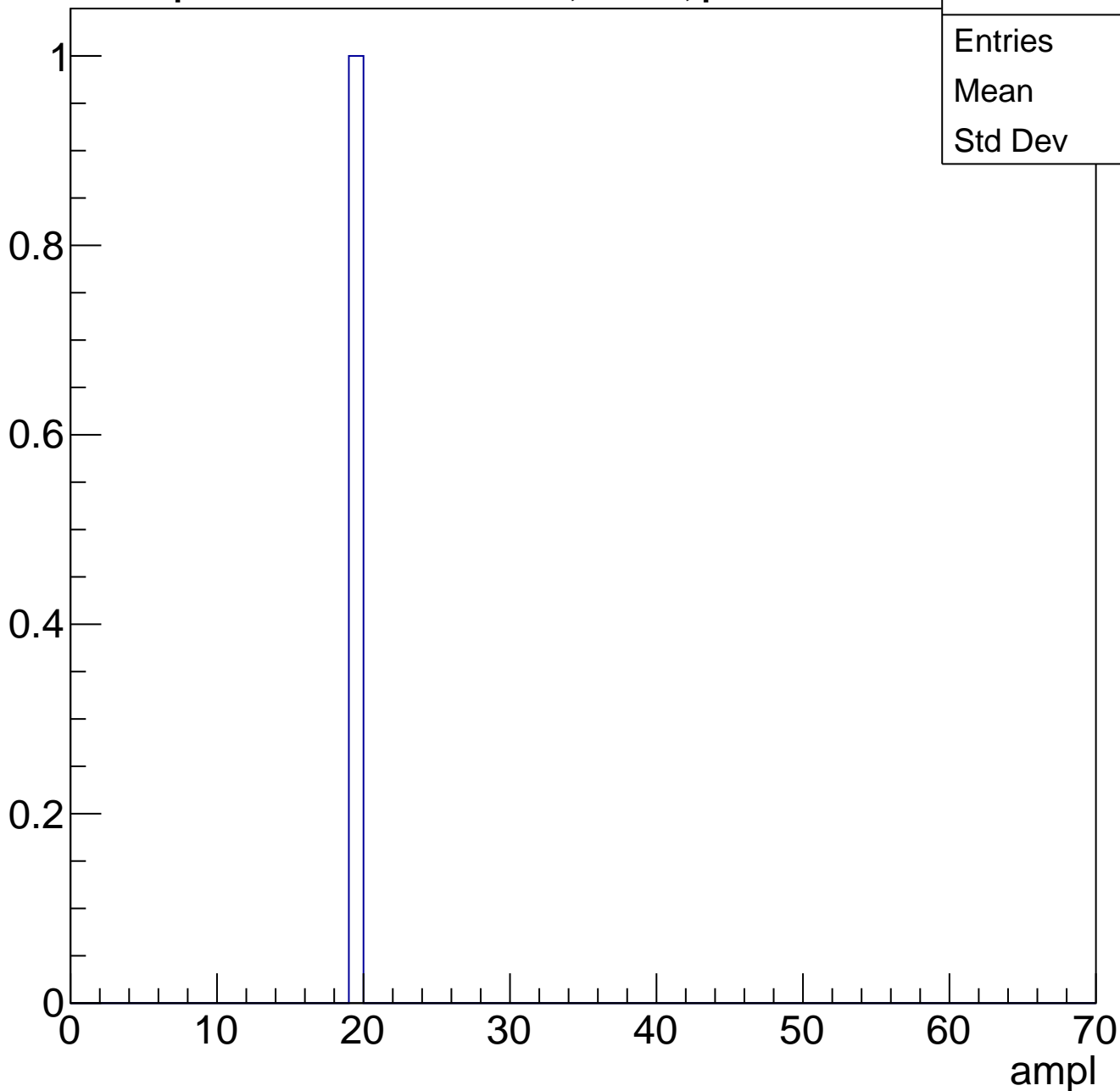
Entries	17
Mean	58.12
Std Dev	14.56



B1L102S, U6-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	19
Std Dev	0

B1L102S, U6-ch7, adc0

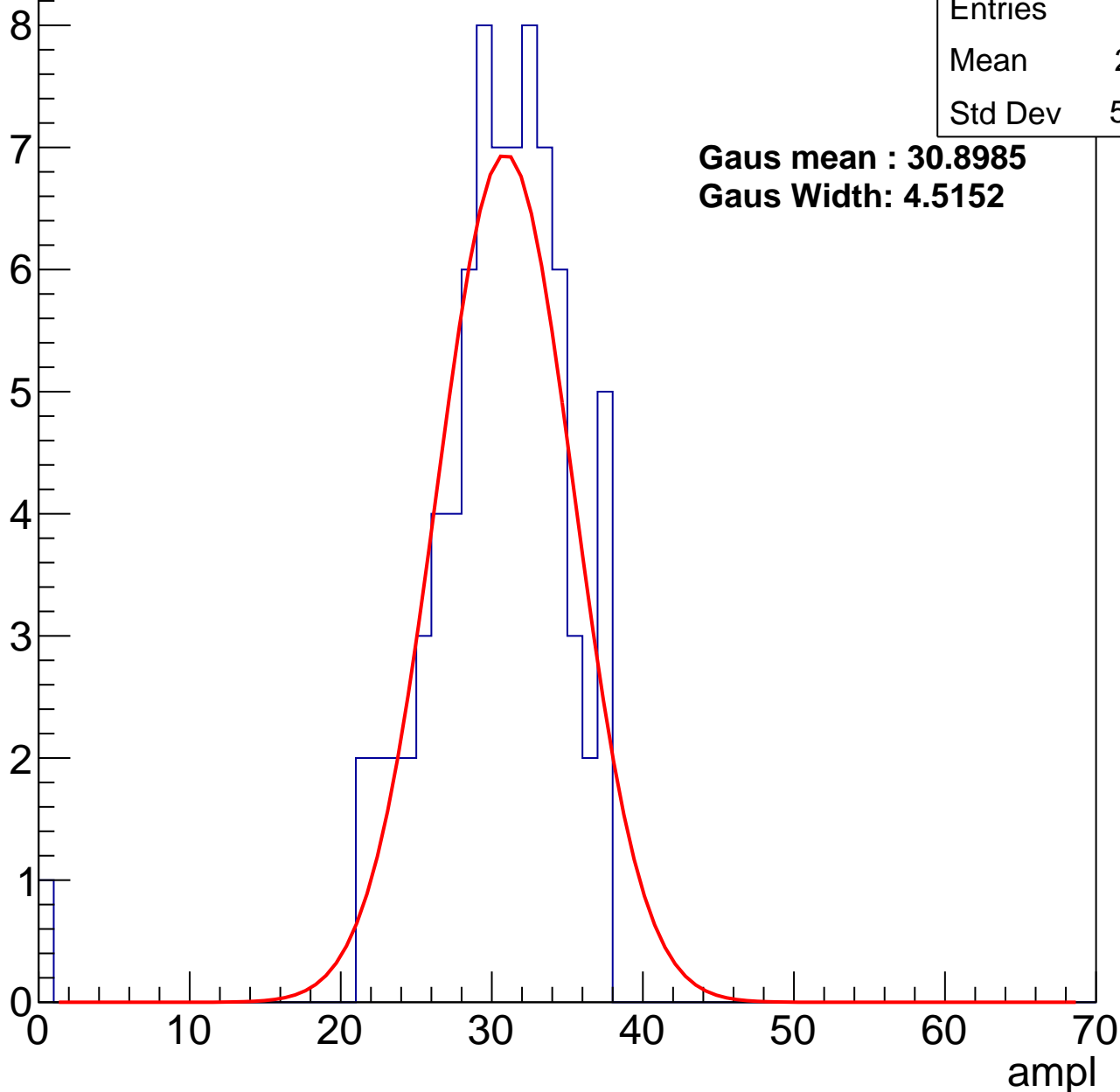
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	29.71
Std Dev	5.217

Gaus mean : 30.8985

Gaus Width: 4.5152



B1L102S, U6-ch7, adc1

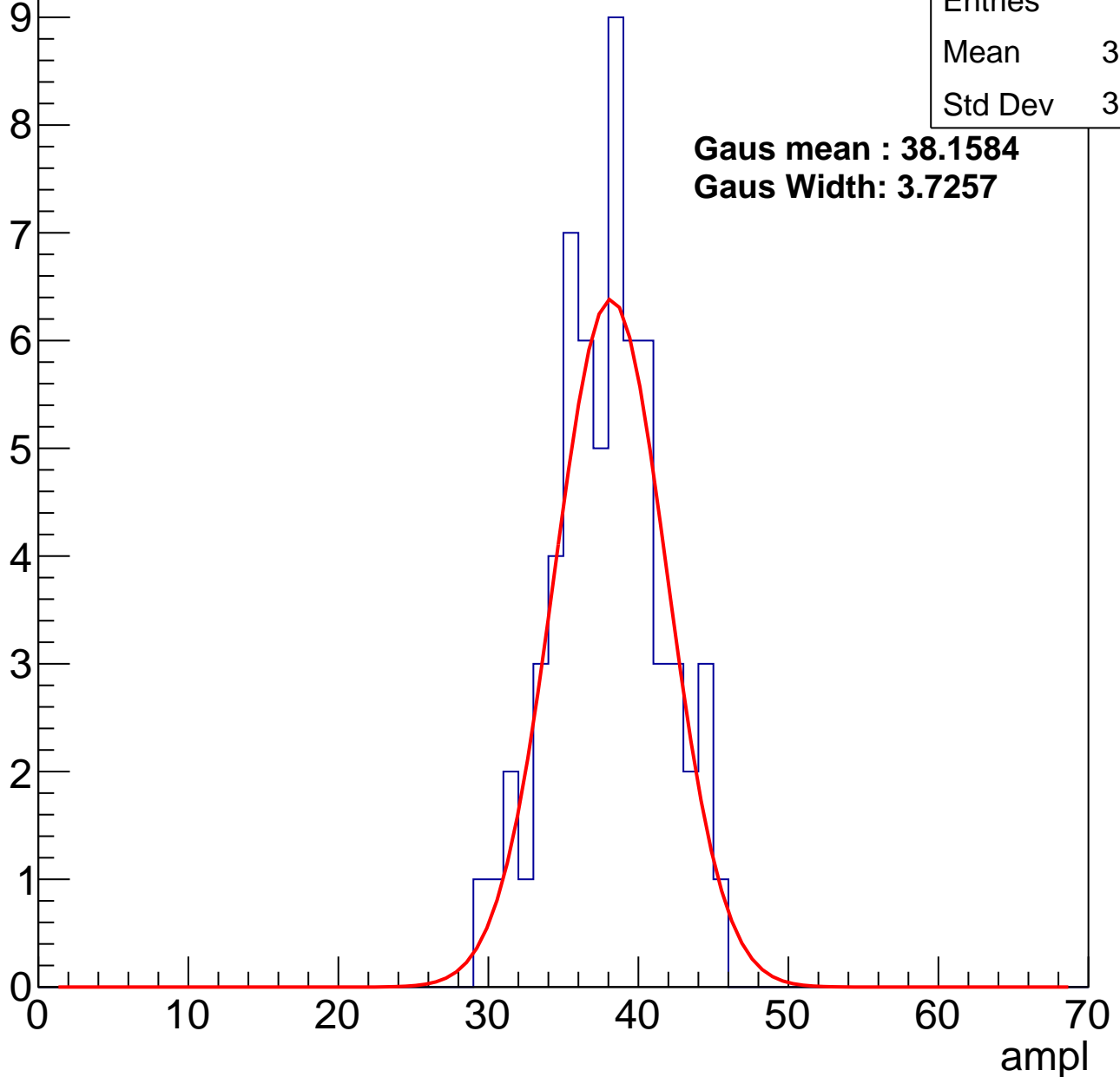
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	37.49
Std Dev	3.576

Gaus mean : 38.1584

Gaus Width: 3.7257



B1L102S, U6-ch7, adc2

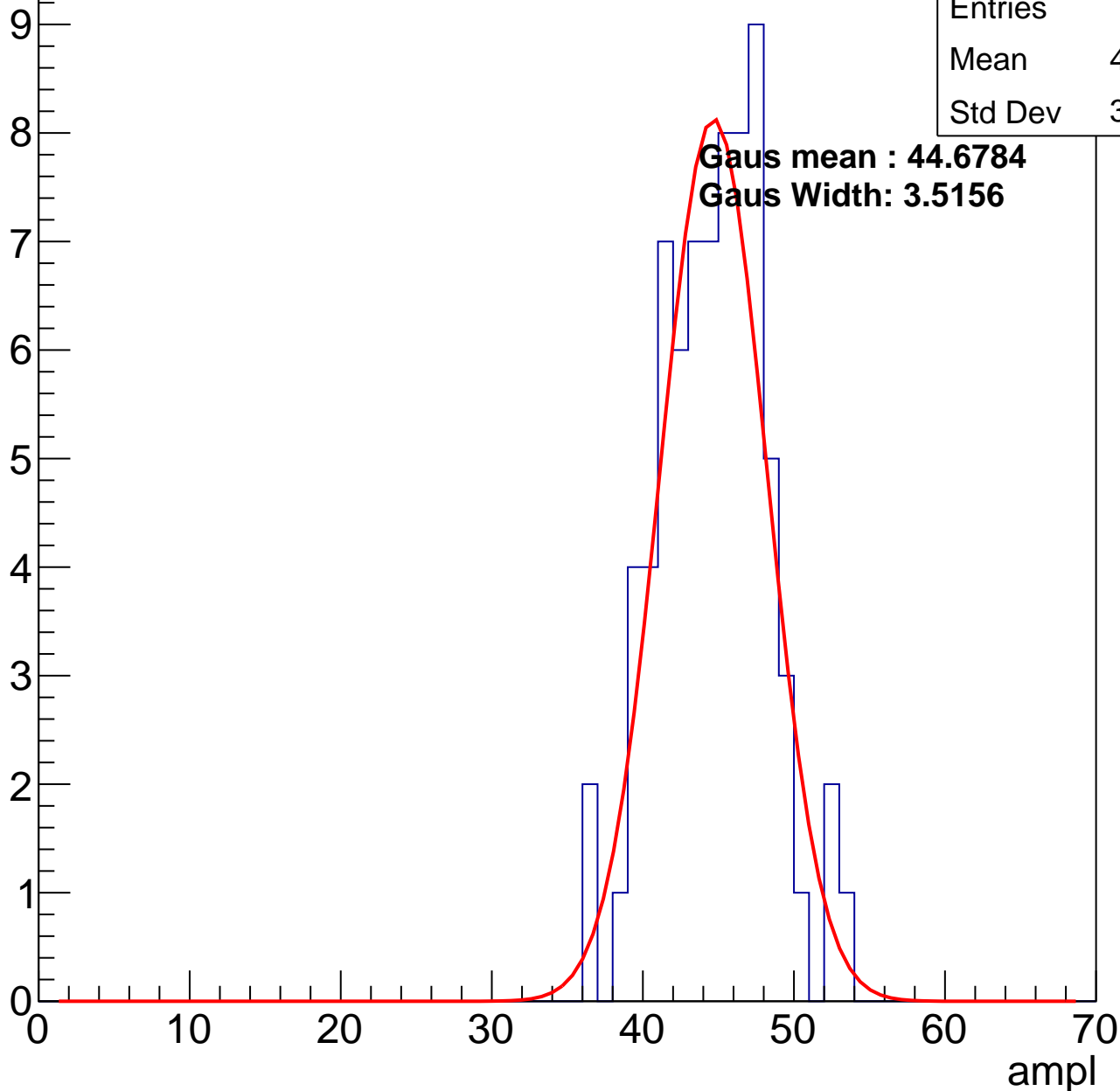
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	44.25
Std Dev	3.537

Gaus mean : 44.6784

Gaus Width: 3.5156

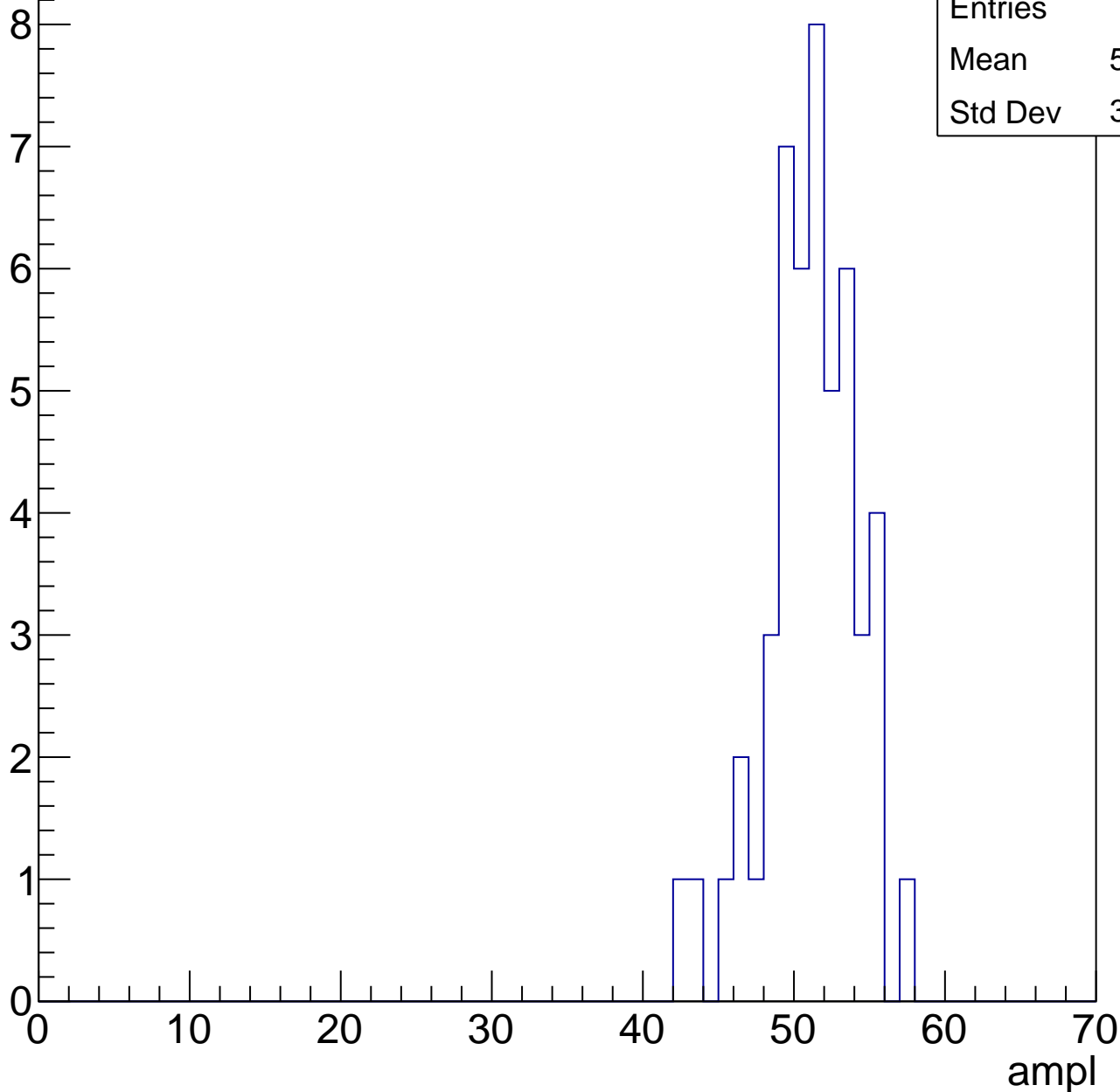


B1L102S, U6-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	50.63
Std Dev	3.055



B1L102S, U6-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	55.18
Std Dev	7.694

Entry

10
8
6
4
2
0

0

10

20

30

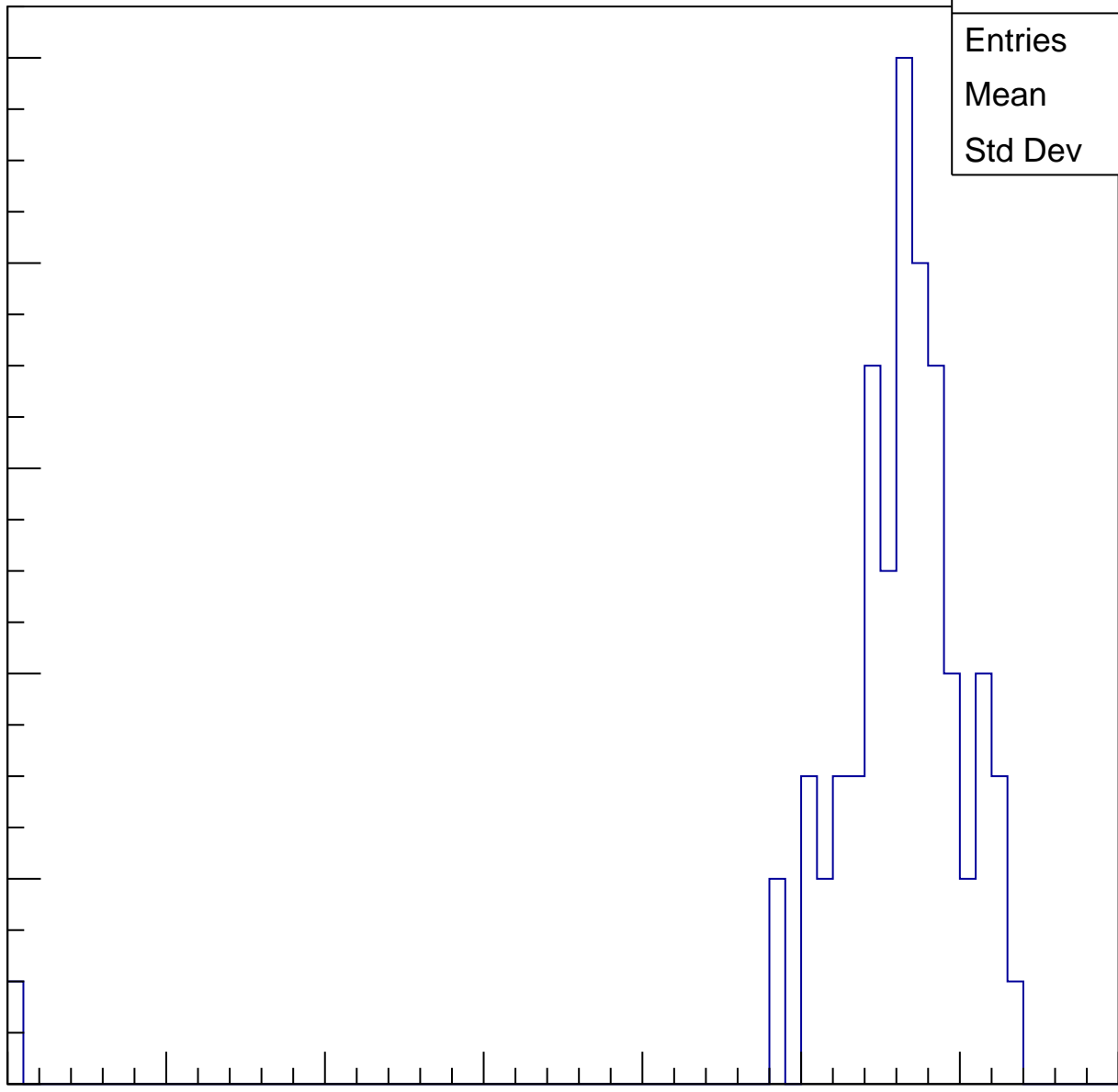
40

50

60

70

ampl

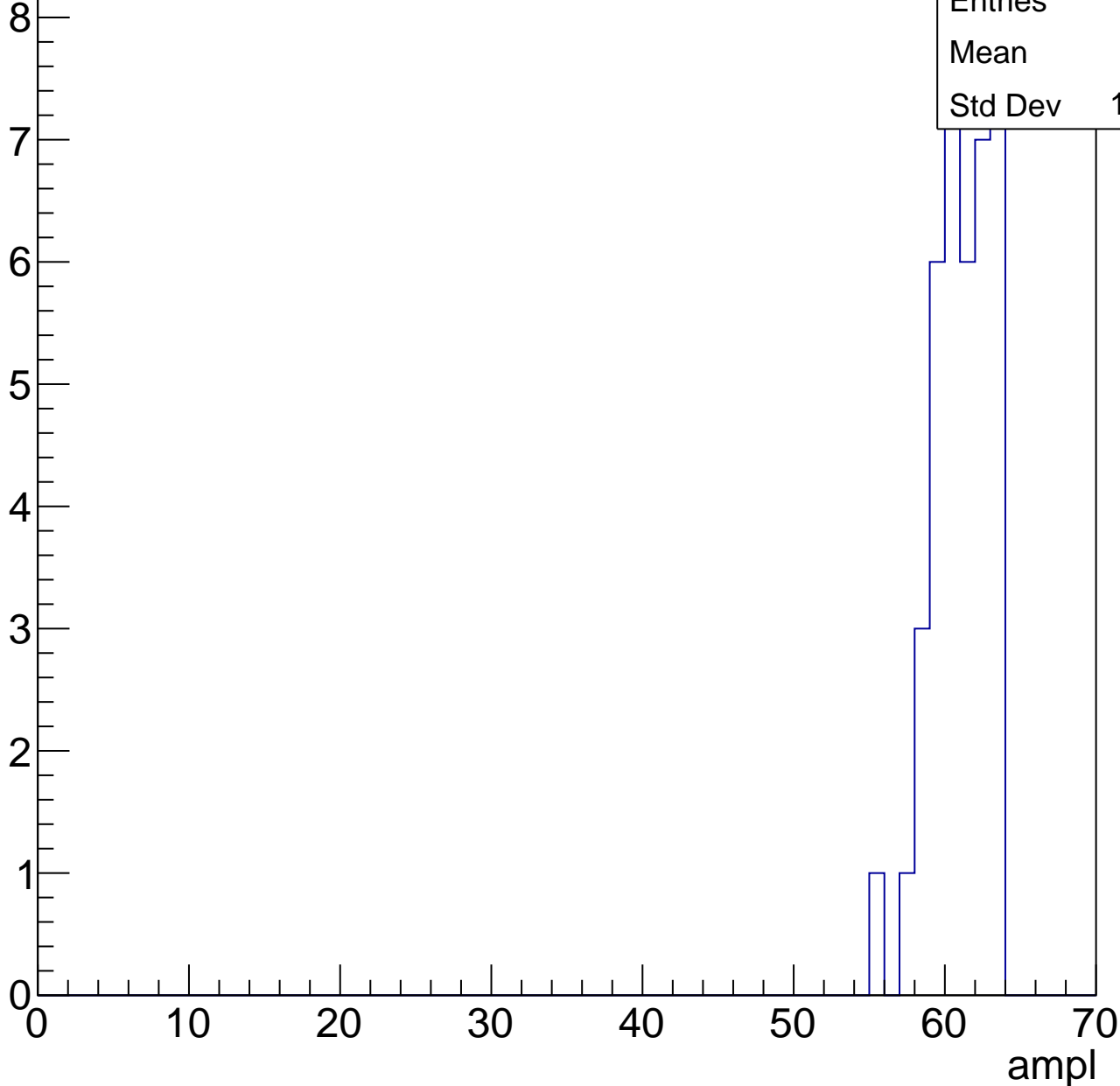


B1L102S, U6-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	60.6
Std Dev	1.895



B1L102S, U6-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch8, adc0

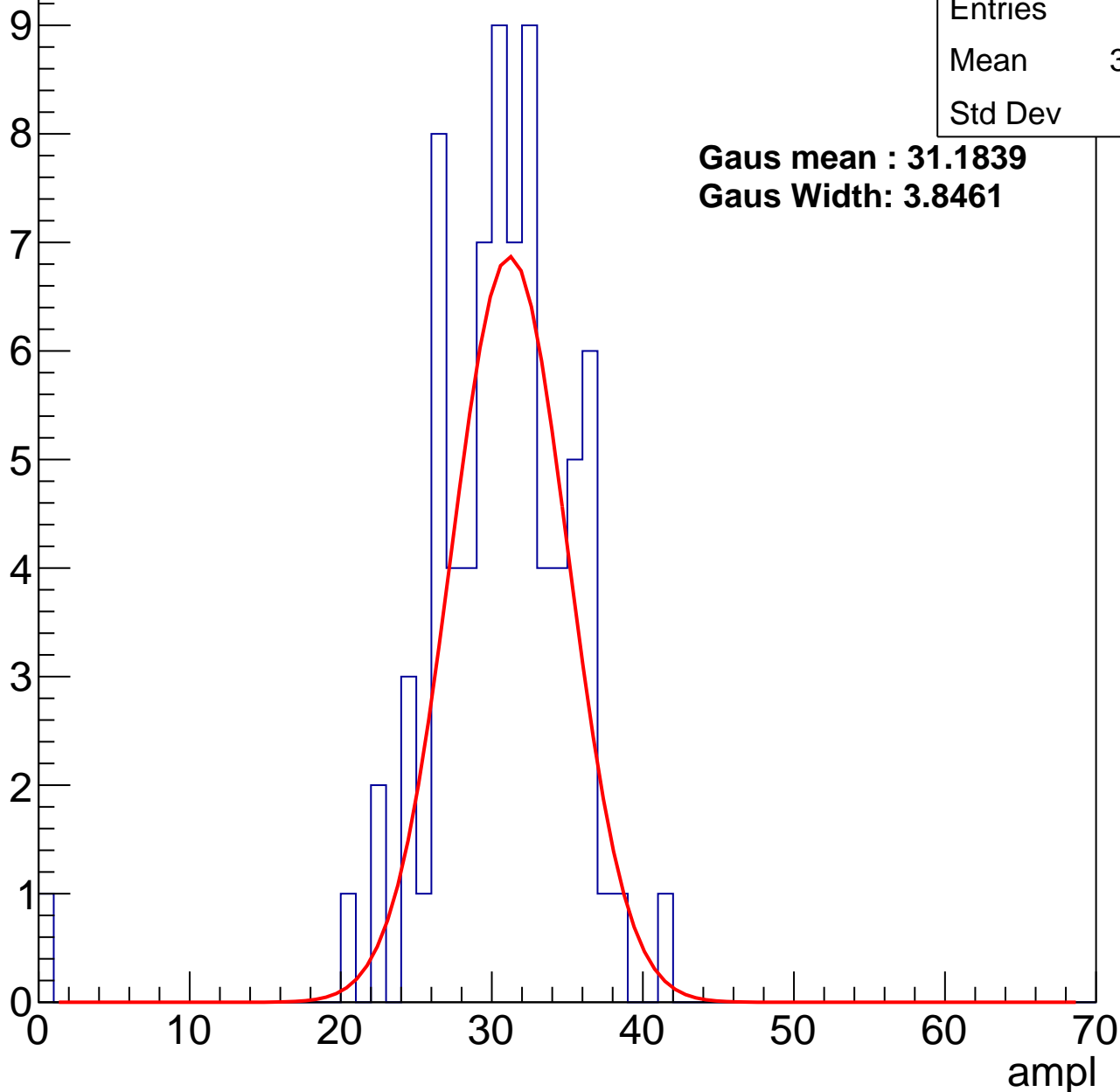
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	30.03
Std Dev	5.27

Gaus mean : 31.1839

Gaus Width: 3.8461



B1L102S, U6-ch8, adc1

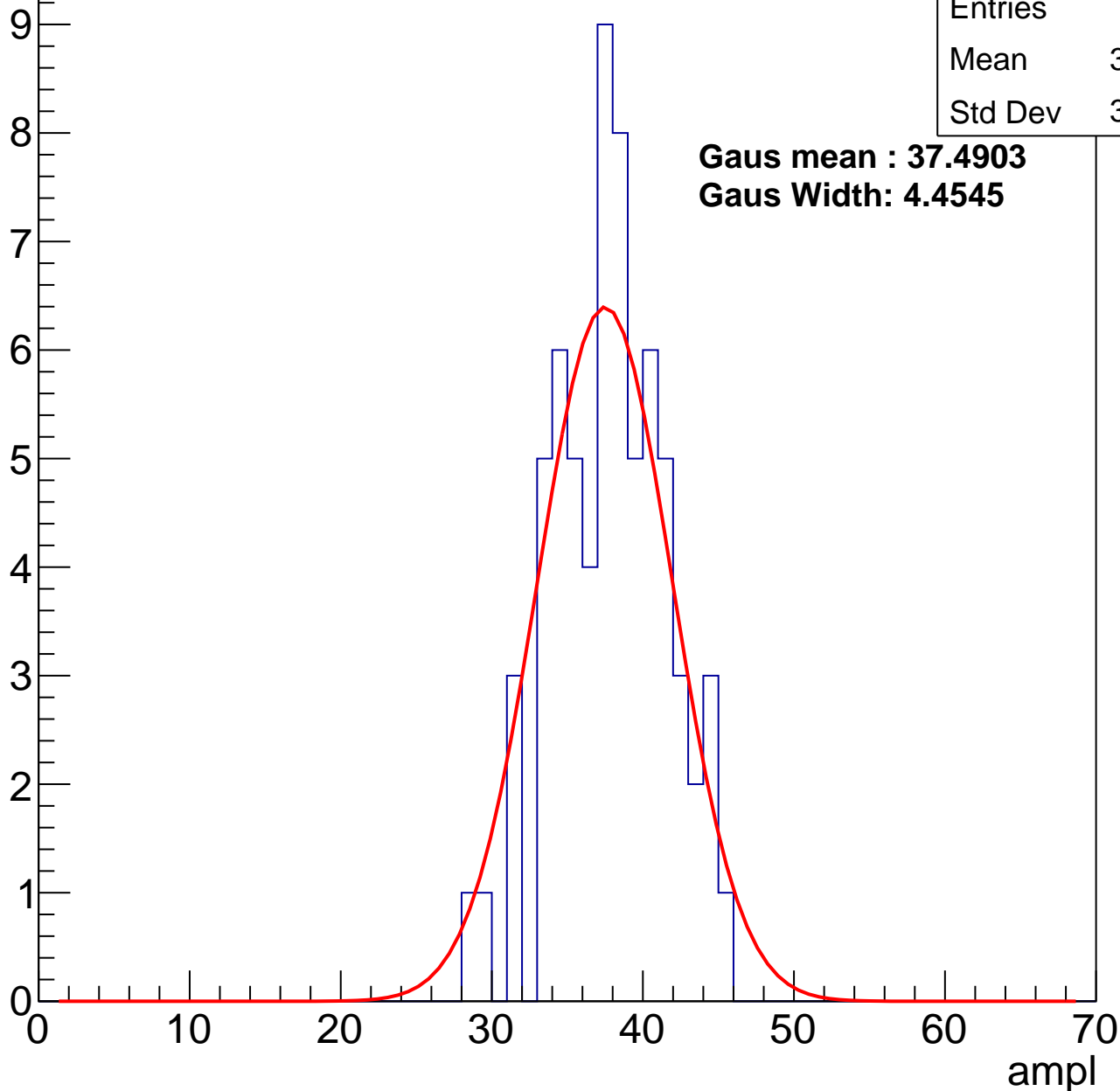
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.37
Std Dev	3.709

Gaus mean : 37.4903

Gaus Width: 4.4545



B1L102S, U6-ch8, adc2

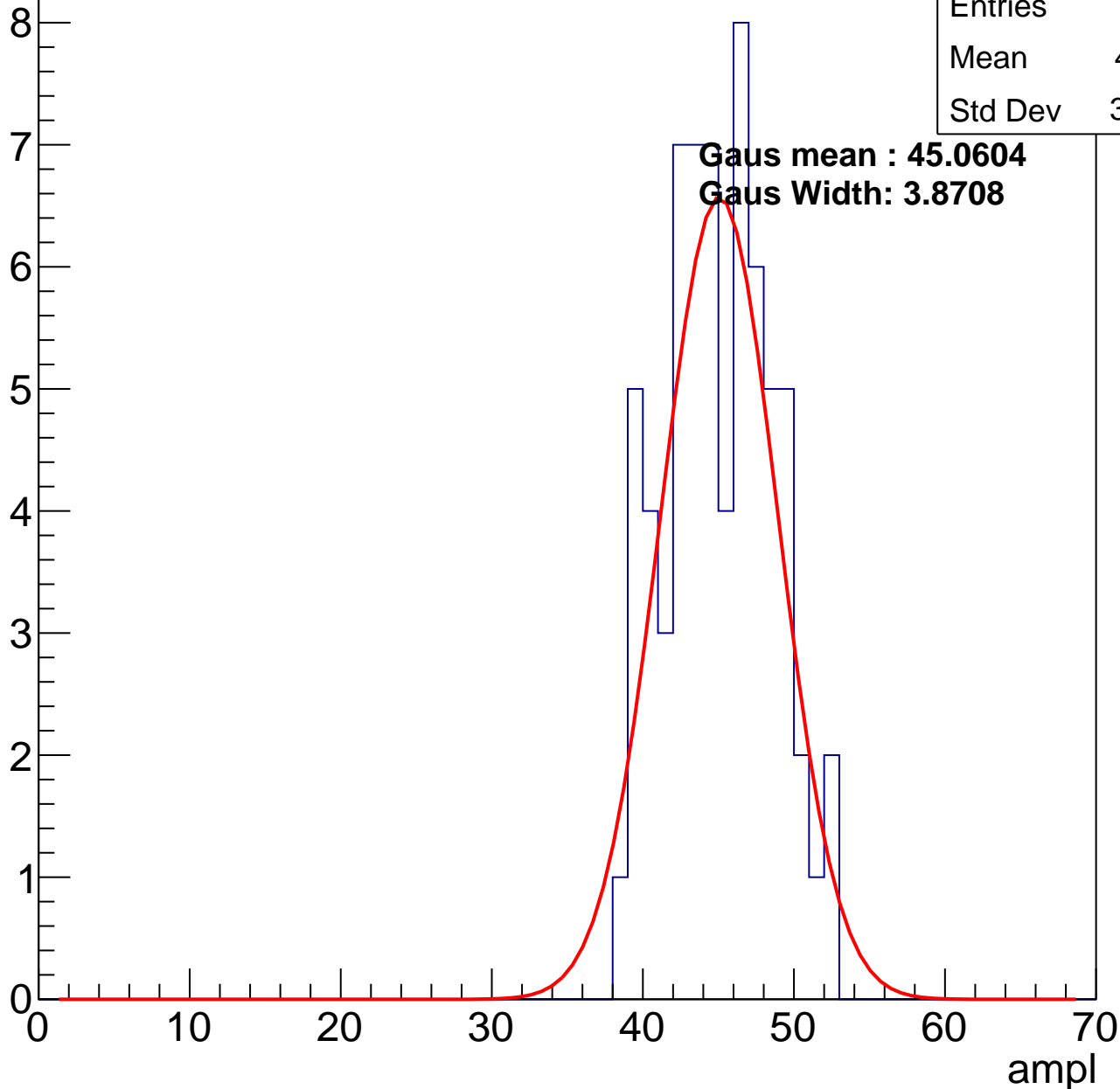
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	44.61
Std Dev	3.468

Gaus mean : 45.0604

Gaus Width: 3.8708

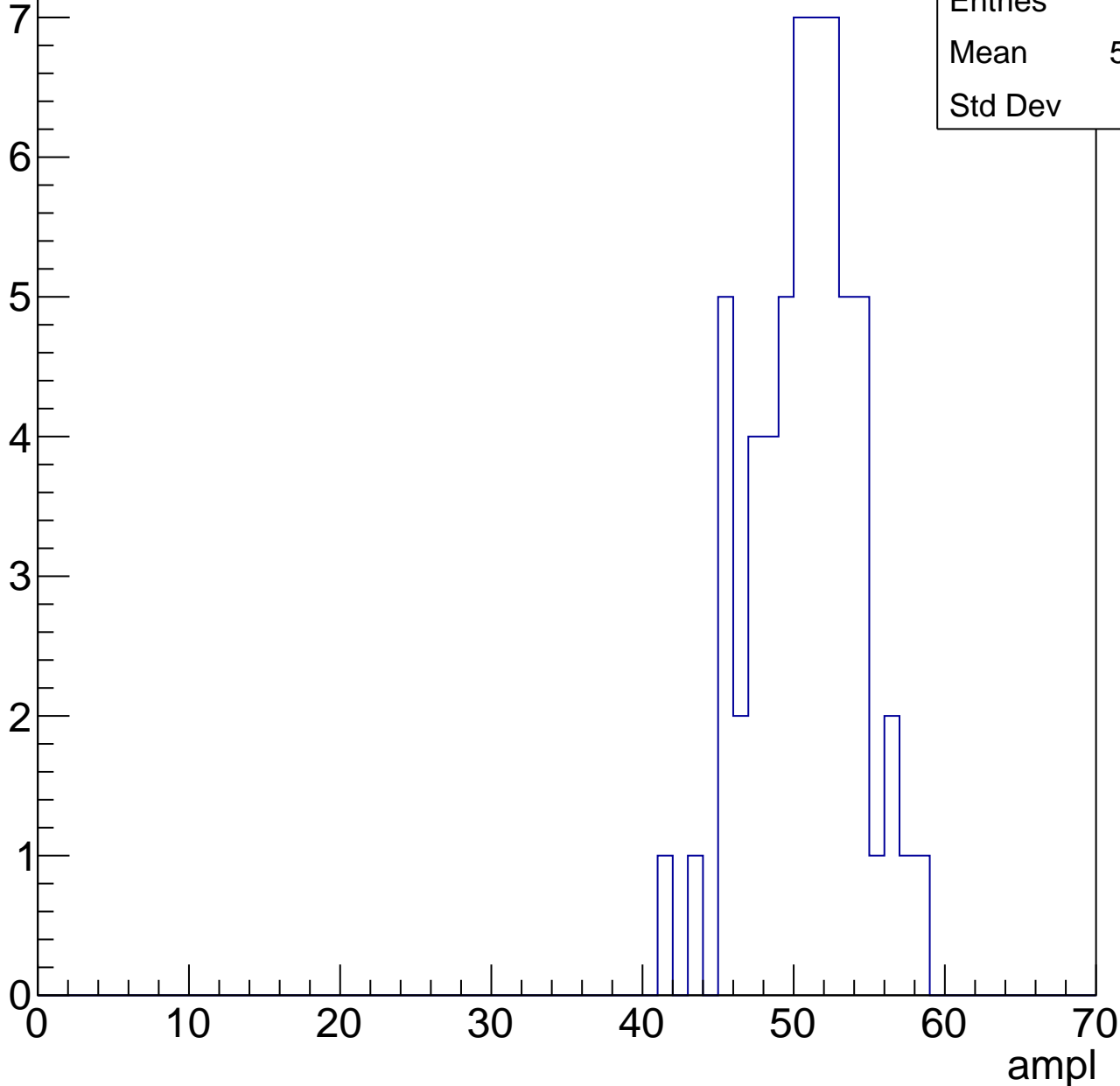


B1L102S, U6-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	50.24
Std Dev	3.5

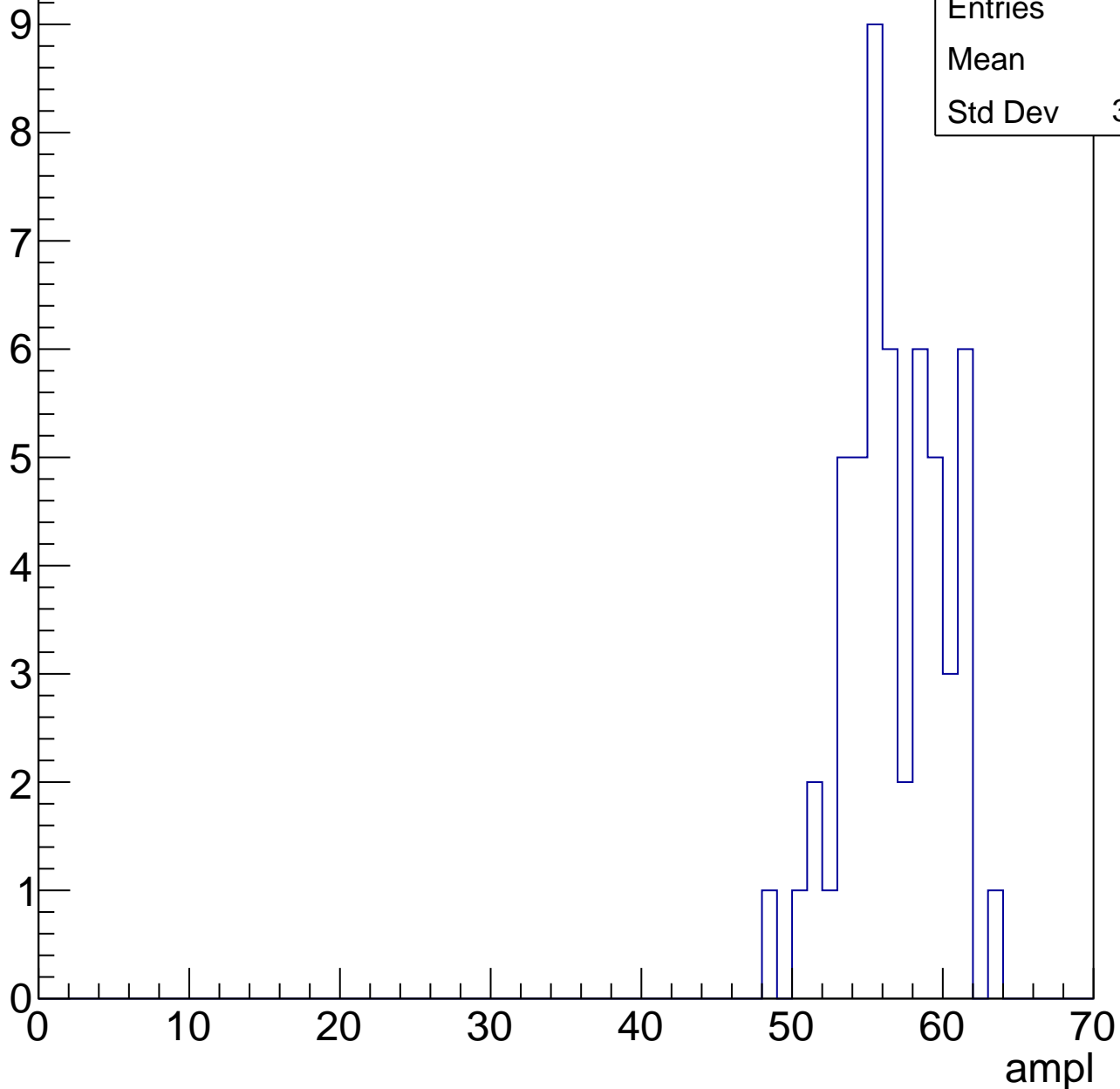


B1L102S, U6-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

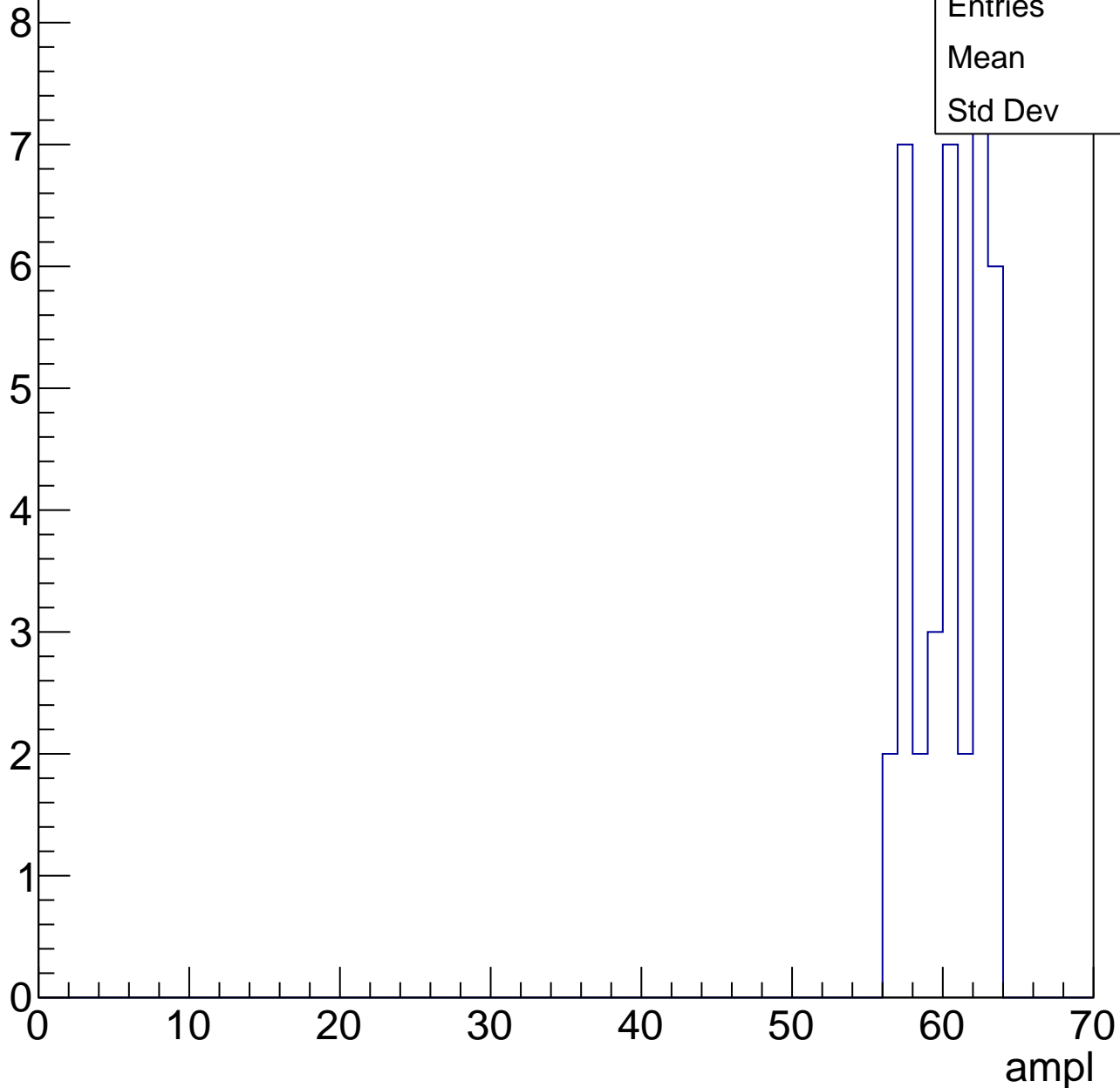
Entries	53
Mean	56.3
Std Dev	3.231



B1L102S, U6-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

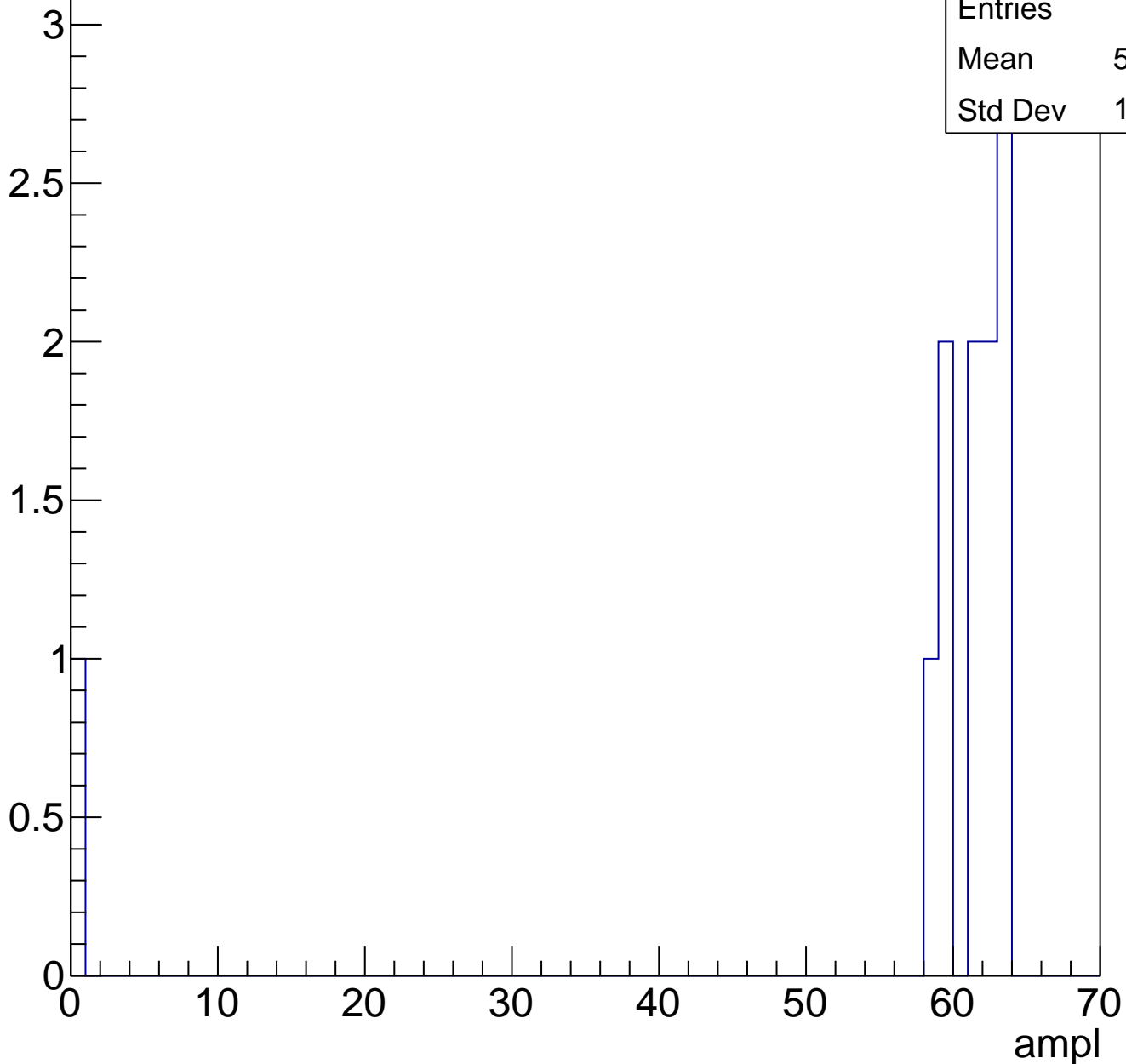
Entry



B1L102S, U6-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch9, adc0

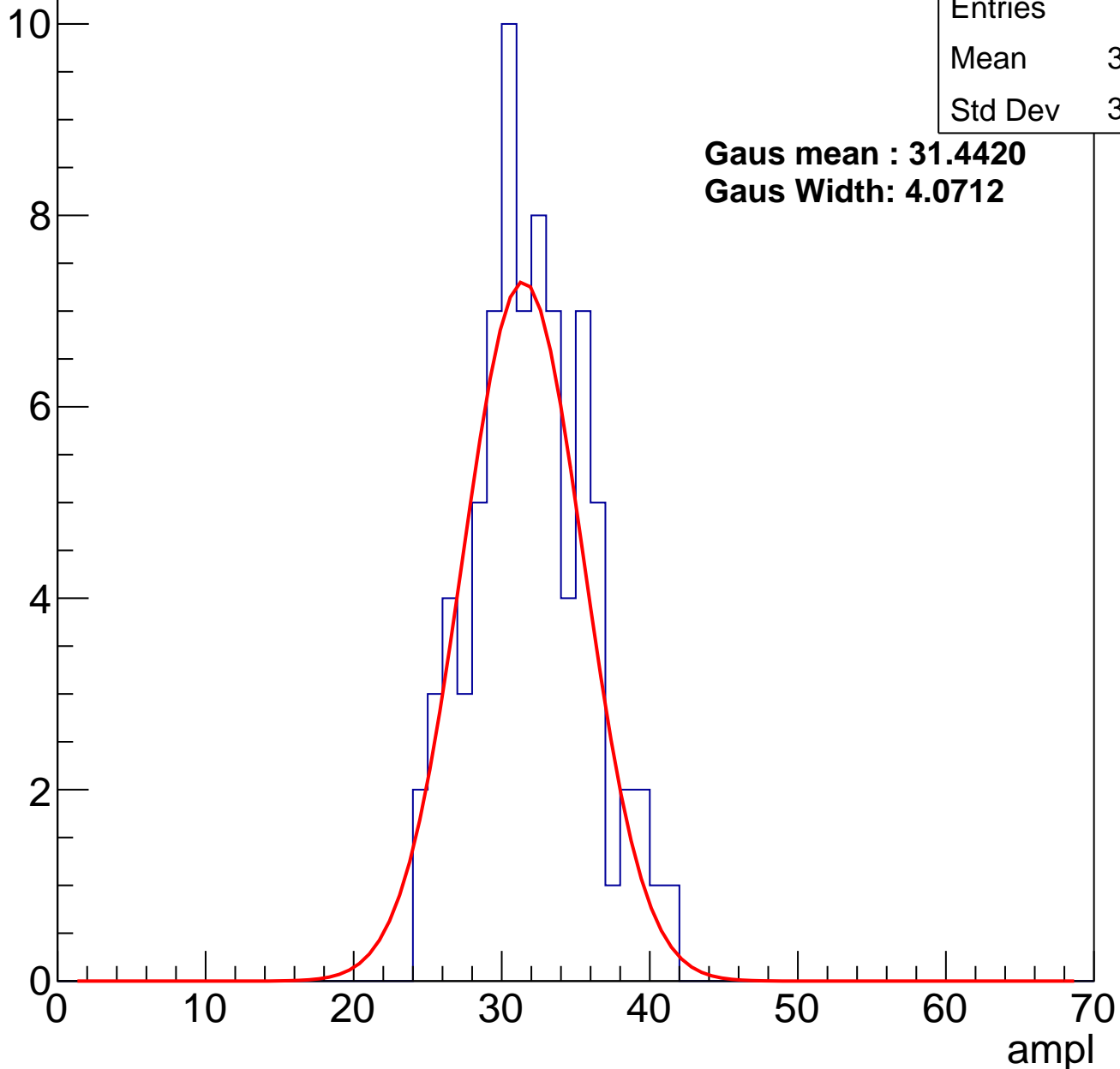
calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	31.49
Std Dev	3.848

Gaus mean : 31.4420

Gaus Width: 4.0712

Entry



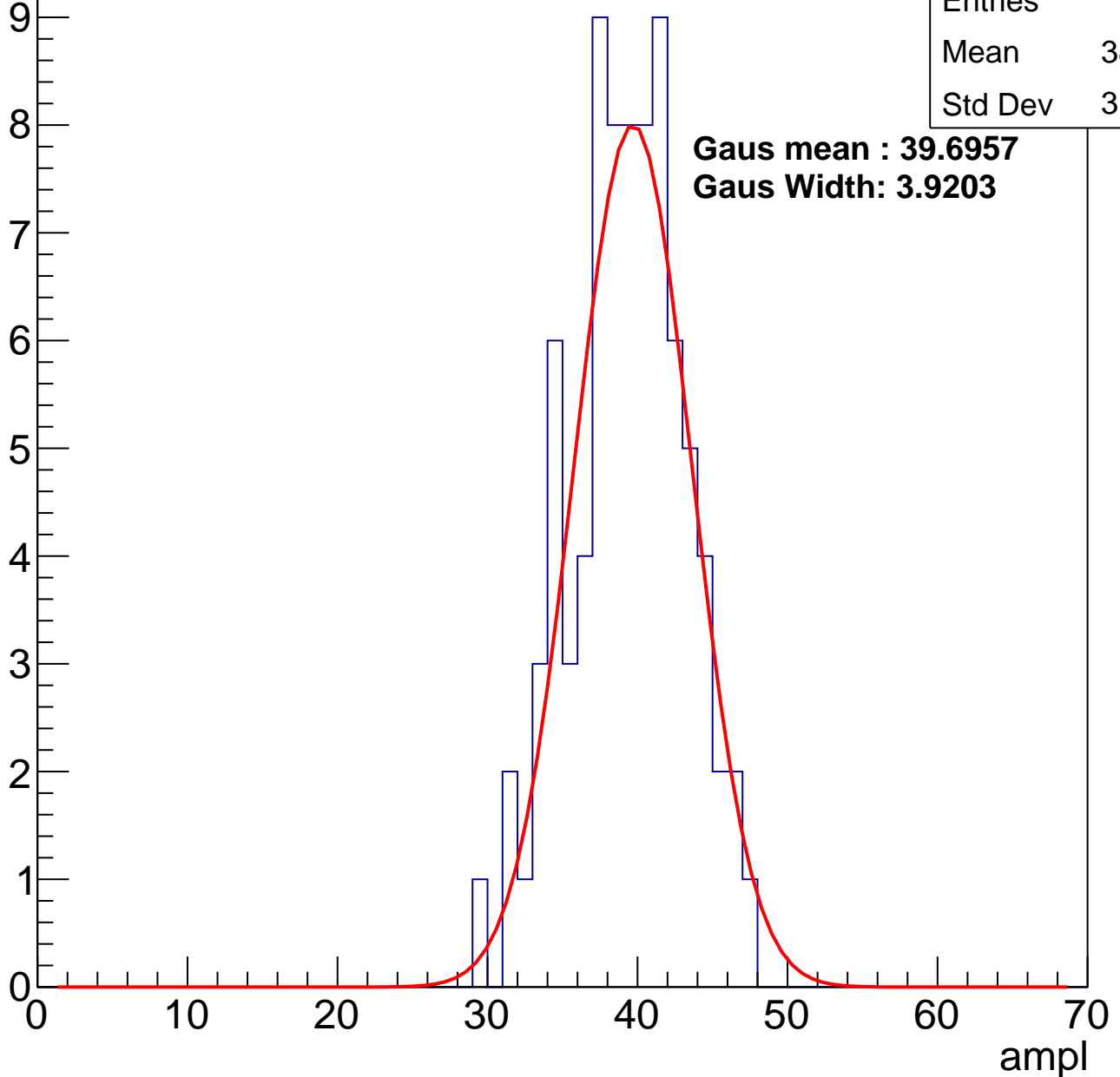
B1L102S, U6-ch9, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	38.84
Std Dev	3.782

Gaus mean : 39.6957
Gaus Width: 3.9203



B1L102S, U6-ch9, adc2

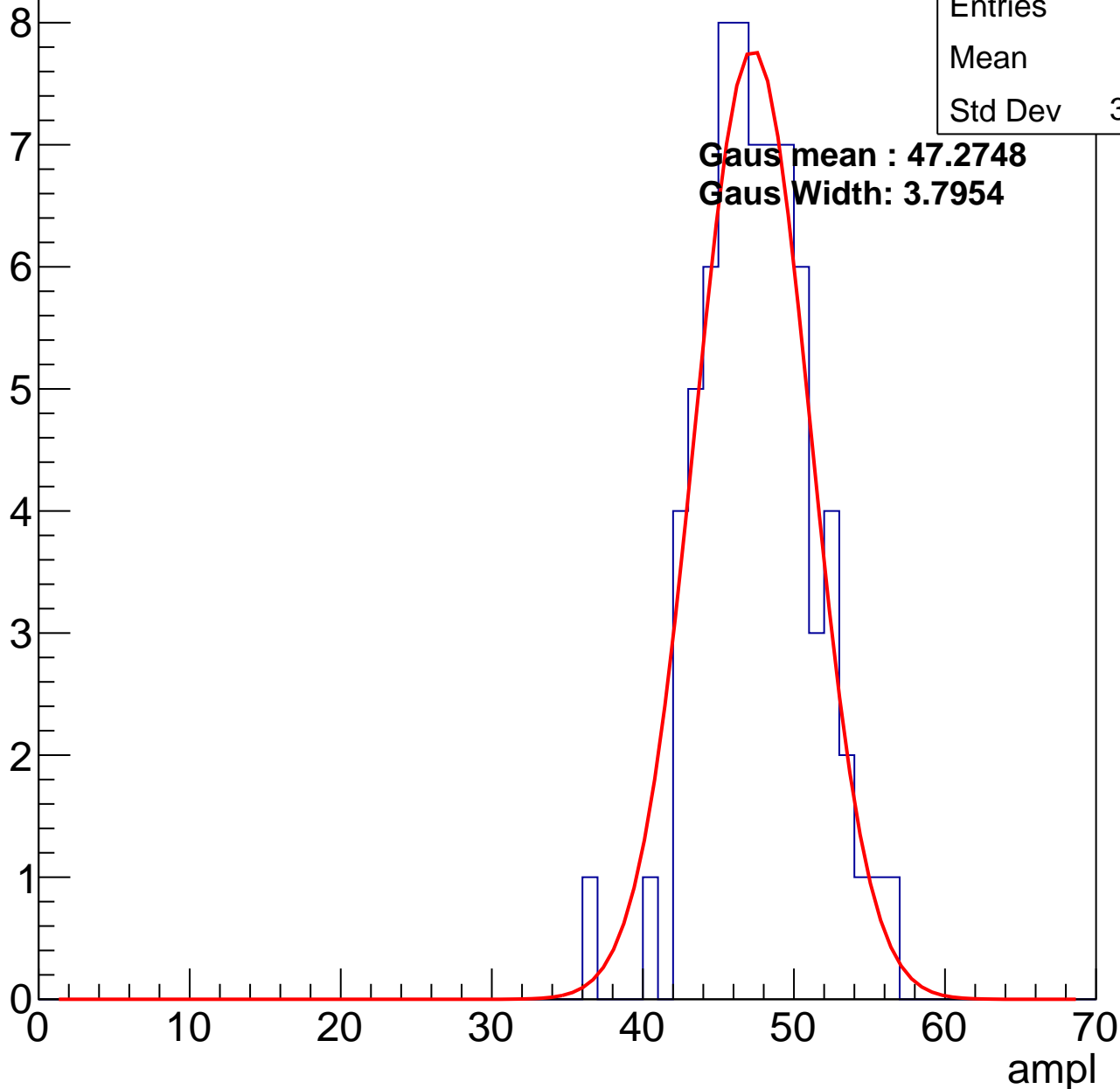
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	47.1
Std Dev	3.625

Gaus mean : 47.2748

Gaus Width: 3.7954

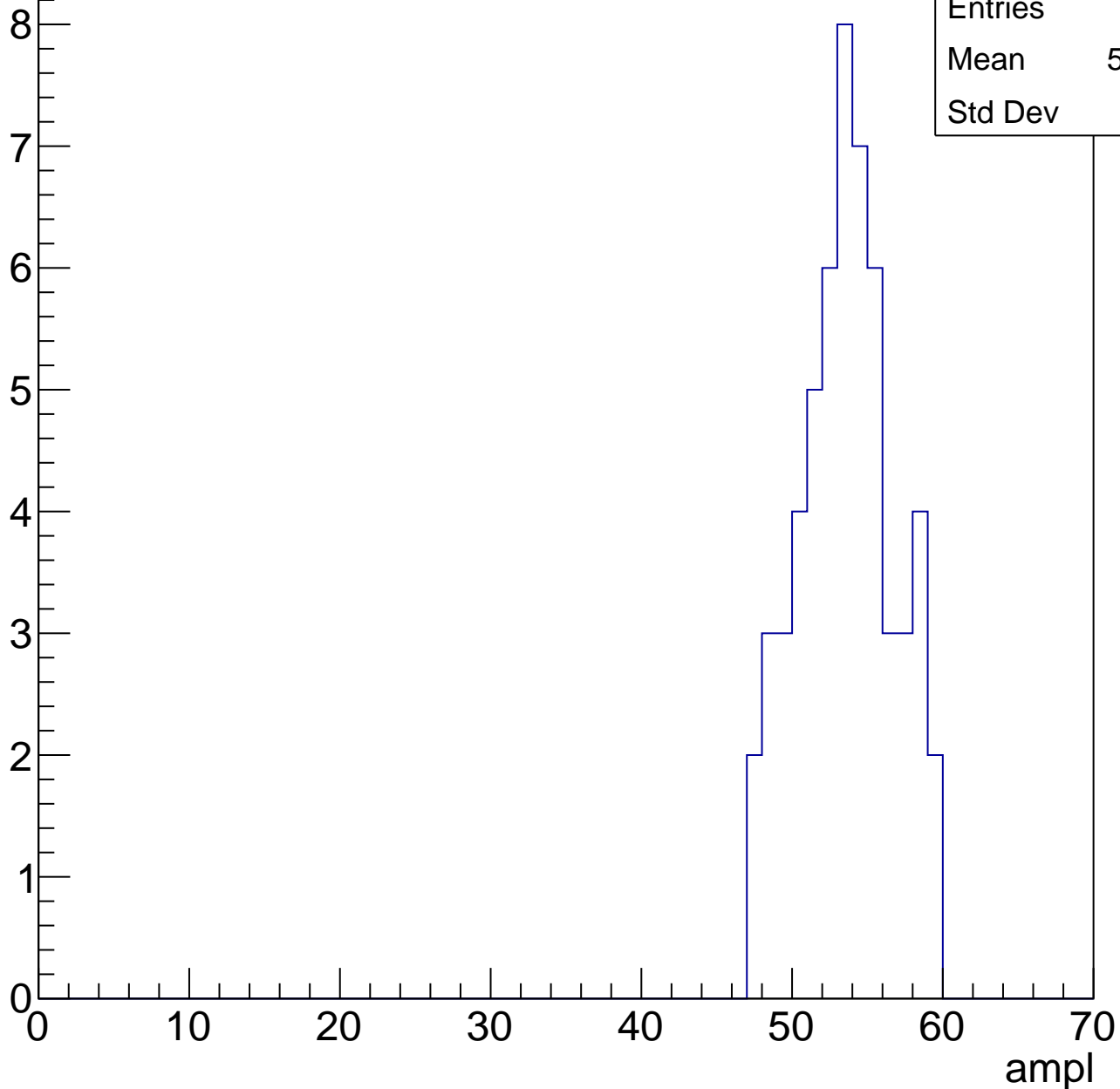


B1L102S, U6-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.09
Std Dev	3.09

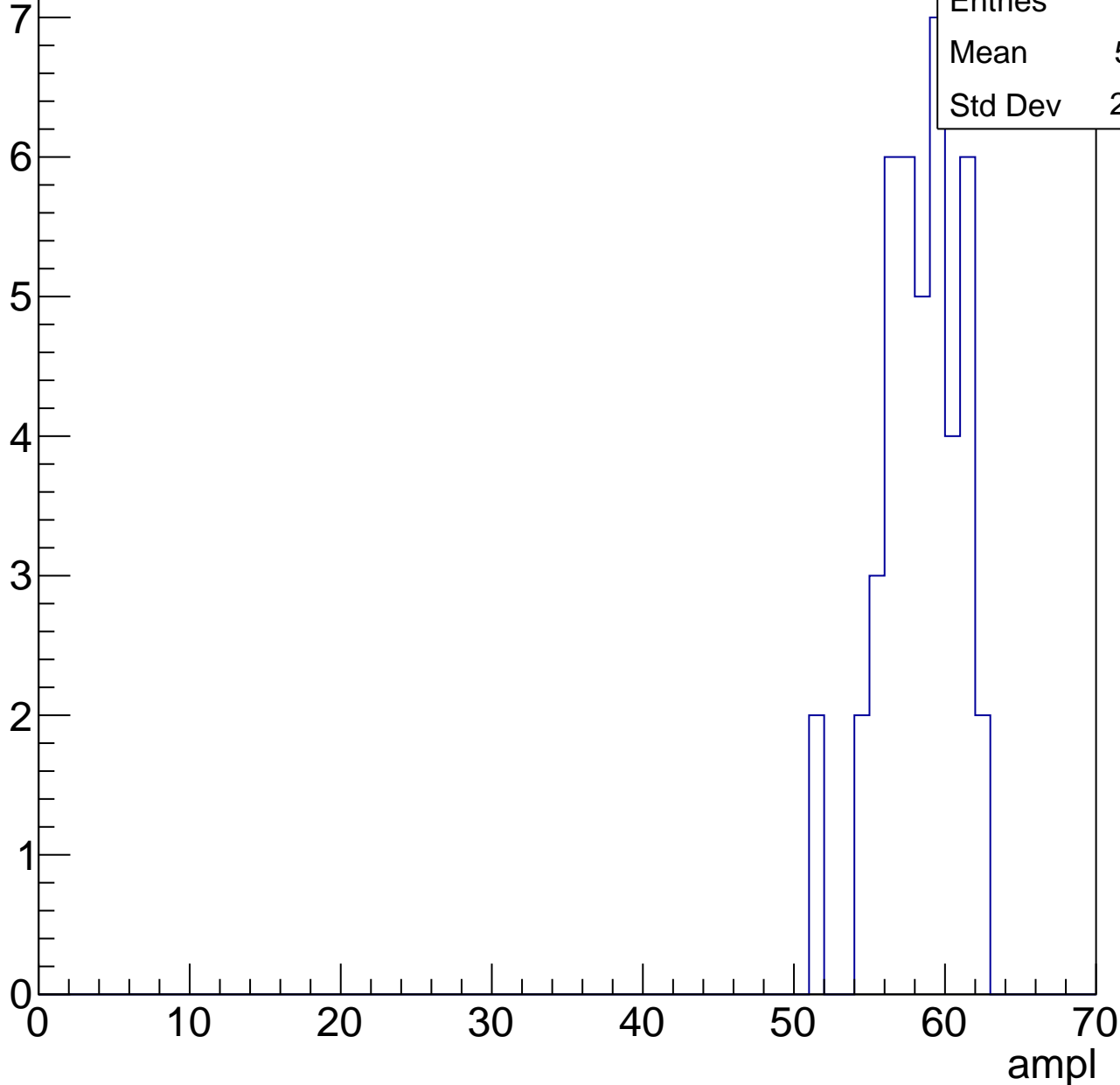


B1L102S, U6-ch9, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	57.81
Std Dev	2.617

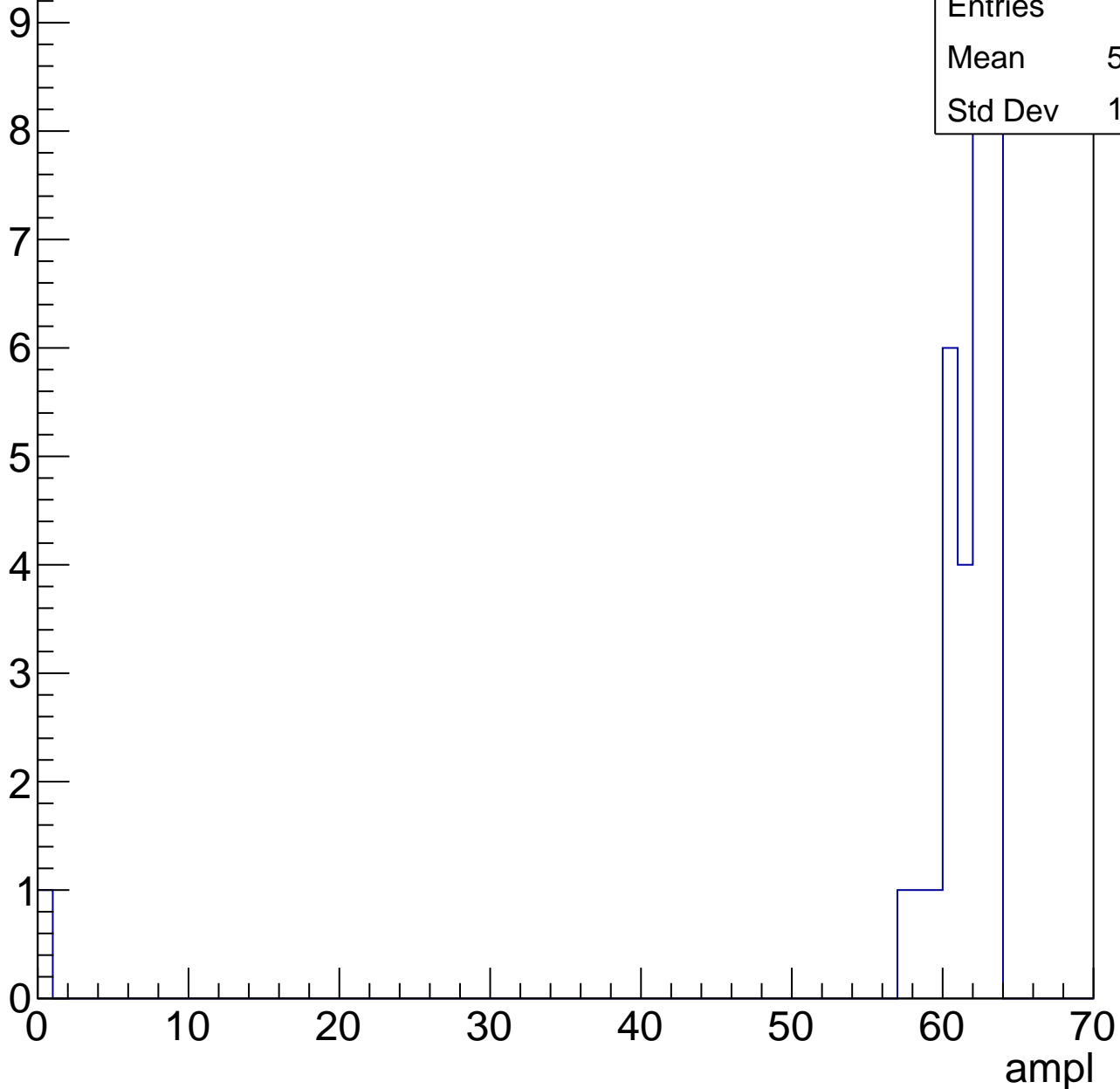


B1L102S, U6-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	31
Mean	59.39
Std Dev	10.95



B1L102S, U6-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch10, adc0

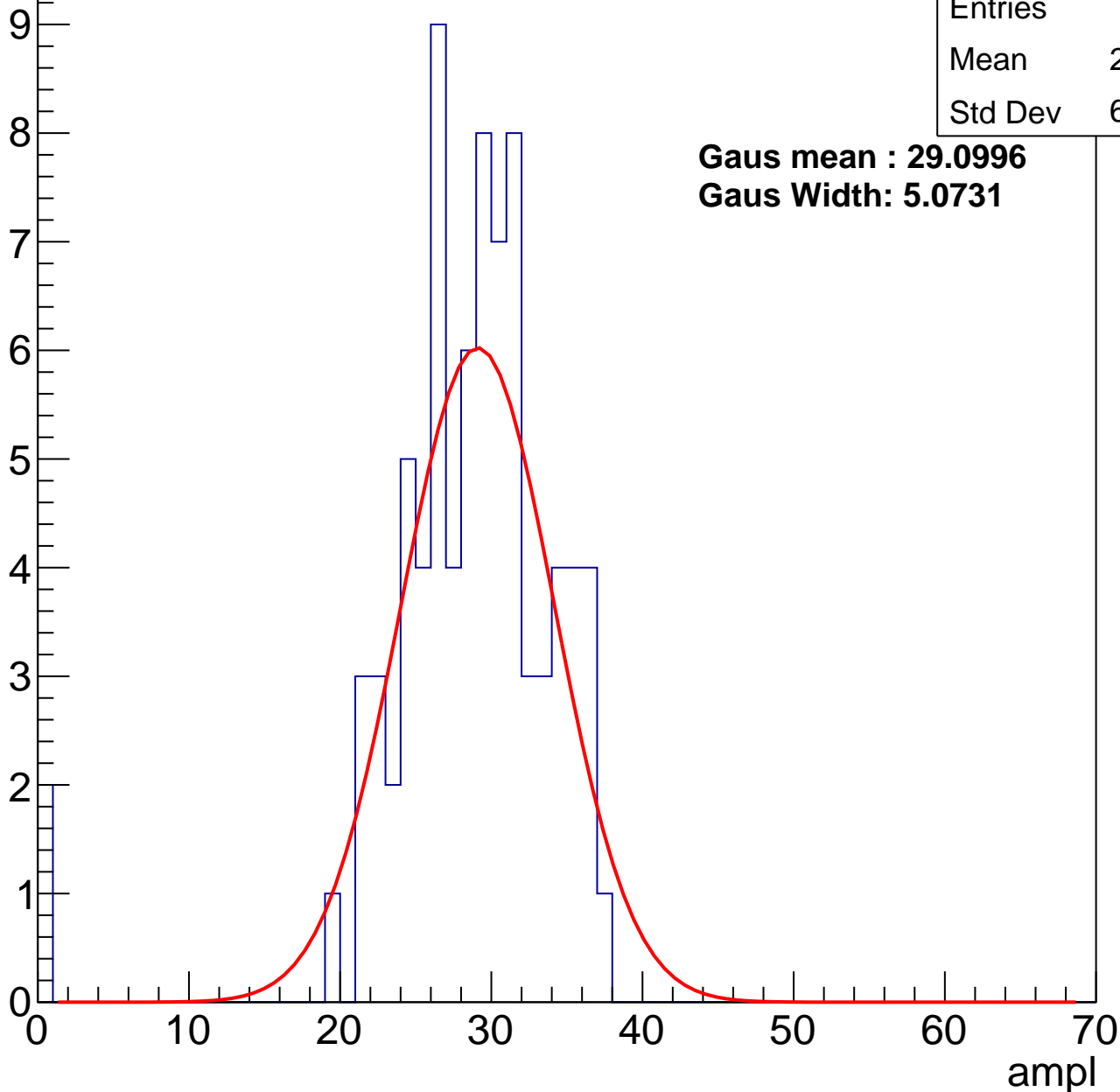
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	27.98
Std Dev	6.104

Gaus mean : 29.0996

Gaus Width: 5.0731



B1L102S, U6-ch10, adc1

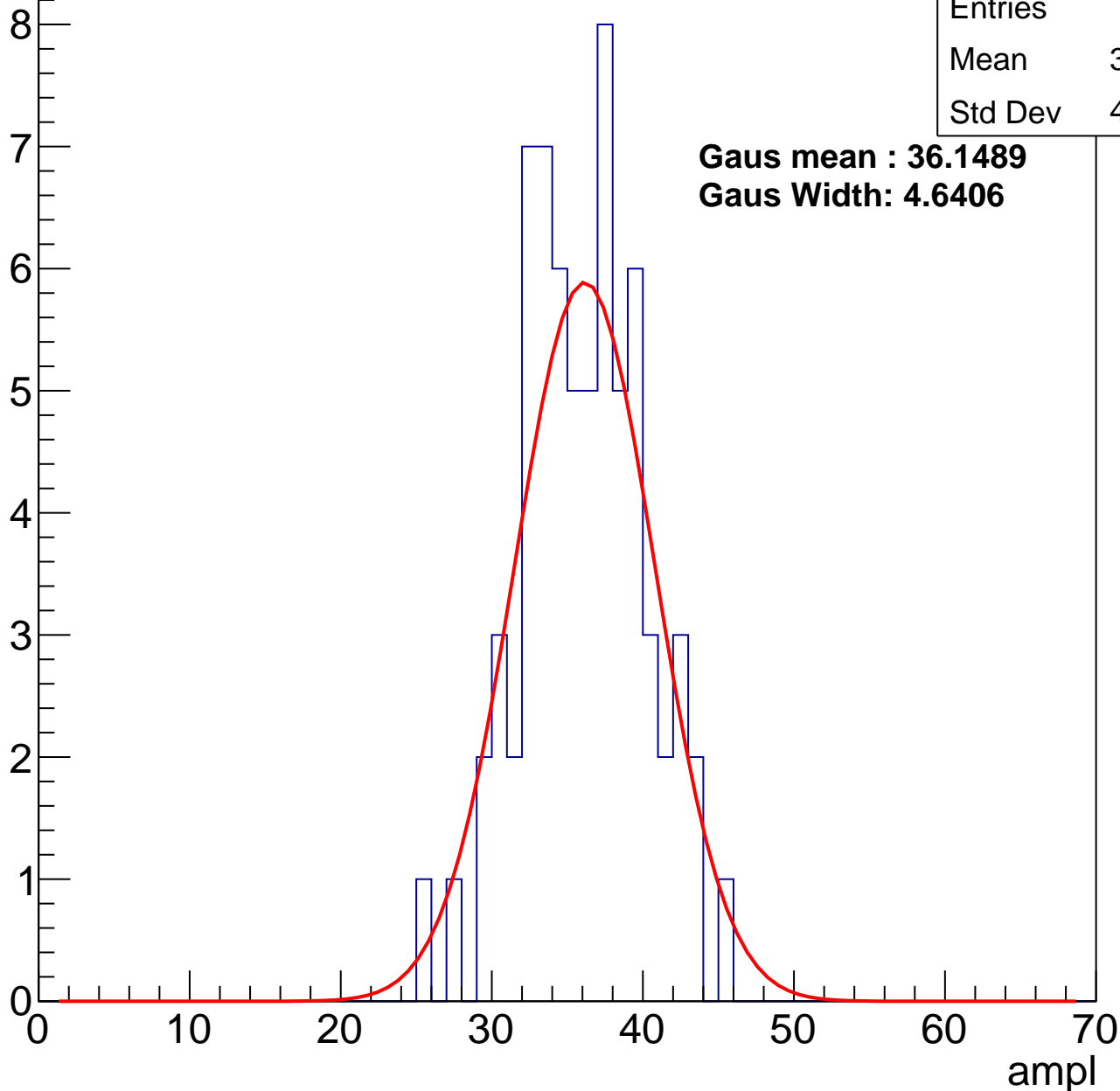
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.58
Std Dev	4.023

Gaus mean : 36.1489

Gaus Width: 4.6406



B1L102S, U6-ch10, adc2

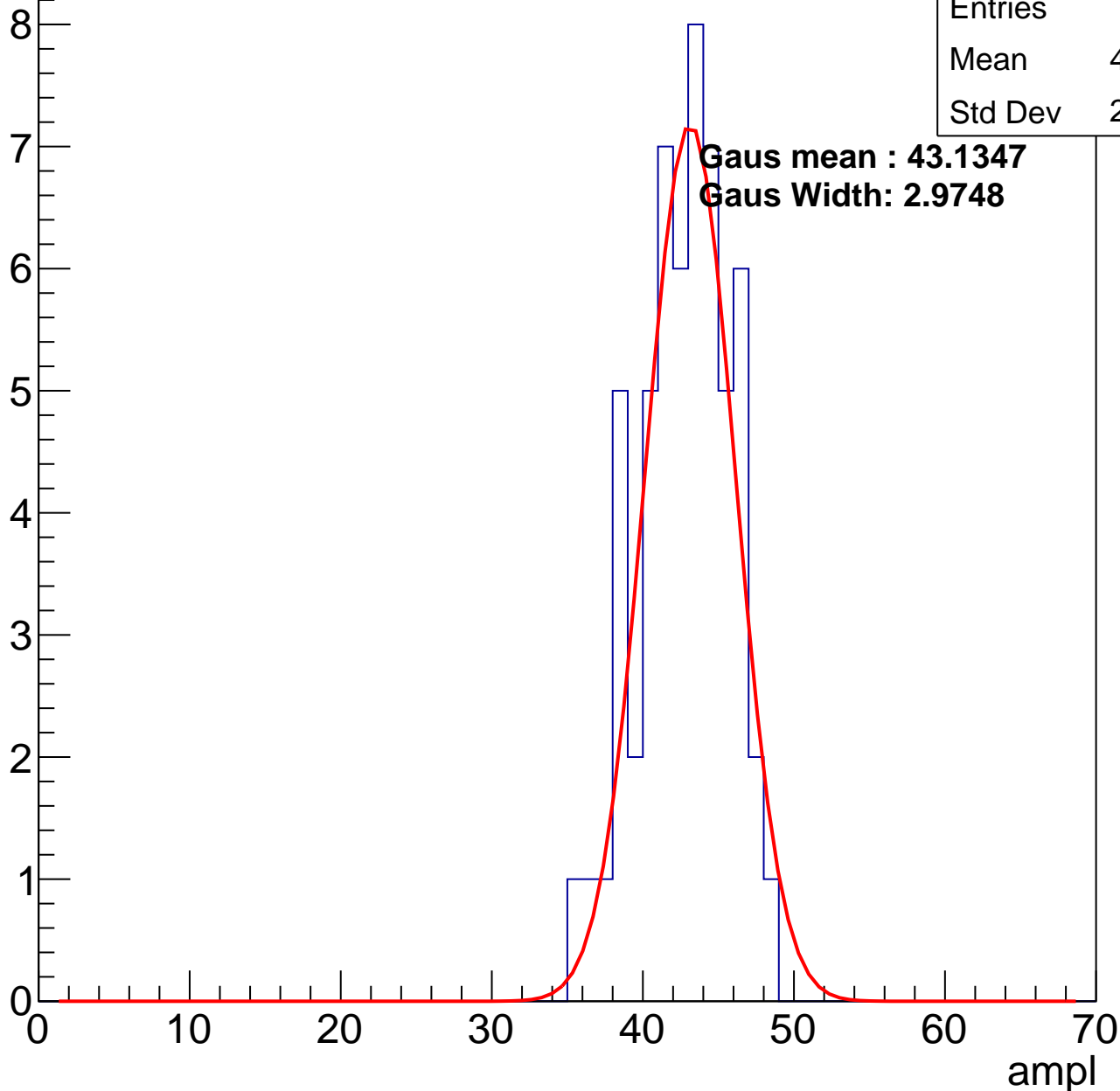
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.28
Std Dev	2.942

Gaus mean : 43.1347

Gaus Width: 2.9748

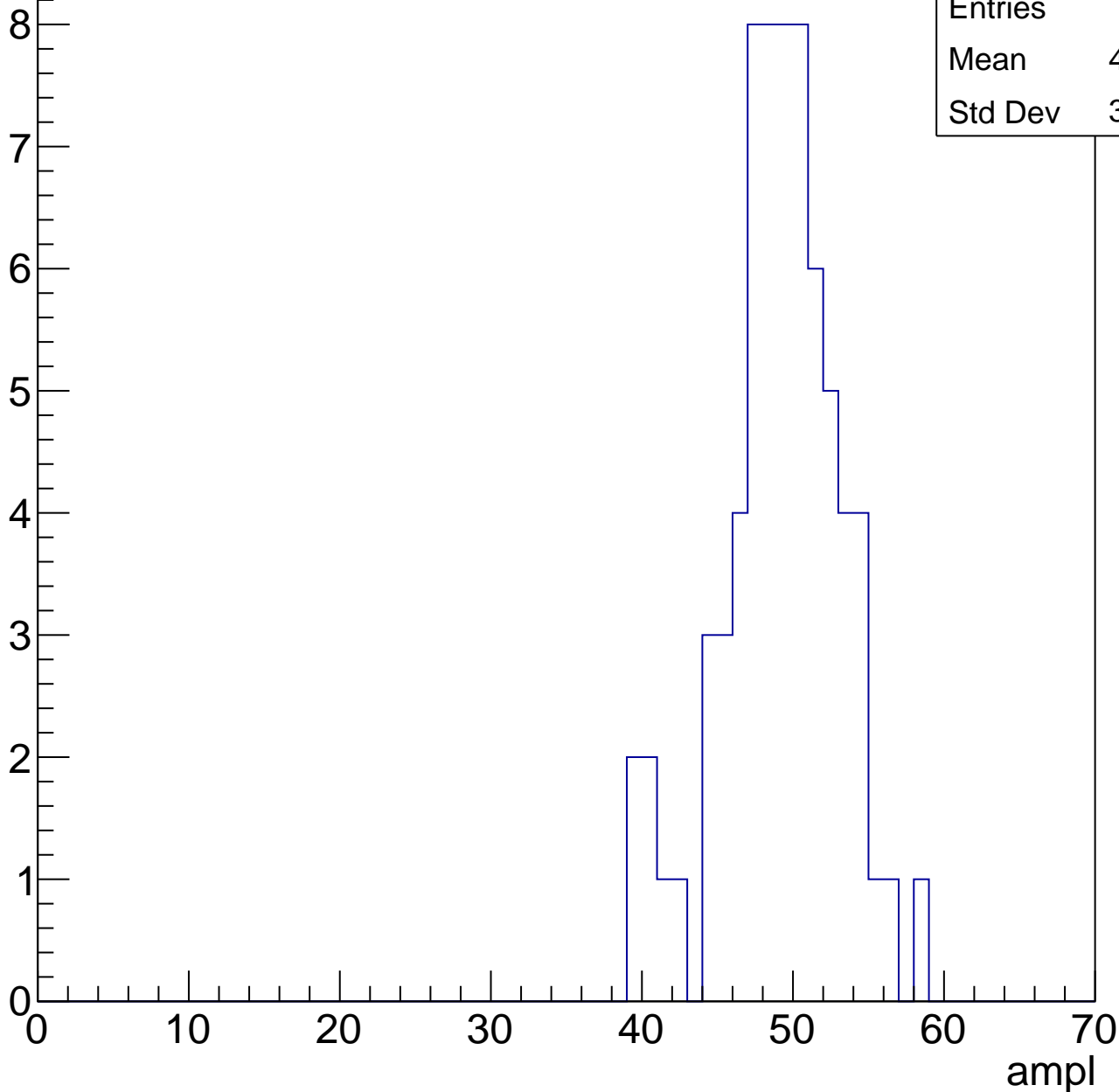


B1L102S, U6-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.67
Std Dev	3.916

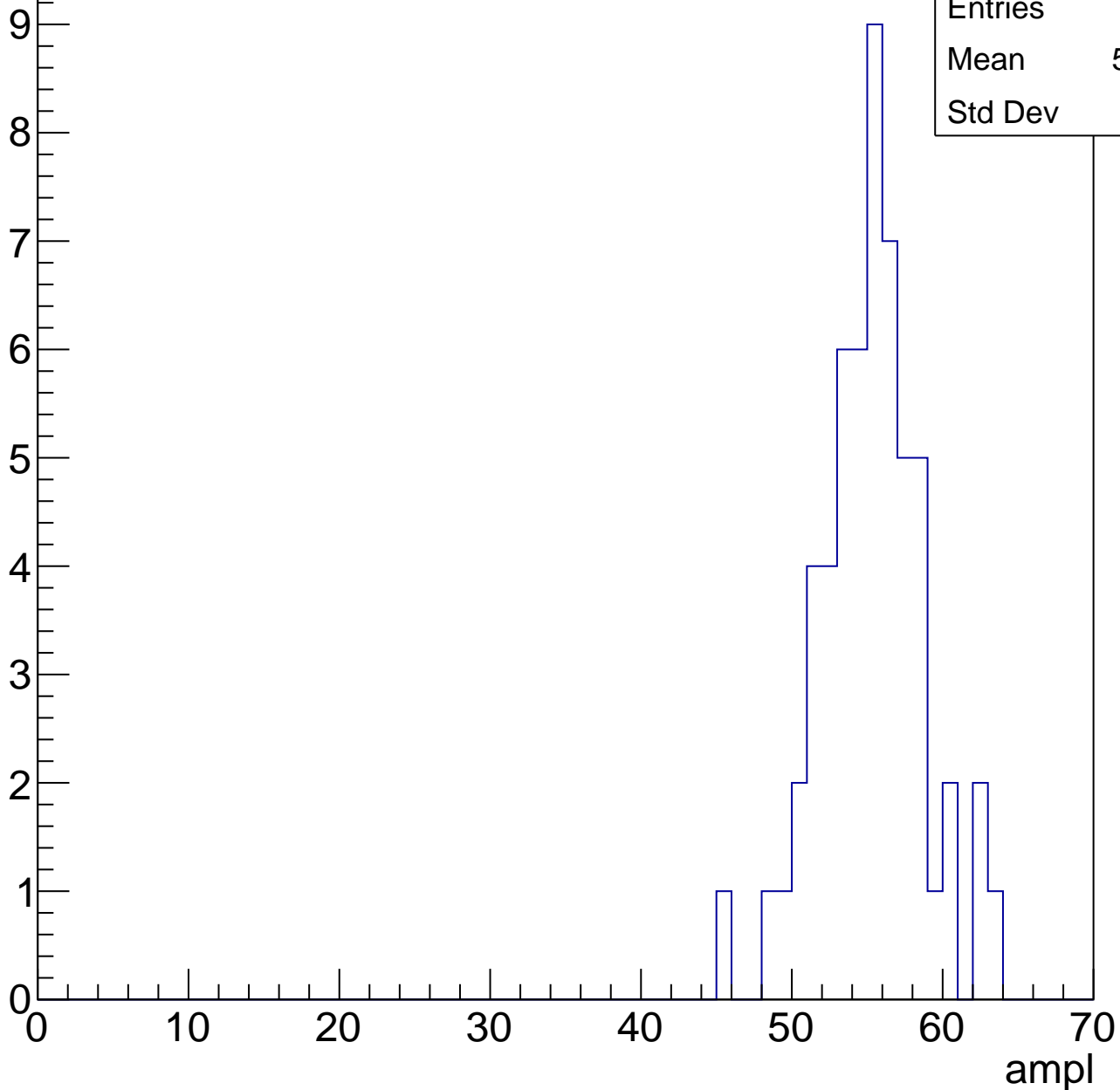


B1L102S, U6-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

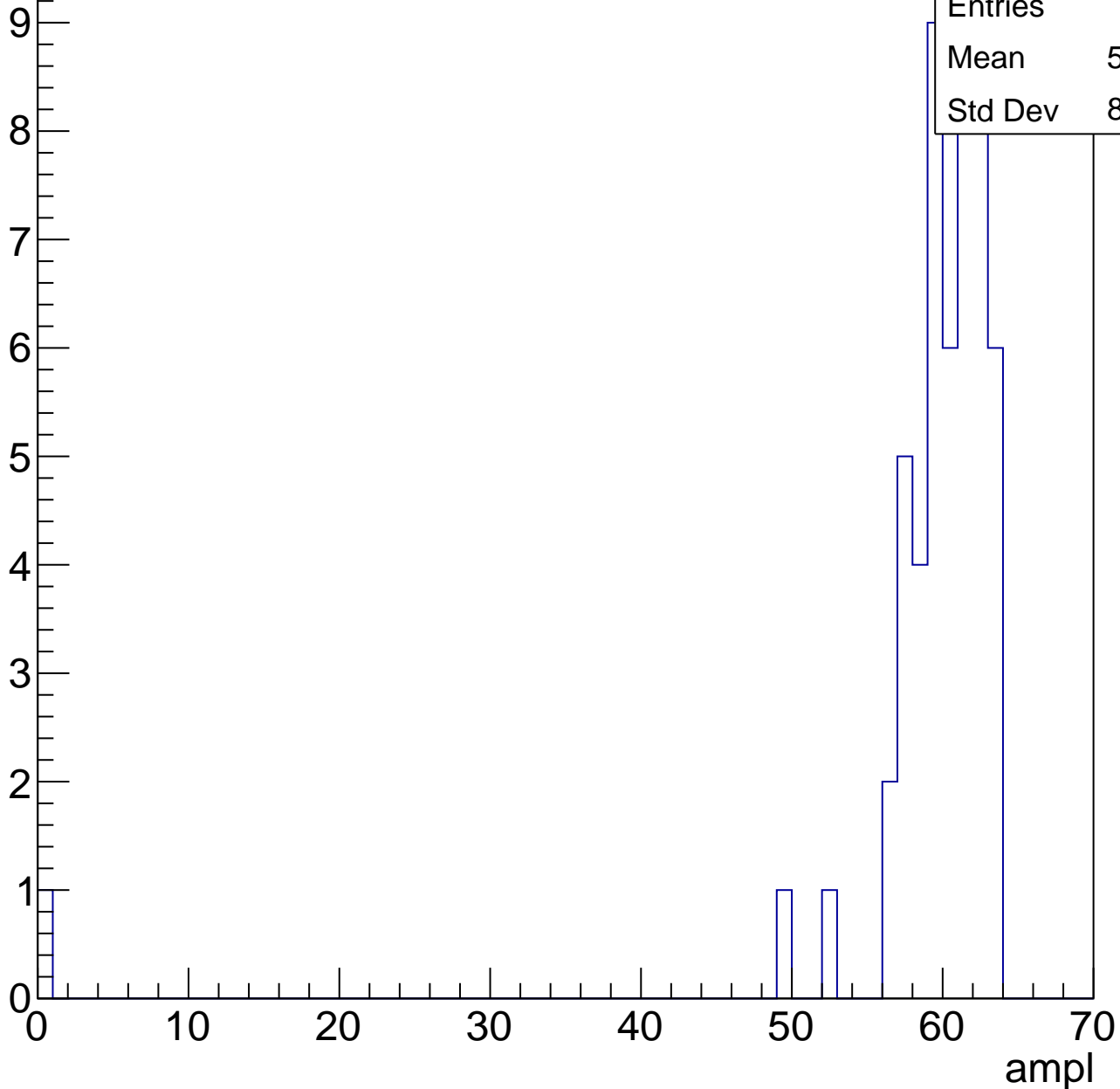
Entries	57
Mean	54.81
Std Dev	3.41



B1L102S, U6-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

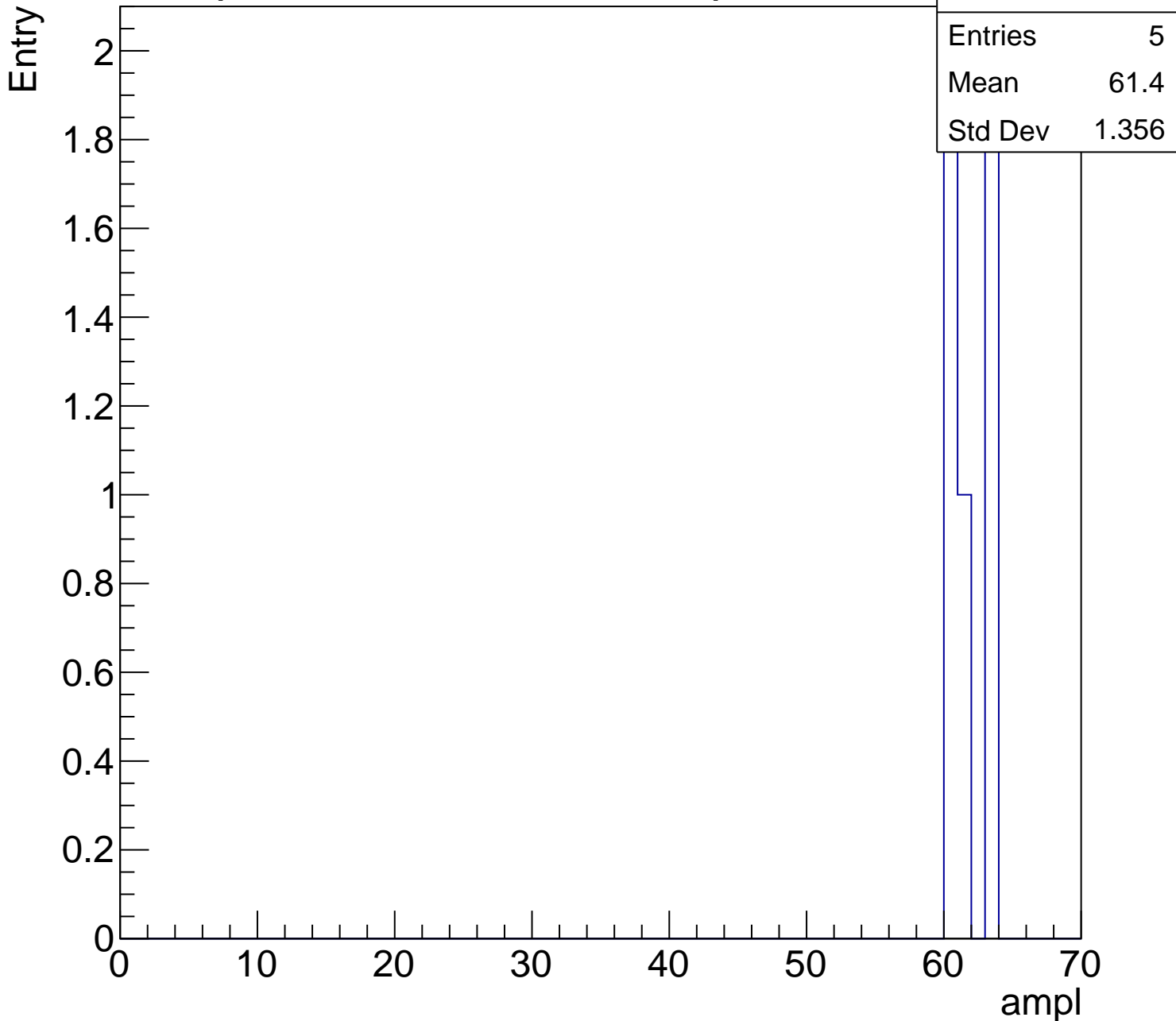
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch11, adc0

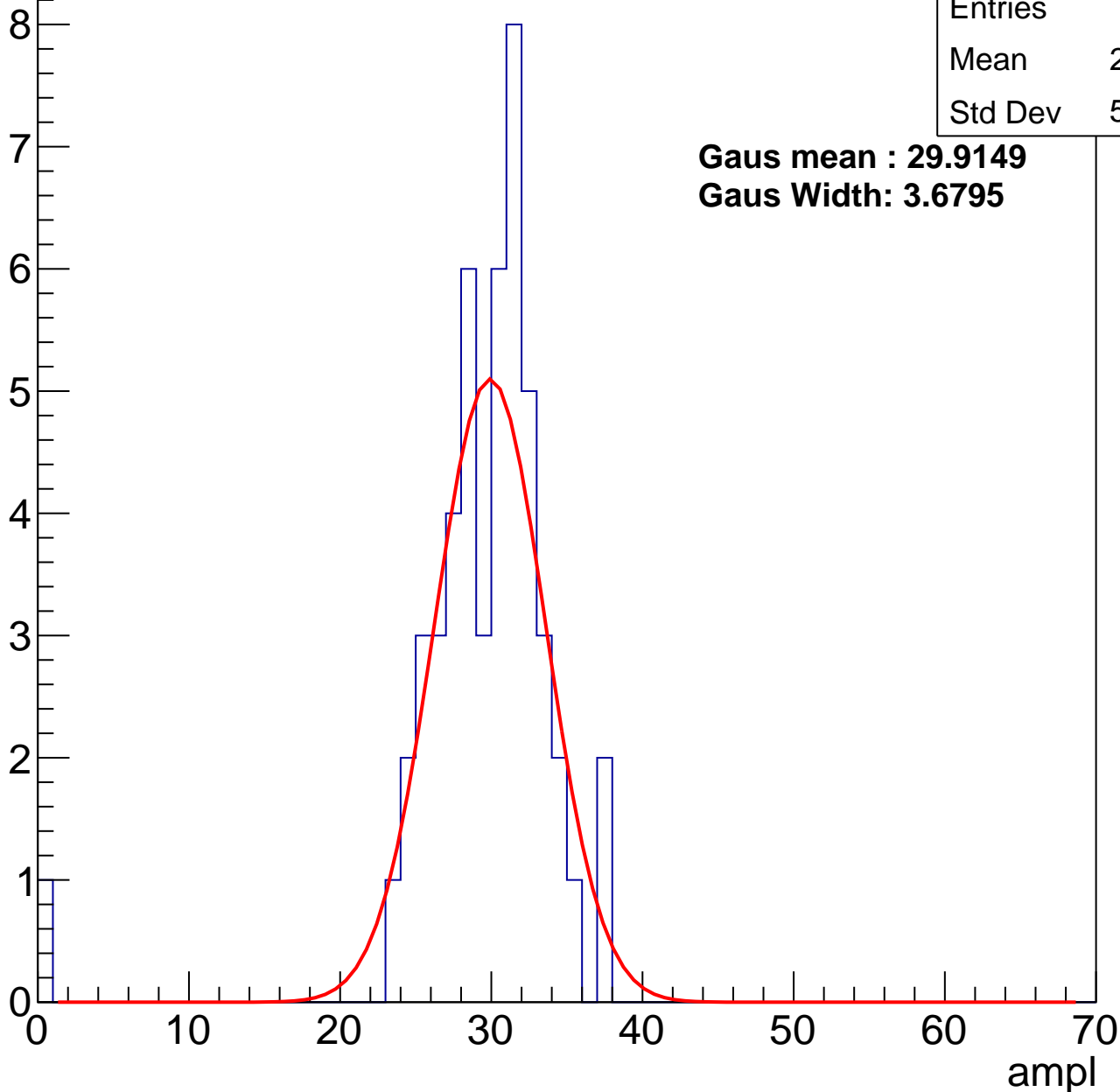
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	29.02
Std Dev	5.233

Gaus mean : 29.9149

Gaus Width: 3.6795



B1L102S, U6-ch11, adc1

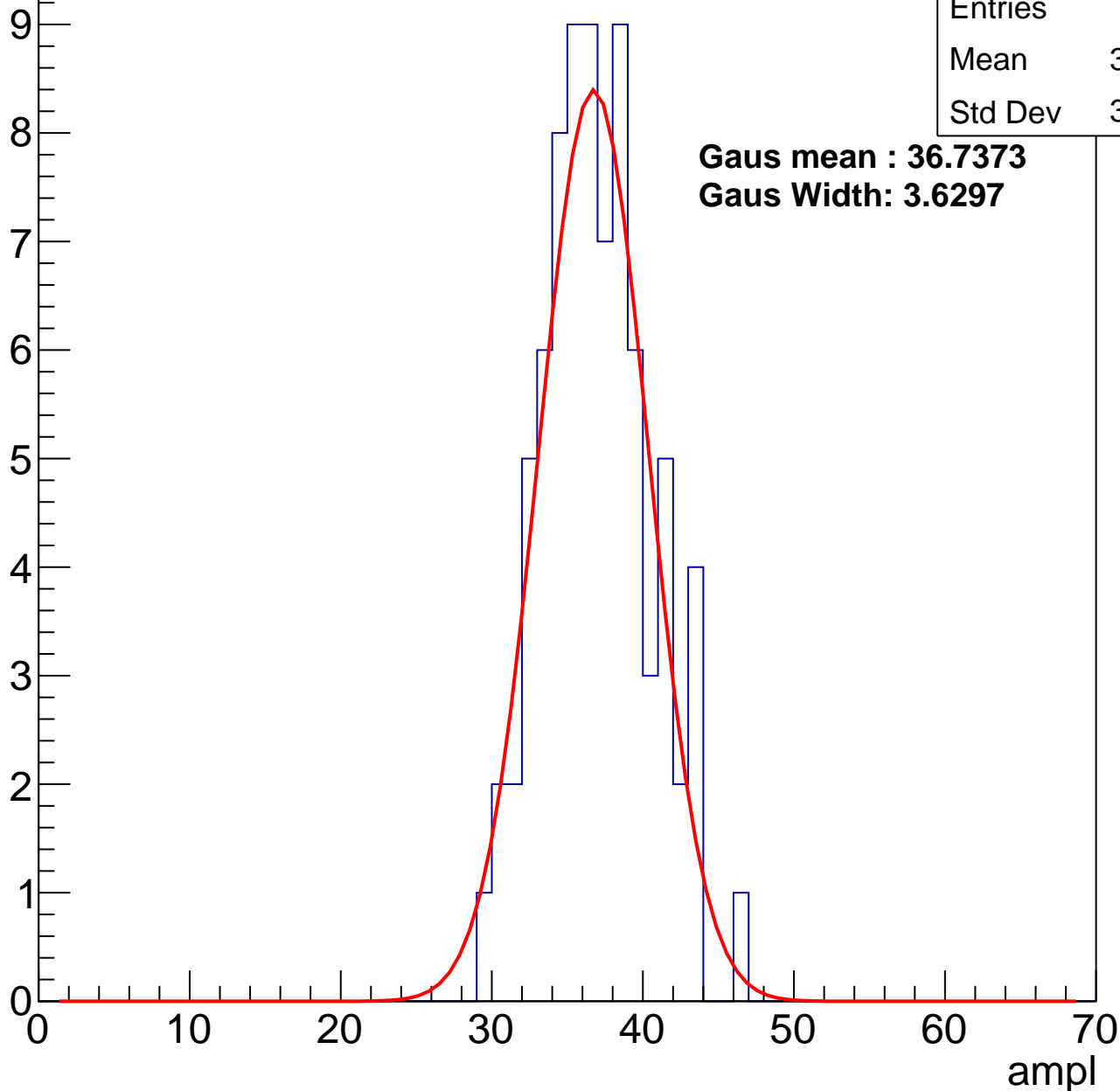
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.48
Std Dev	3.496

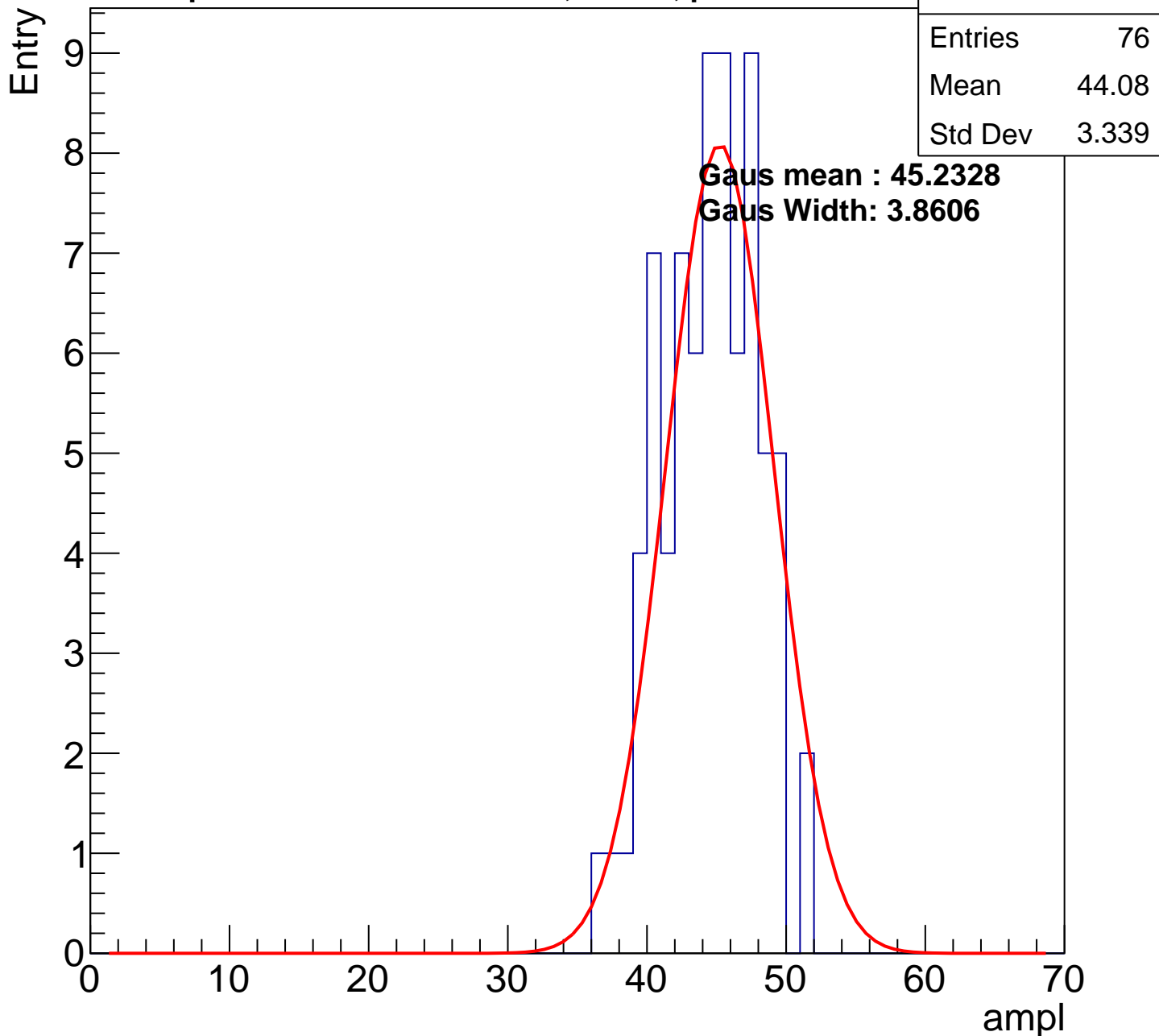
Gaus mean : 36.7373

Gaus Width: 3.6297



B1L102S, U6-ch11, adc2

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

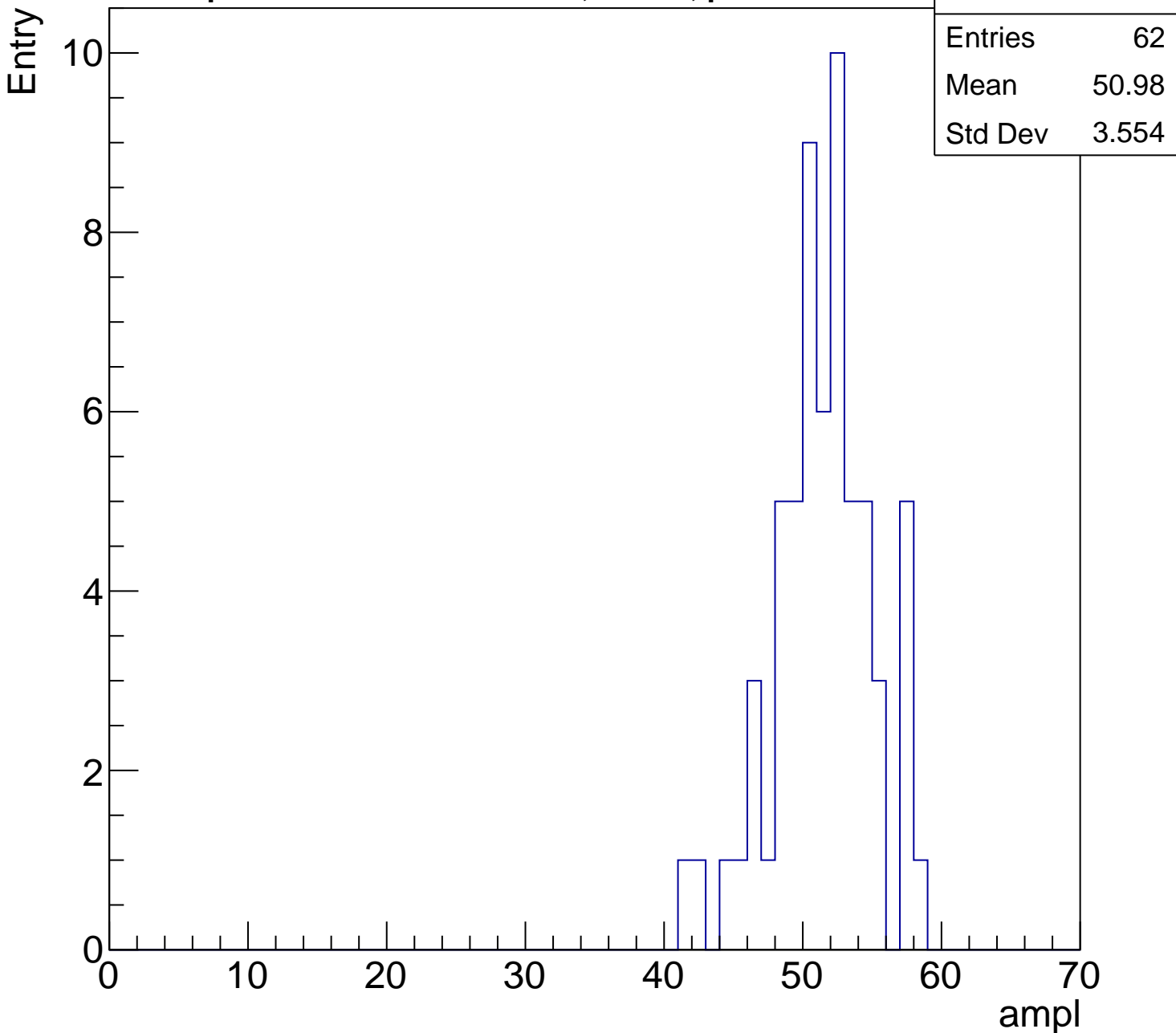
Entries	62
Mean	50.98
Std Dev	3.554

Entry

10
8
6
4
2
0

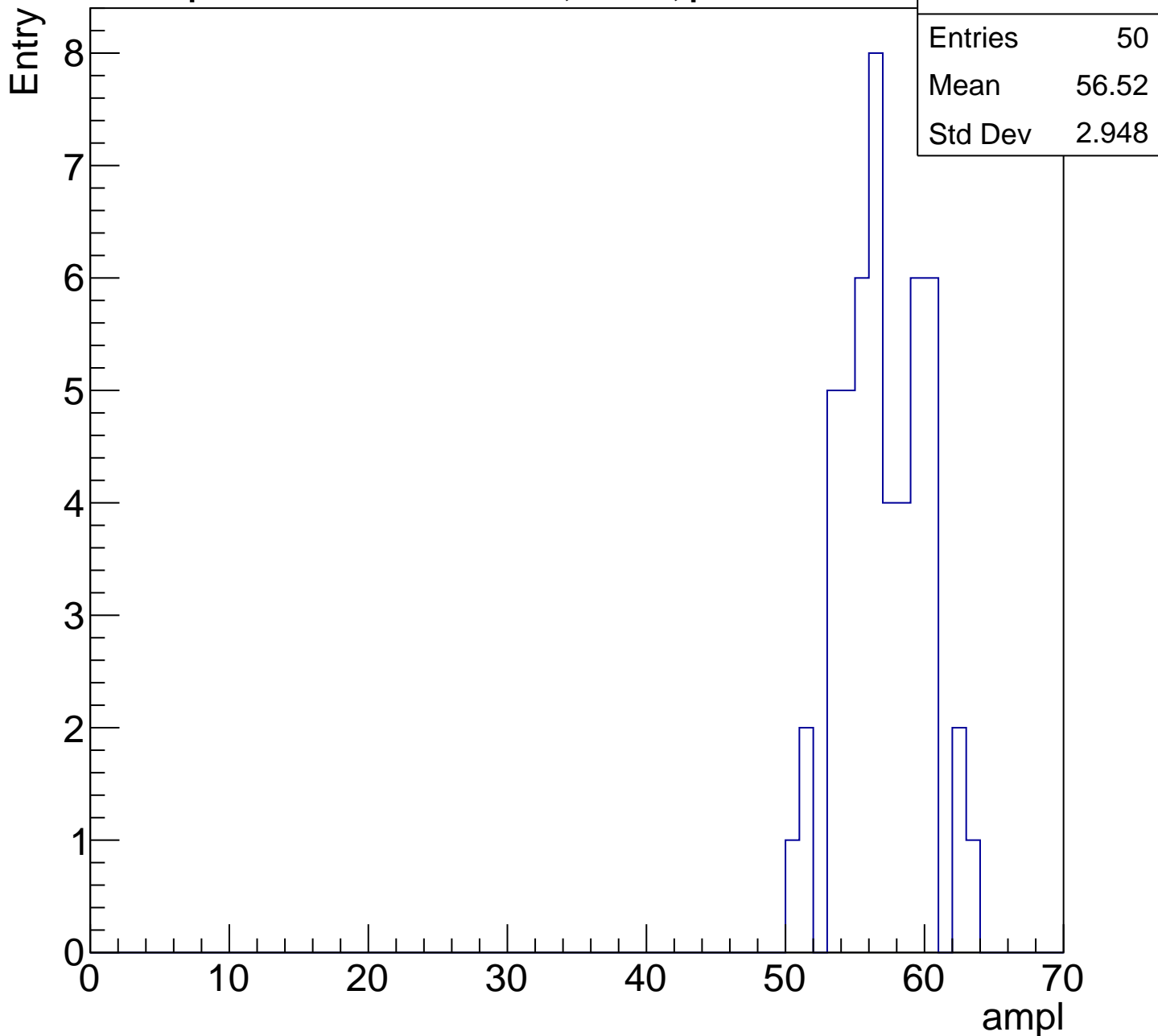
0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

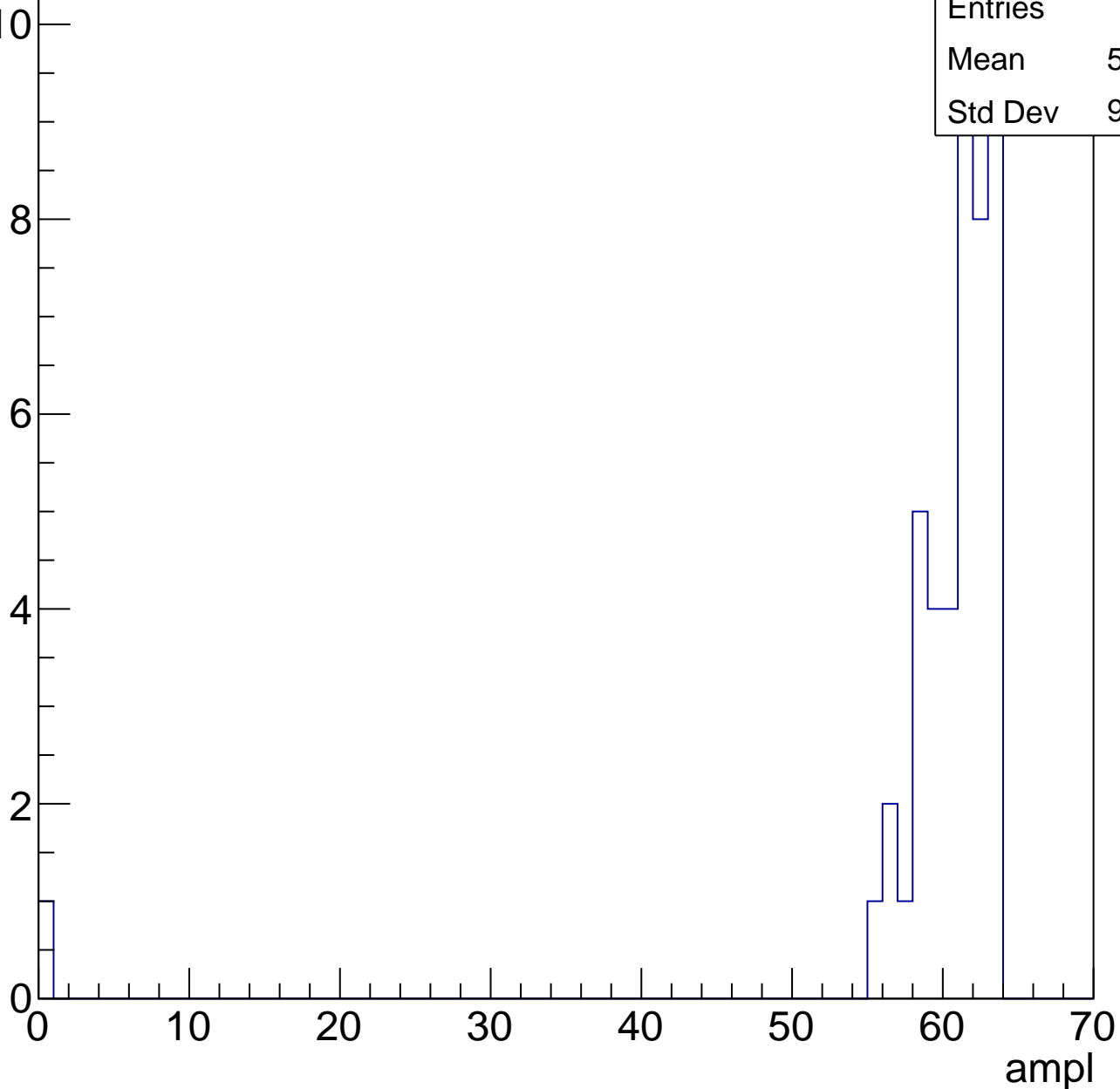


B1L102S, U6-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.18
Std Dev	9.168



B1L102S, U6-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch12, adc0

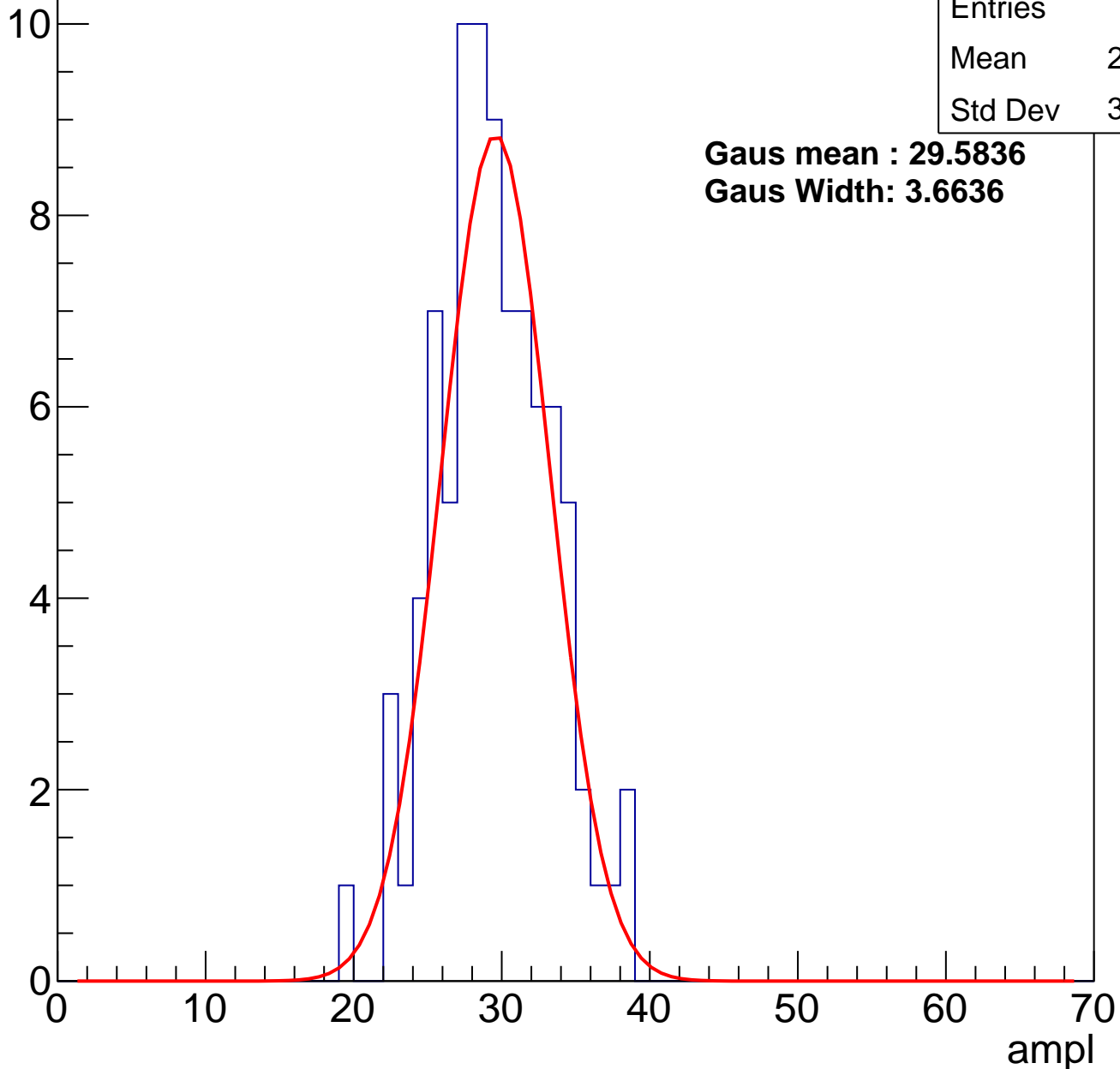
calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	29.03
Std Dev	3.786

Gaus mean : 29.5836

Gaus Width: 3.6636

Entry



B1L102S, U6-ch12, adc1

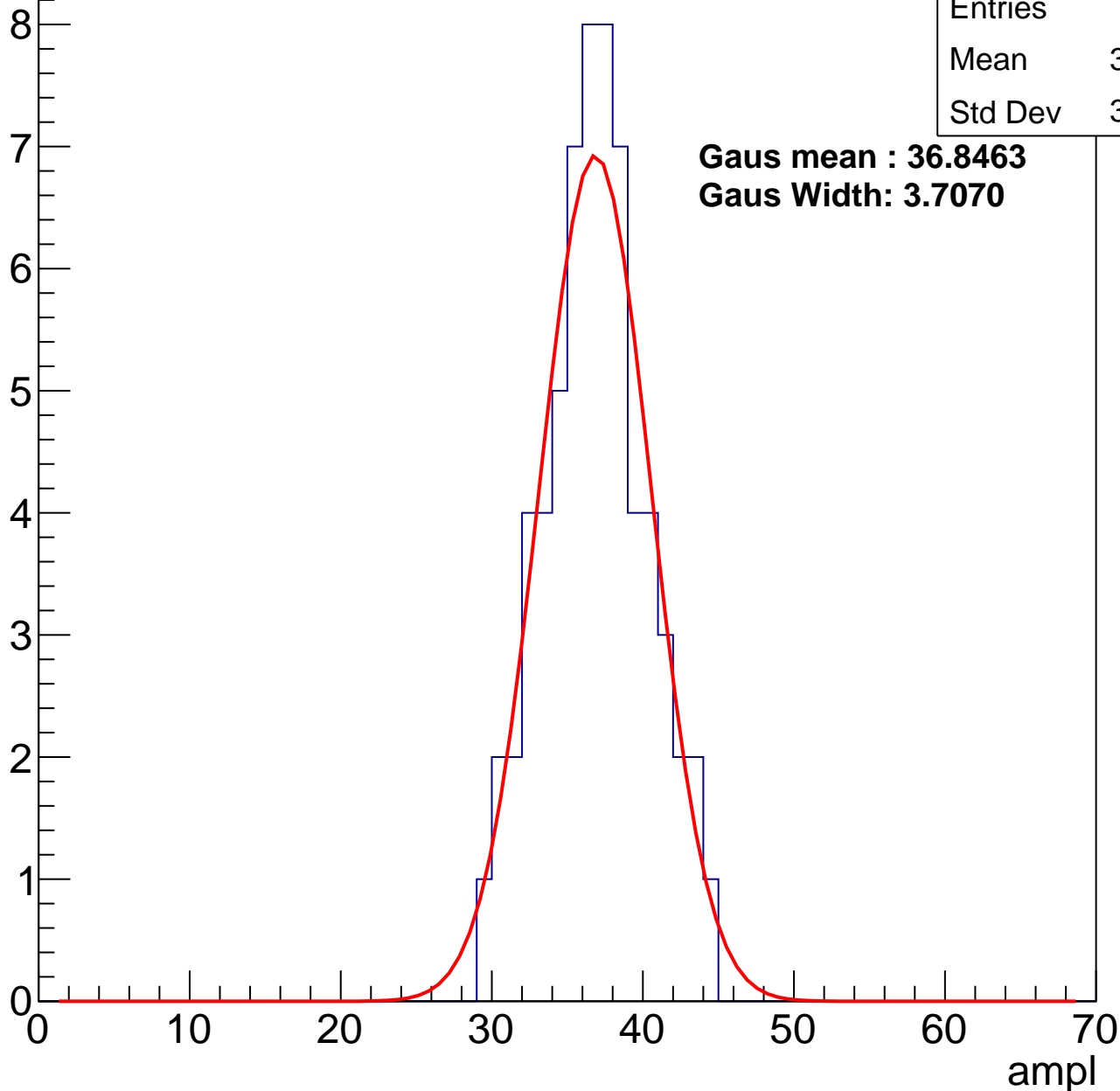
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	36.39
Std Dev	3.385

Gaus mean : 36.8463

Gaus Width: 3.7070

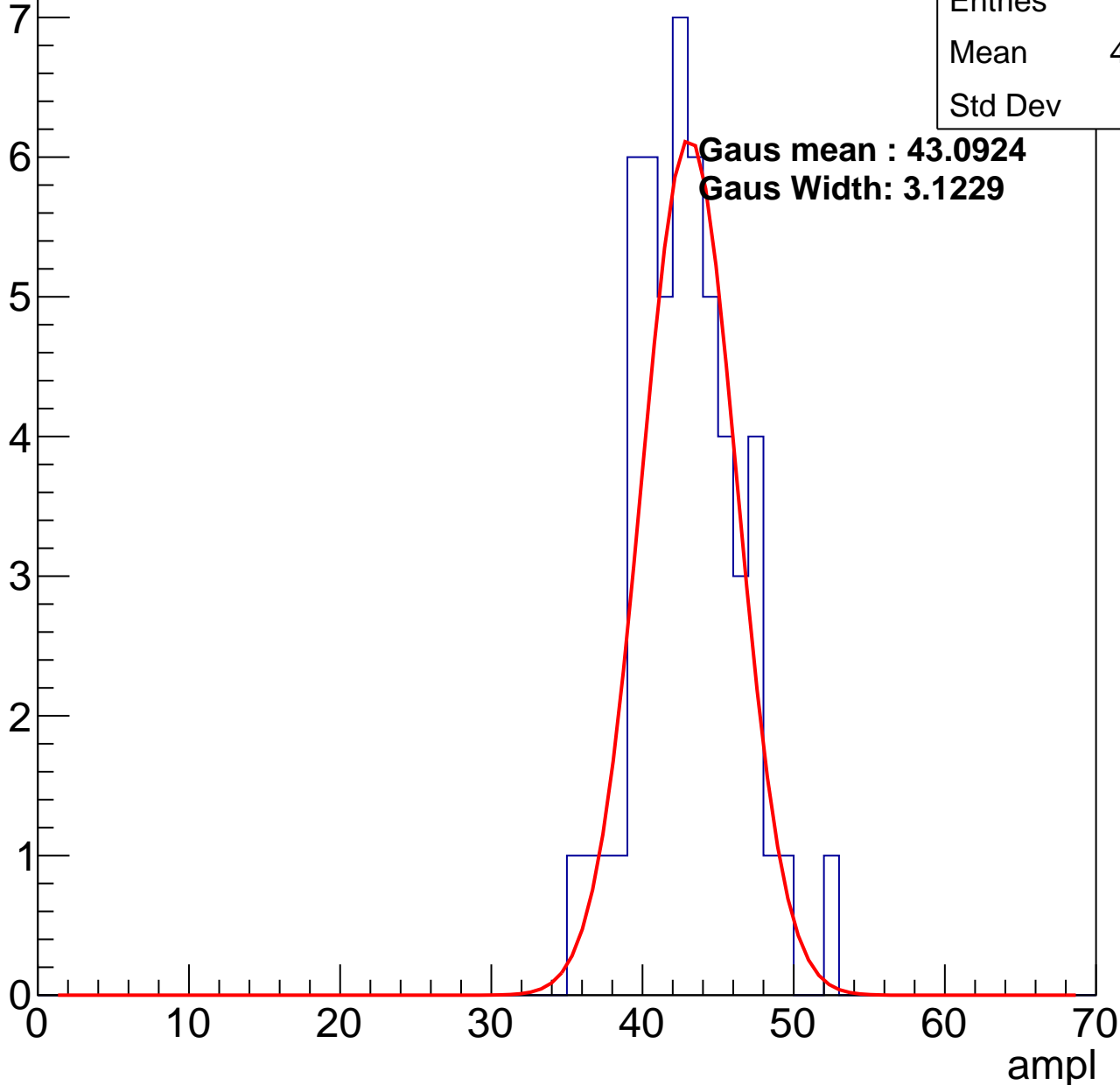


B1L102S, U6-ch12, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.49
Std Dev	3.34

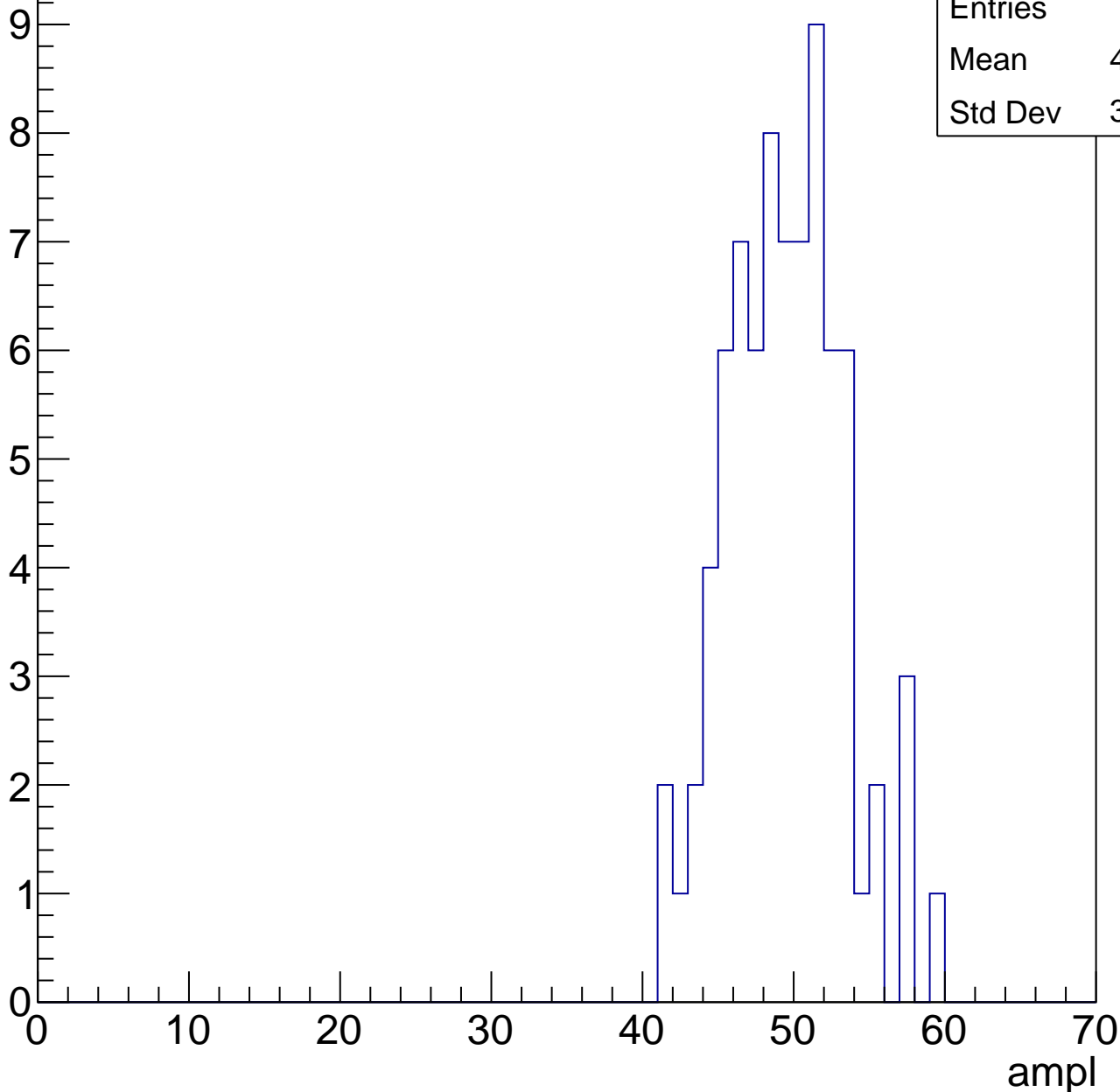


B1L102S, U6-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

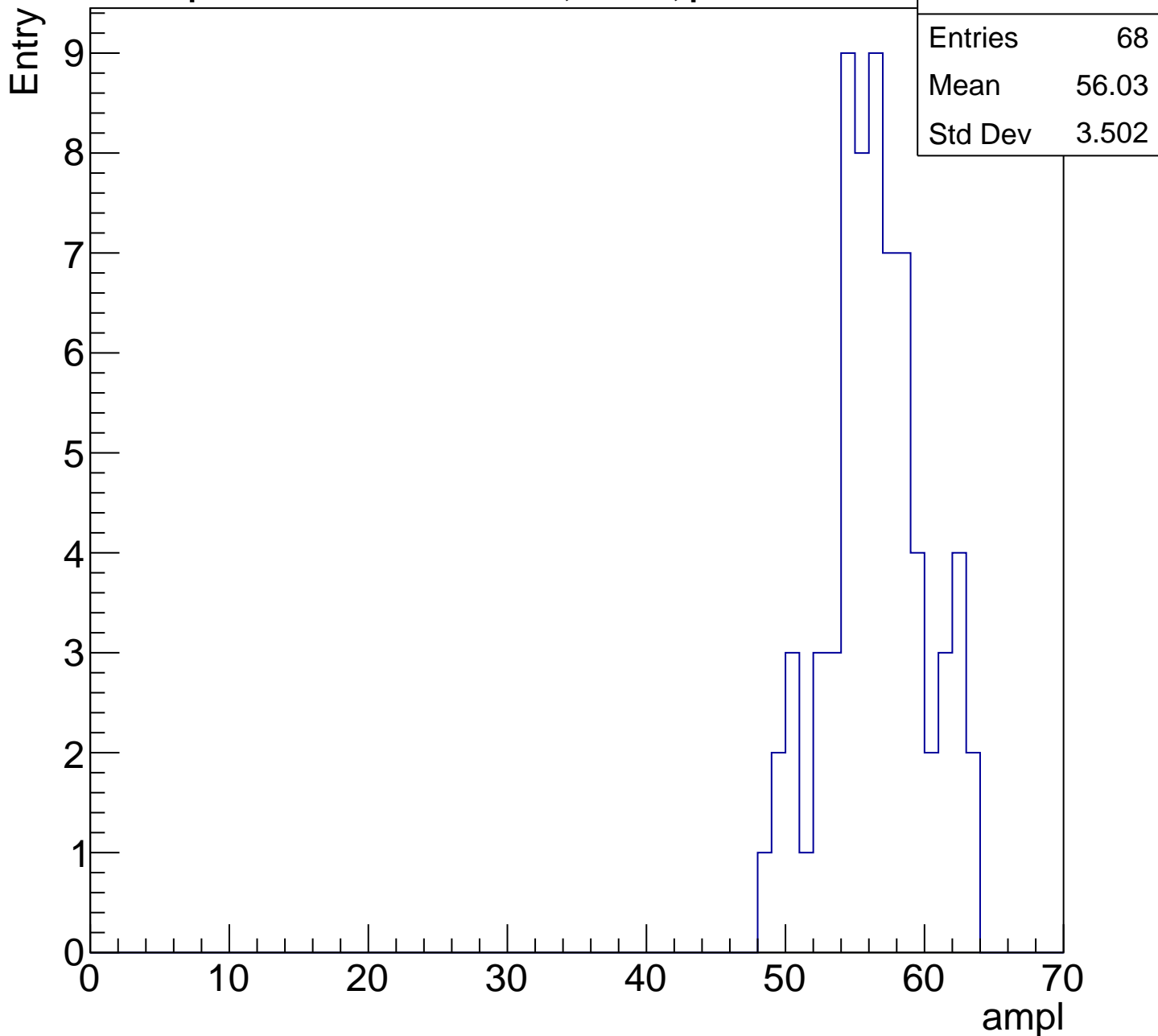
Entry

Entries	78
Mean	48.97
Std Dev	3.793



B1L102S, U6-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

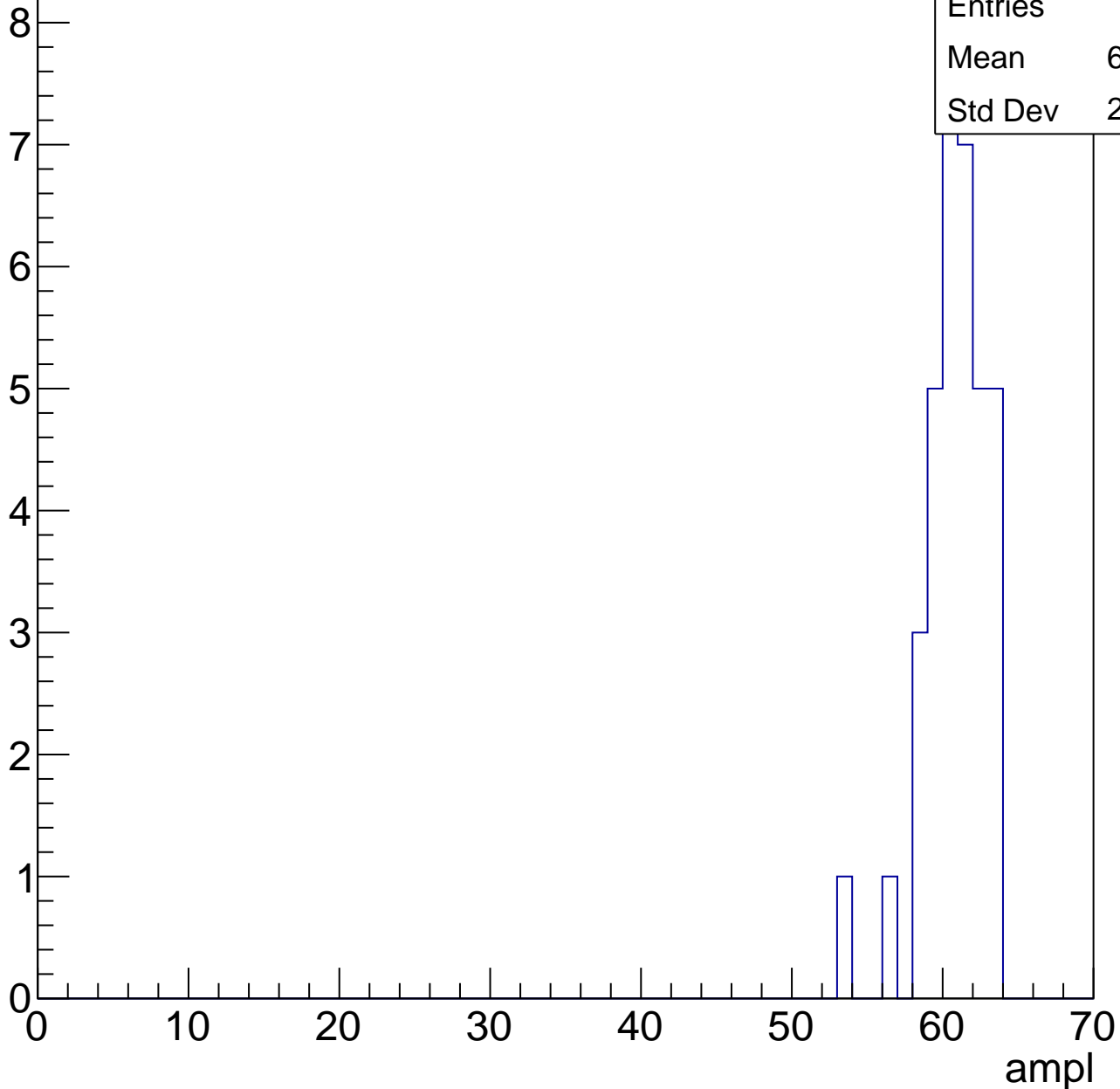


B1L102S, U6-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

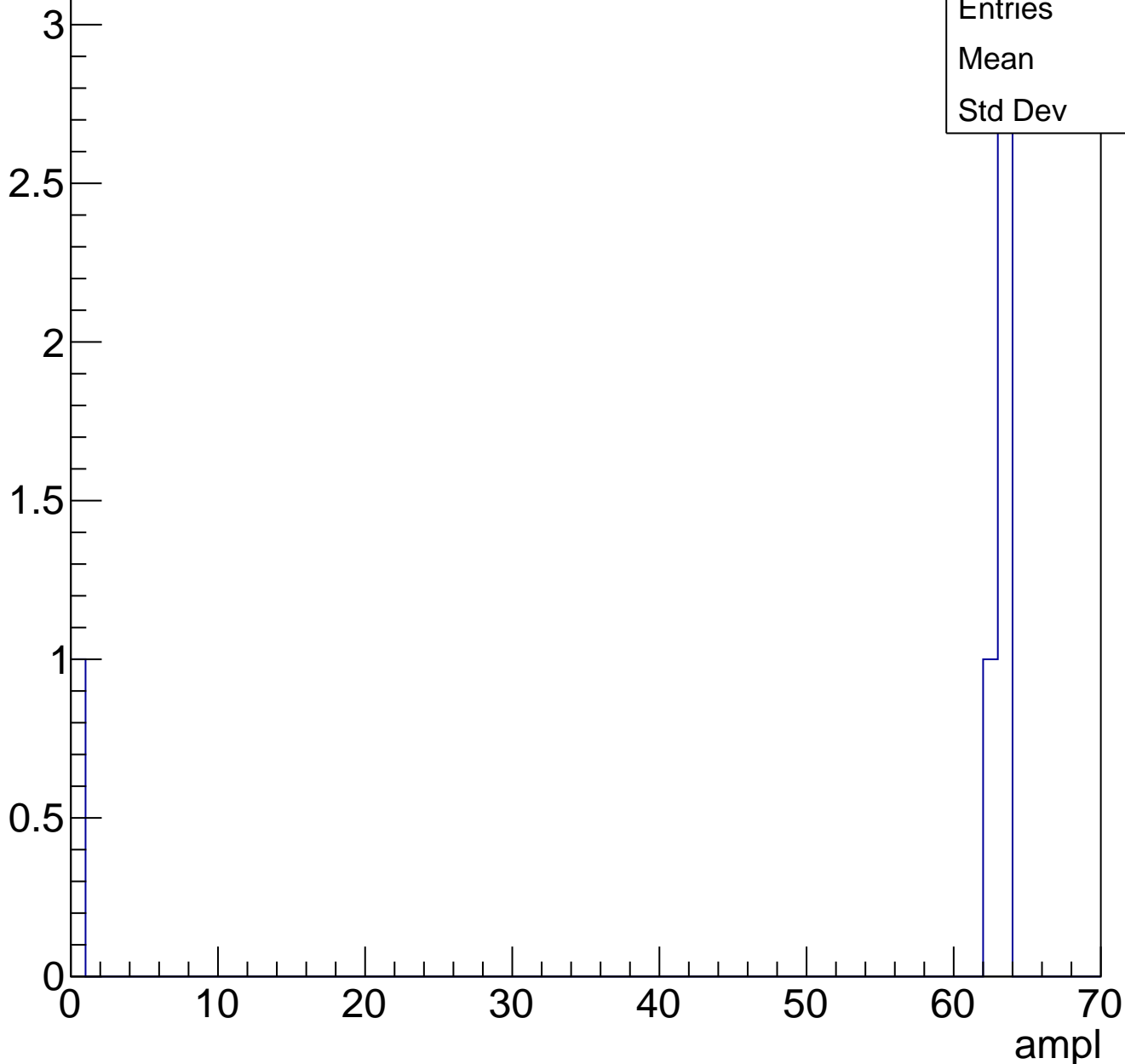
Entries	35
Mean	60.29
Std Dev	2.078



B1L102S, U6-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

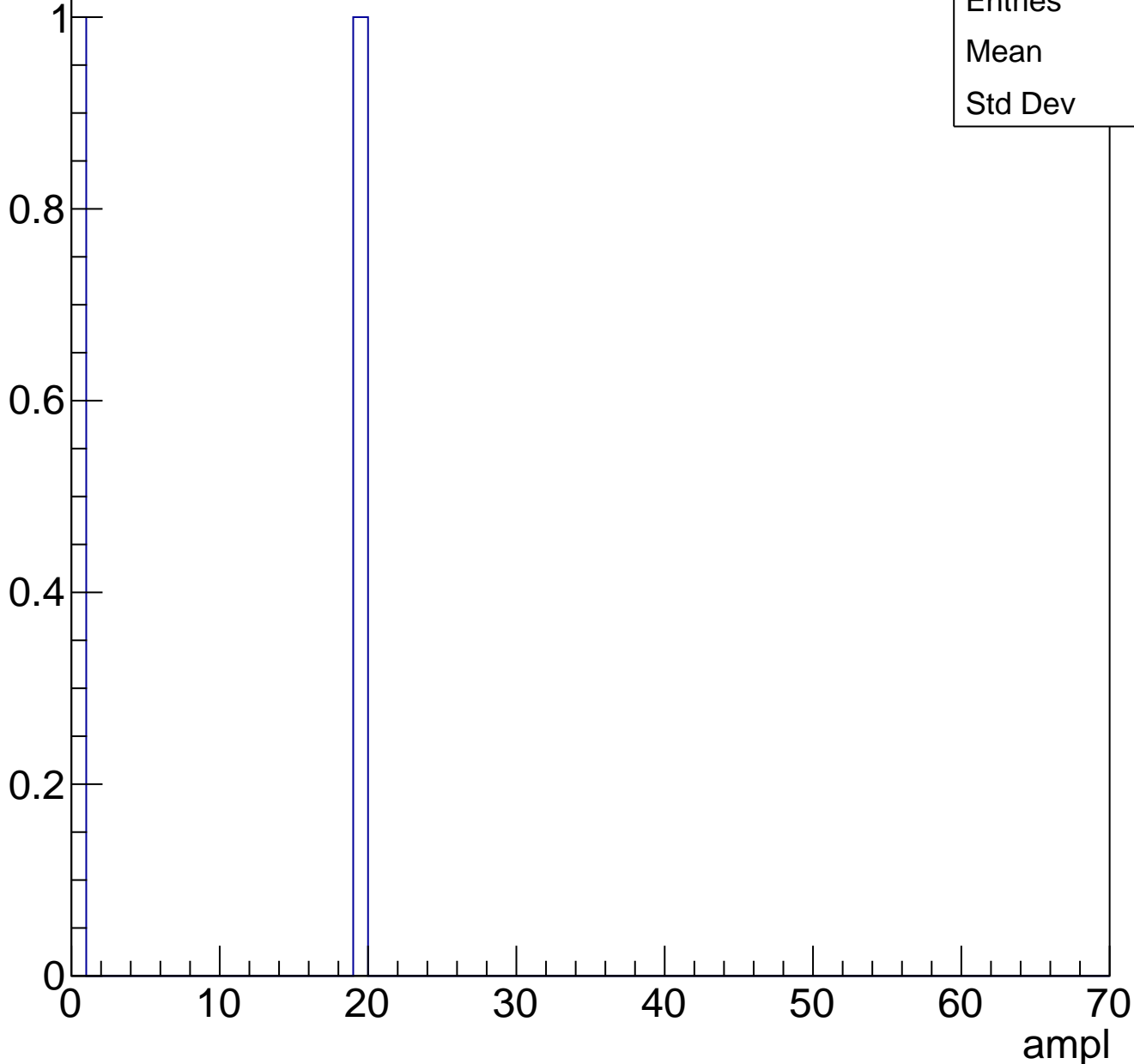
Entry



B1L102S, U6-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch13, adc0

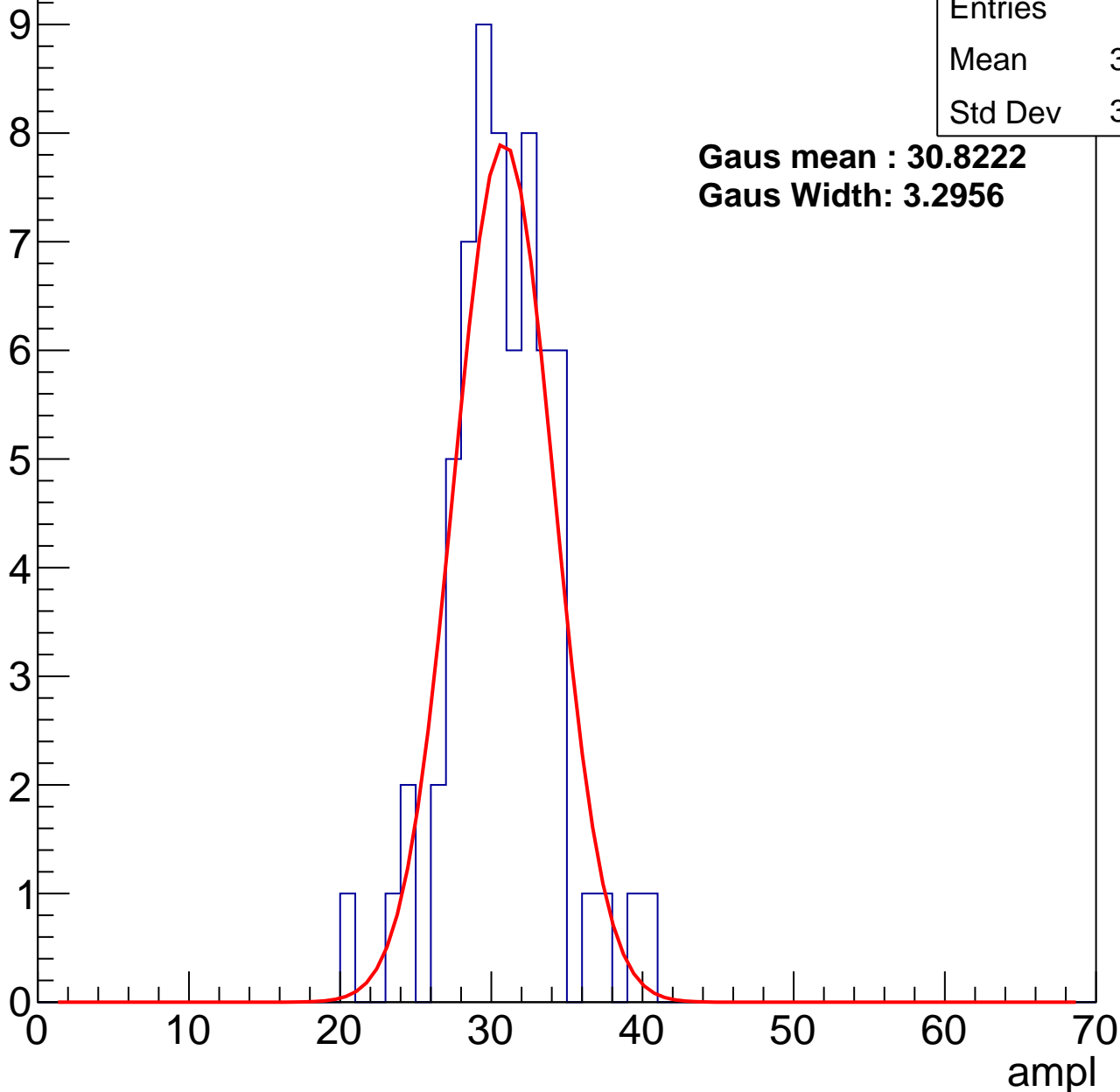
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	30.32
Std Dev	3.469

Gaus mean : 30.8222

Gaus Width: 3.2956



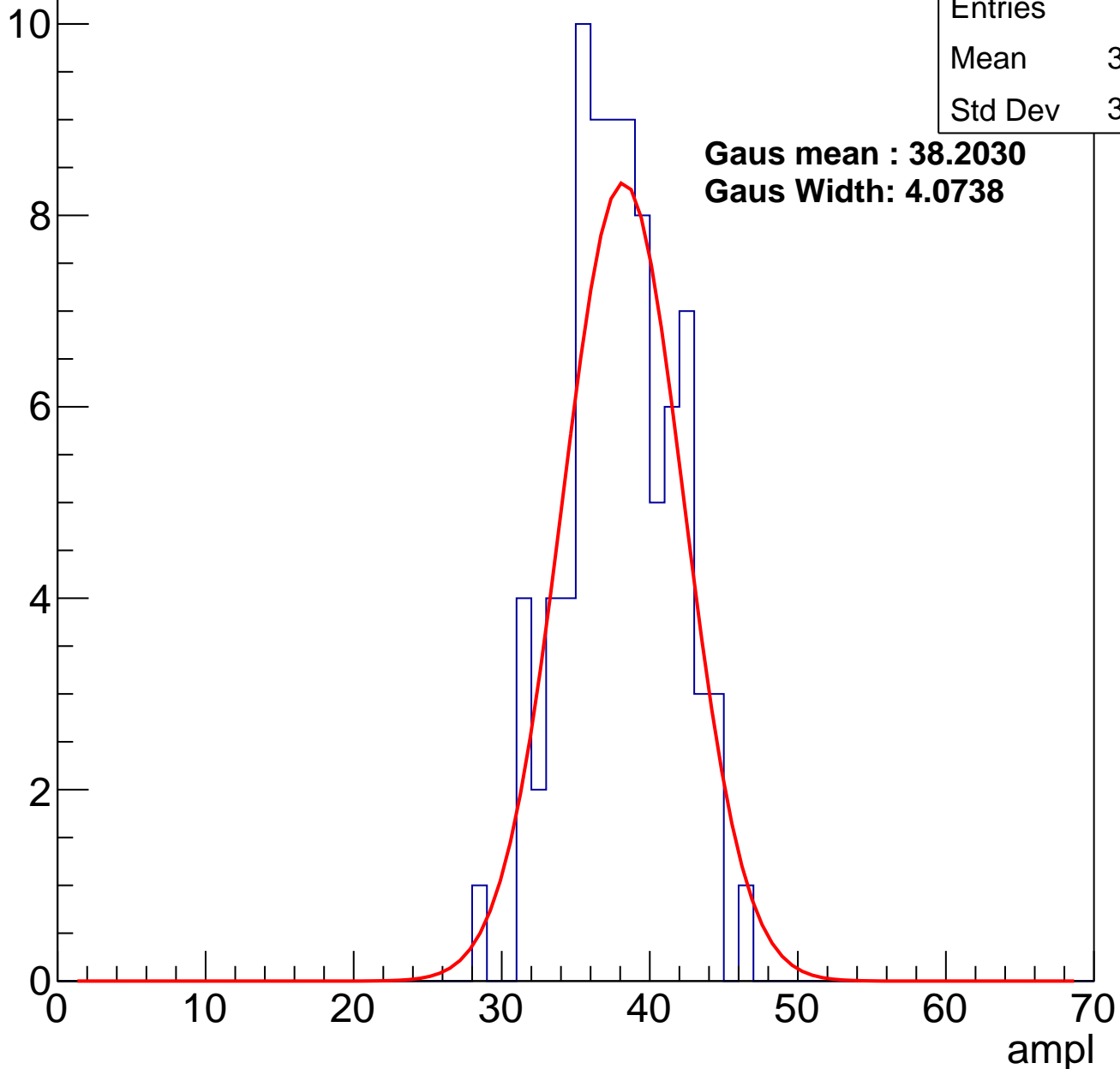
B1L102S, U6-ch13, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	85
Mean	37.55
Std Dev	3.583

Gaus mean : 38.2030
Gaus Width: 4.0738

Entry



B1L102S, U6-ch13, adc2

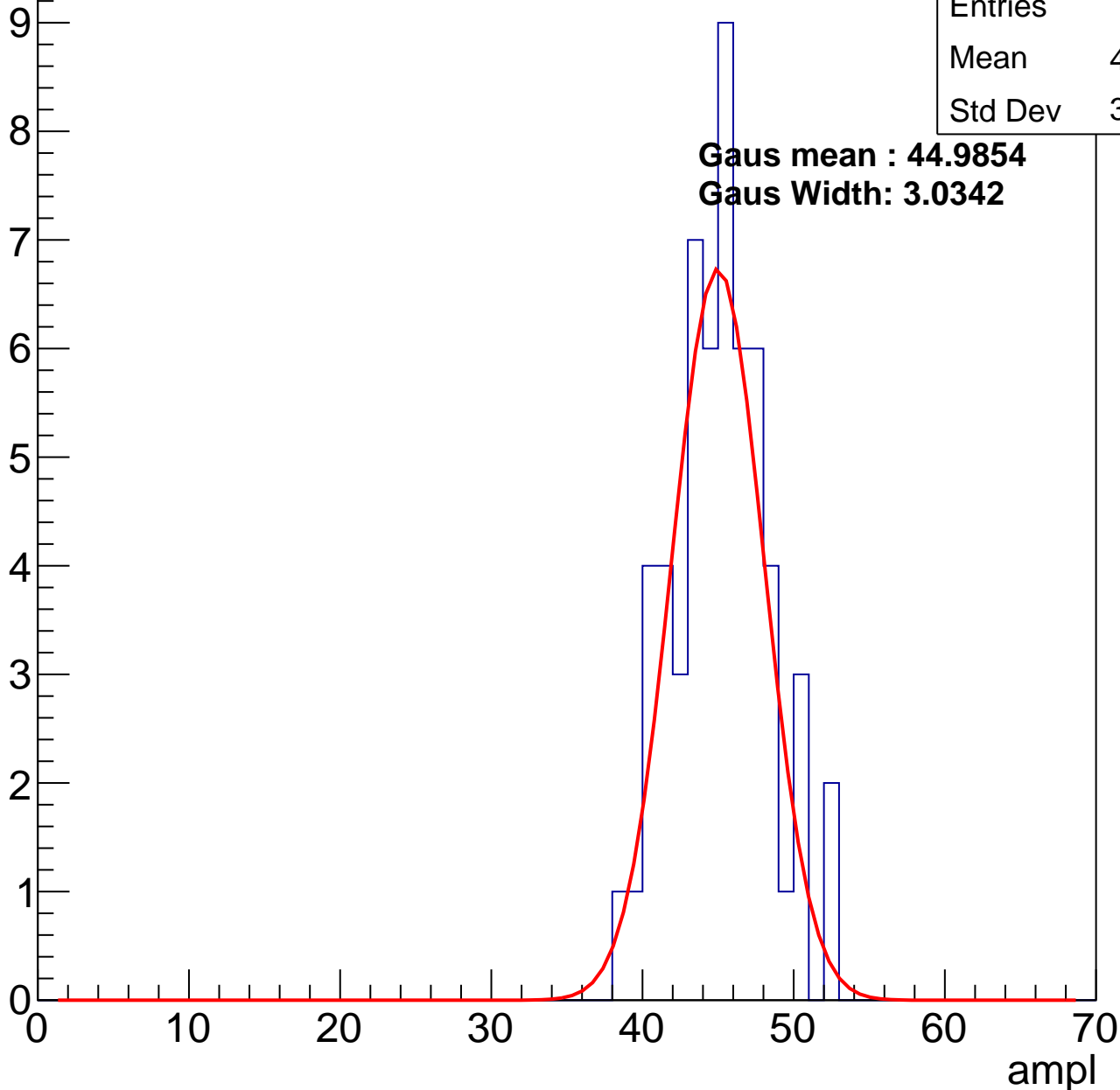
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	44.74
Std Dev	3.137

Gaus mean : 44.9854

Gaus Width: 3.0342

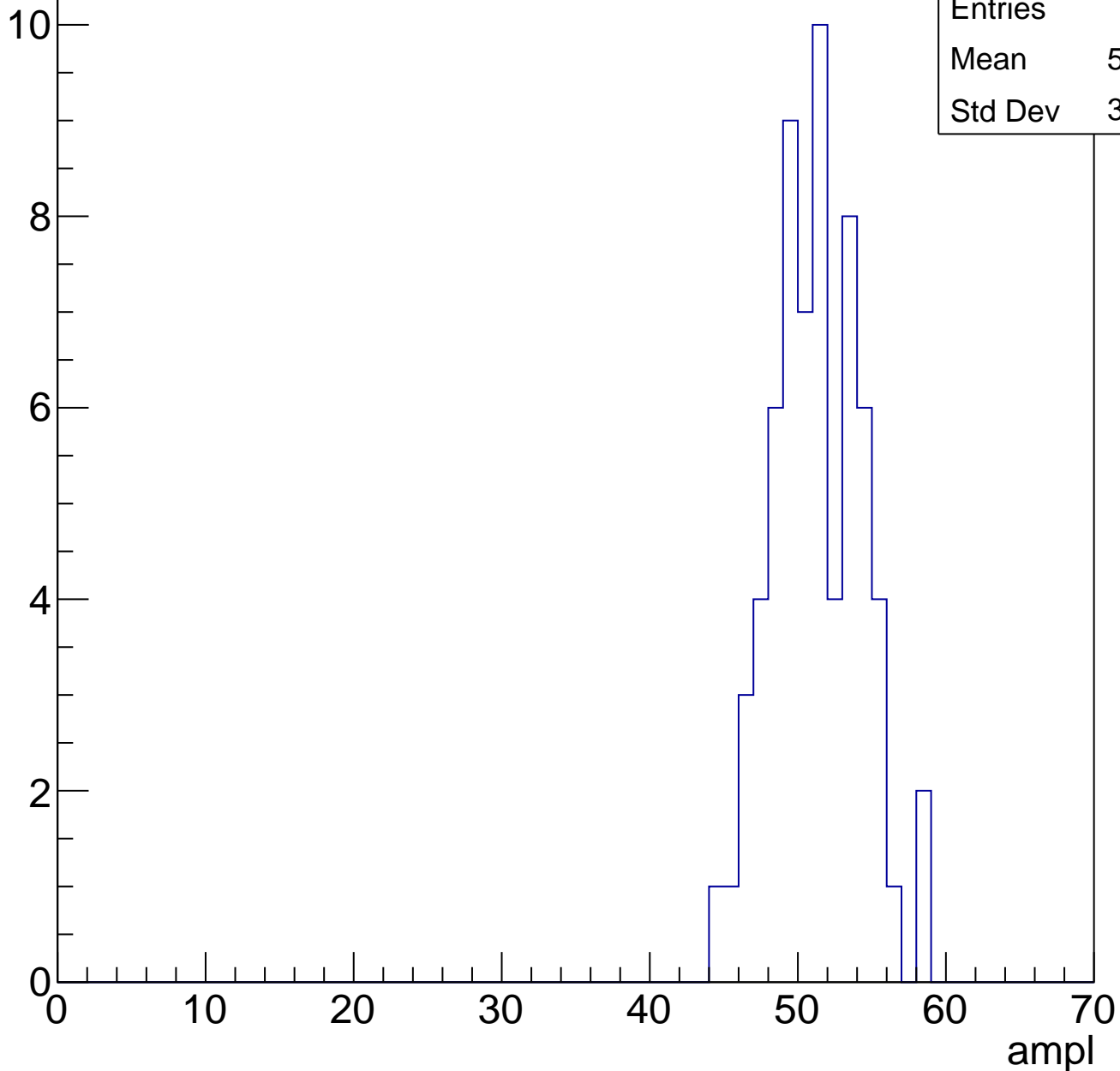


B1L102S, U6-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	50.79
Std Dev	3.003

Entry

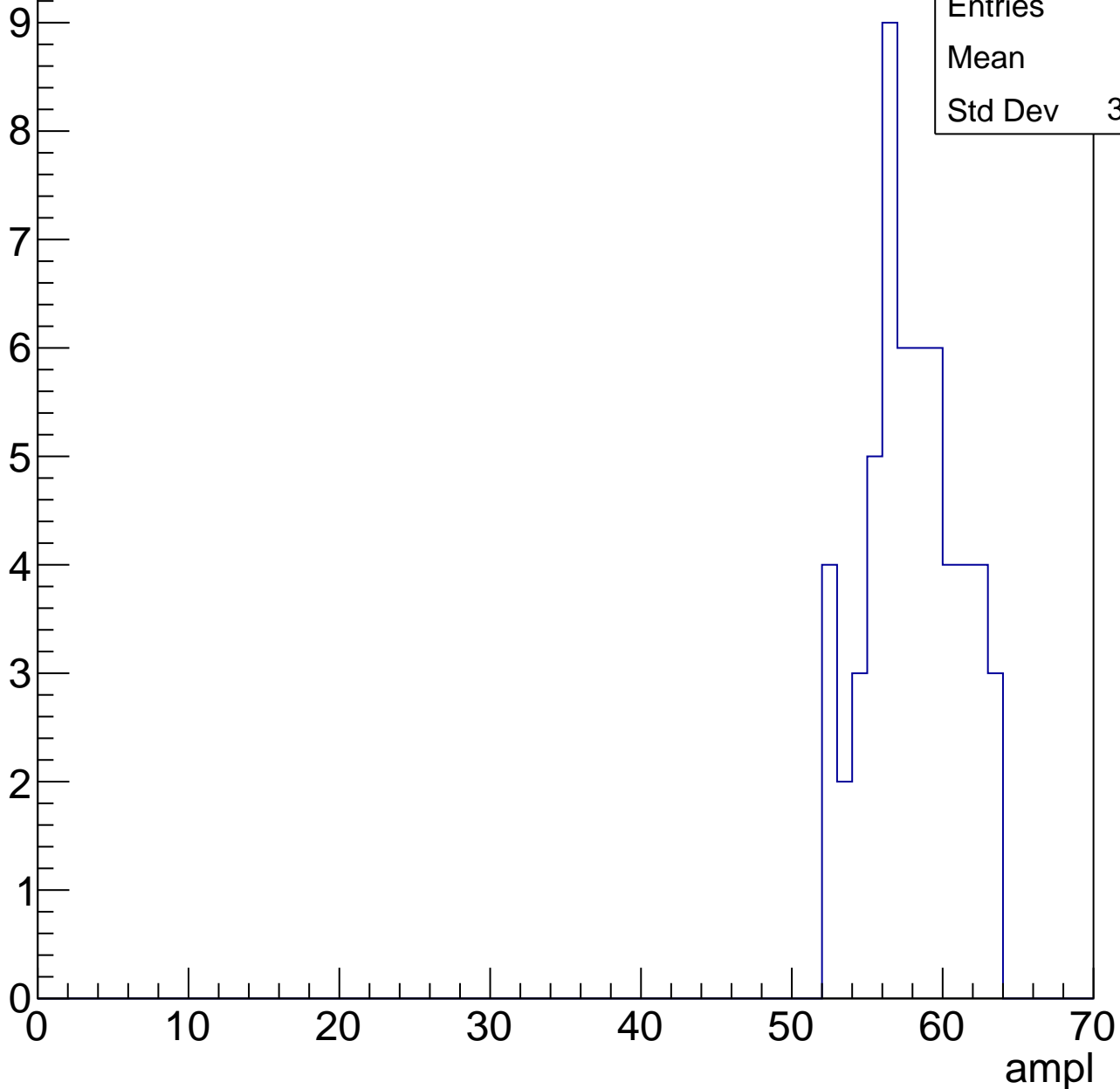


B1L102S, U6-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	57.5
Std Dev	3.024

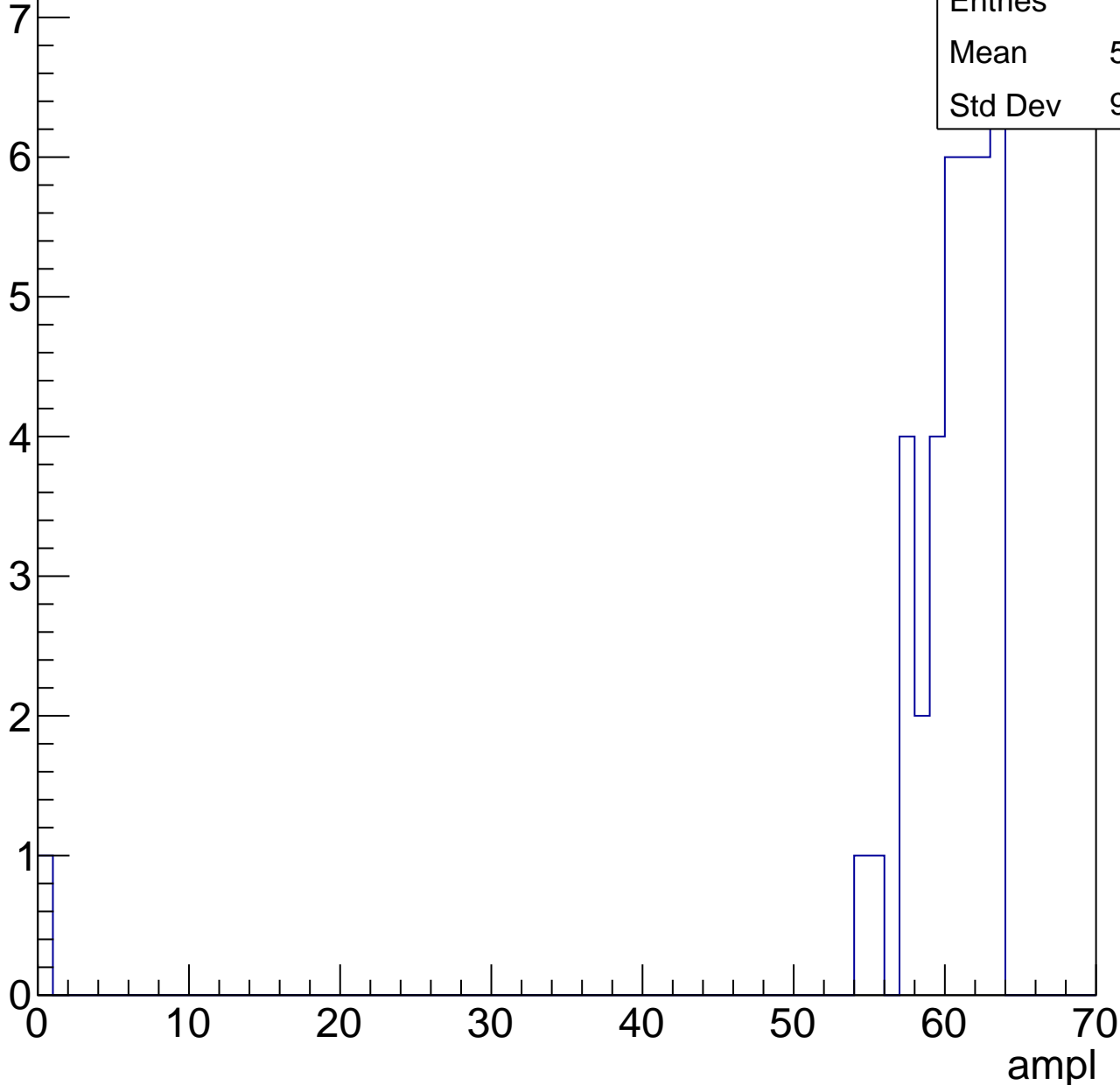


B1L102S, U6-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	58.63
Std Dev	9.909



B1L102S, U6-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch14, adc0

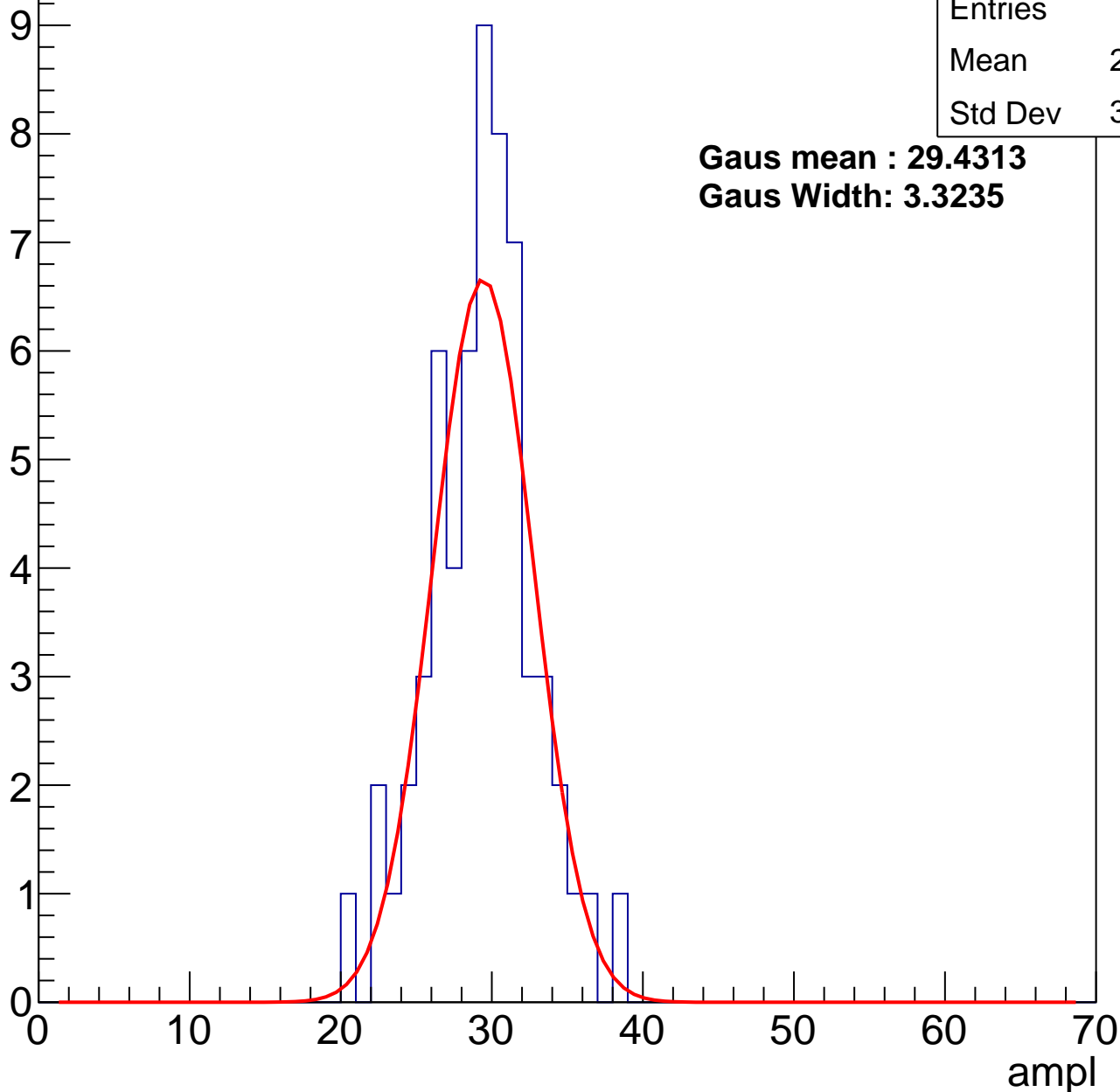
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.87
Std Dev	3.452

Gaus mean : 29.4313

Gaus Width: 3.3235



B1L102S, U6-ch14, adc1

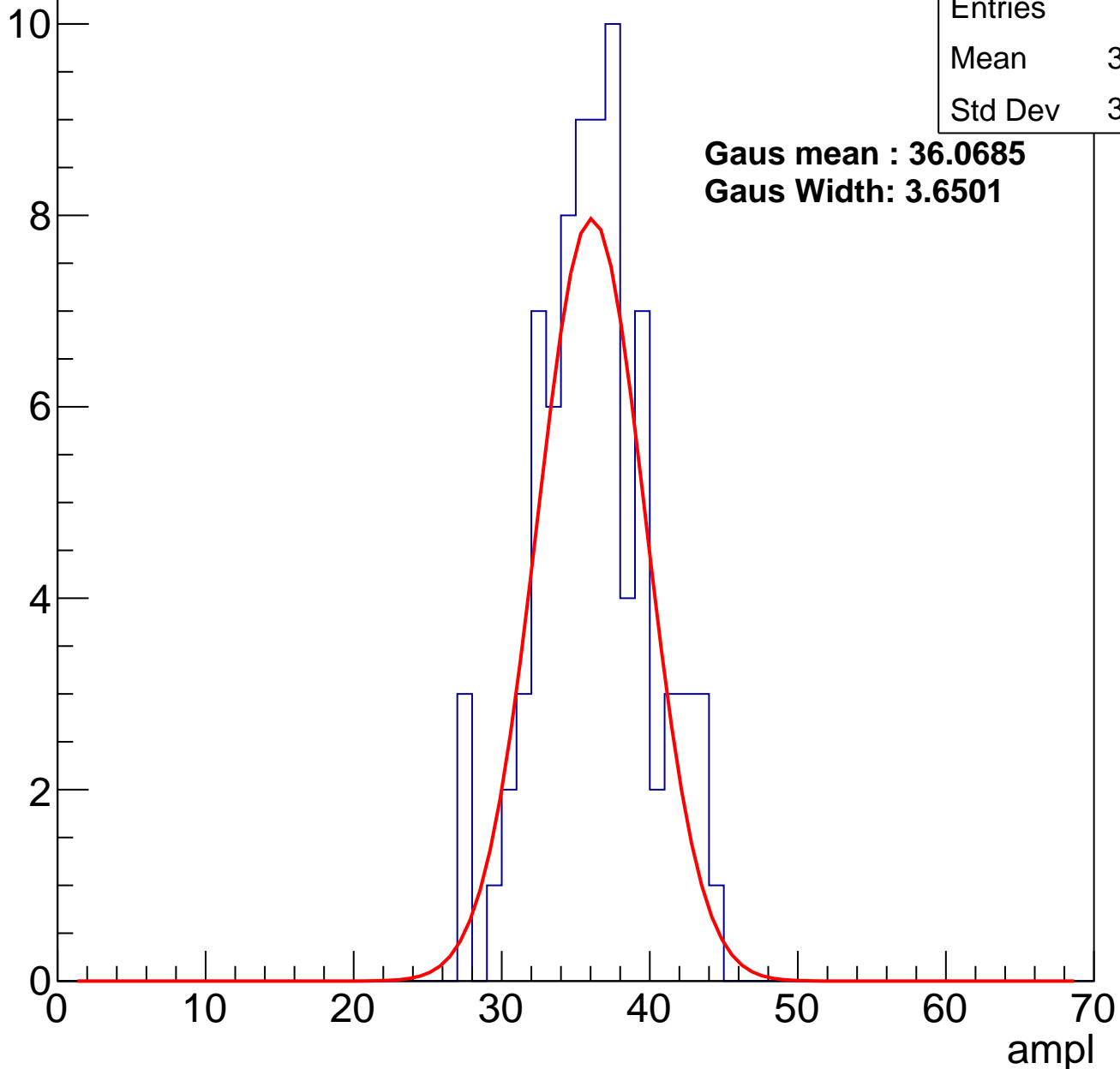
calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	35.72
Std Dev	3.775

Gaus mean : 36.0685

Gaus Width: 3.6501

Entry



B1L102S, U6-ch14, adc2

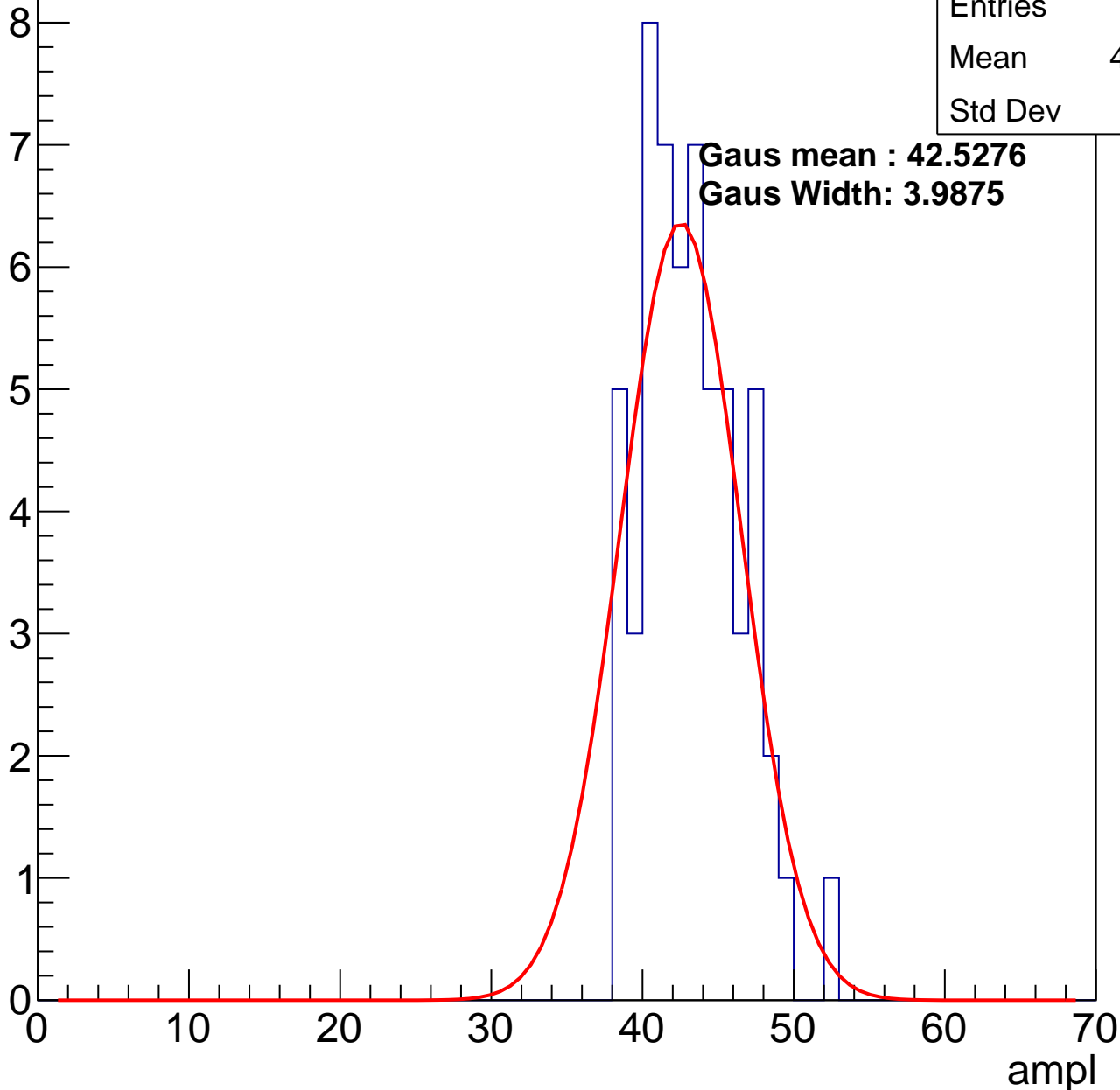
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.79
Std Dev	3.15

Gaus mean : 42.5276

Gaus Width: 3.9875

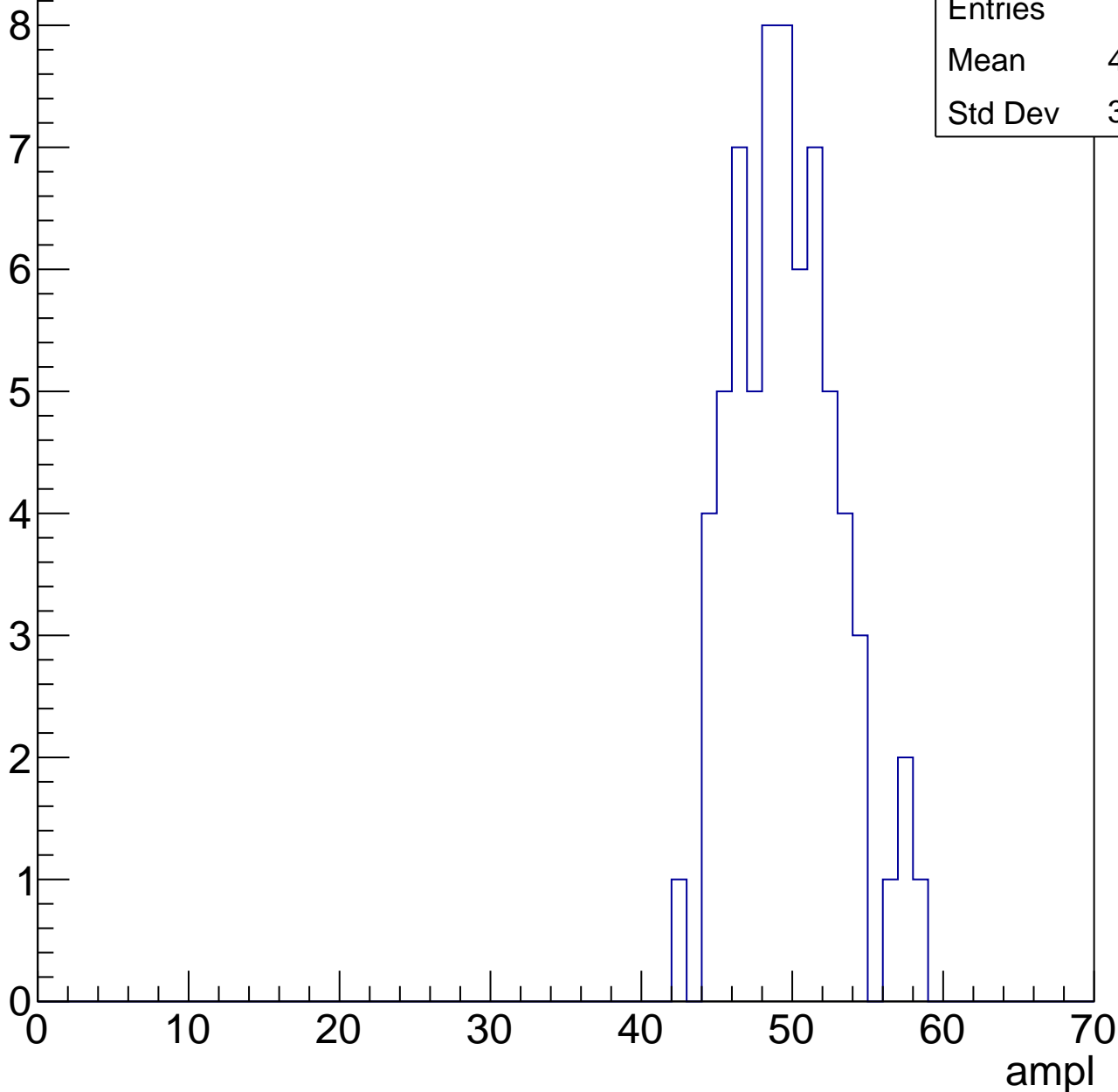


B1L102S, U6-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

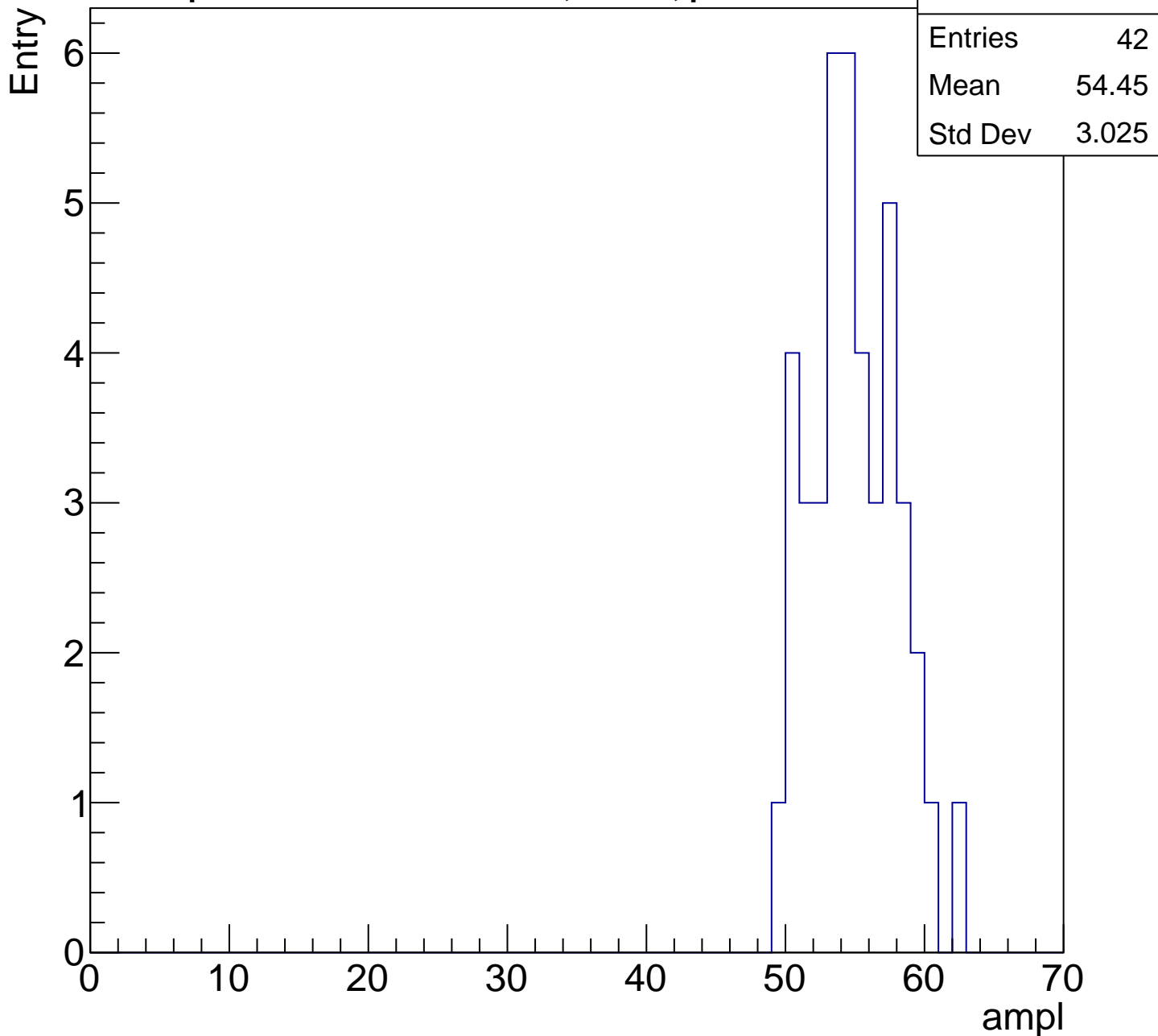
Entry

Entries	67
Mean	49.18
Std Dev	3.442



B1L102S, U6-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch14, adc5

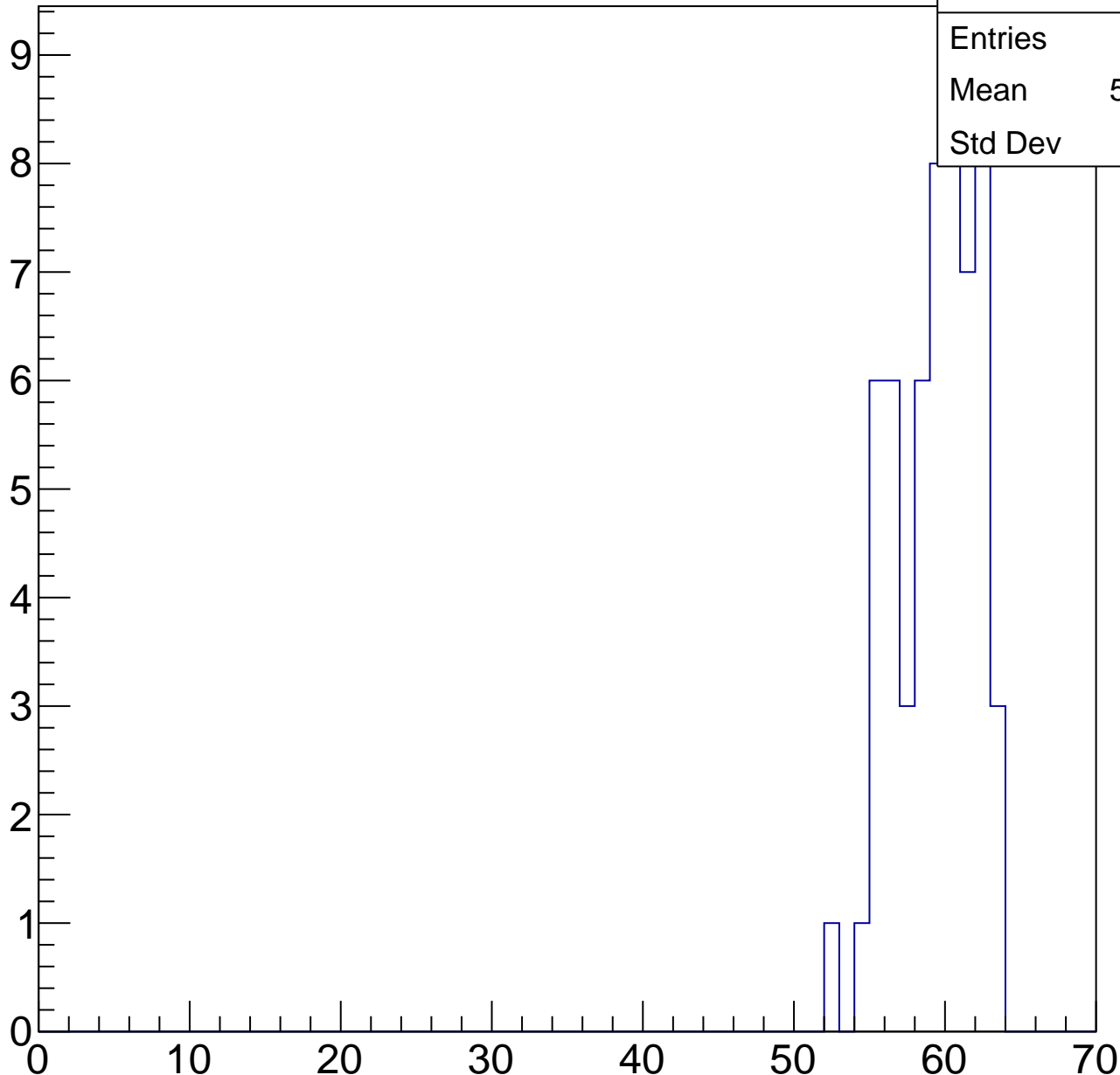
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.88
Std Dev	2.62

ampl

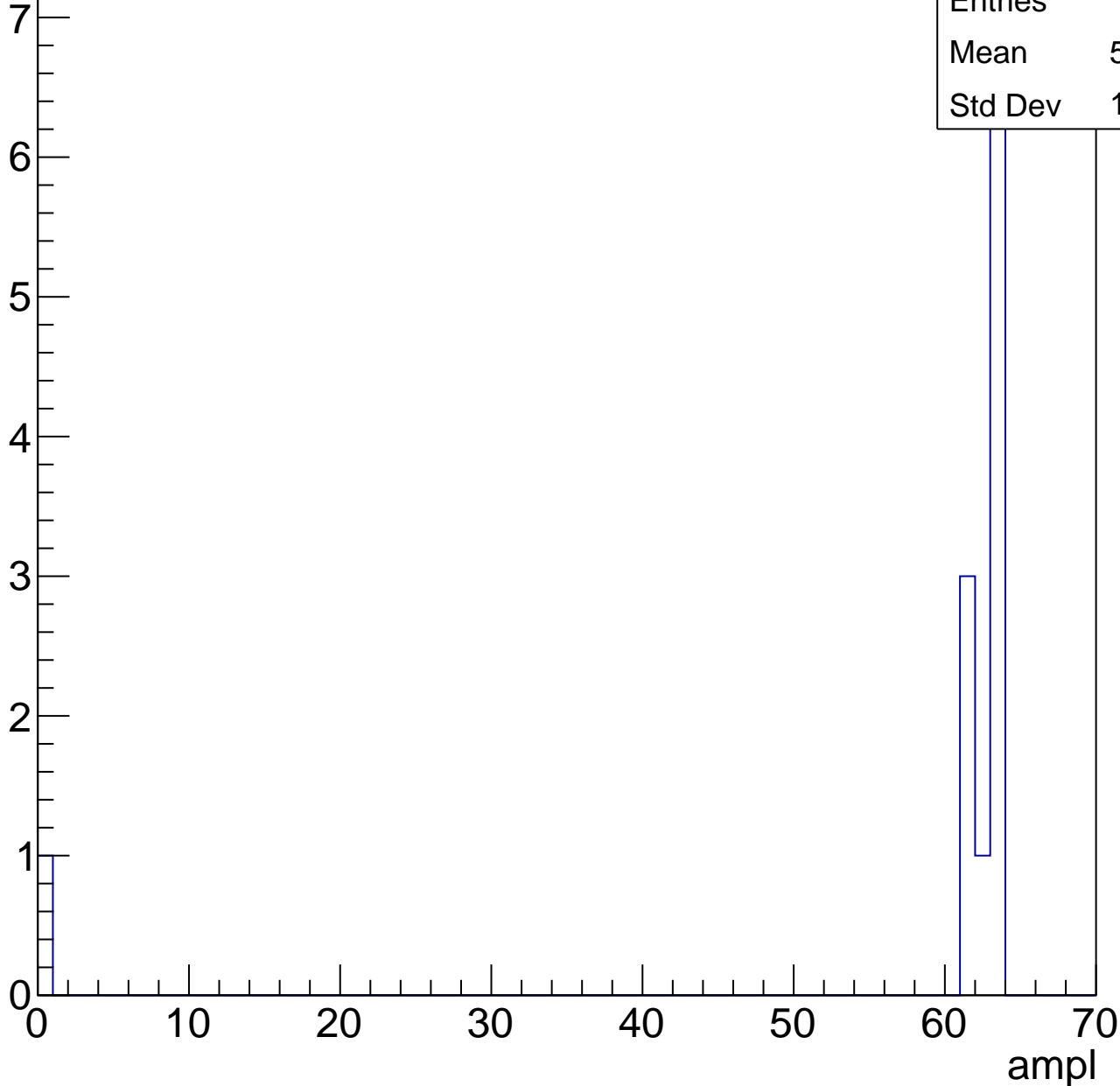


B1L102S, U6-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	57.17
Std Dev	17.26



B1L102S, U6-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch15, adc0

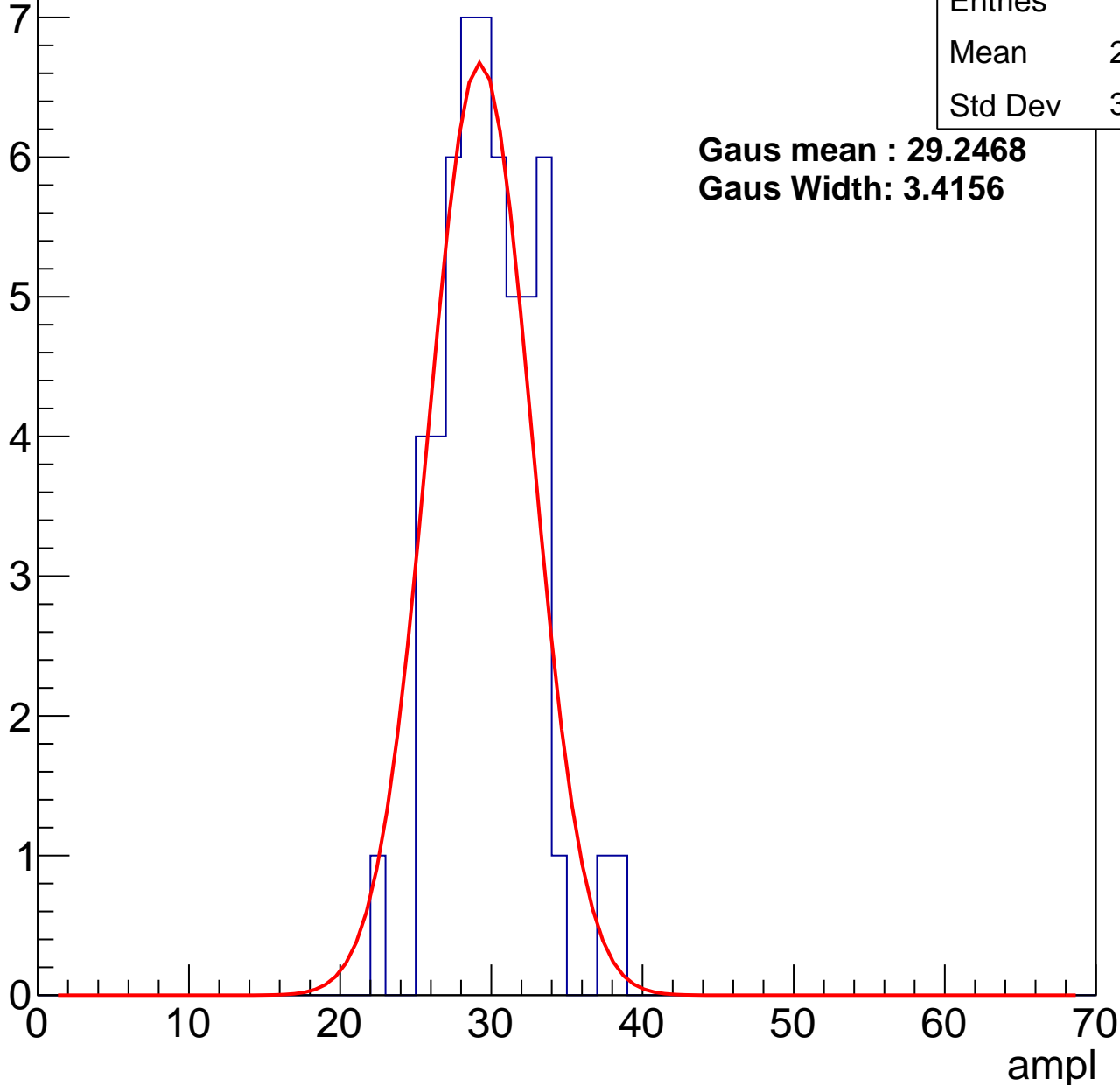
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	29.43
Std Dev	3.065

Gaus mean : 29.2468

Gaus Width: 3.4156



B1L102S, U6-ch15, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	34.92
Std Dev	2.92

Gaus mean : 36.0022

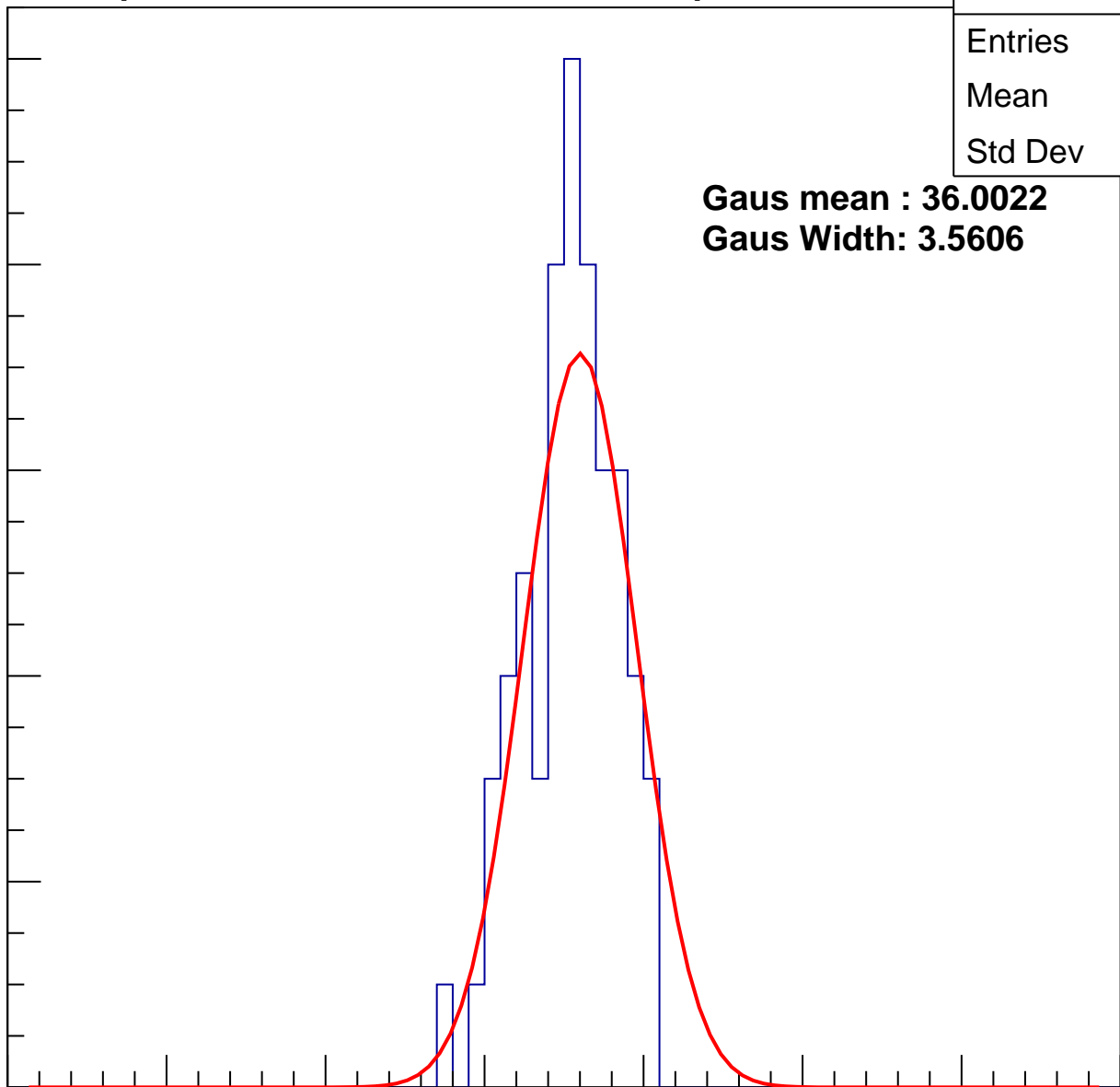
Gaus Width: 3.5606

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch15, adc2

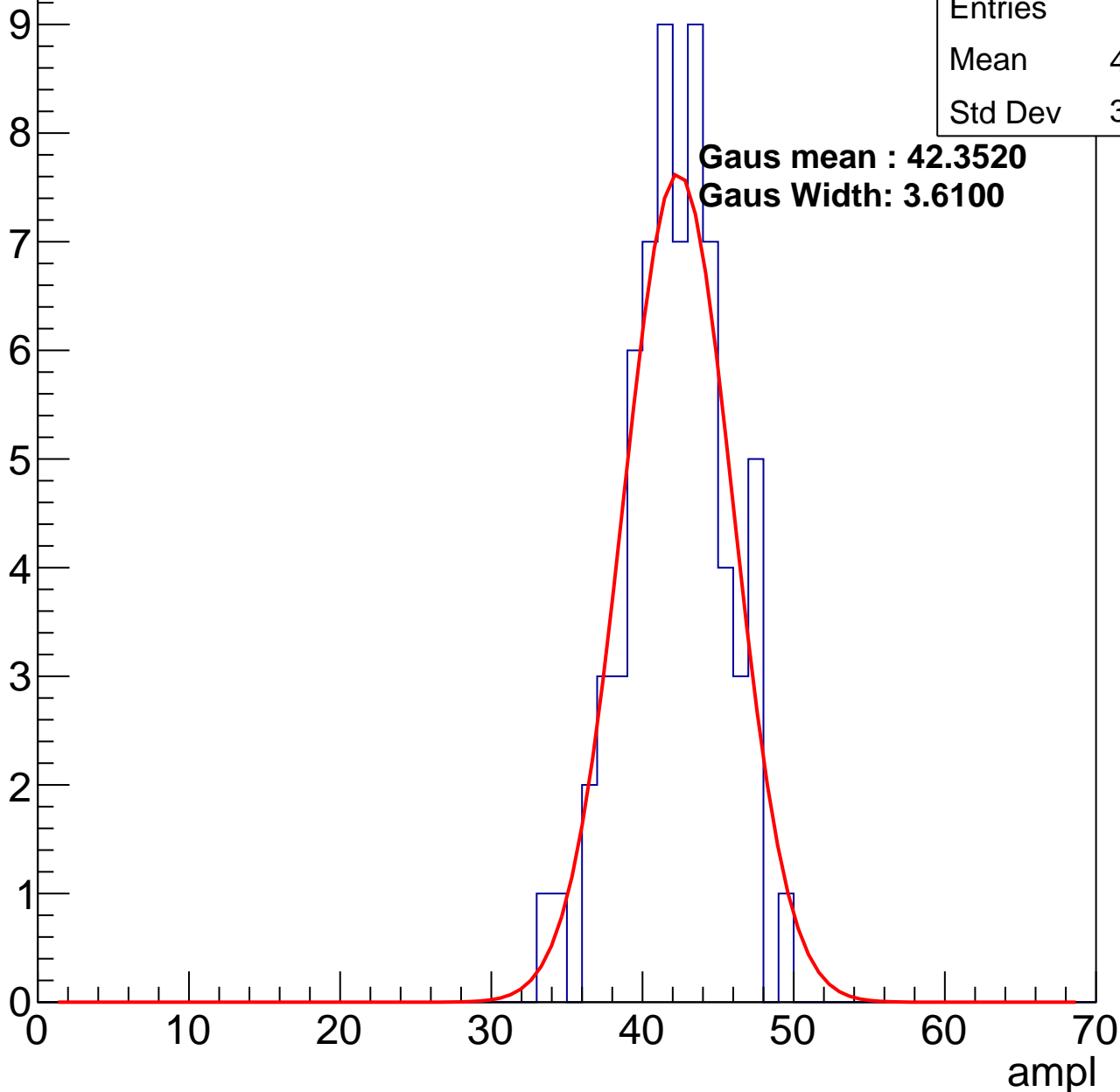
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.74
Std Dev	3.257

Gaus mean : 42.3520

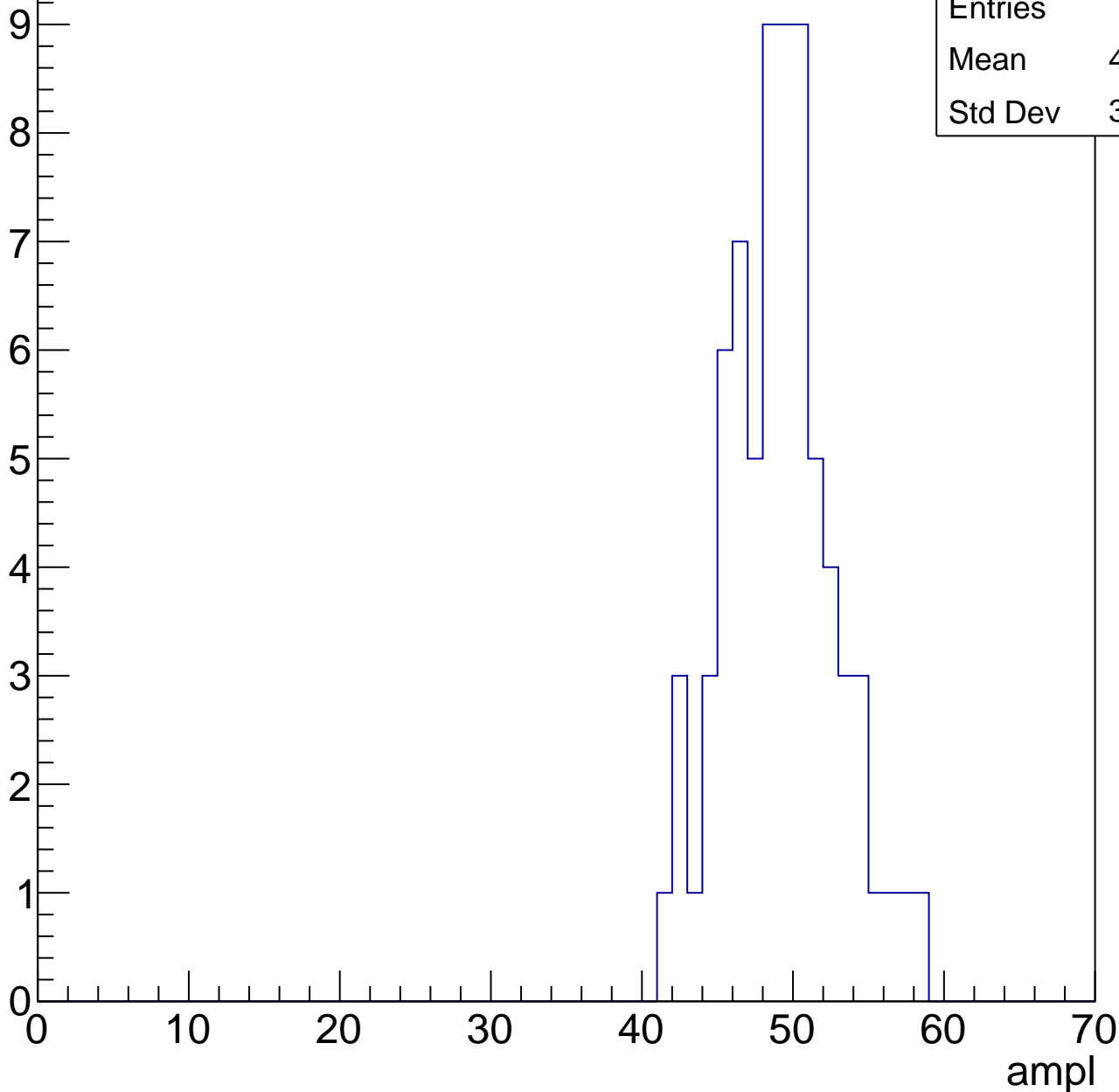
Gaus Width: 3.6100



B1L102S, U6-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

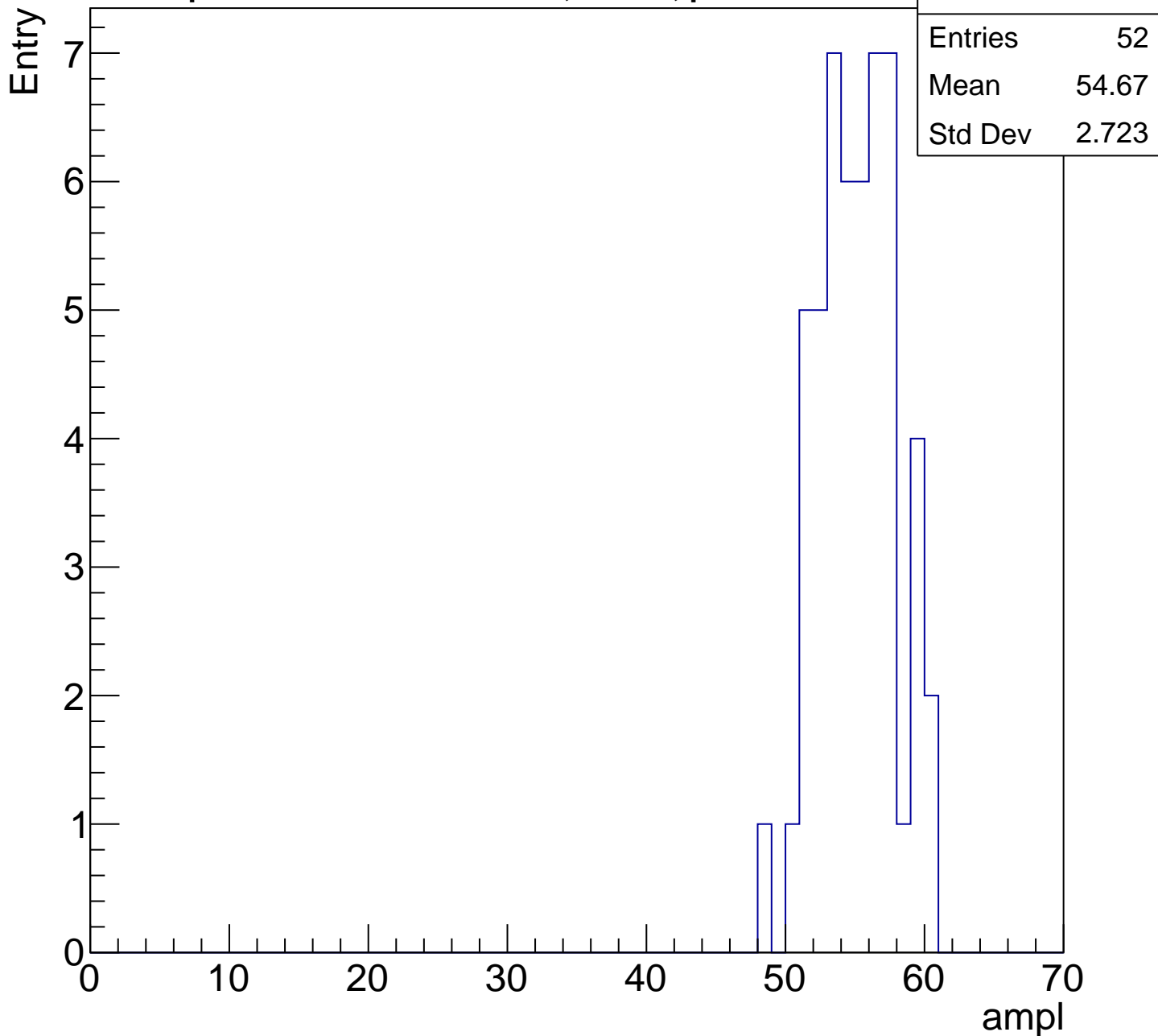
Entry



Entries	72
Mean	48.64
Std Dev	3.572

B1L102S, U6-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

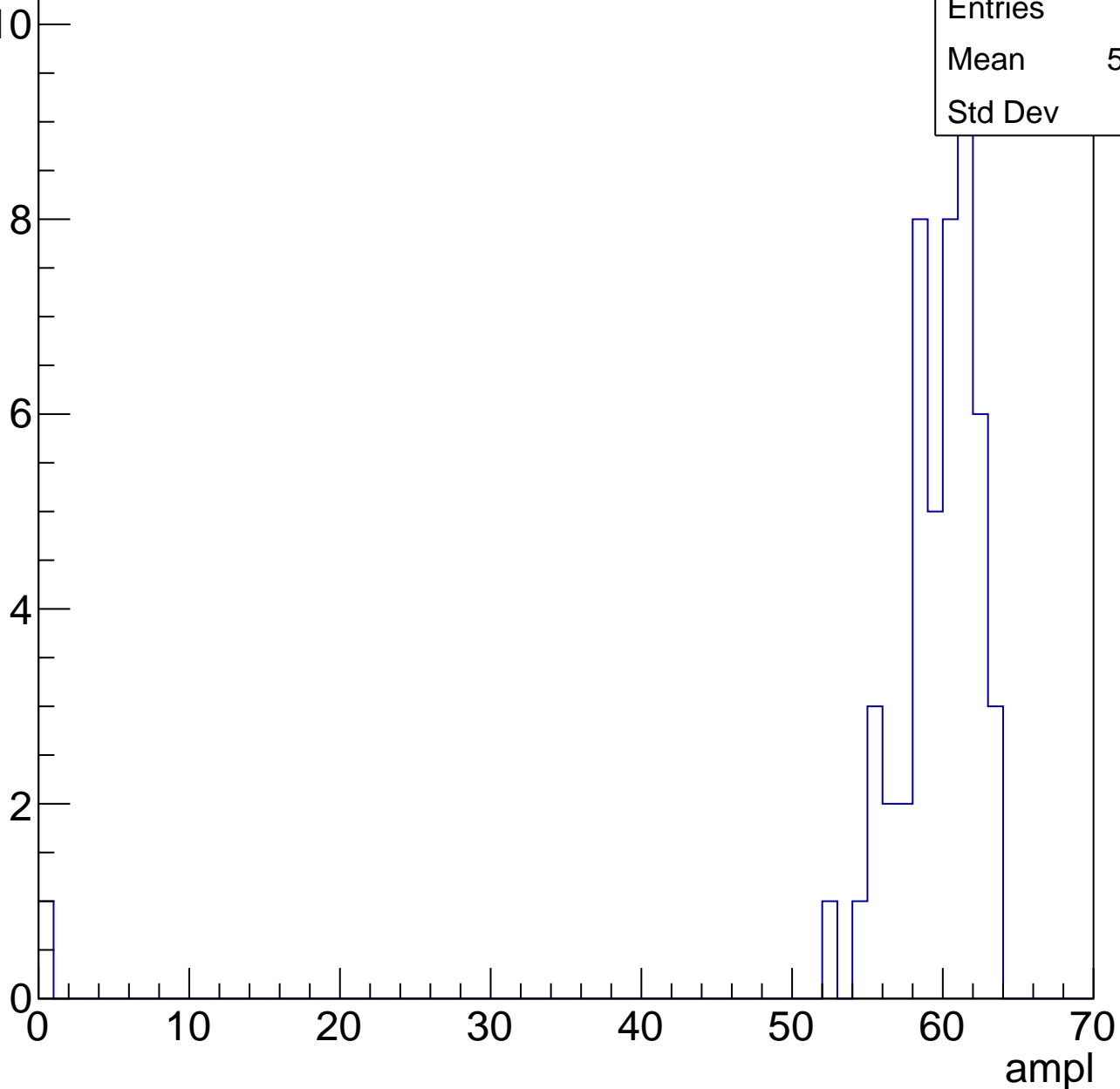


B1L102S, U6-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.14
Std Dev	8.66

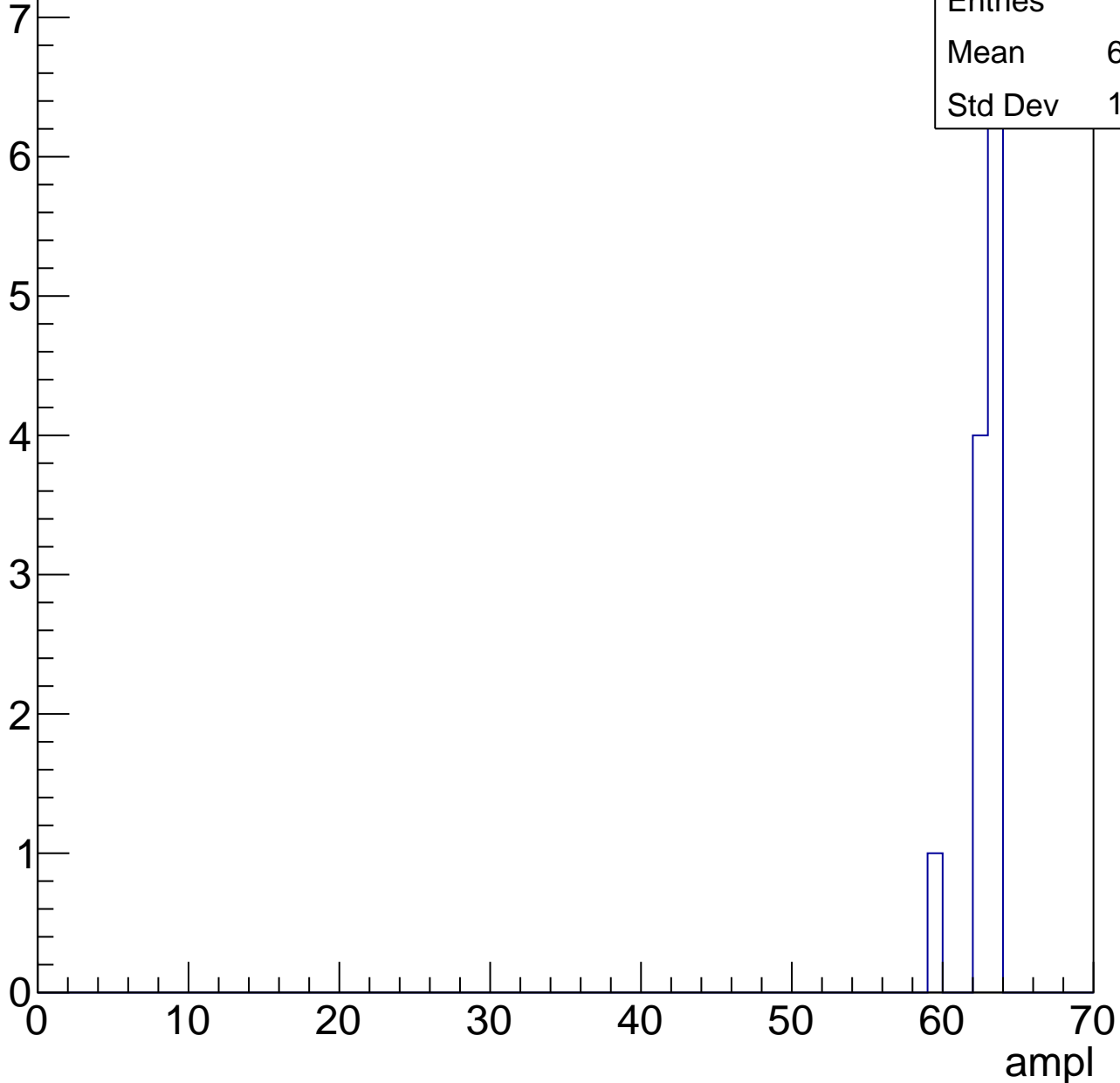


B1L102S, U6-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	62.33
Std Dev	1.106



B1L102S, U6-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch16, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	28.06
Std Dev	4.718

Gaus mean : 29.6136

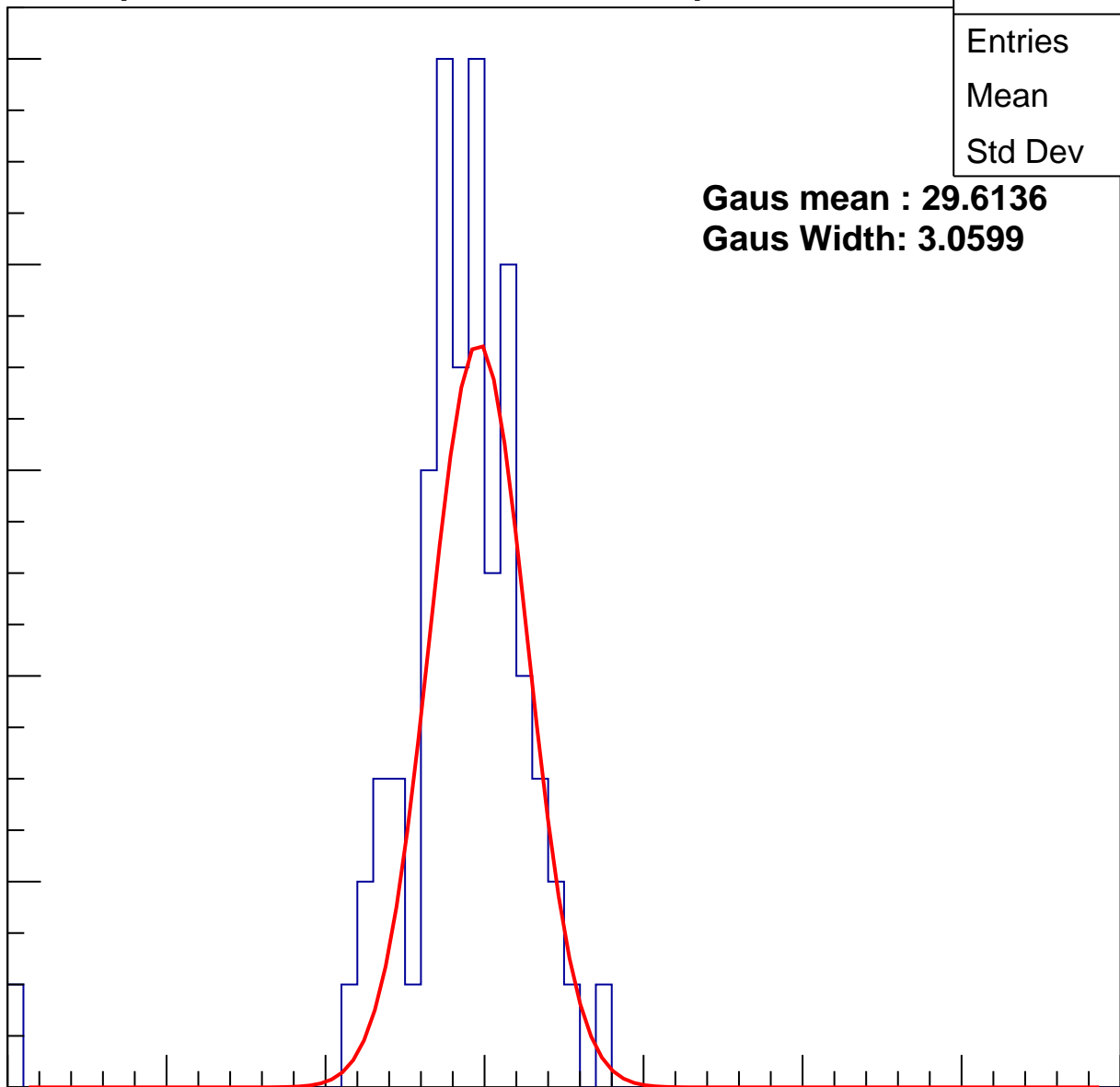
Gaus Width: 3.0599

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch16, adc1

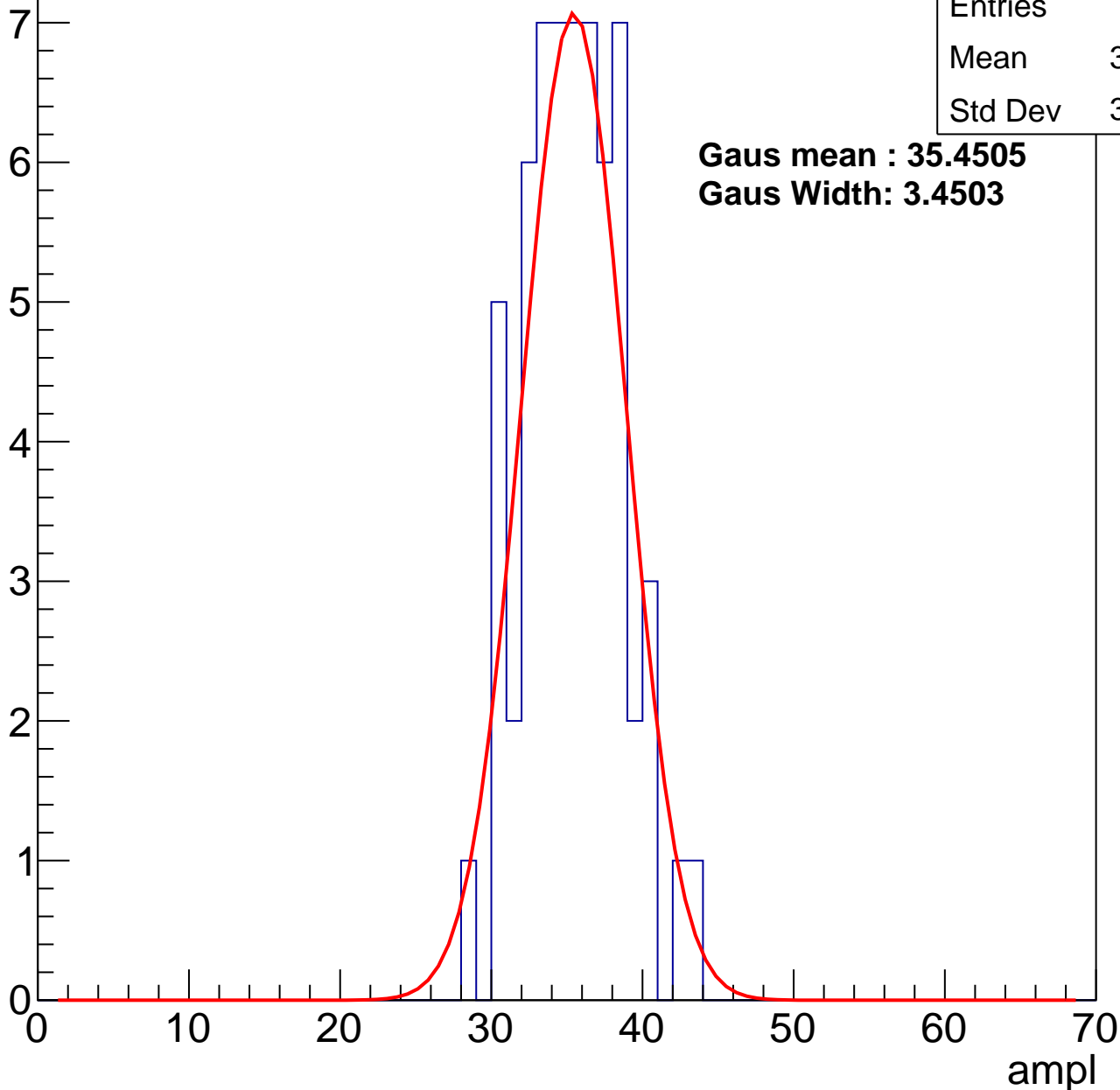
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.98
Std Dev	3.134

Gaus mean : 35.4505

Gaus Width: 3.4503



B1L102S, U6-ch16, adc2

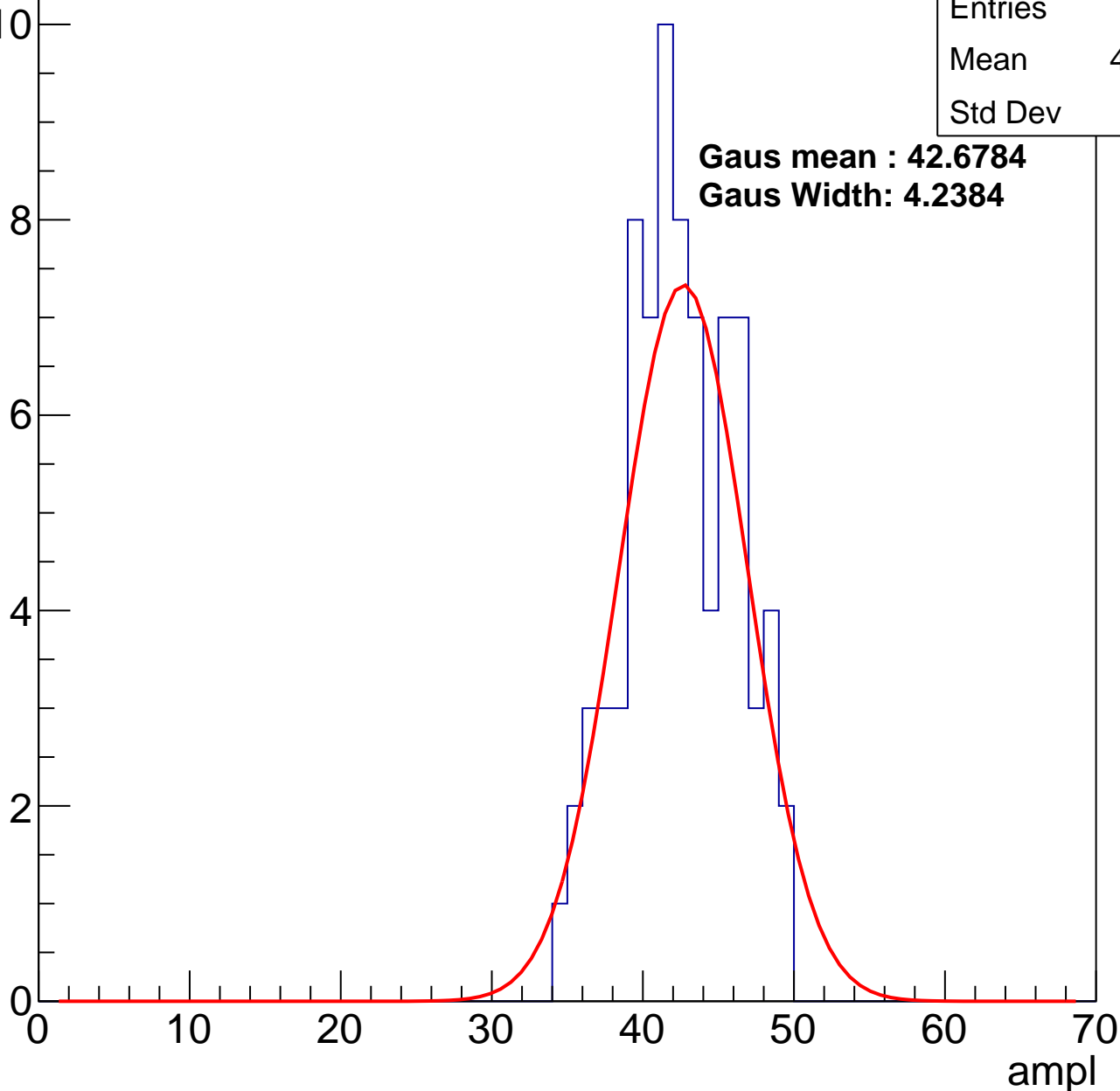
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	42.03
Std Dev	3.59

Gaus mean : 42.6784

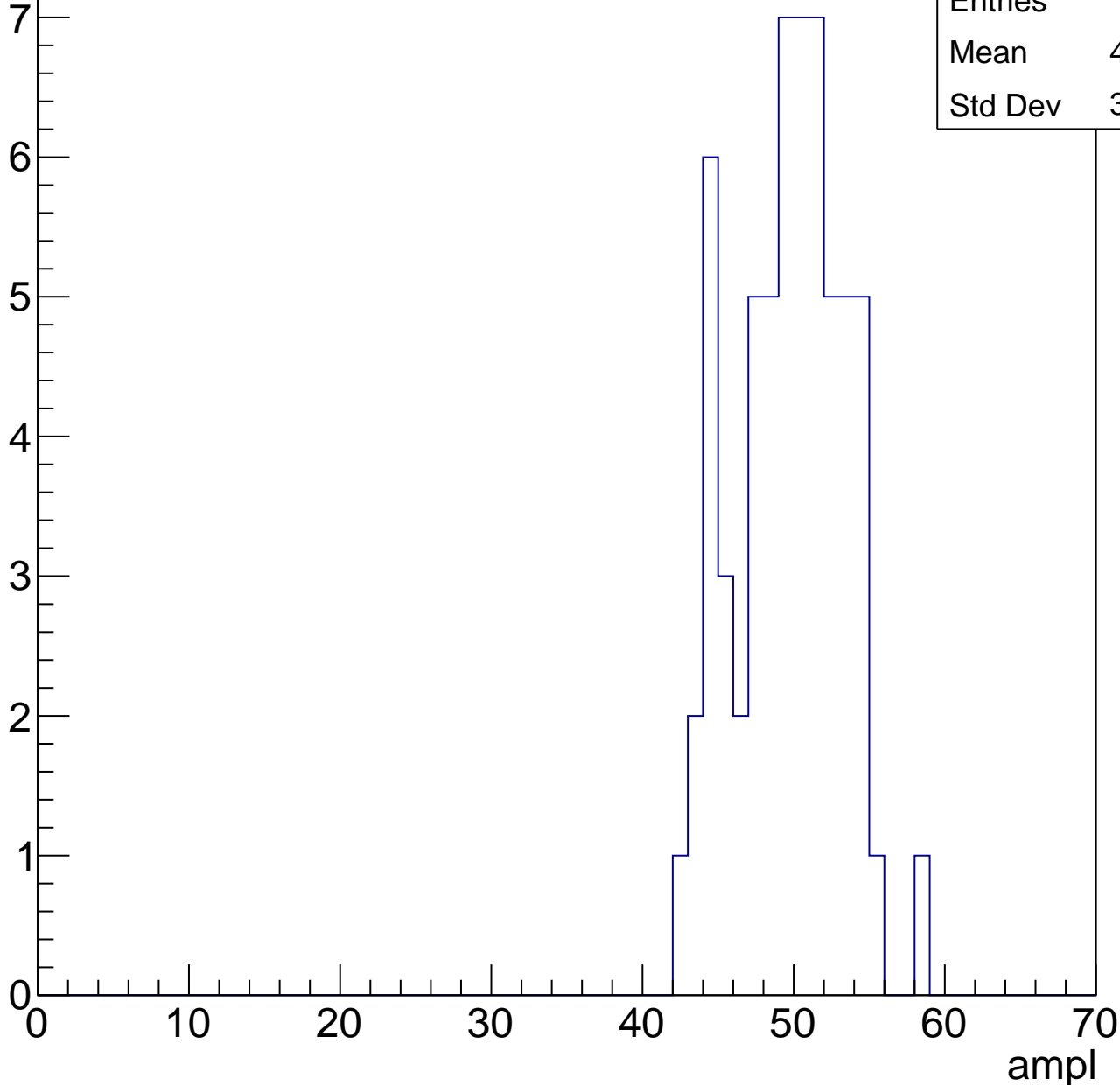
Gaus Width: 4.2384



B1L102S, U6-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	62
Mean	49.23
Std Dev	3.503

B1L102S, U6-ch16, adc4

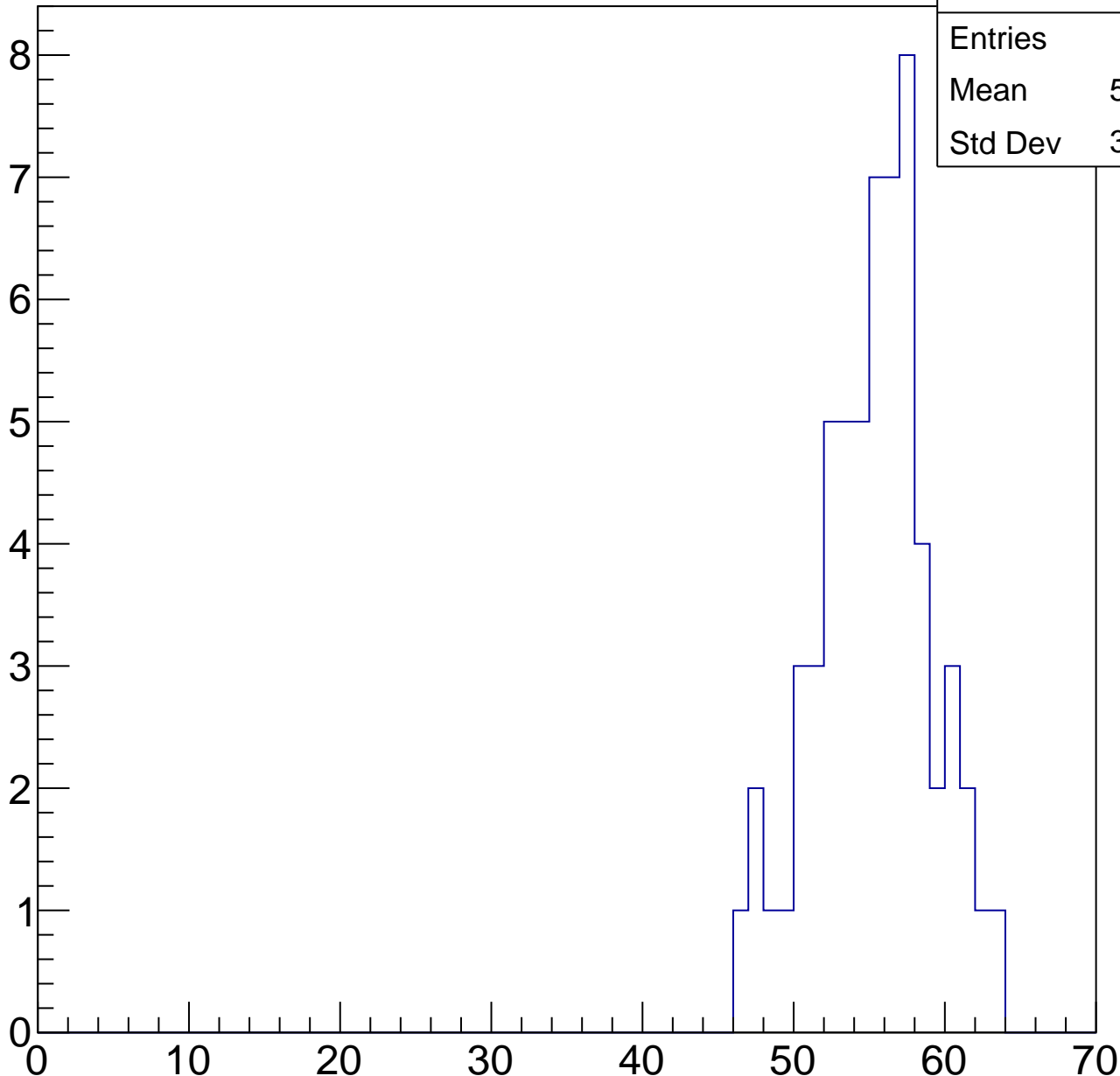
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	54.84
Std Dev	3.738

ampl



B1L102S, U6-ch16, adc5

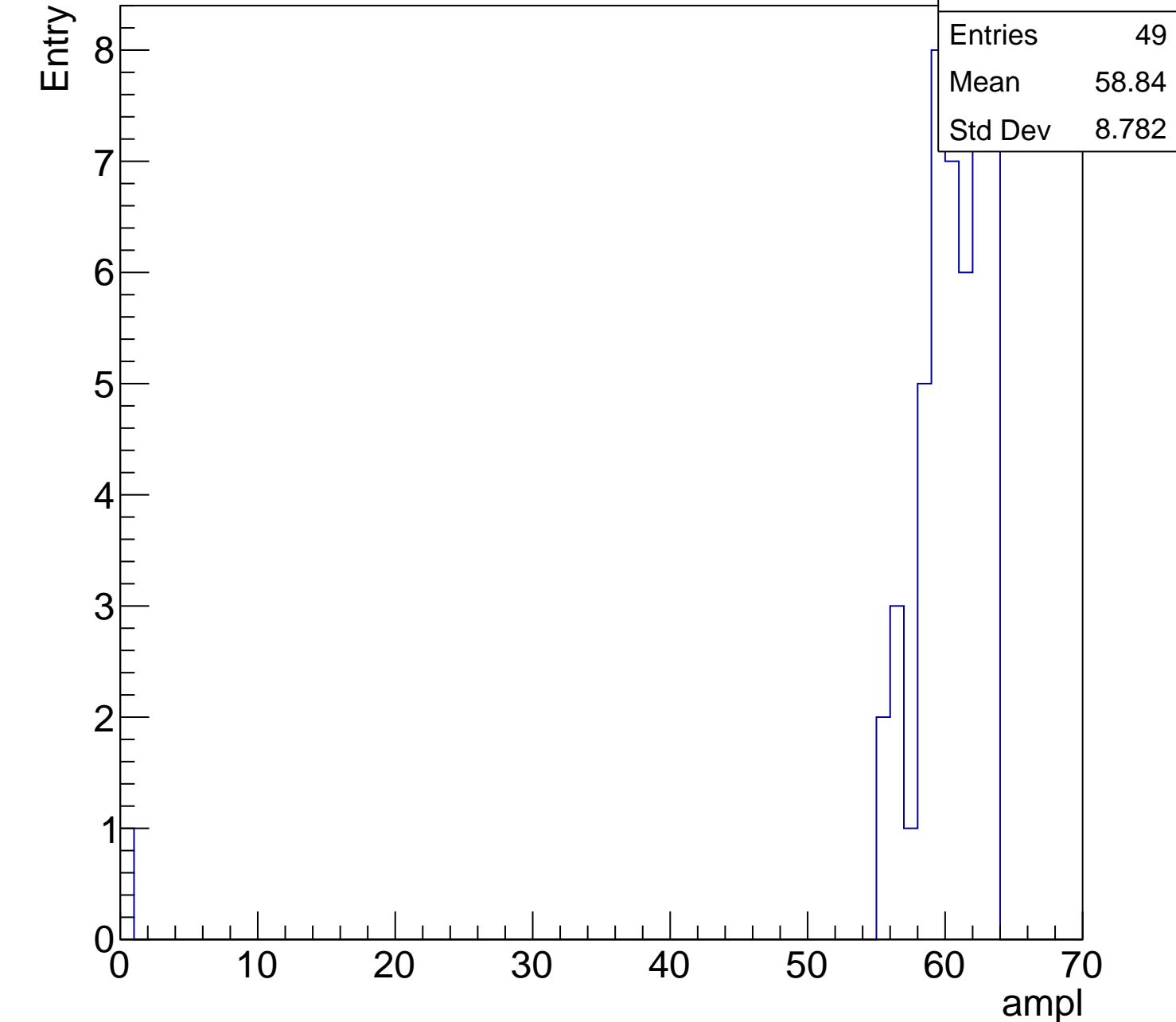
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.84
Std Dev	8.782

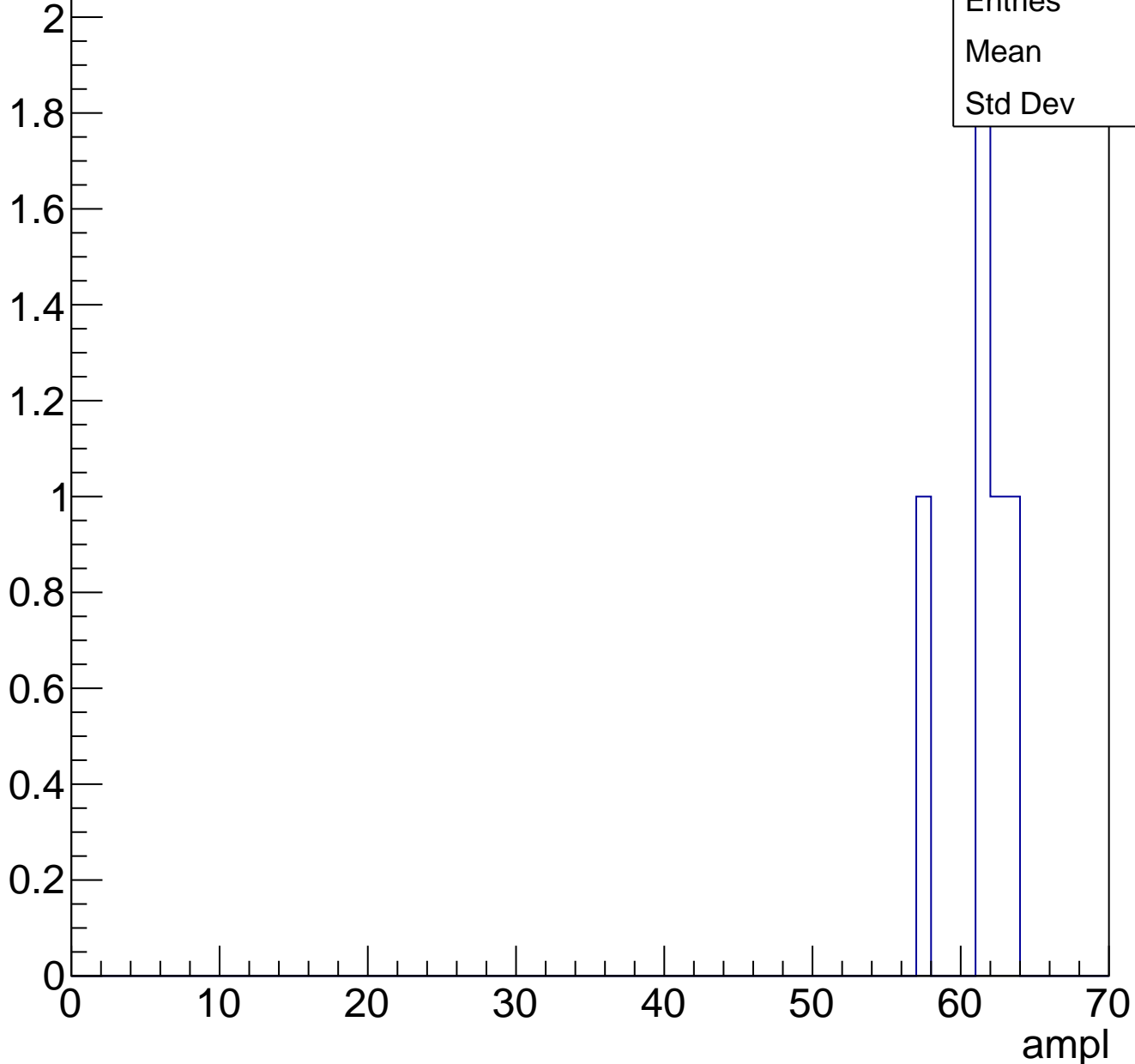
ampl



B1L102S, U6-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	60.8
Std Dev	2.04

B1L102S, U6-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch17, adc0

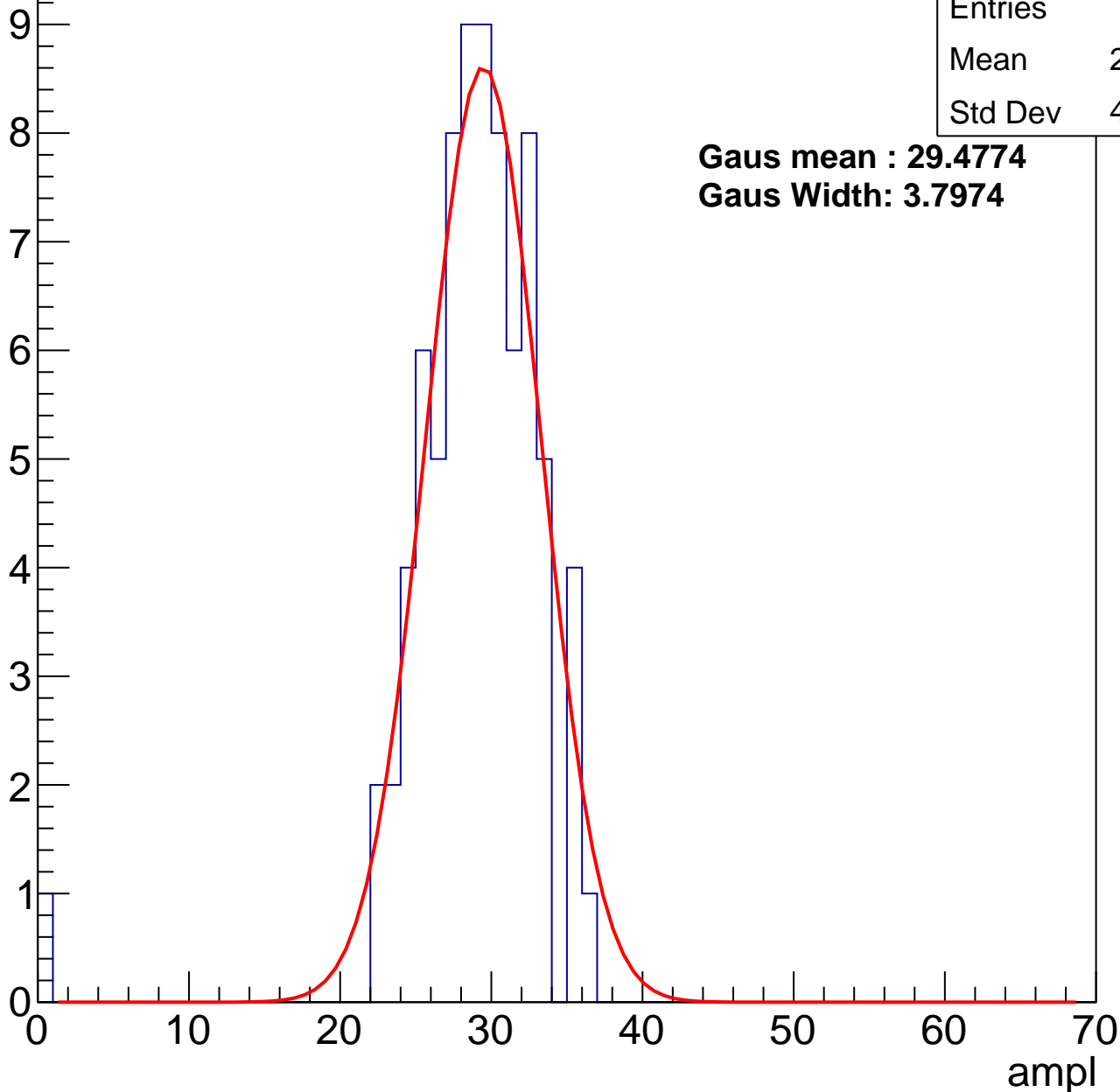
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.44
Std Dev	4.593

Gaus mean : 29.4774

Gaus Width: 3.7974



B1L102S, U6-ch17, adc1

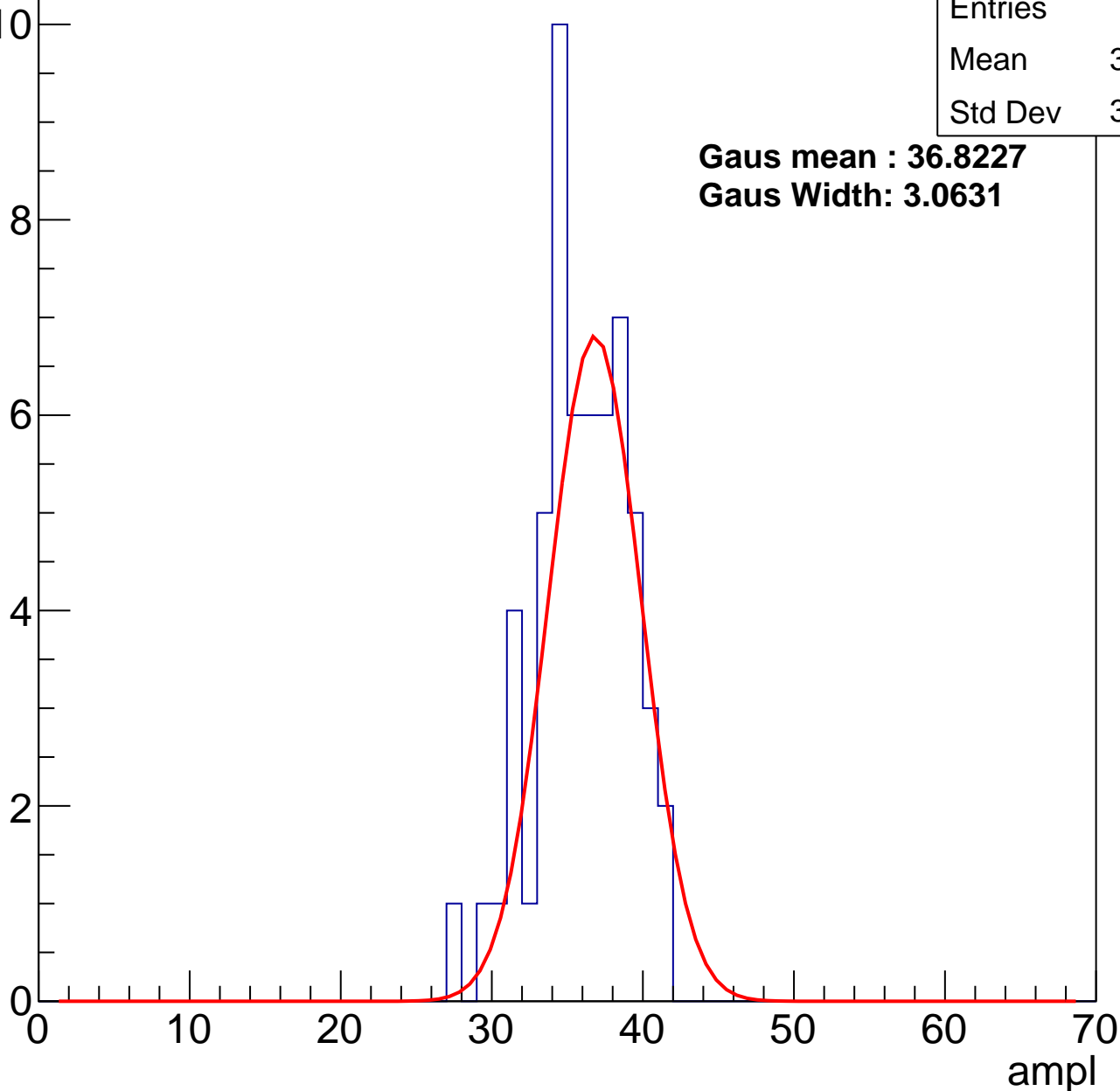
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	35.48
Std Dev	3.036

Gaus mean : 36.8227

Gaus Width: 3.0631



B1L102S, U6-ch17, adc2

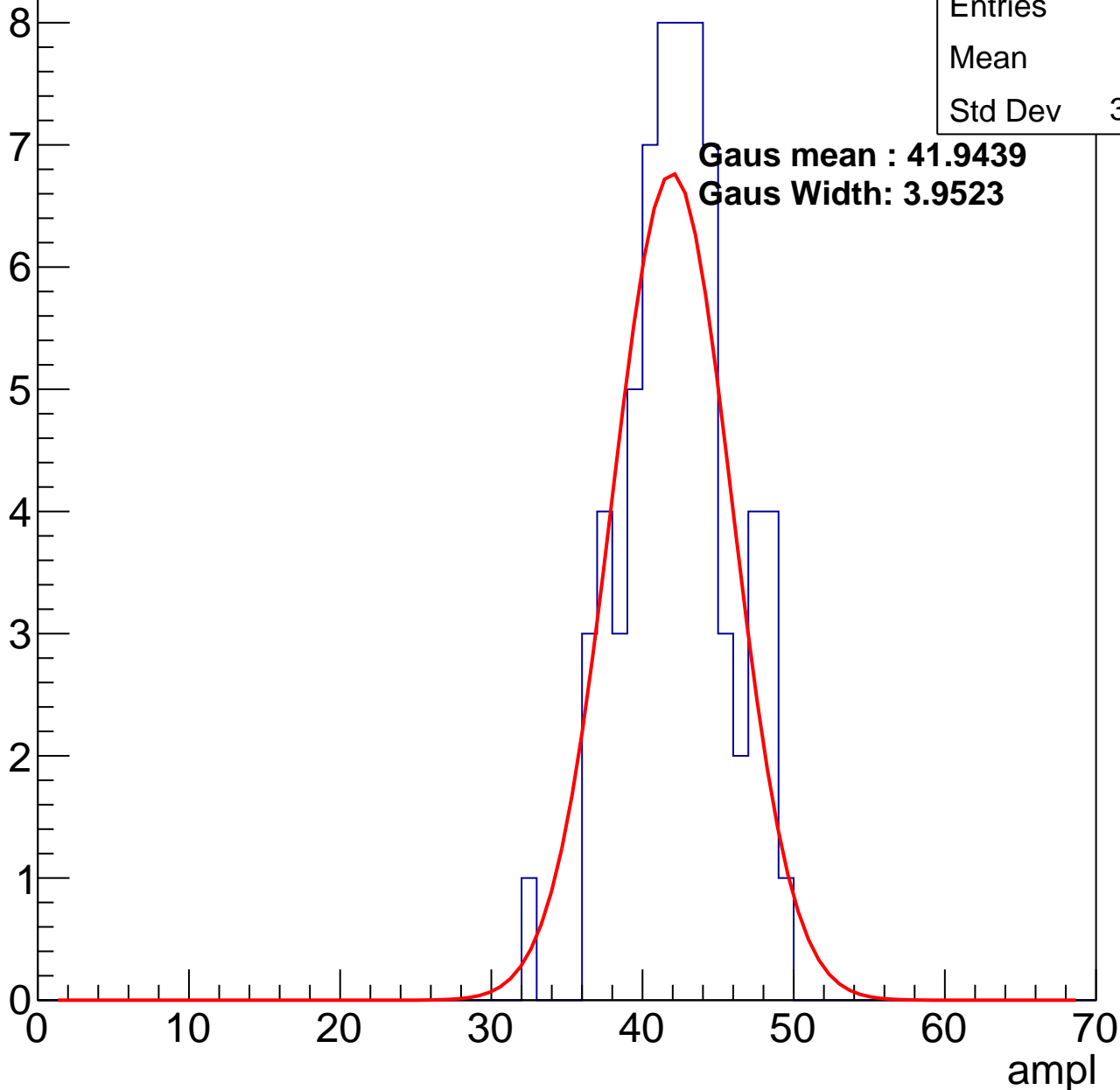
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	41.9
Std Dev	3.482

Gaus mean : 41.9439

Gaus Width: 3.9523

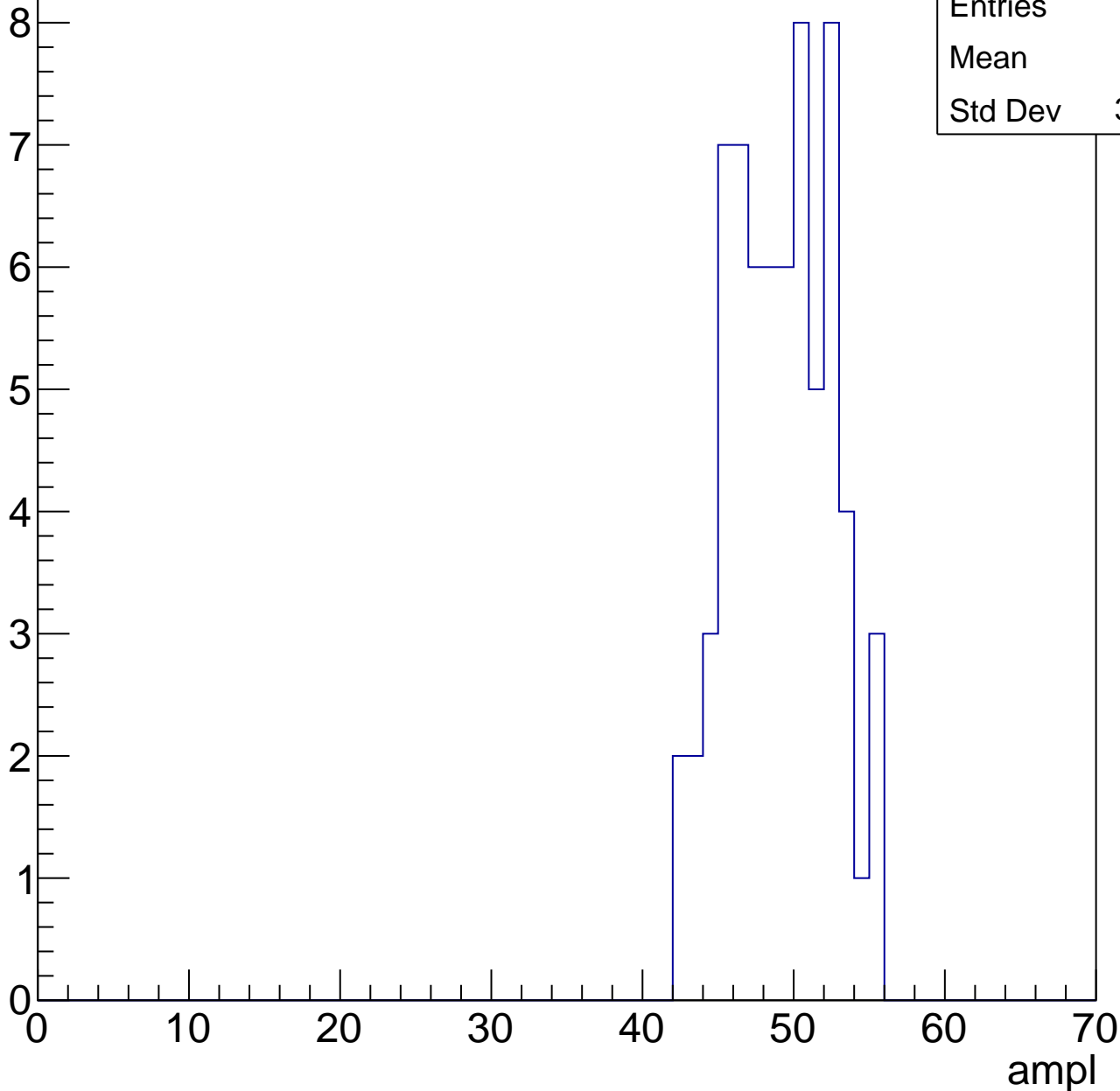


B1L102S, U6-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.6
Std Dev	3.291

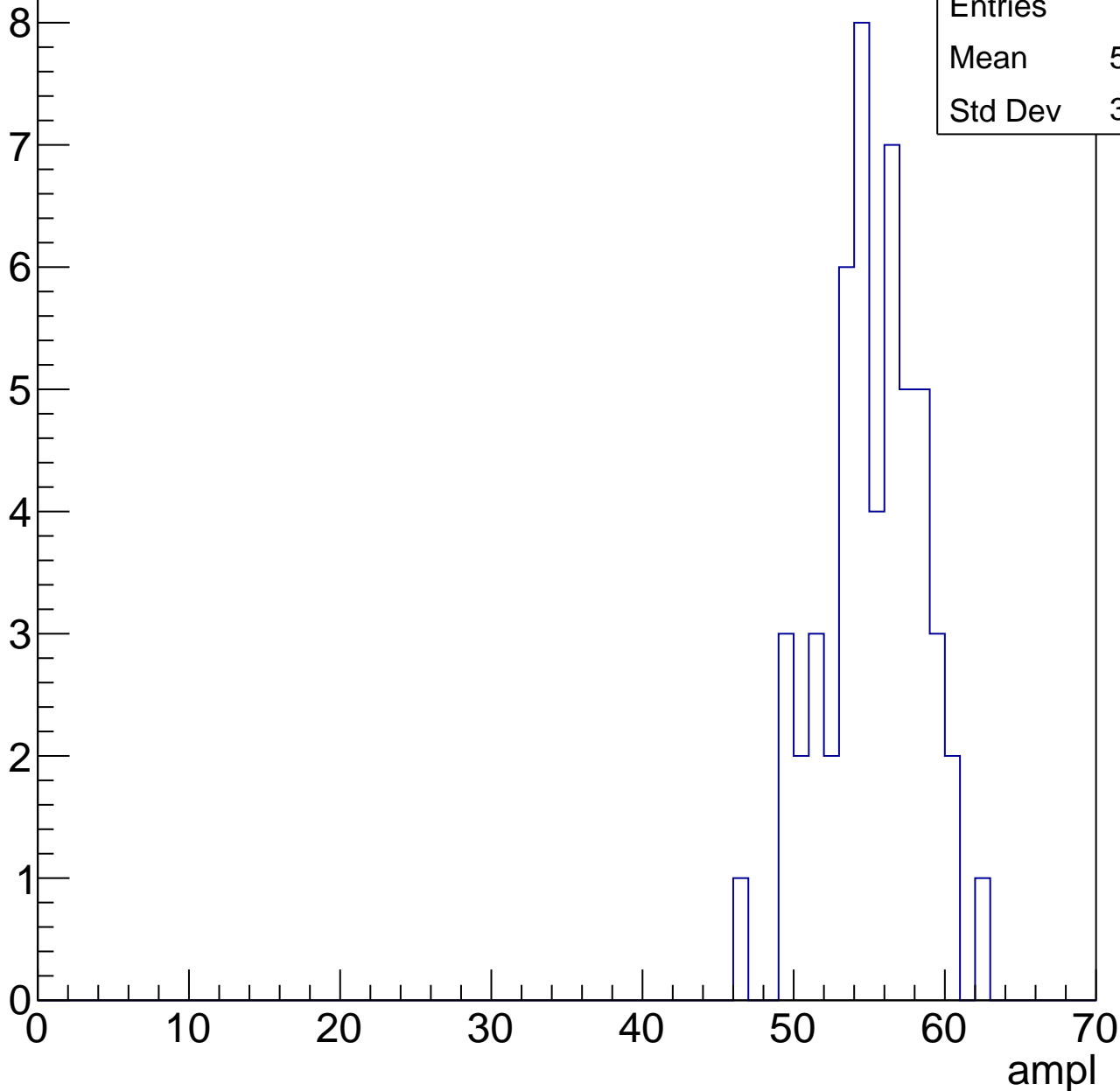


B1L102S, U6-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.73
Std Dev	3.259

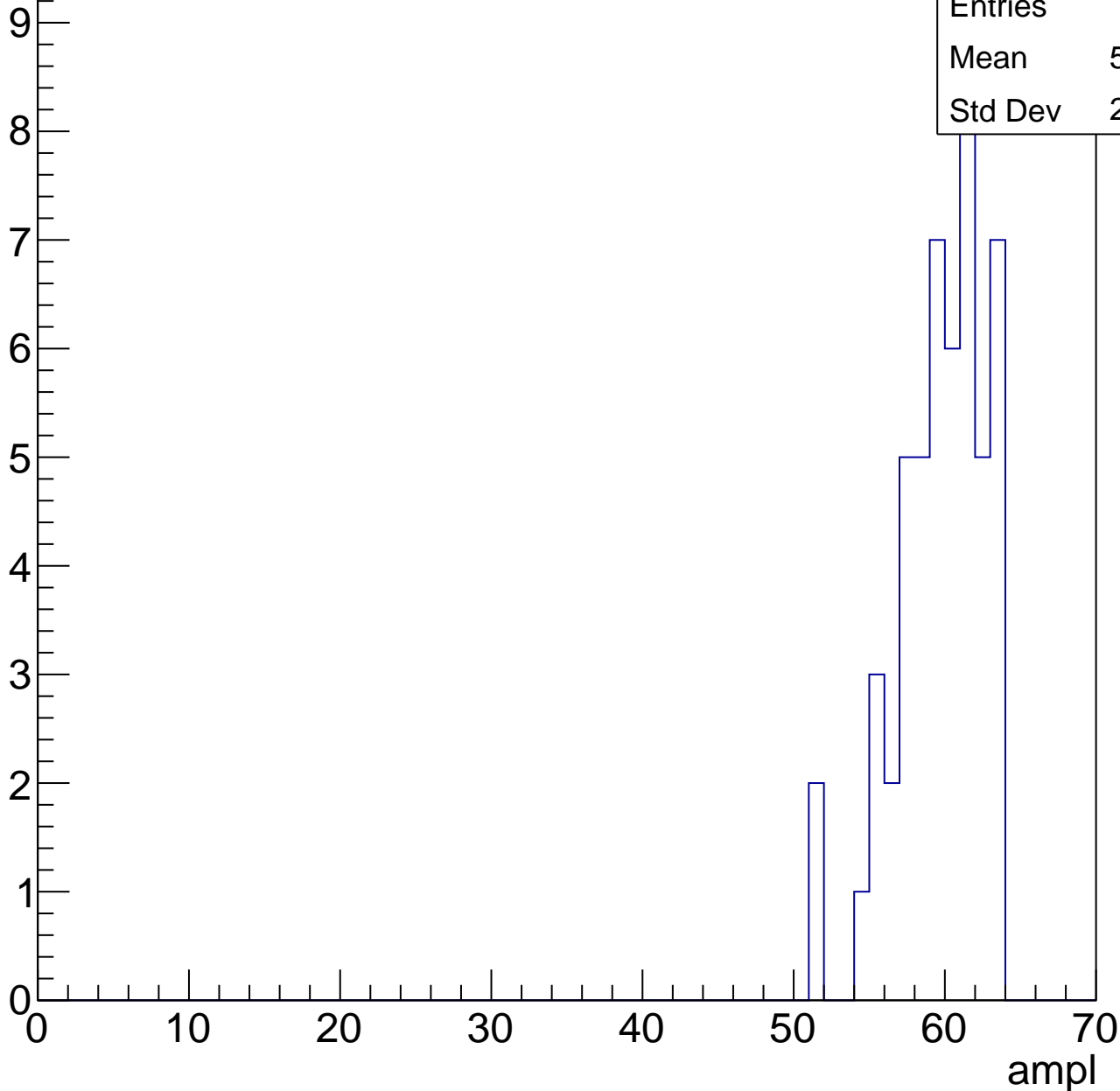


B1L102S, U6-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

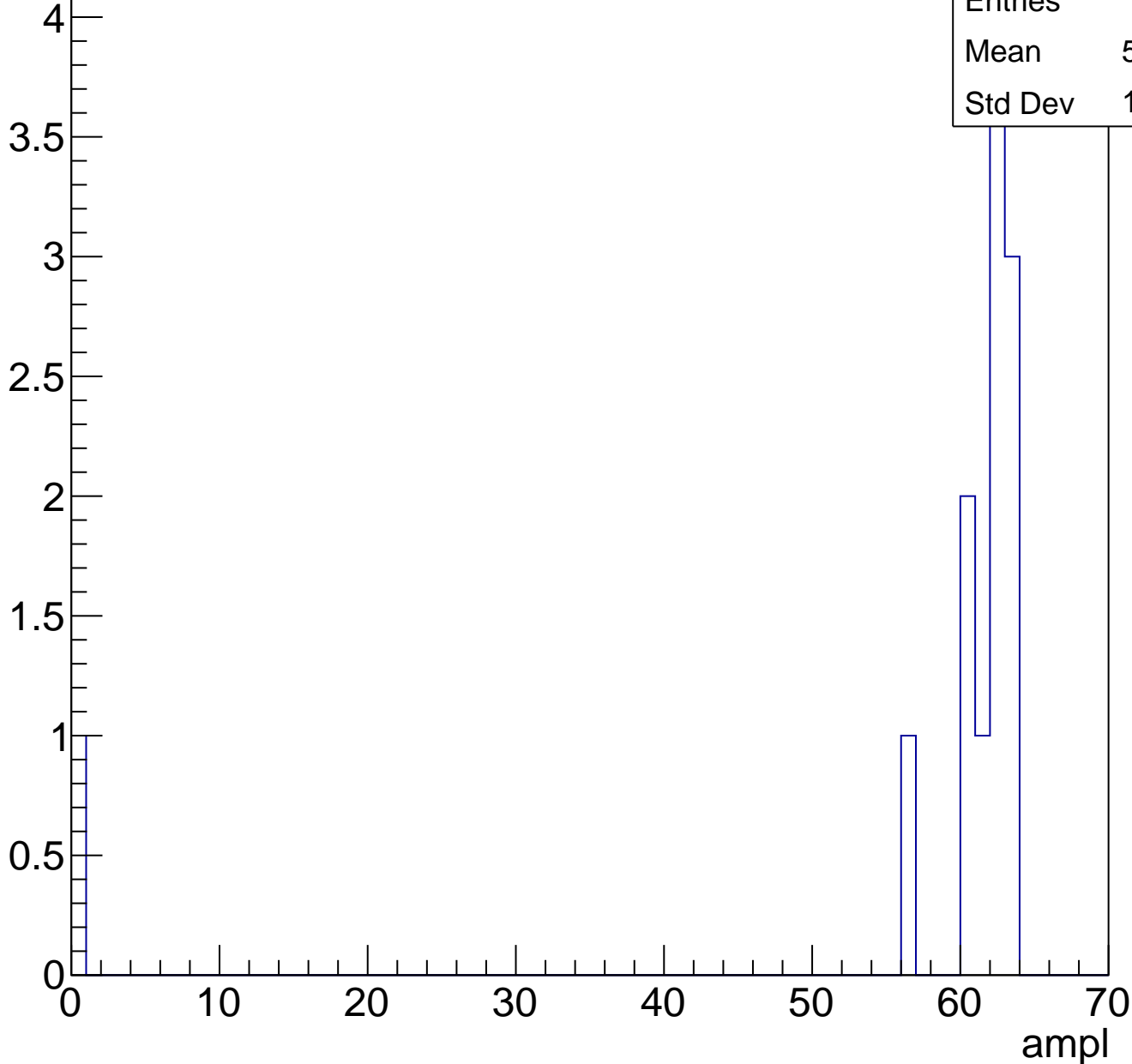
Entries	52
Mean	59.25
Std Dev	2.908



B1L102S, U6-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L102S, U6-ch18, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	27.18
Std Dev	4.689

Gaus mean : 28.8525

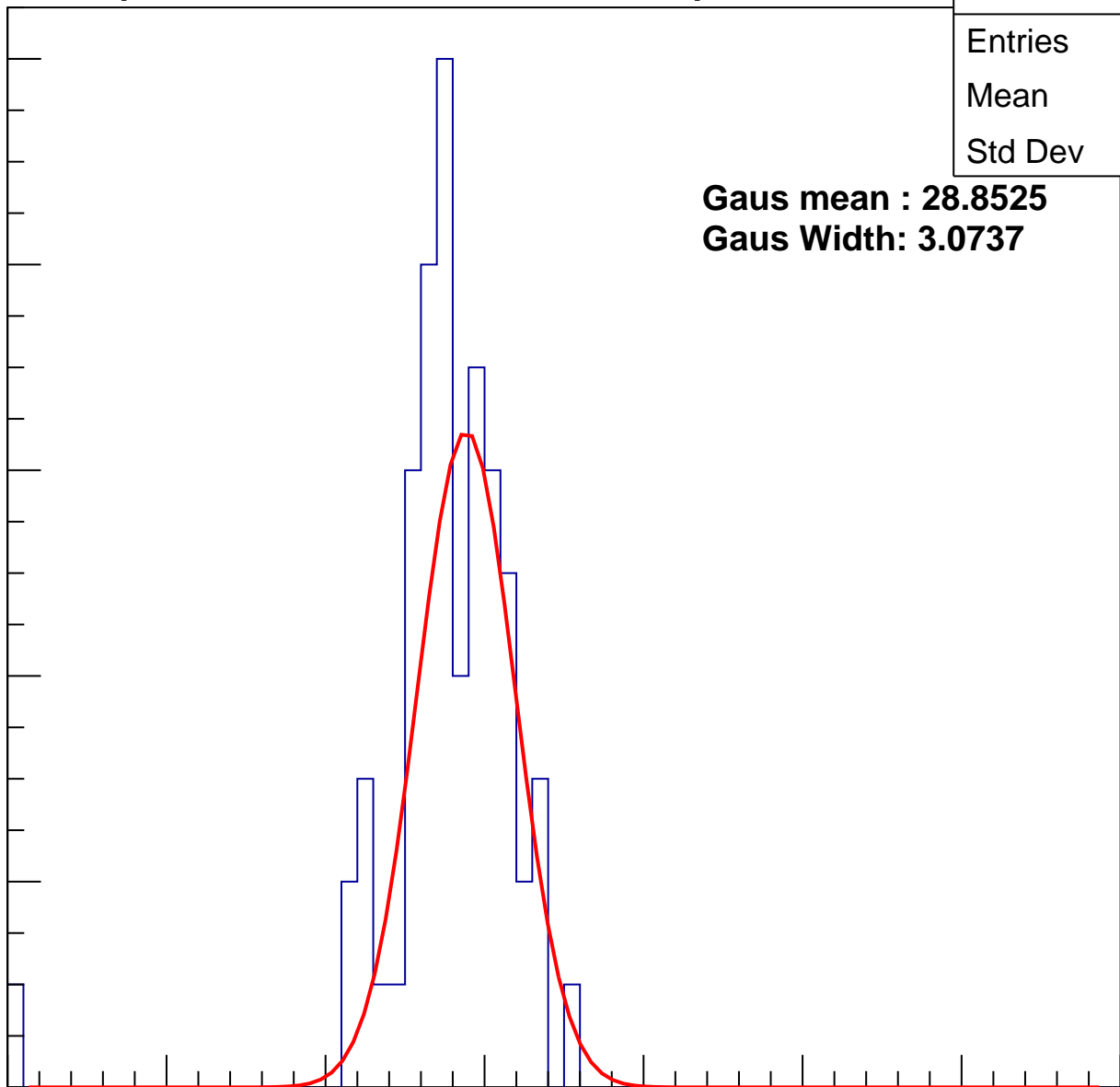
Gaus Width: 3.0737

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch18, adc1

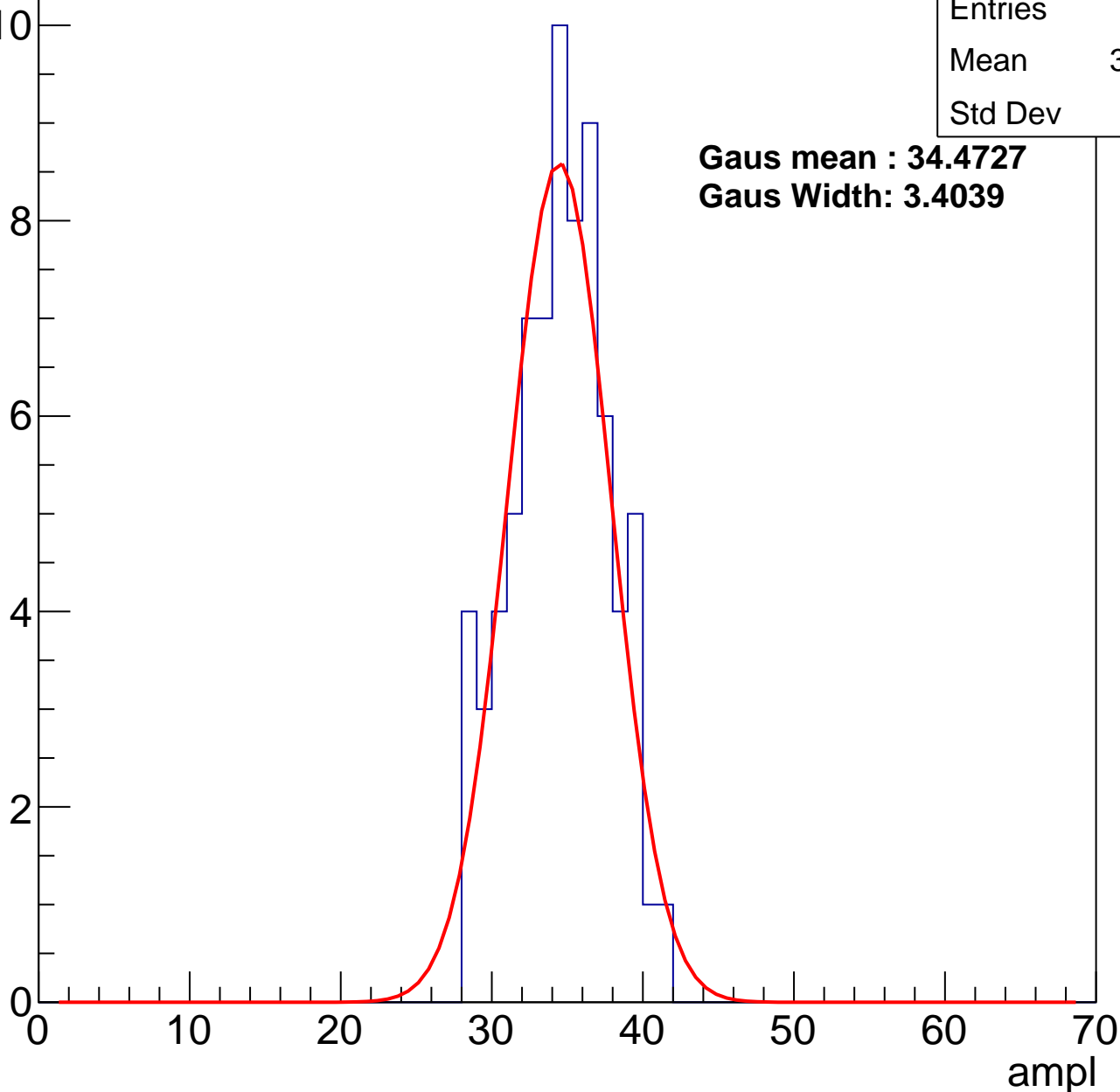
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.09
Std Dev	3.15

Gaus mean : 34.4727

Gaus Width: 3.4039



B1L102S, U6-ch18, adc2

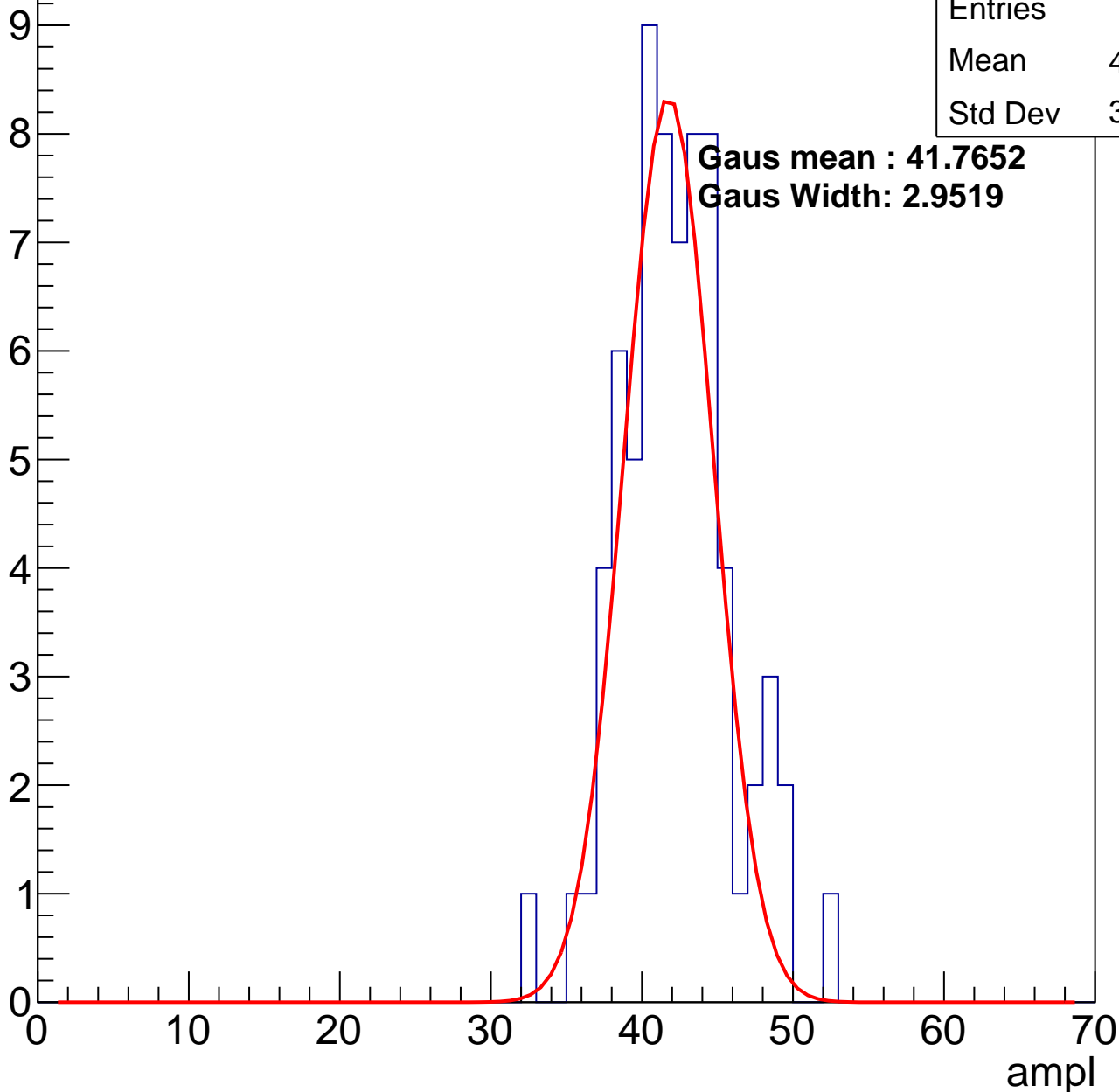
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.77
Std Dev	3.589

Gaus mean : 41.7652

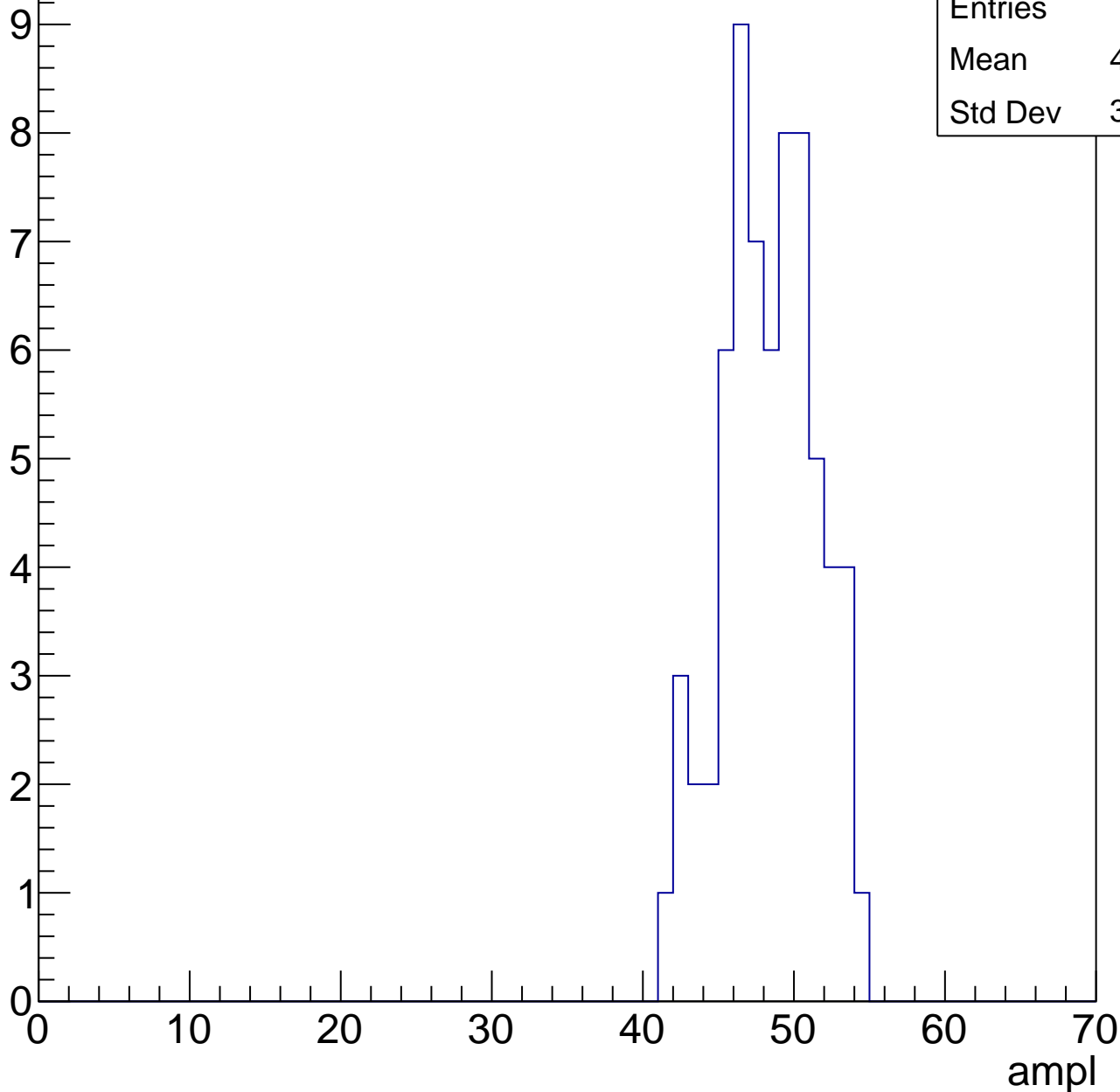
Gaus Width: 2.9519



B1L102S, U6-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

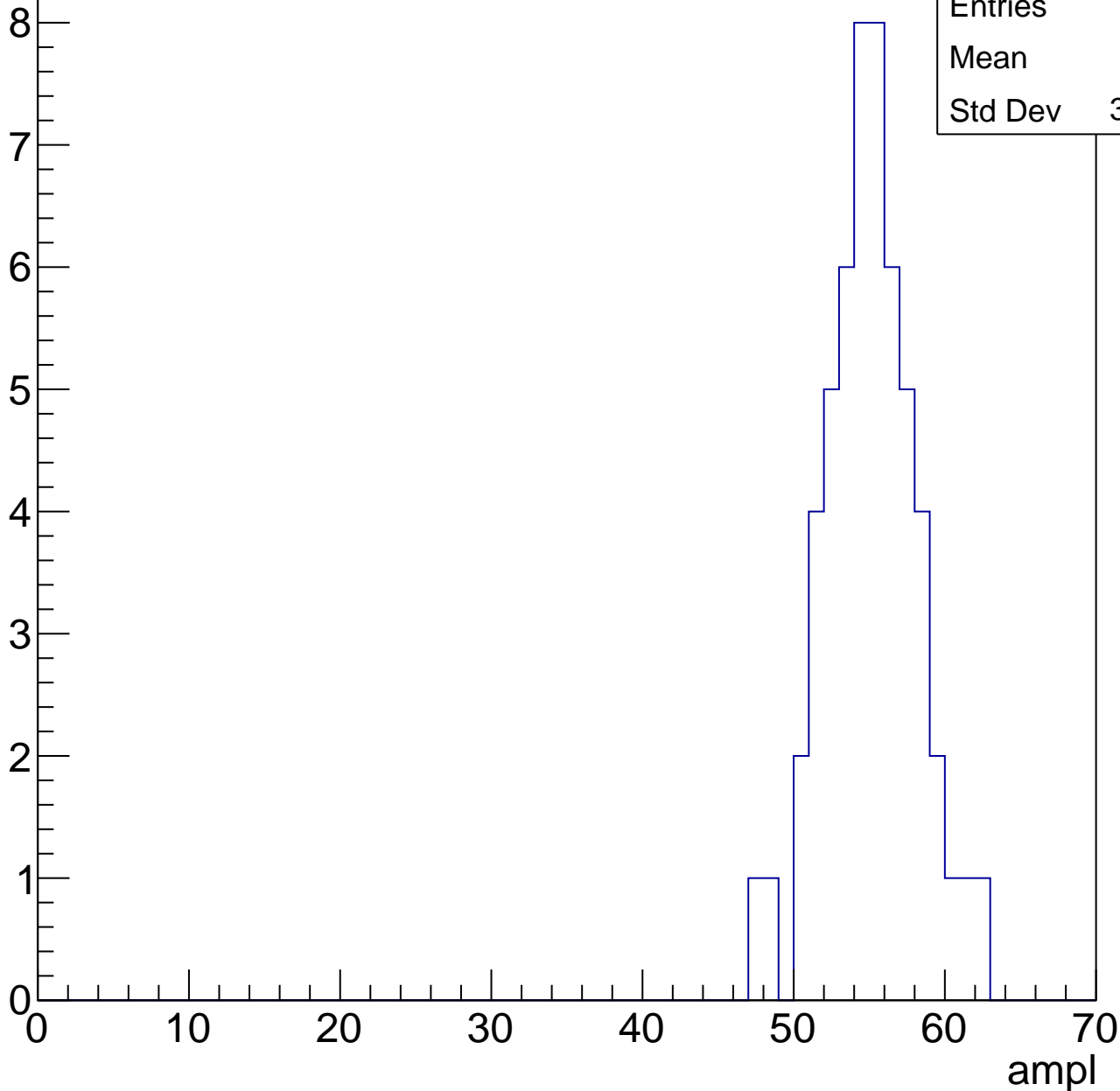


B1L102S, U6-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.6
Std Dev	3.013



B1L102S, U6-ch18, adc5

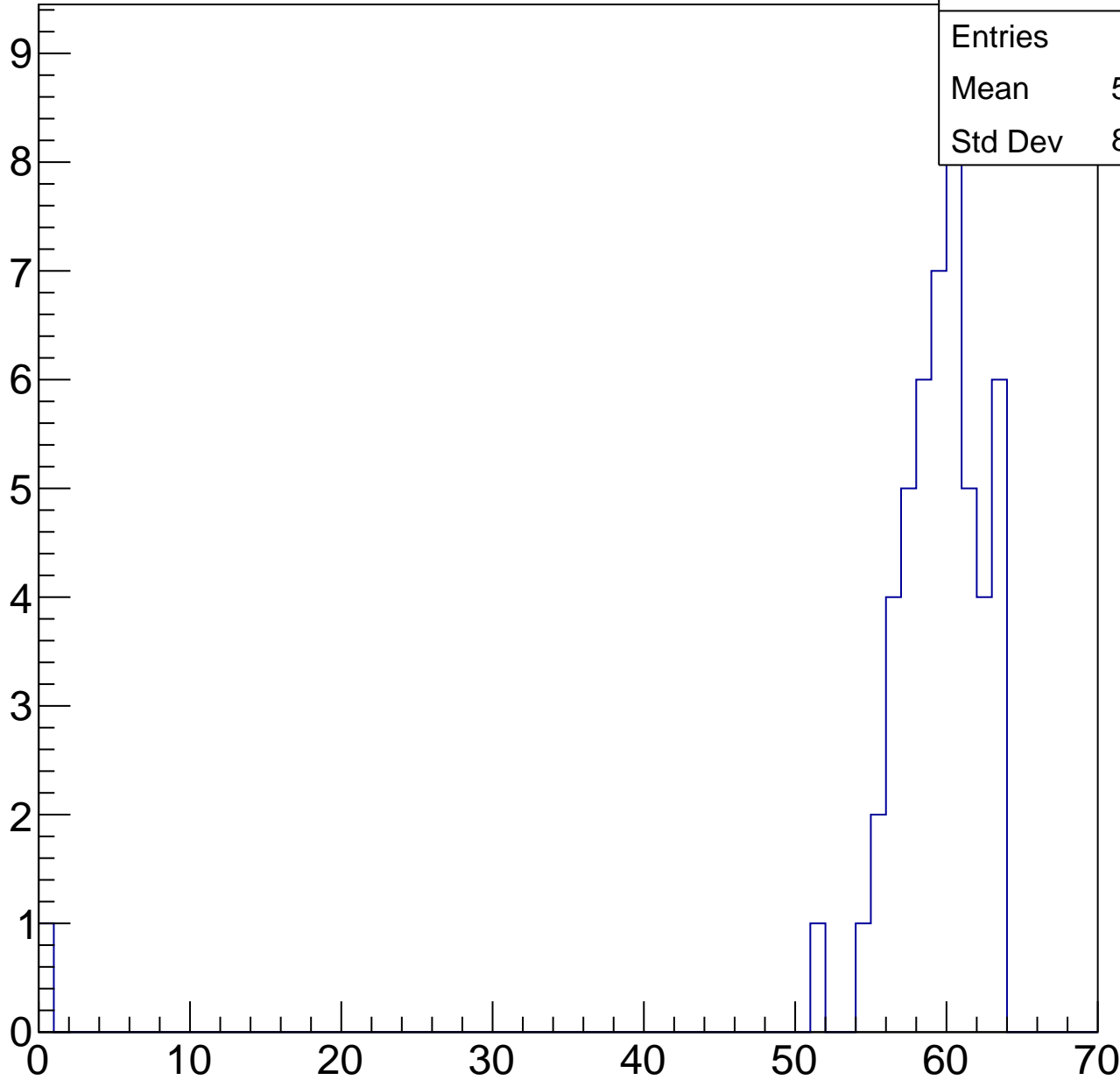
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	57.96
Std Dev	8.598

ampl

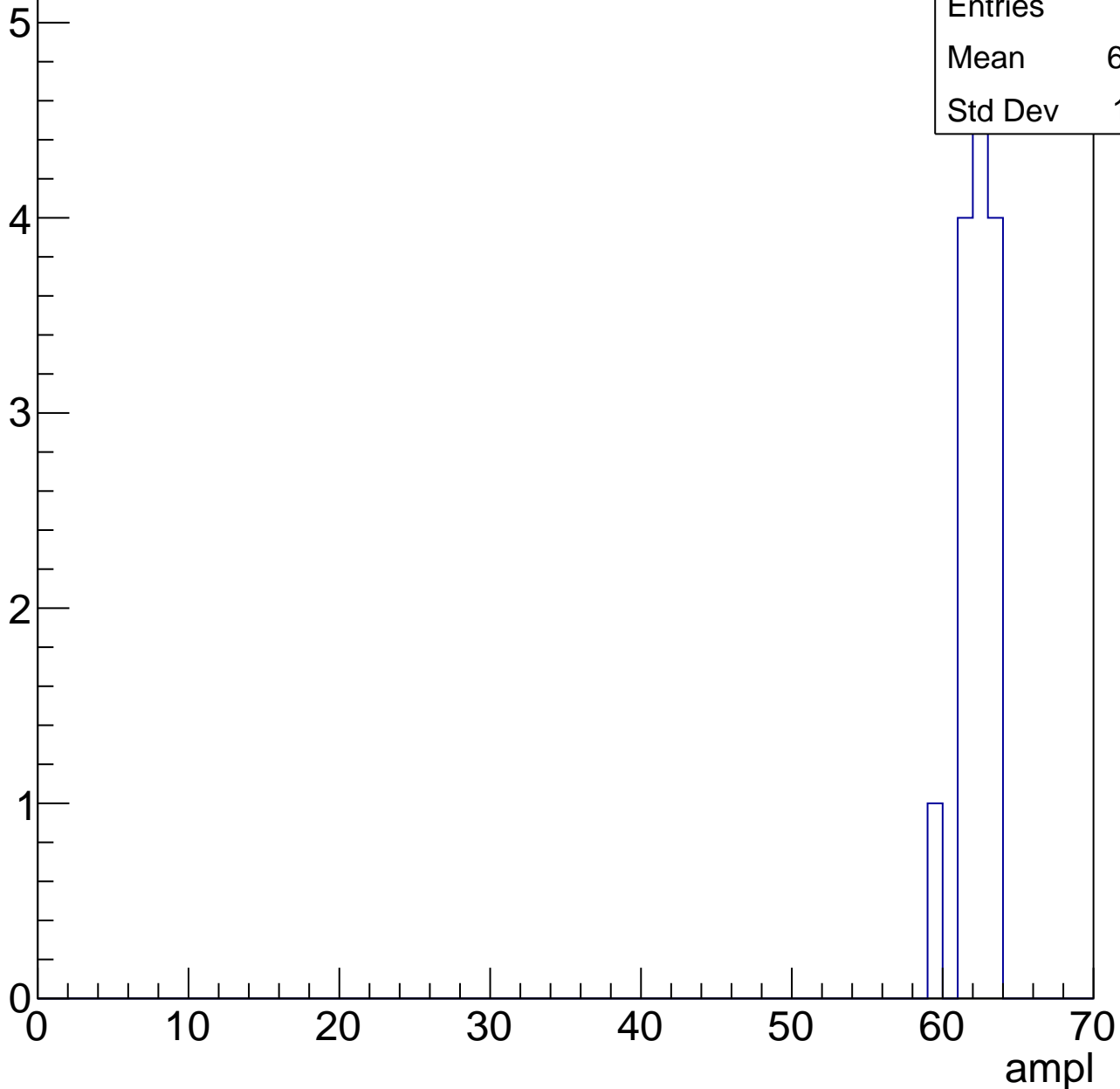


B1L102S, U6-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.79
Std Dev	1.081



B1L102S, U6-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch19, adc0

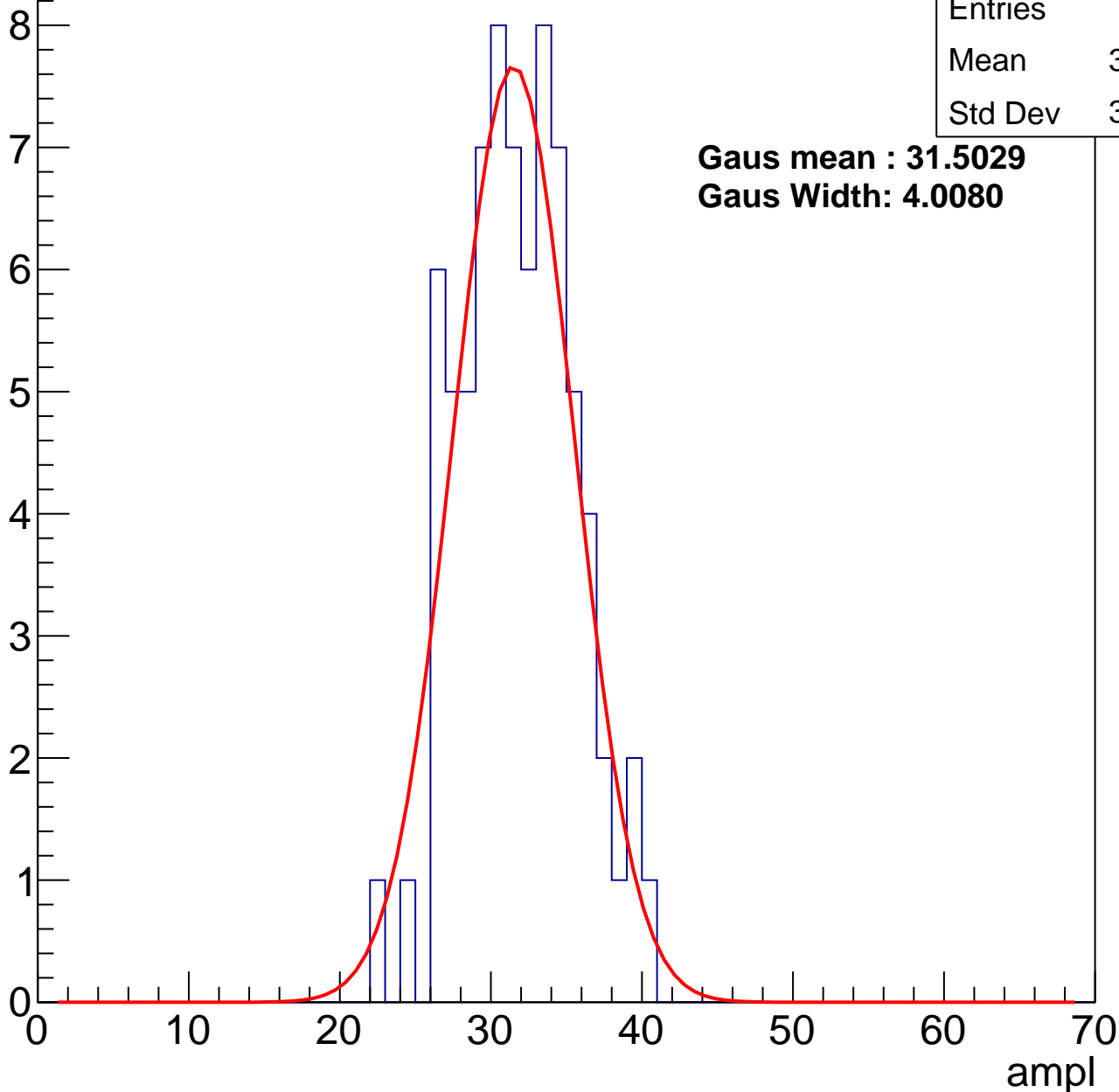
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	31.32
Std Dev	3.707

Gaus mean : 31.5029

Gaus Width: 4.0080



B1L102S, U6-ch19, adc1

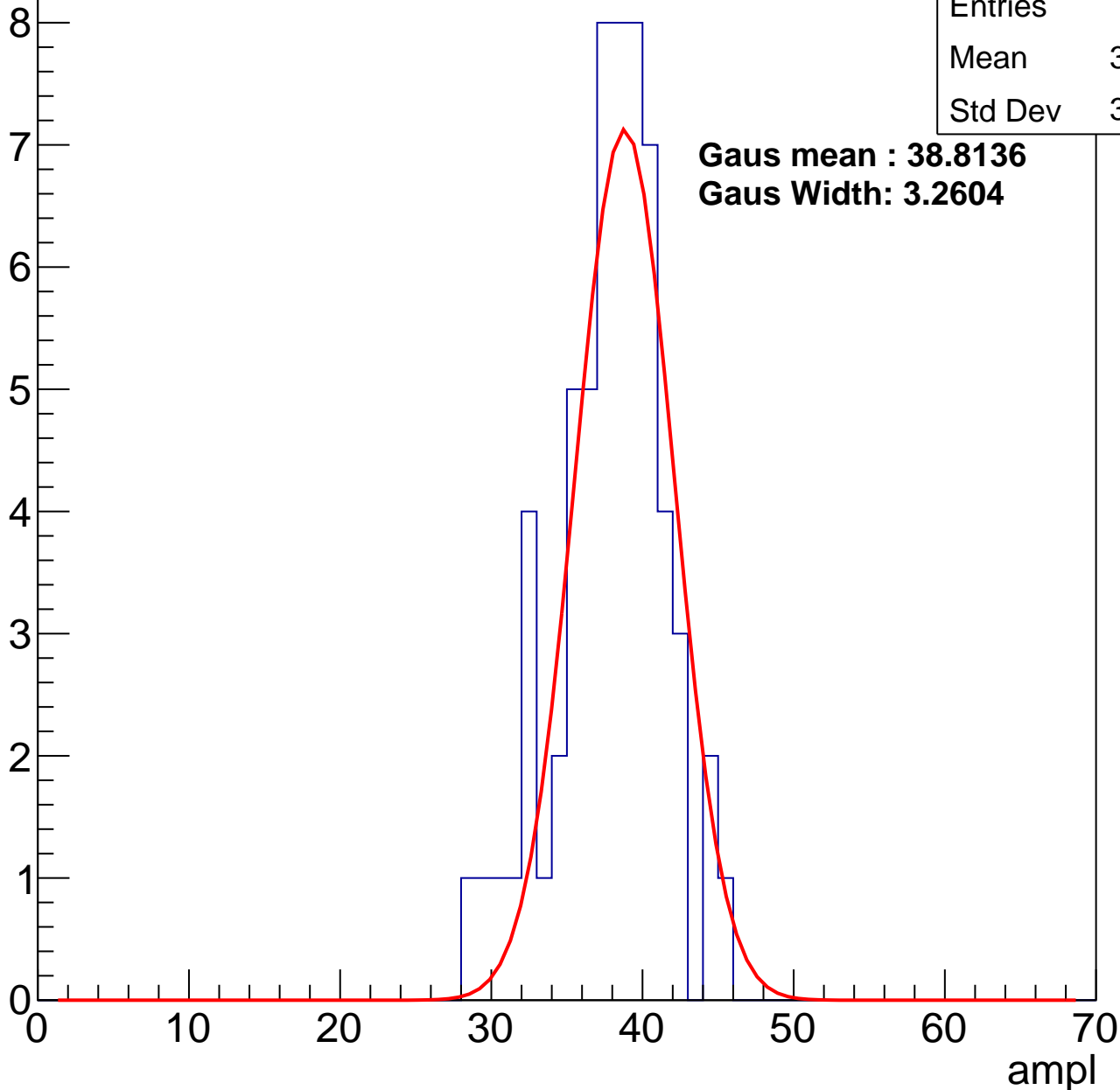
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.37
Std Dev	3.557

Gaus mean : 38.8136

Gaus Width: 3.2604



B1L102S, U6-ch19, adc2

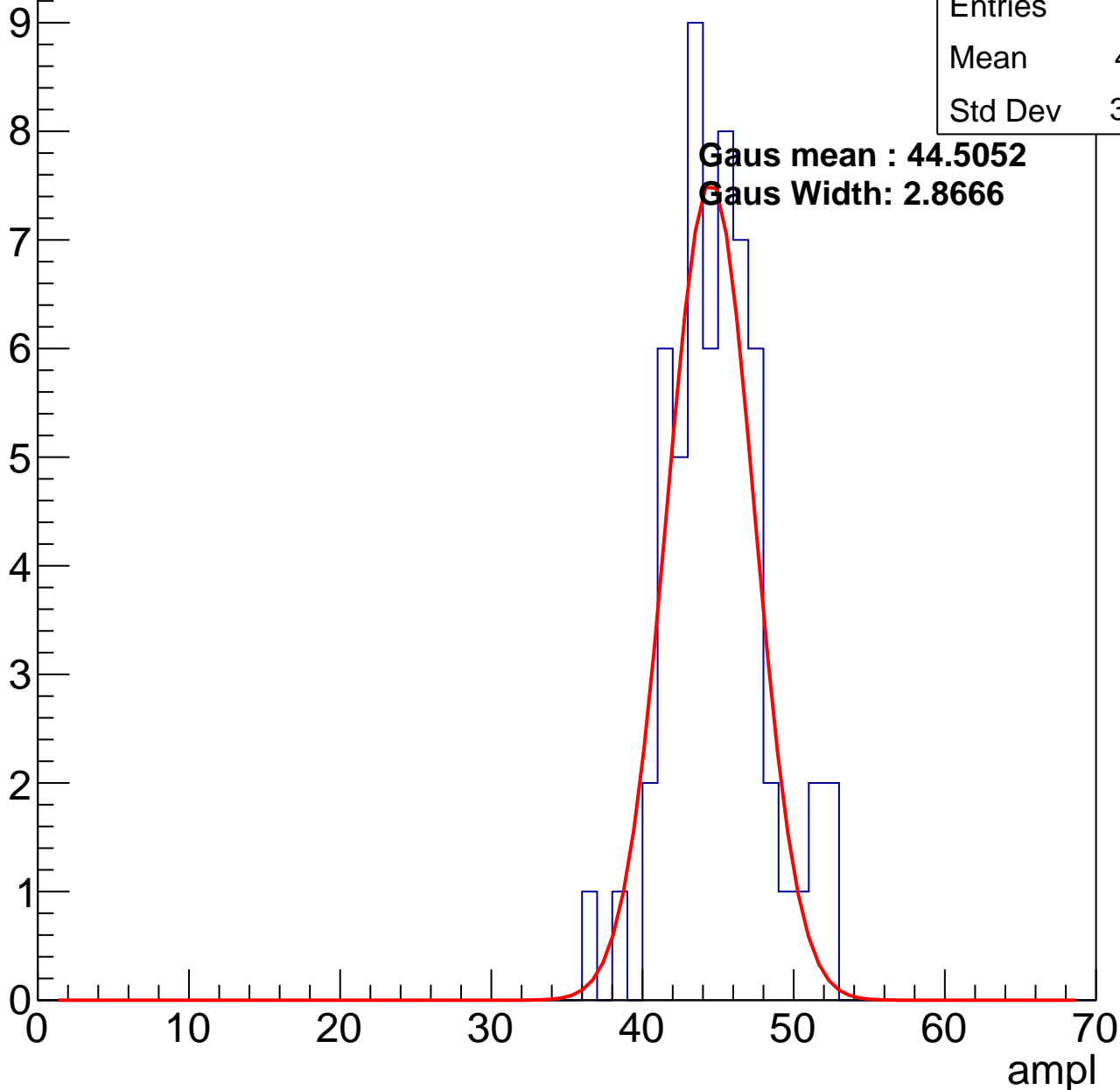
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	44.51
Std Dev	3.212

Gaus mean : 44.5052

Gaus Width: 2.8666

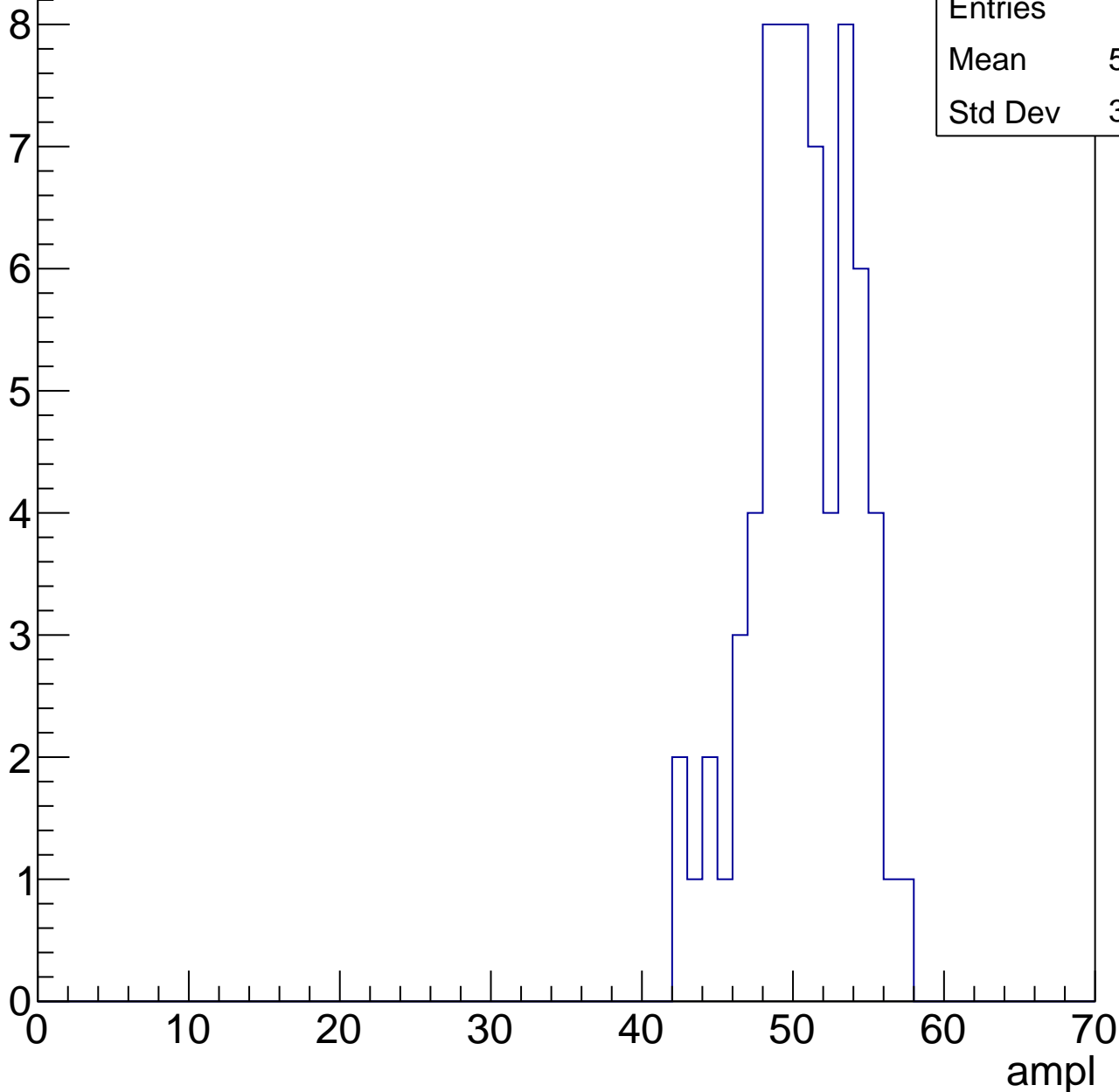


B1L102S, U6-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

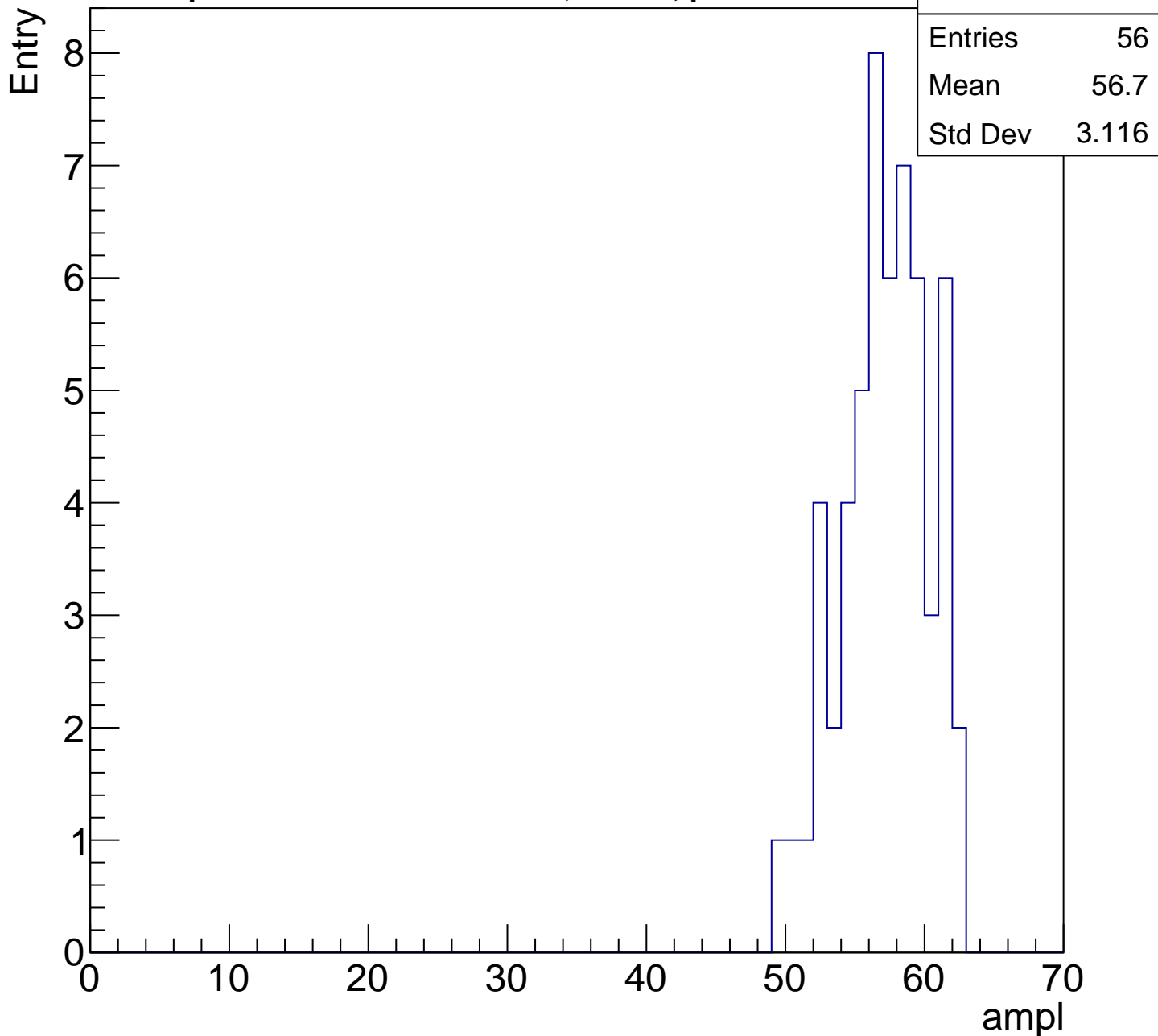
Entry

Entries	68
Mean	50.12
Std Dev	3.372



B1L102S, U6-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	59
Std Dev	9.576

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L102S, U6-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L102S, U6-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch20, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	27.31
Std Dev	5.602

Gaus mean : 28.7734

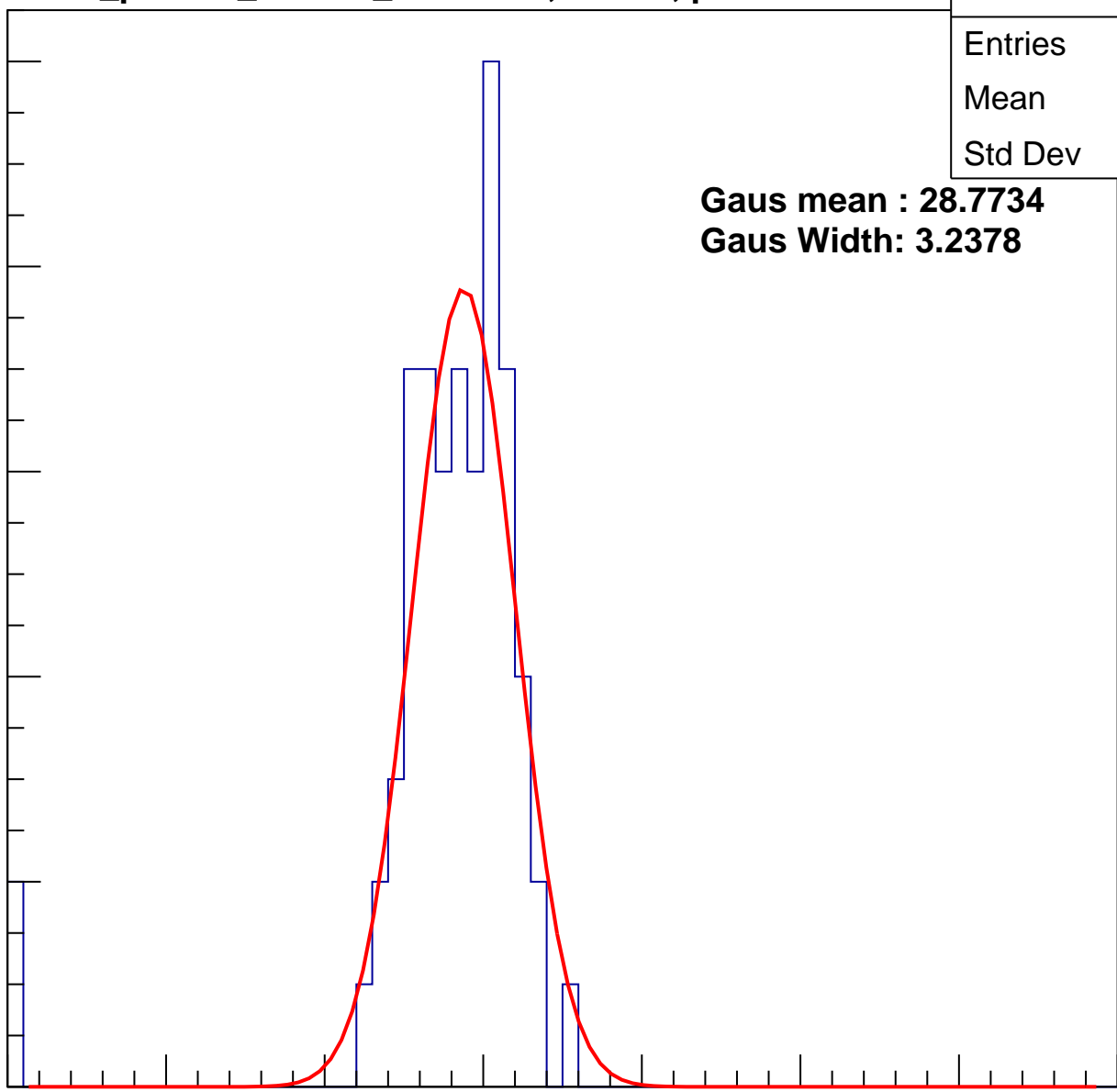
Gaus Width: 3.2378

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch20, adc1

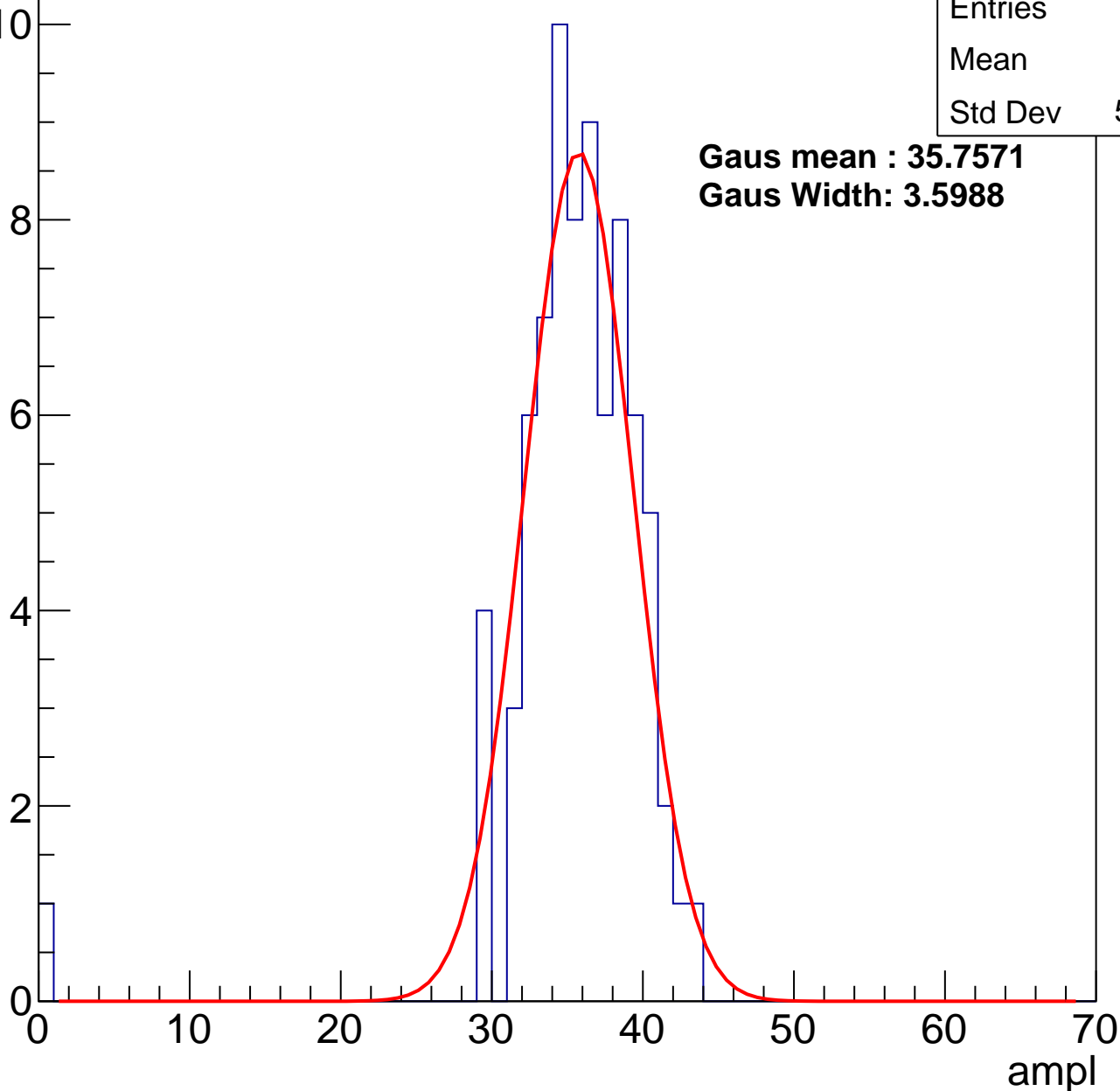
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.1
Std Dev	5.121

Gaus mean : 35.7571

Gaus Width: 3.5988



B1L102S, U6-ch20, adc2

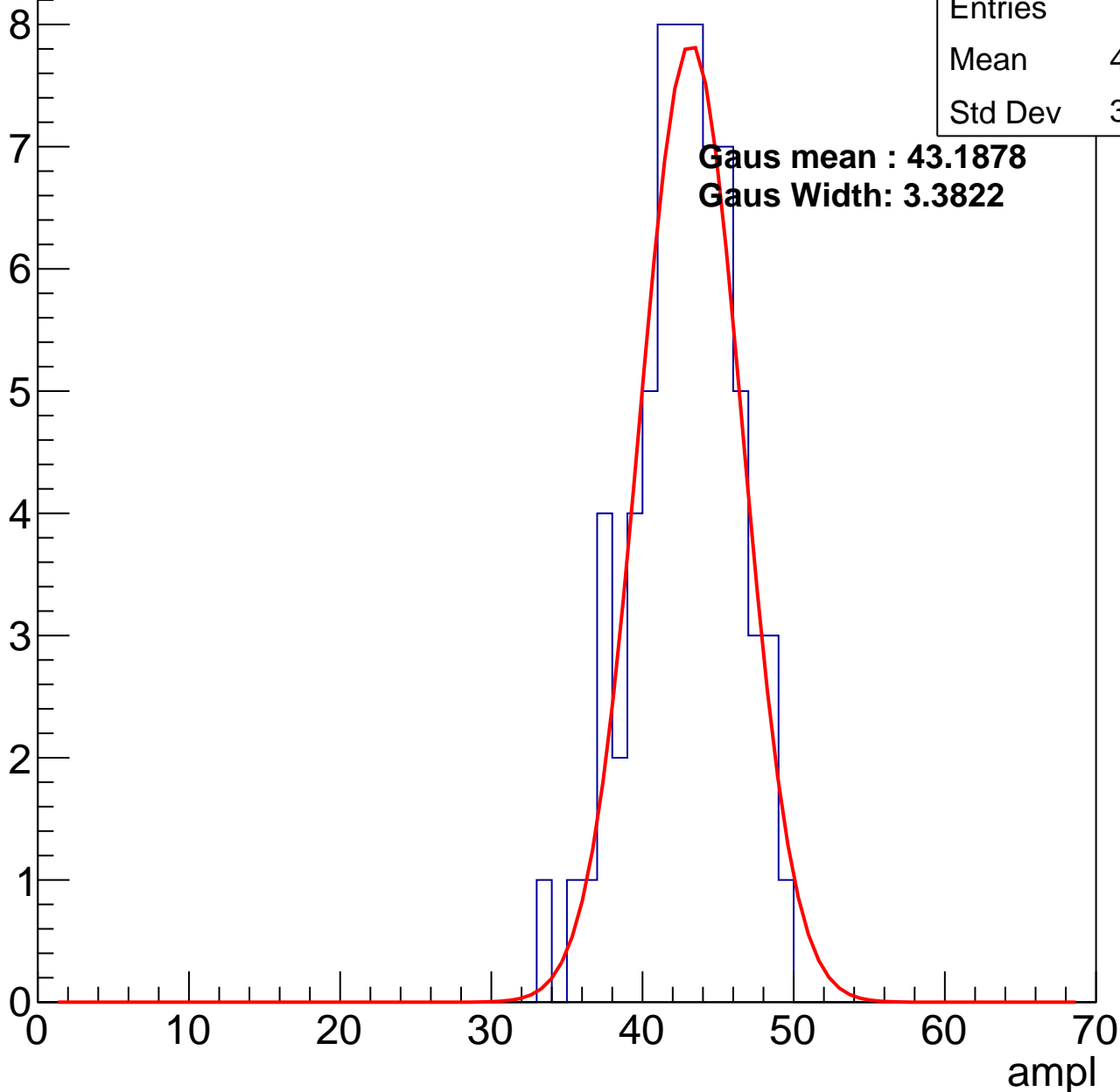
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.34
Std Dev	3.355

Gaus mean : 43.1878

Gaus Width: 3.3822

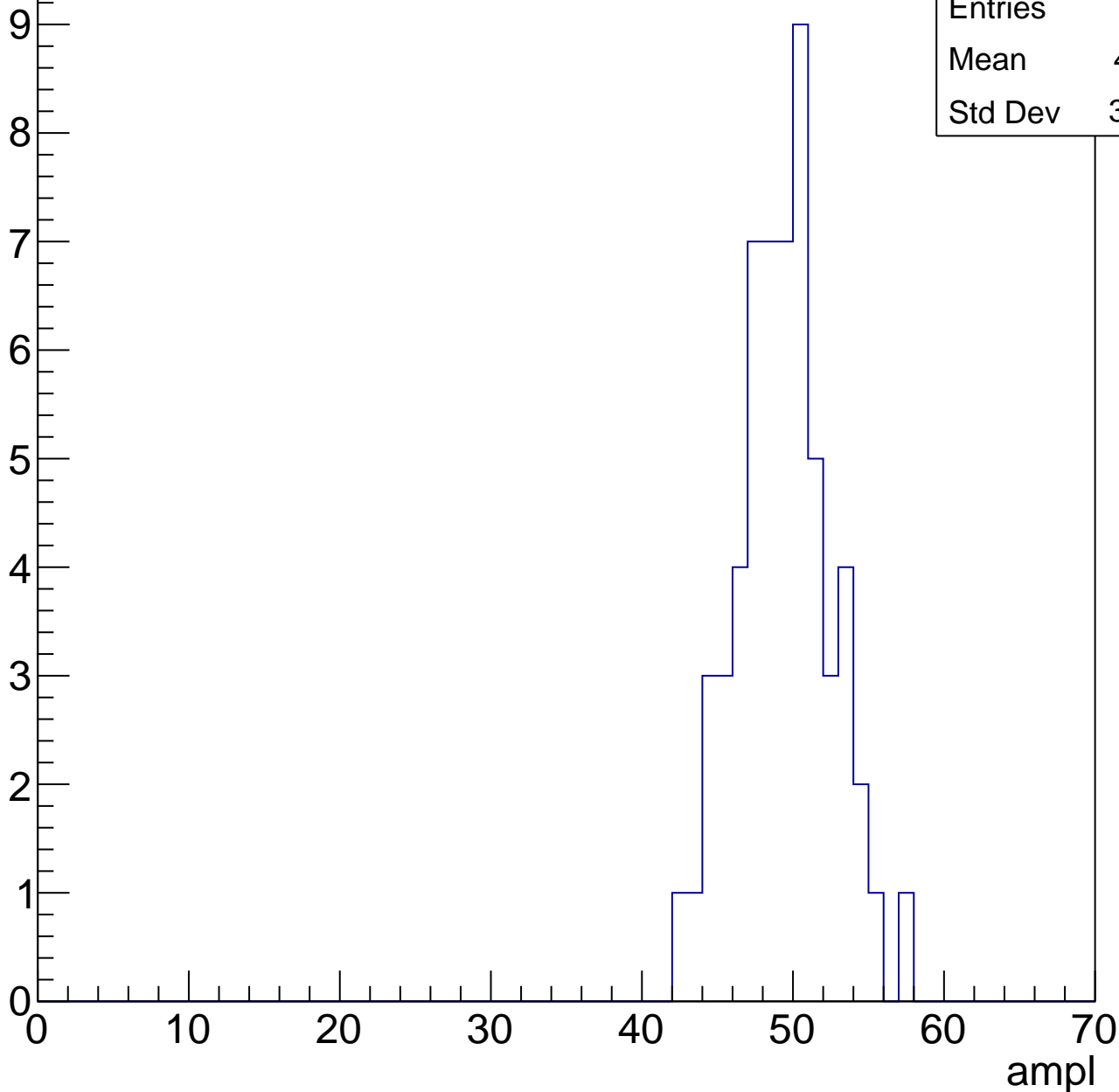


B1L102S, U6-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

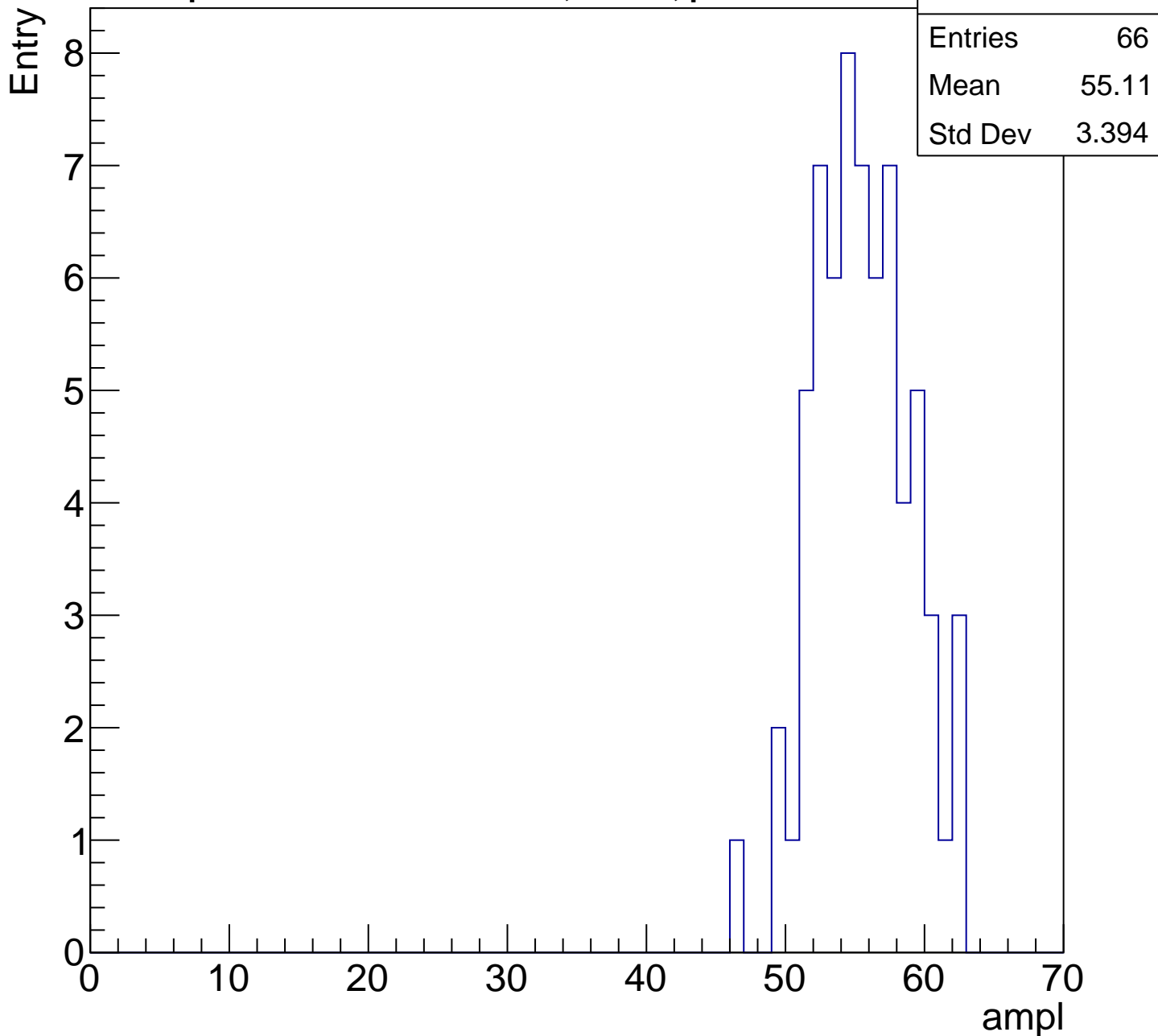
Entry

Entries	58
Mean	48.91
Std Dev	3.075



B1L102S, U6-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

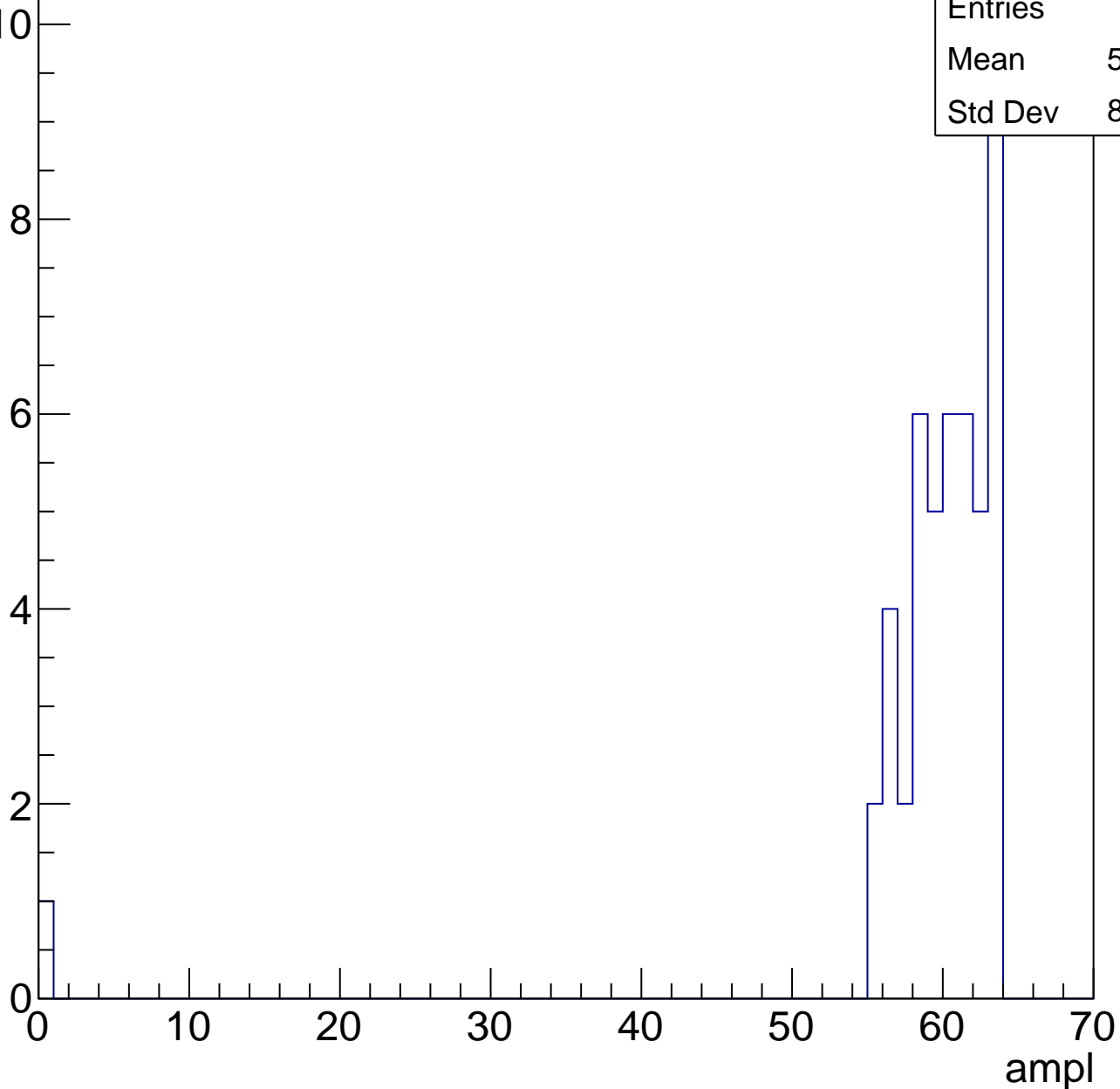


B1L102S, U6-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.66
Std Dev	8.983



B1L102S, U6-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

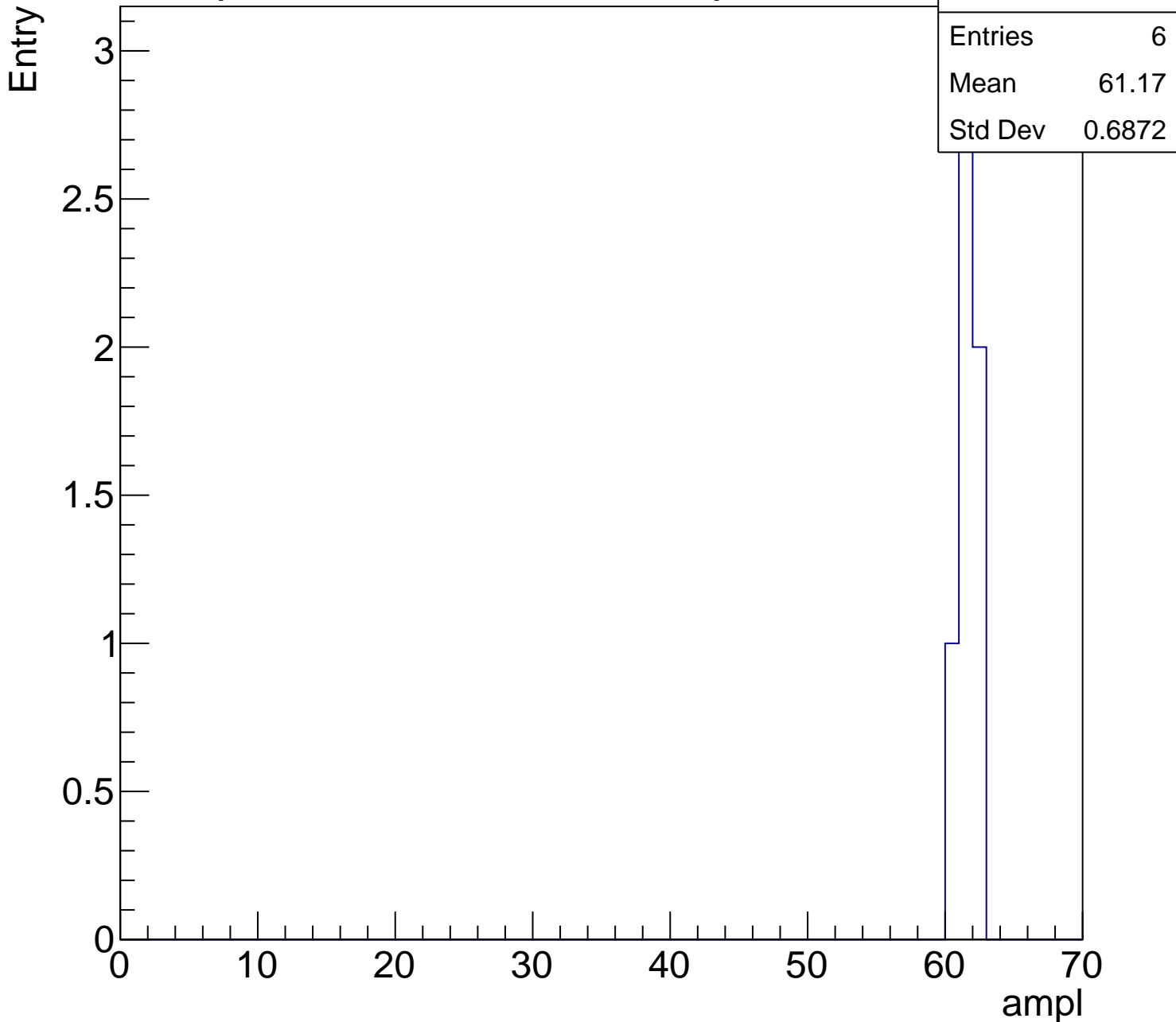
6

Mean

61.17

Std Dev

0.6872



B1L102S, U6-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch21, adc0

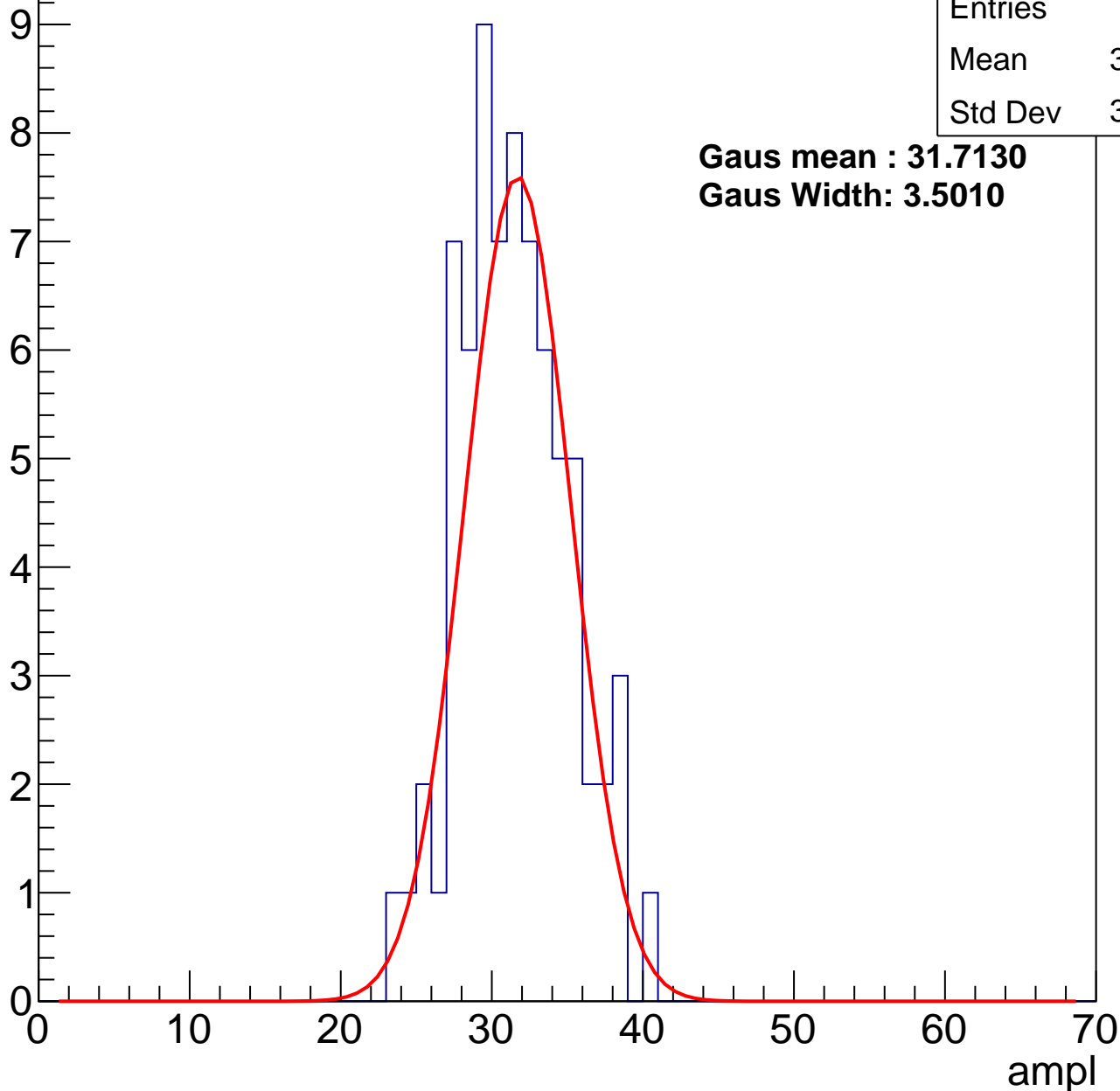
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	31.04
Std Dev	3.559

Gaus mean : 31.7130

Gaus Width: 3.5010



B1L102S, U6-ch21, adc1

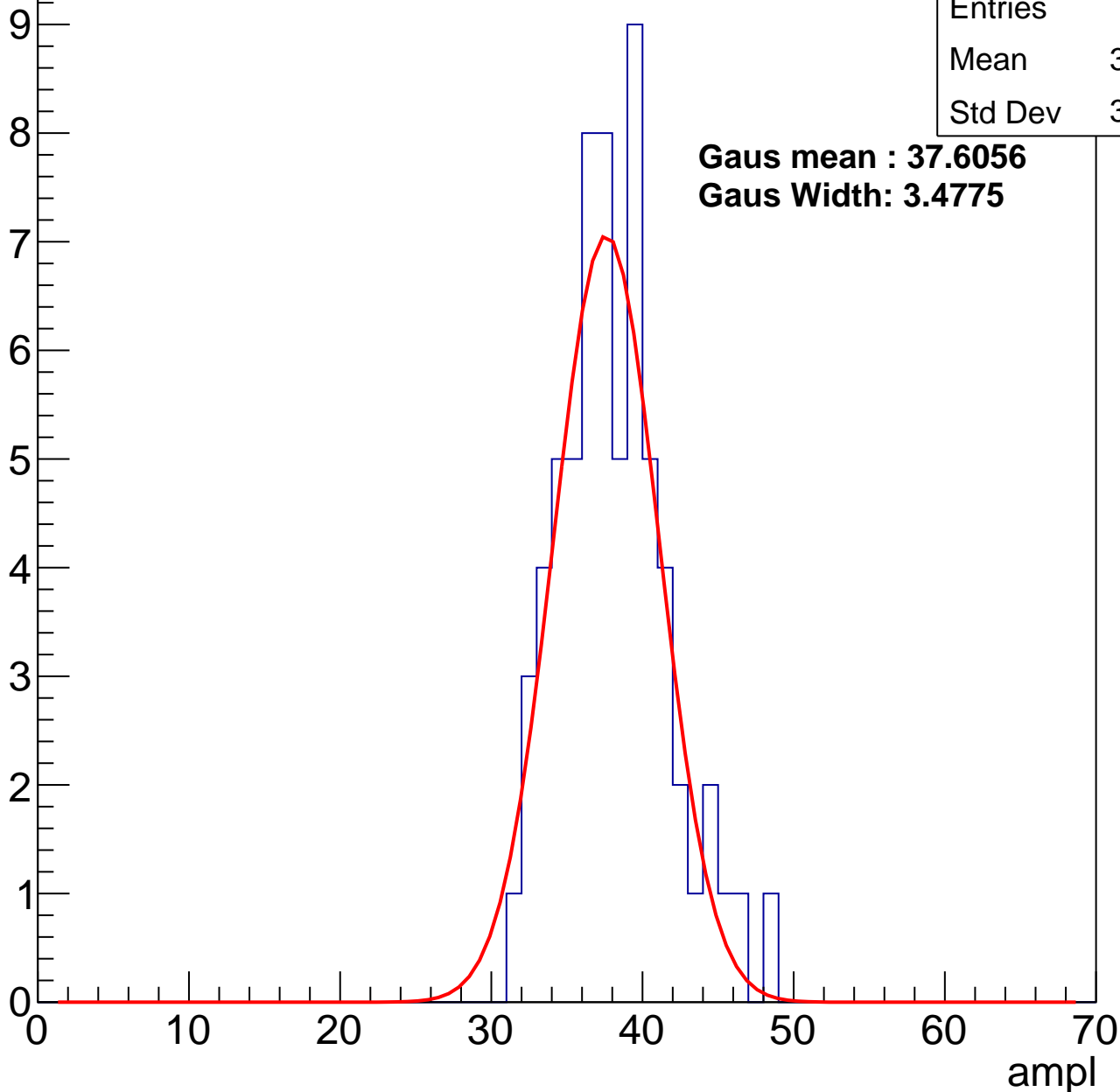
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	37.65
Std Dev	3.554

Gaus mean : 37.6056

Gaus Width: 3.4775



B1L102S, U6-ch21, adc2

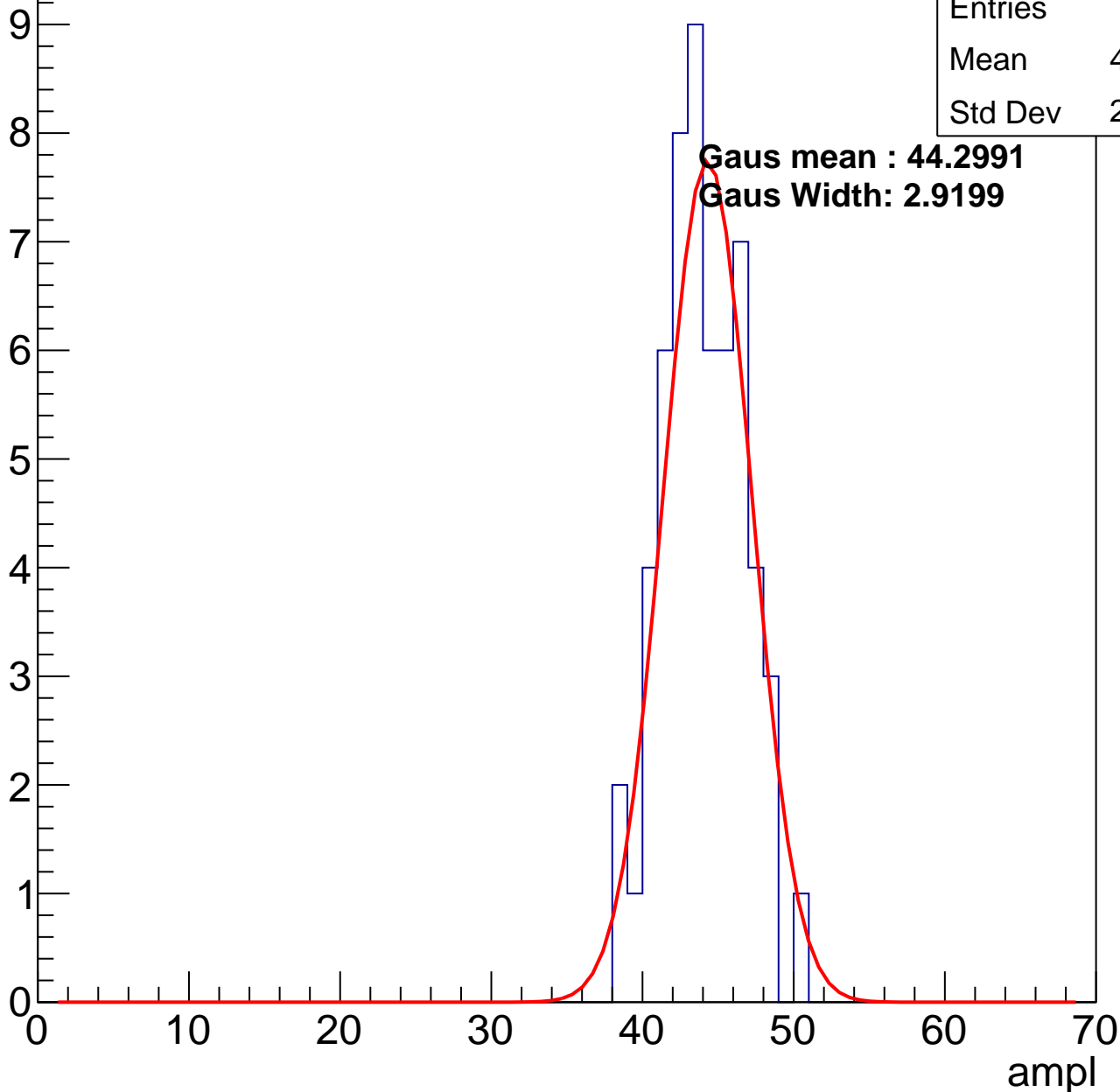
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.54
Std Dev	2.643

Gaus mean : 44.2991

Gaus Width: 2.9199

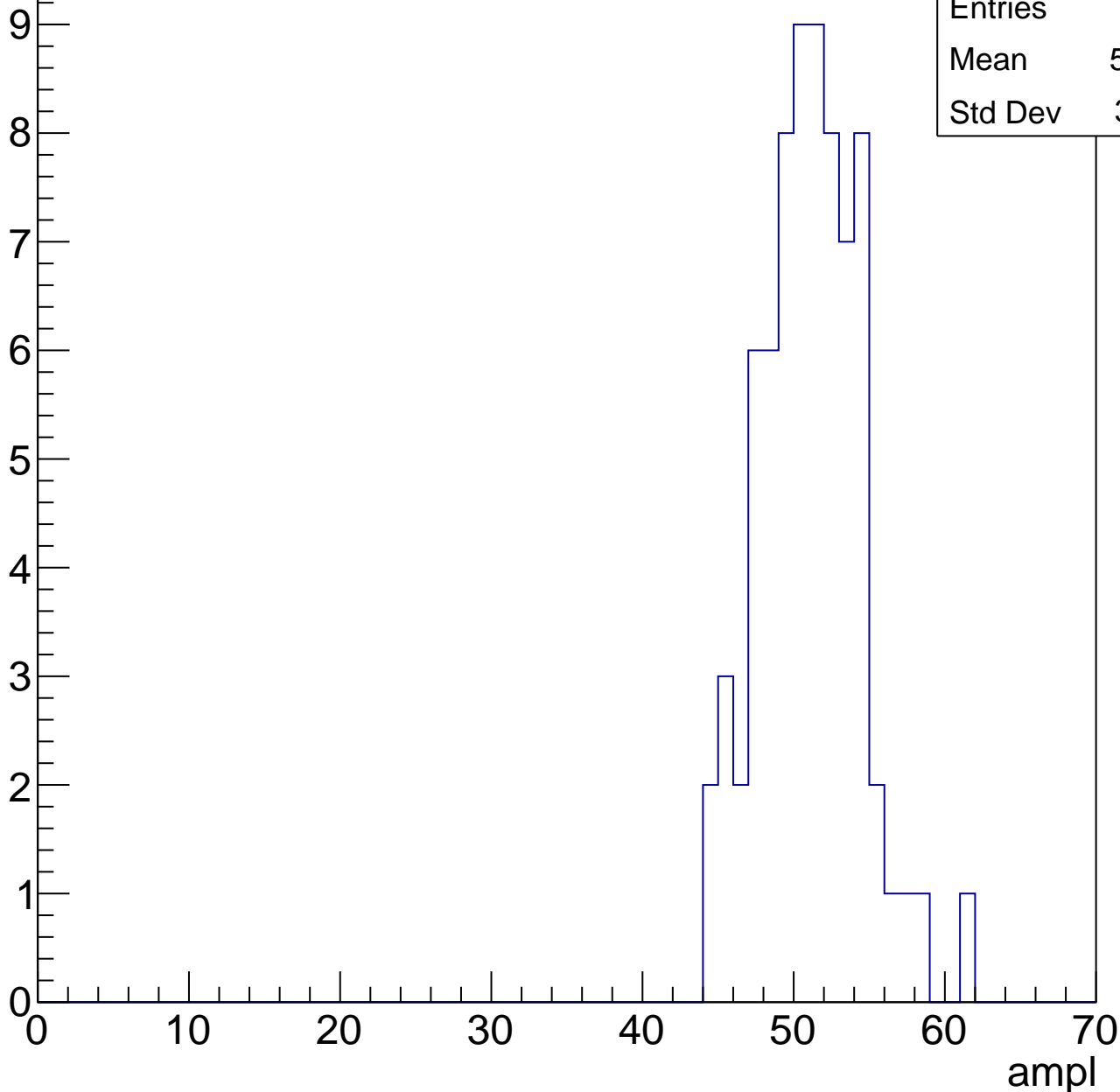


B1L102S, U6-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	50.64
Std Dev	3.261

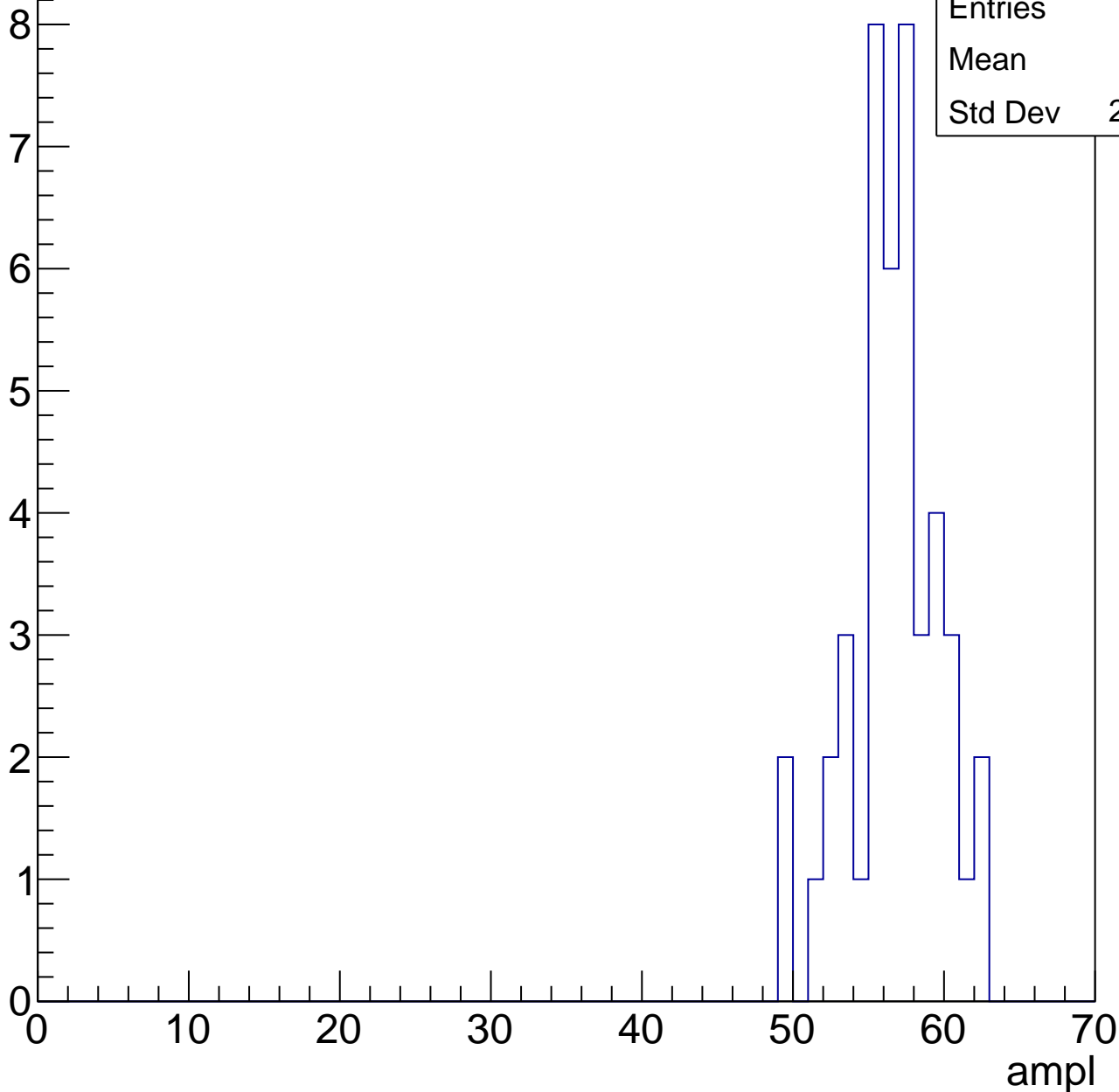


B1L102S, U6-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	56.2
Std Dev	2.989

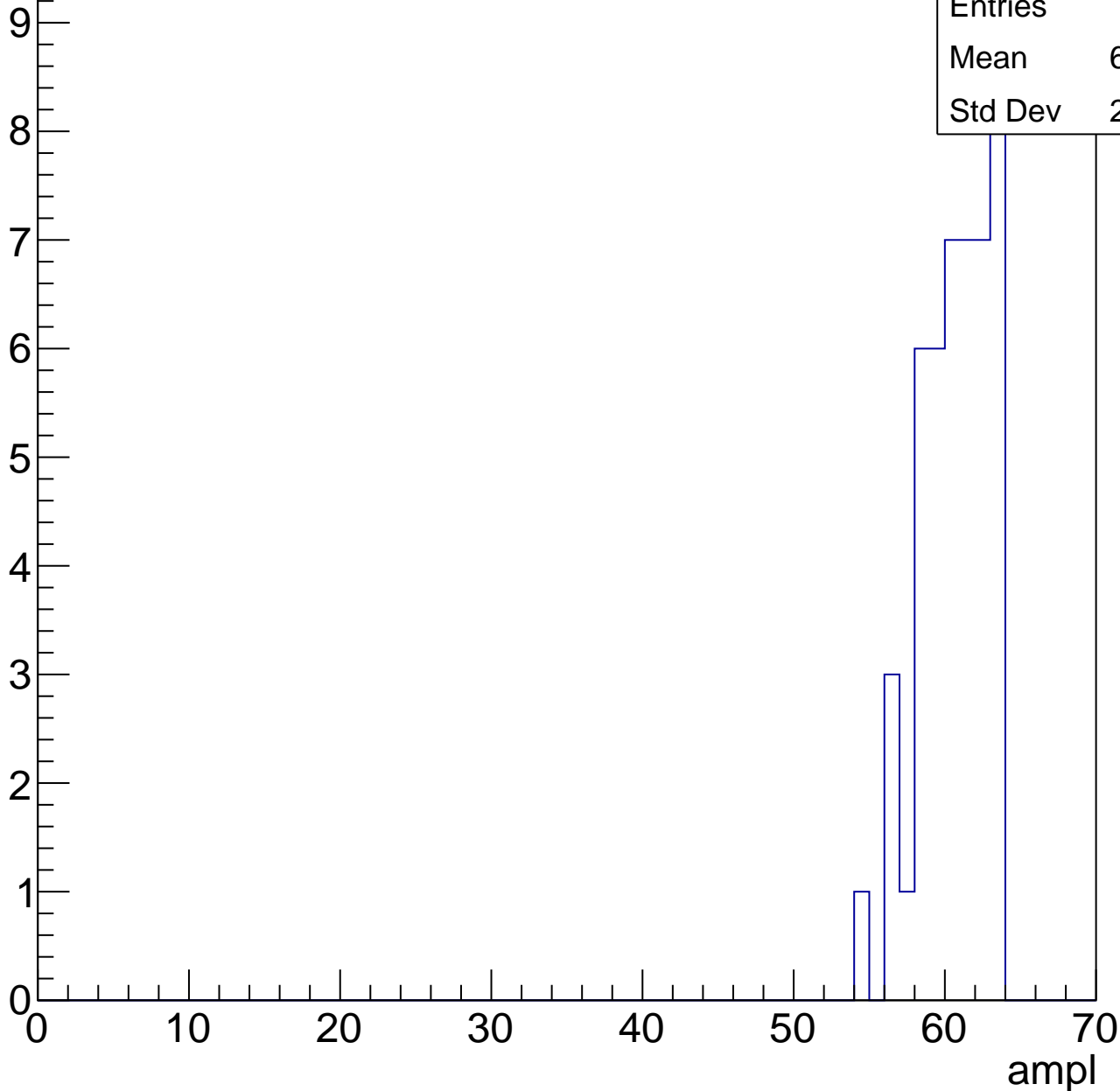


B1L102S, U6-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

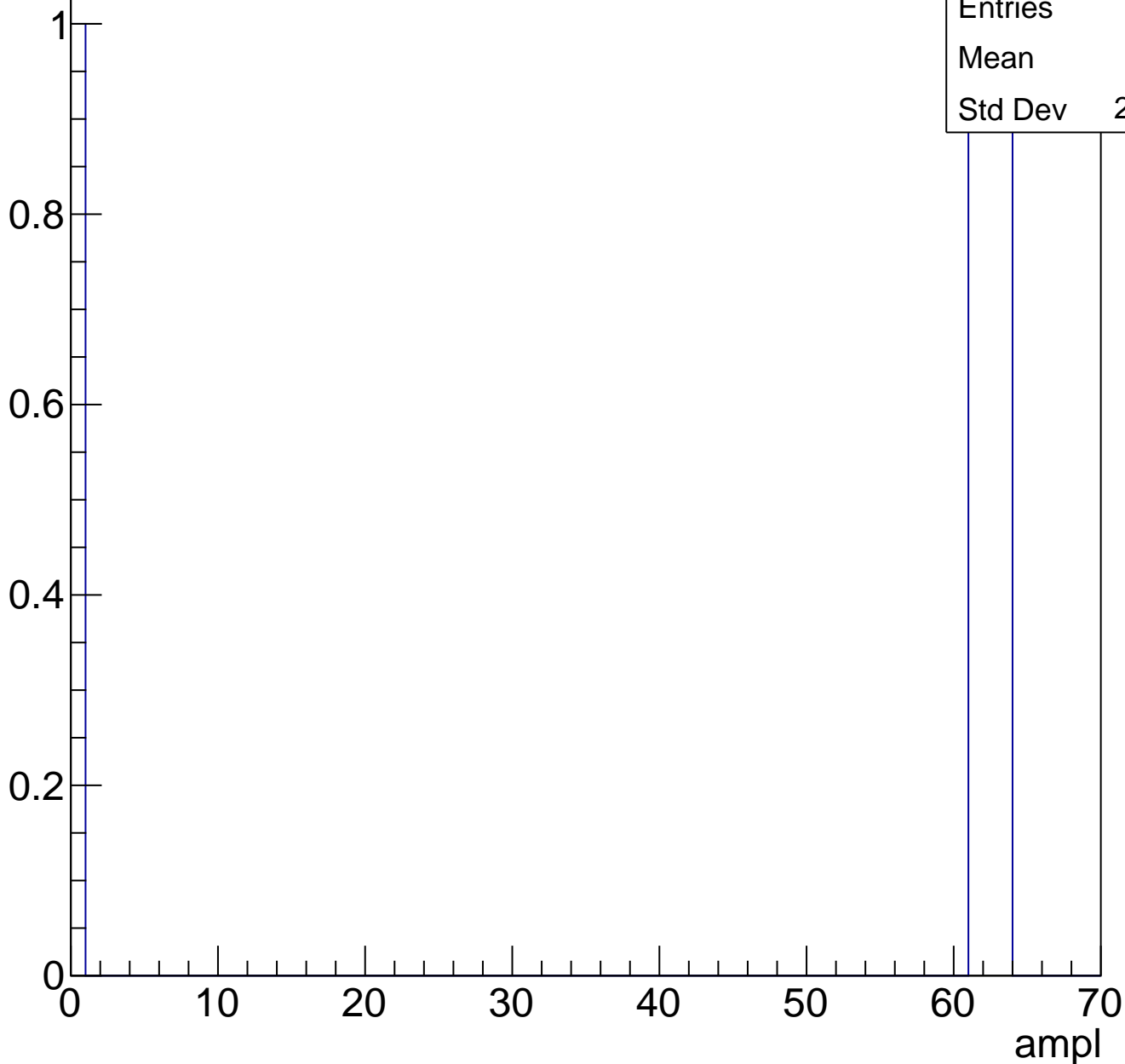
Entries	47
Mean	60.19
Std Dev	2.247



B1L102S, U6-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch22, adc0

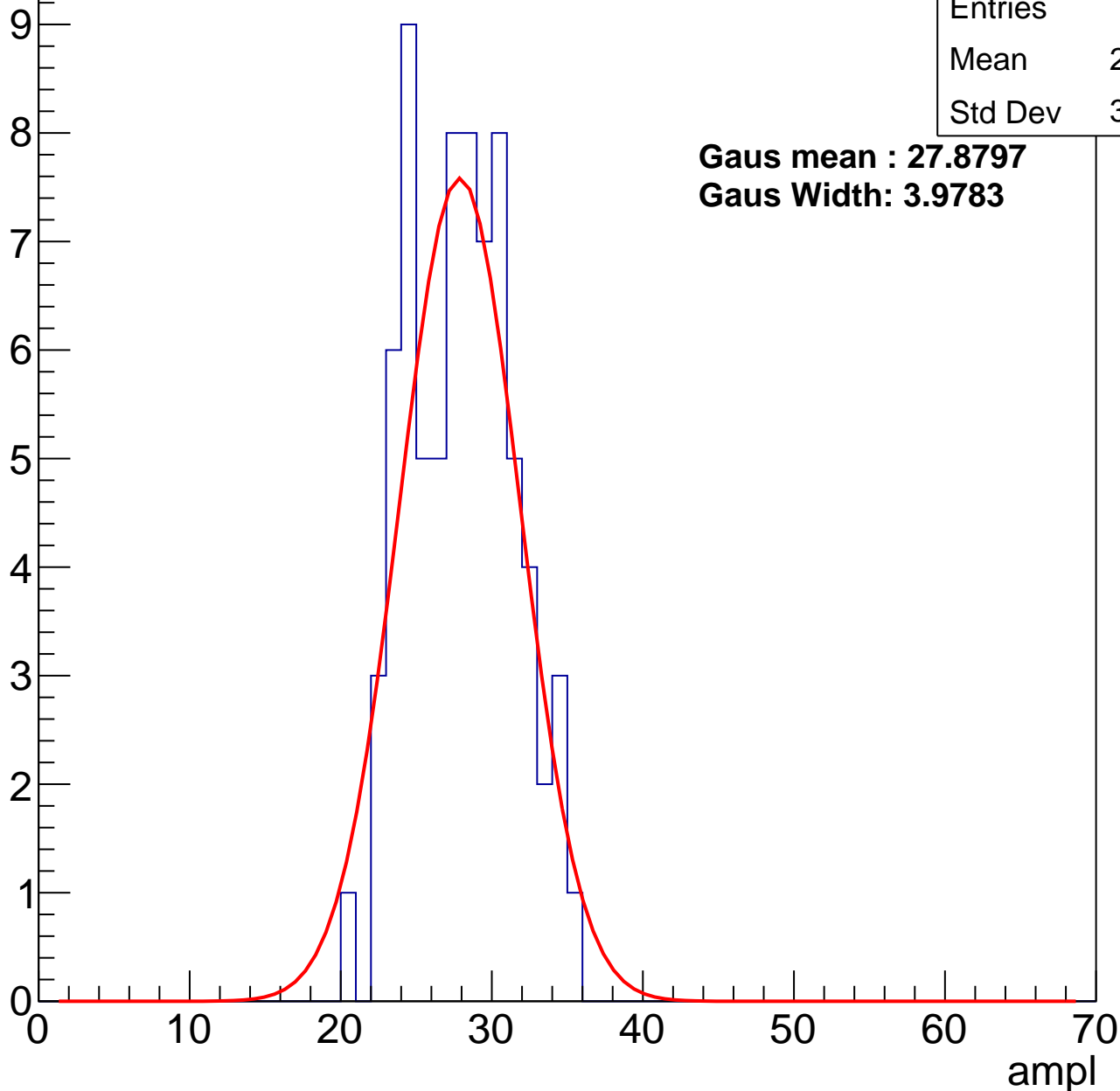
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.52
Std Dev	3.419

Gaus mean : 27.8797

Gaus Width: 3.9783



B1L102S, U6-ch22, adc1

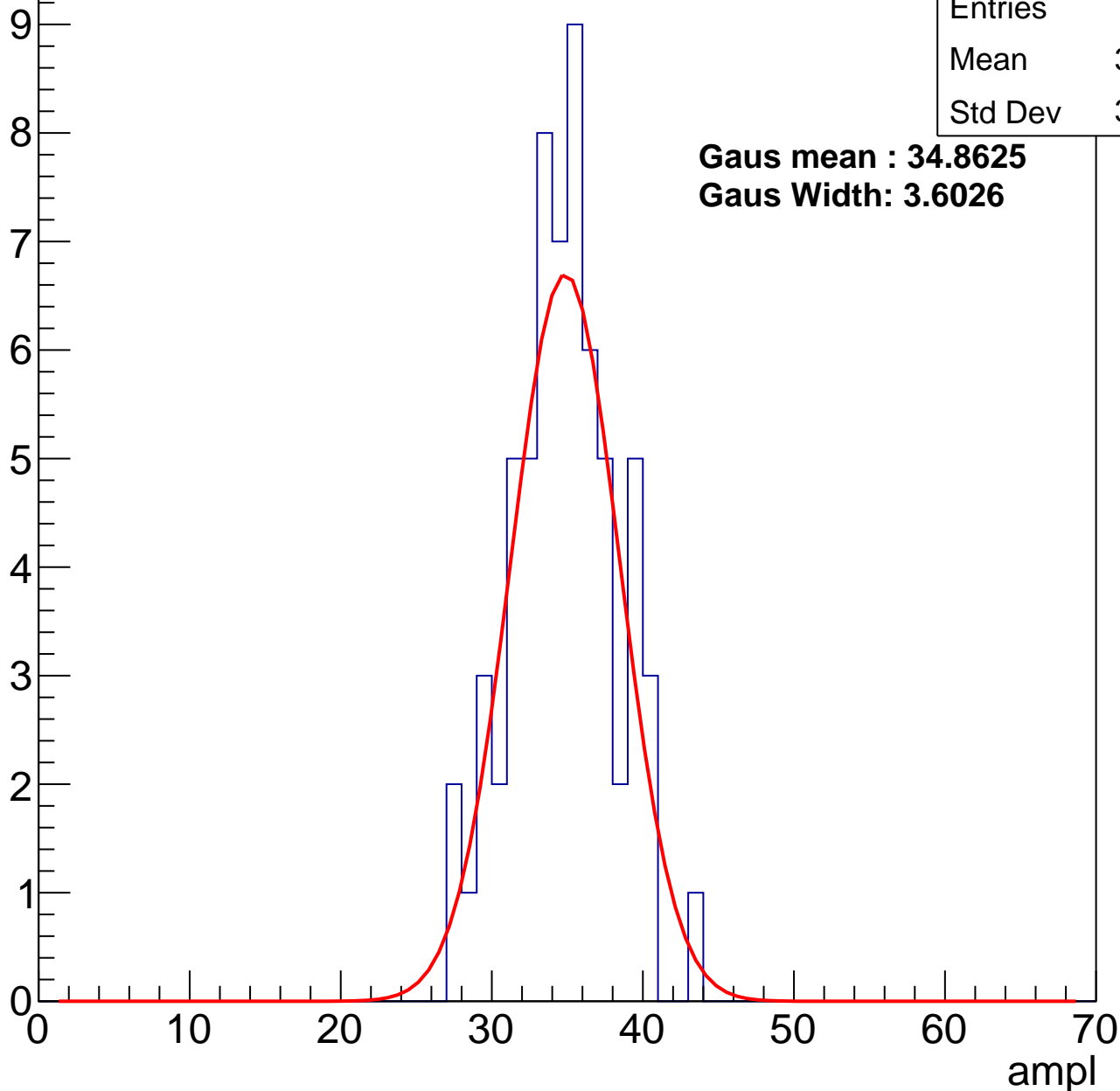
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	34.31
Std Dev	3.381

Gaus mean : 34.8625

Gaus Width: 3.6026



B1L102S, U6-ch22, adc2

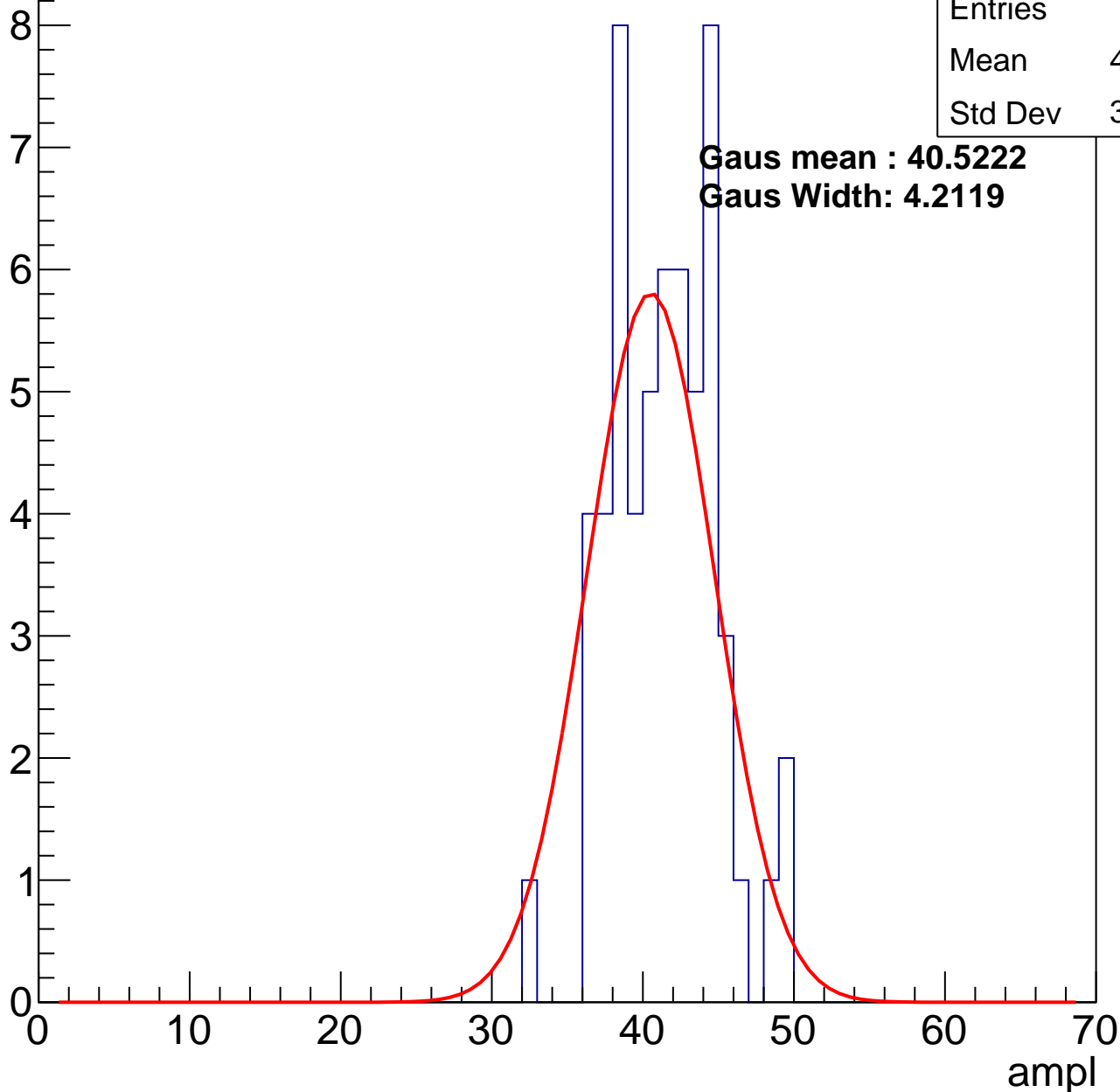
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.97
Std Dev	3.439

Gaus mean : 40.5222

Gaus Width: 4.2119

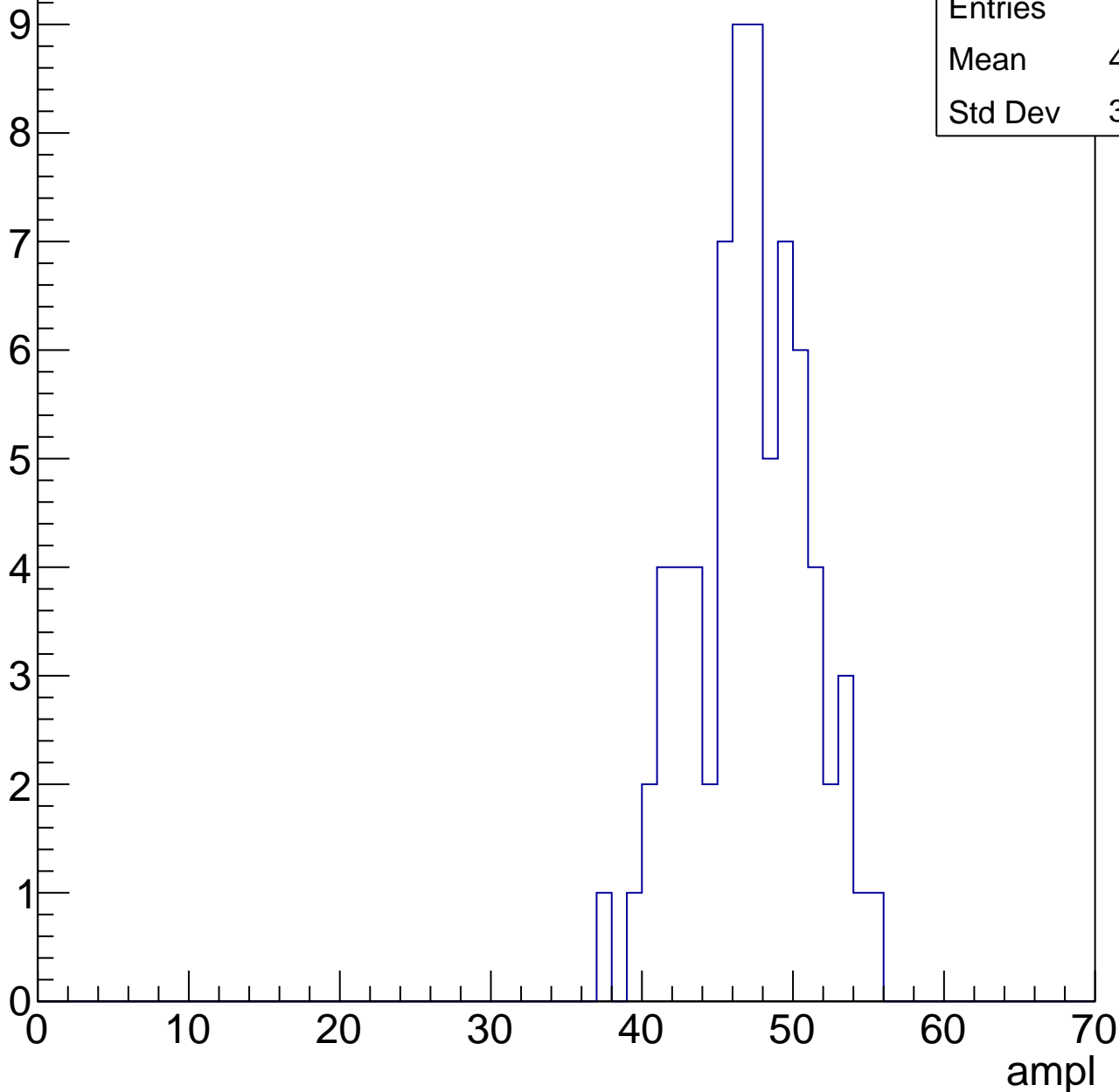


B1L102S, U6-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	46.65
Std Dev	3.798

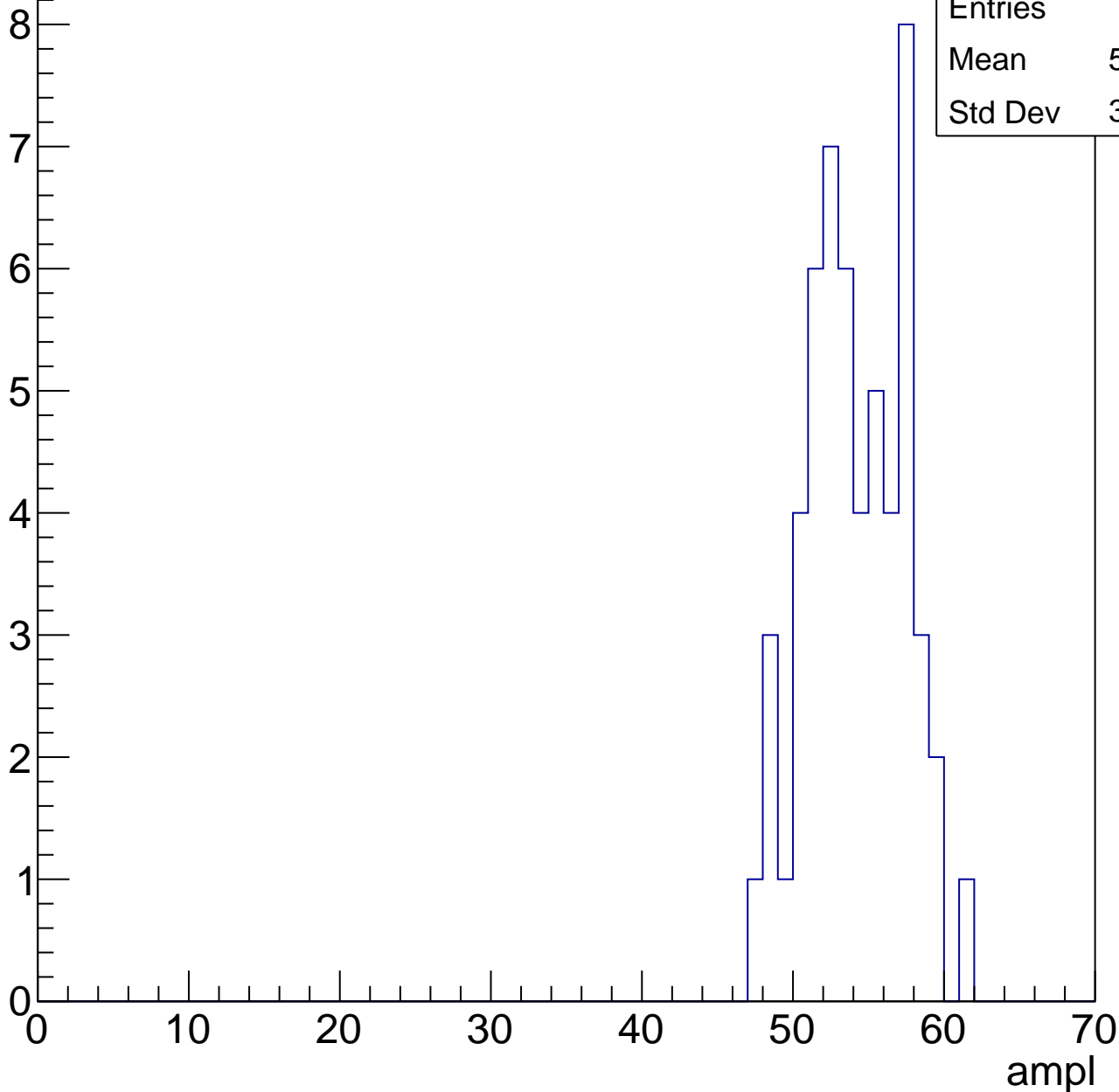


B1L102S, U6-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.67
Std Dev	3.214

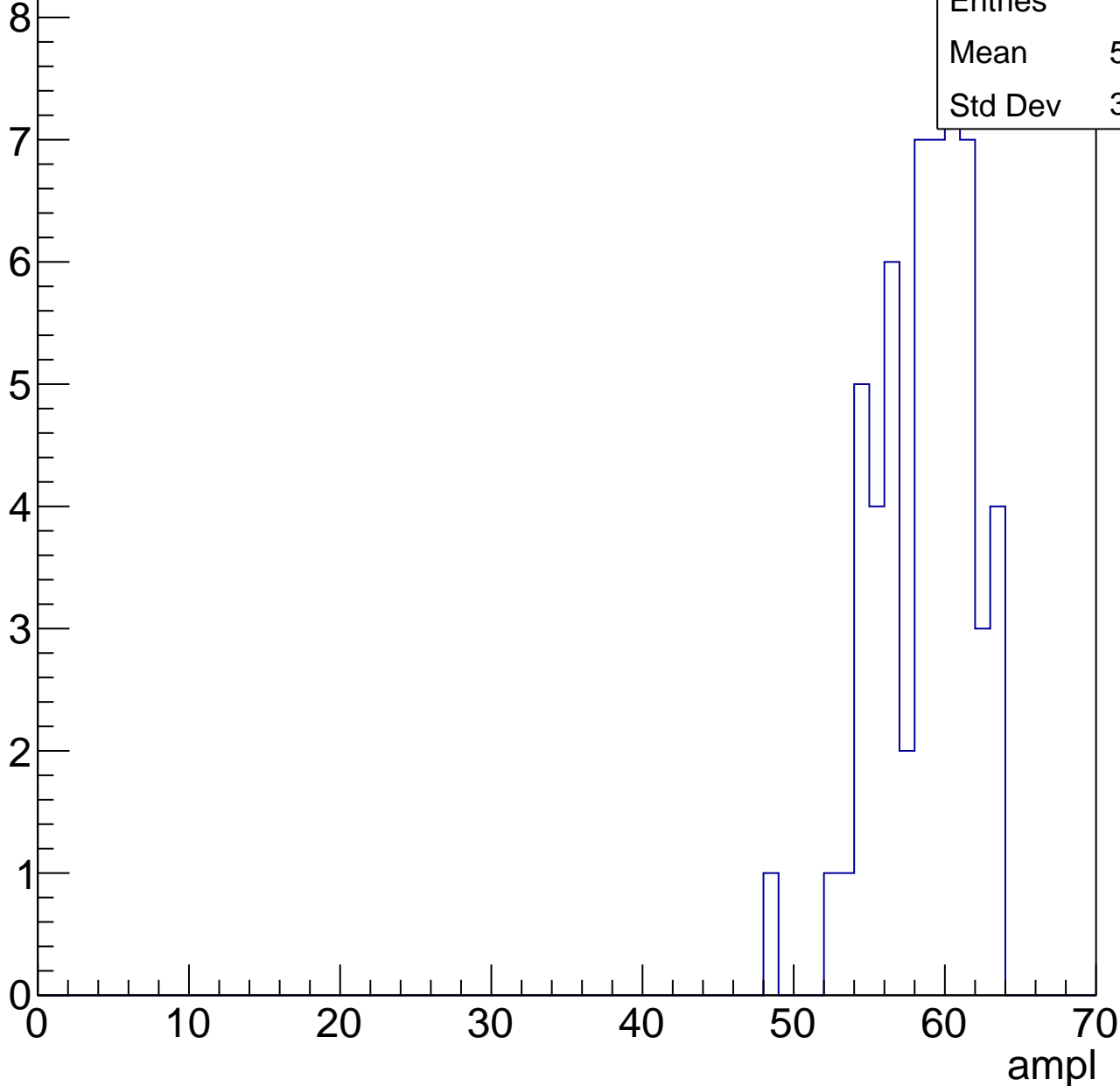


B1L102S, U6-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.16
Std Dev	3.138

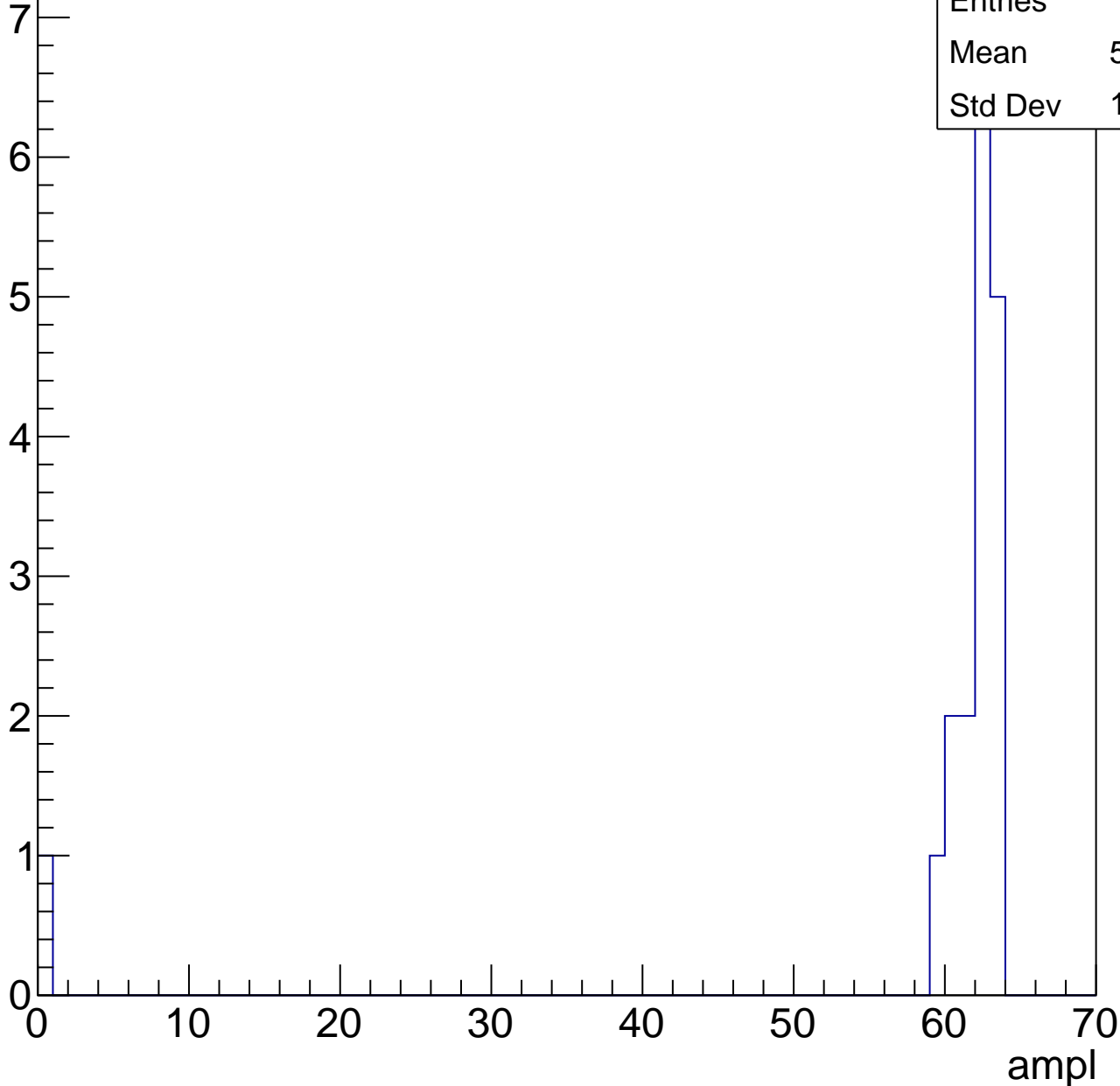


B1L102S, U6-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58.33
Std Dev	14.19



B1L102S, U6-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch23, adc0

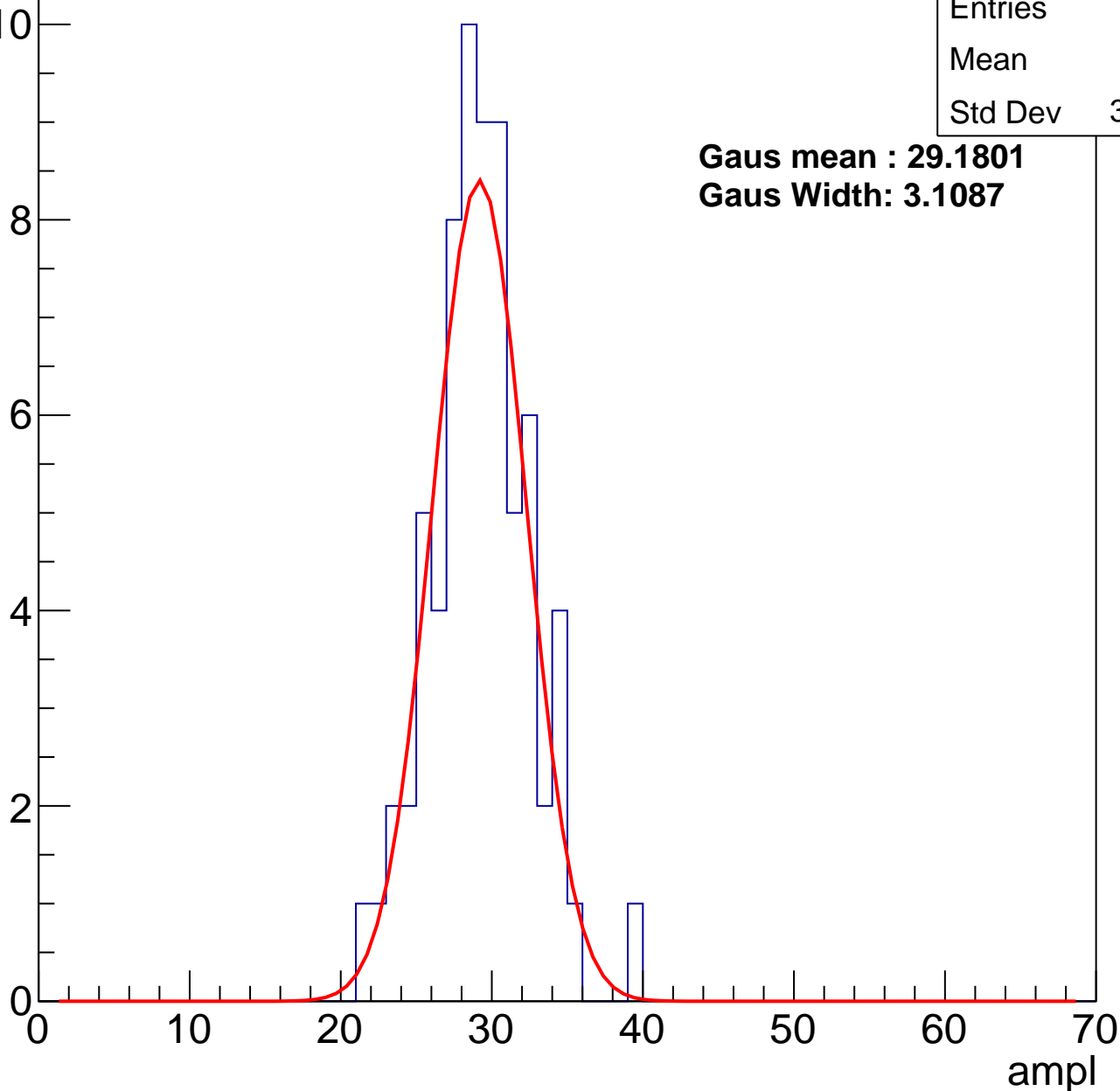
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.8
Std Dev	3.254

Gaus mean : 29.1801

Gaus Width: 3.1087



B1L102S, U6-ch23, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	35.77
Std Dev	3.029

Gaus mean : 36.0534

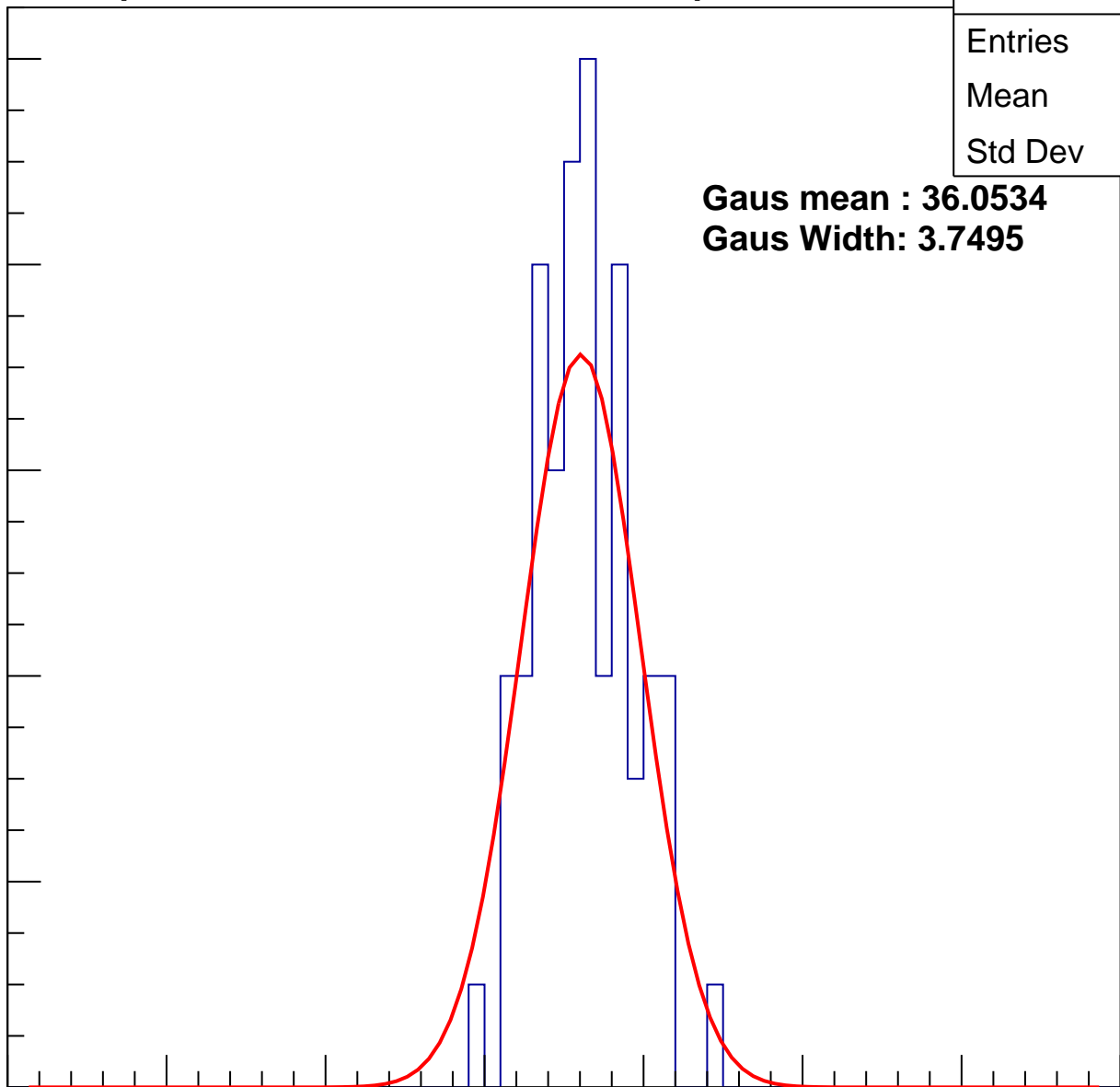
Gaus Width: 3.7495

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch23, adc2

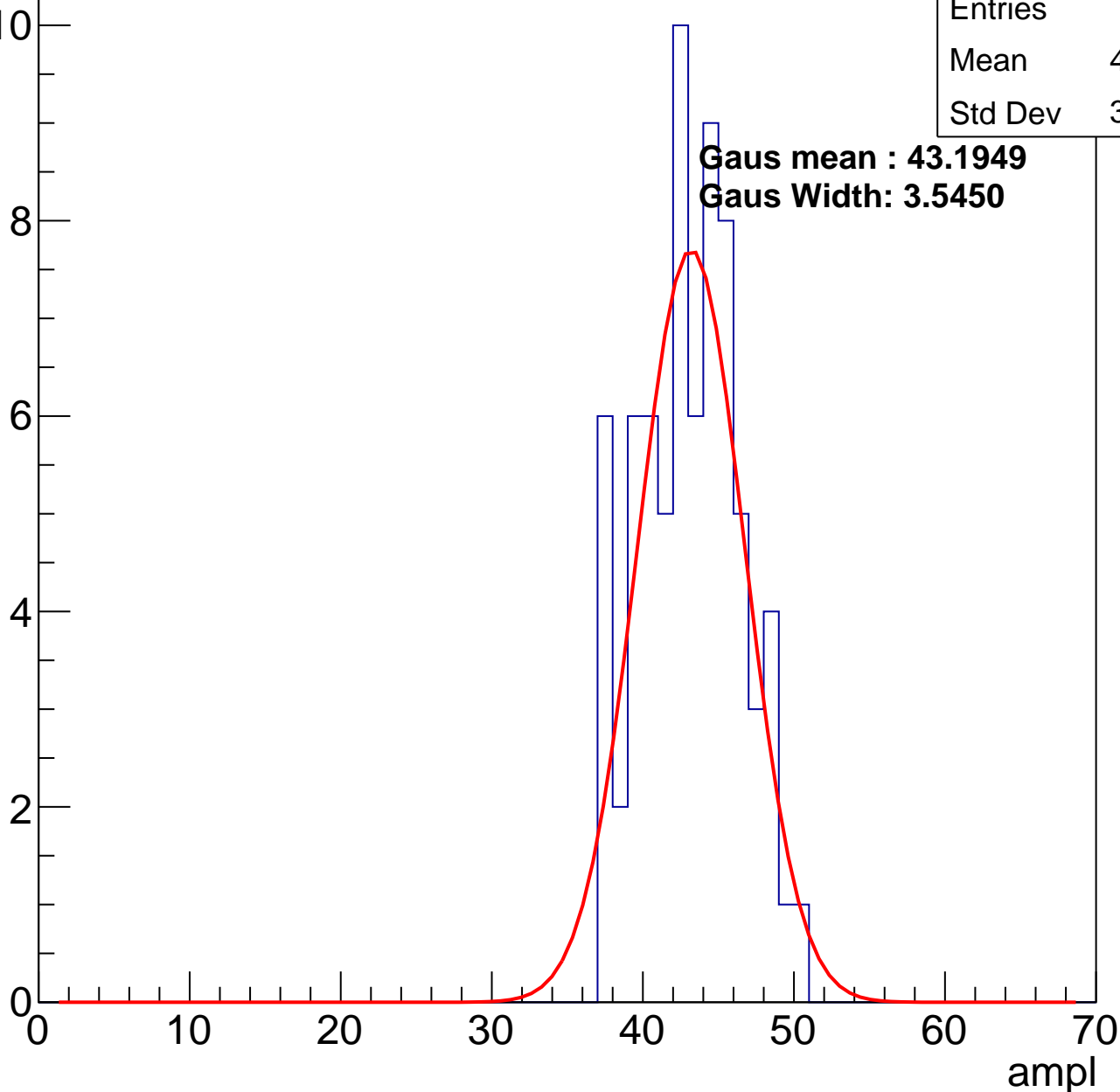
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.68
Std Dev	3.244

Gaus mean : 43.1949

Gaus Width: 3.5450

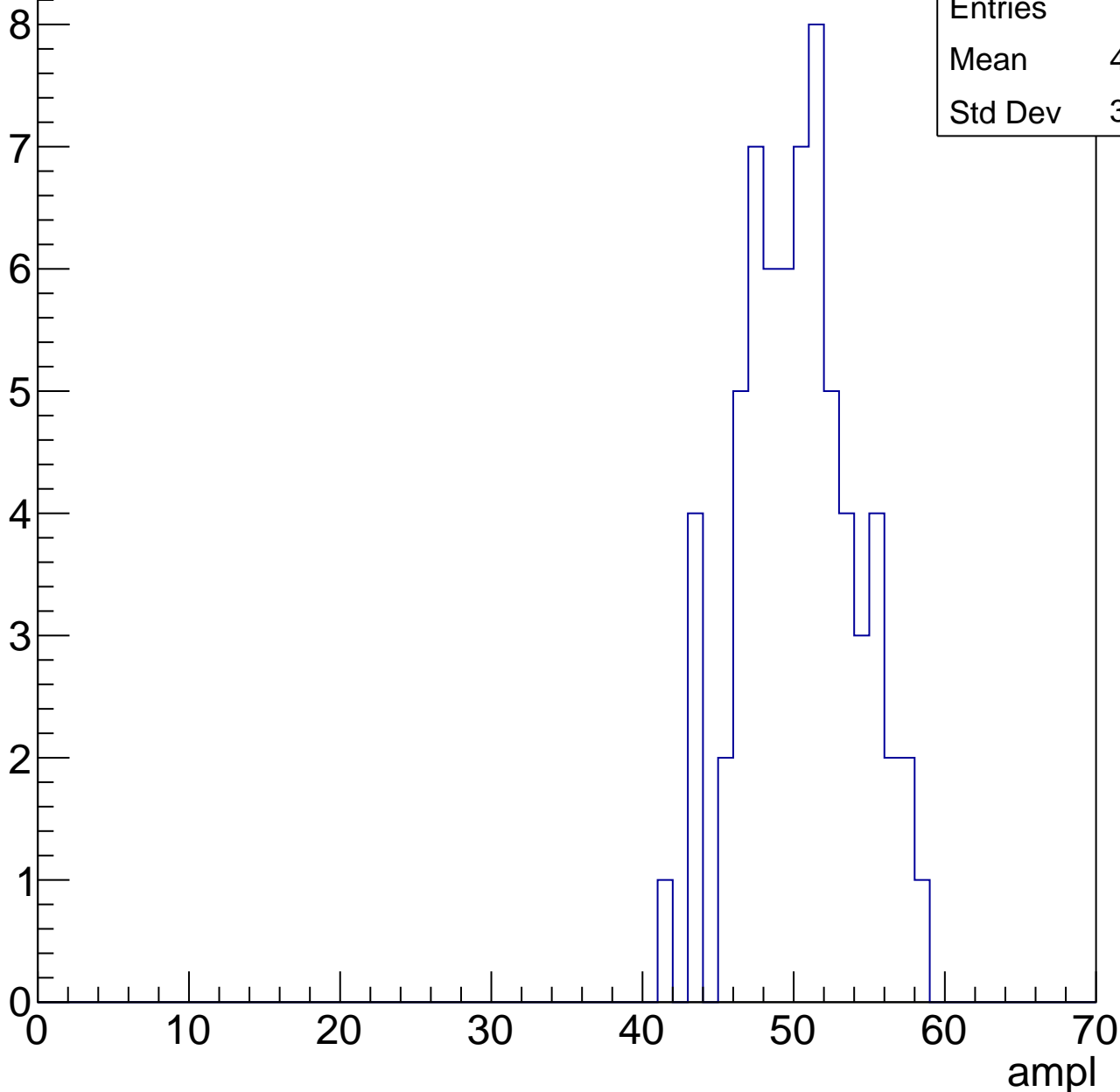


B1L102S, U6-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.85
Std Dev	3.755

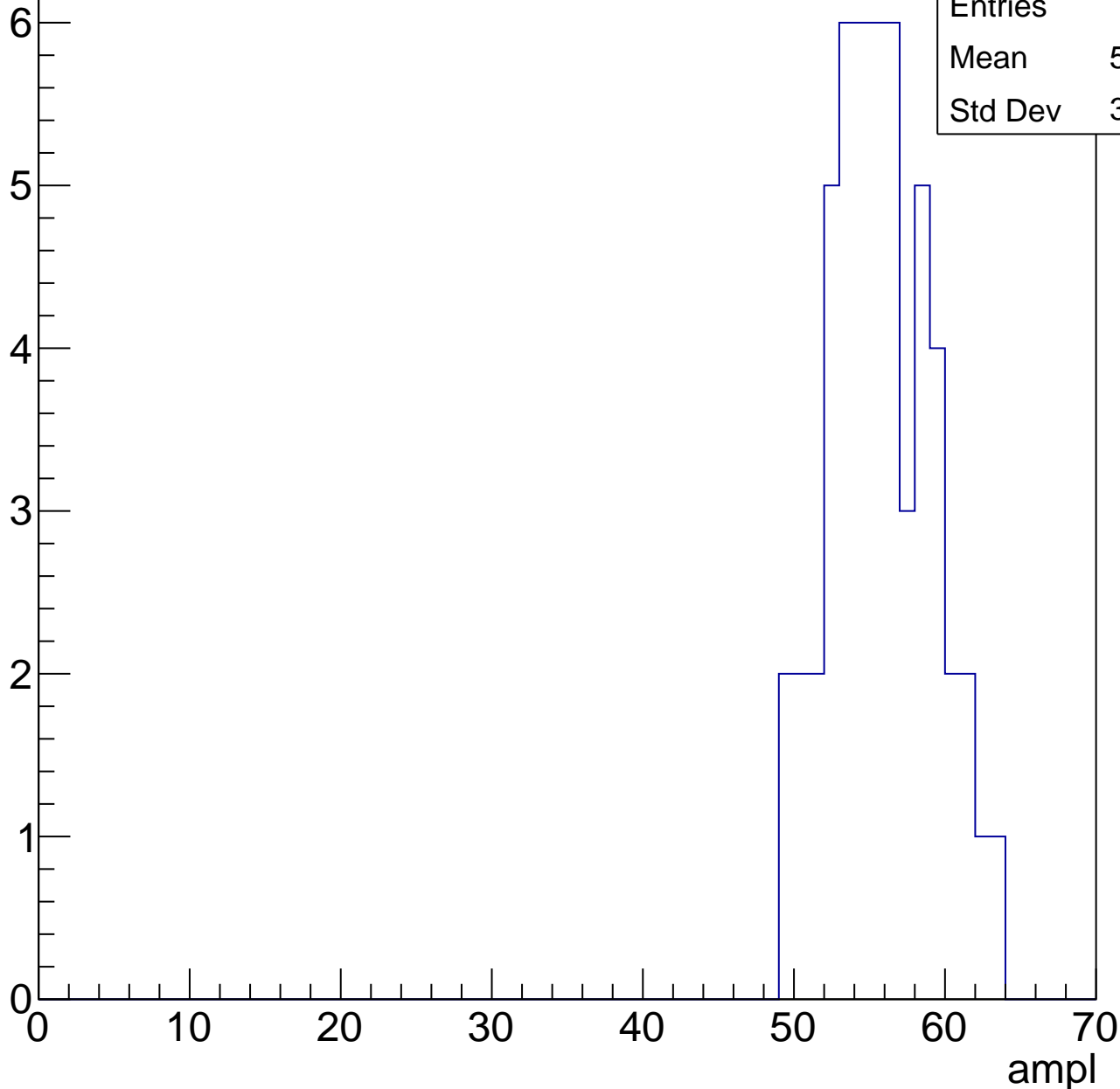


B1L102S, U6-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.32
Std Dev	3.324



B1L102S, U6-ch23, adc5

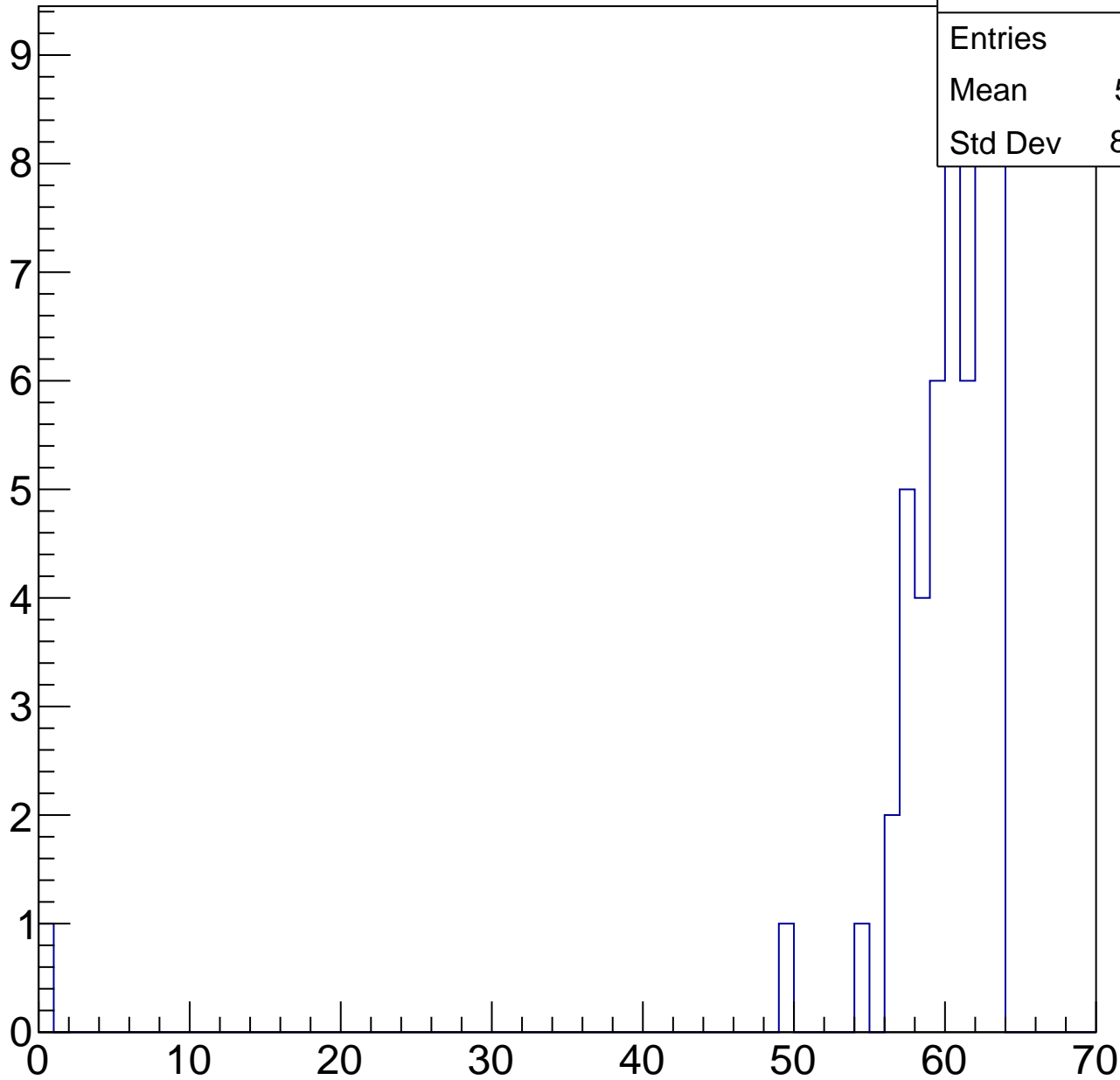
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.71
Std Dev	8.727

ampl



B1L102S, U6-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



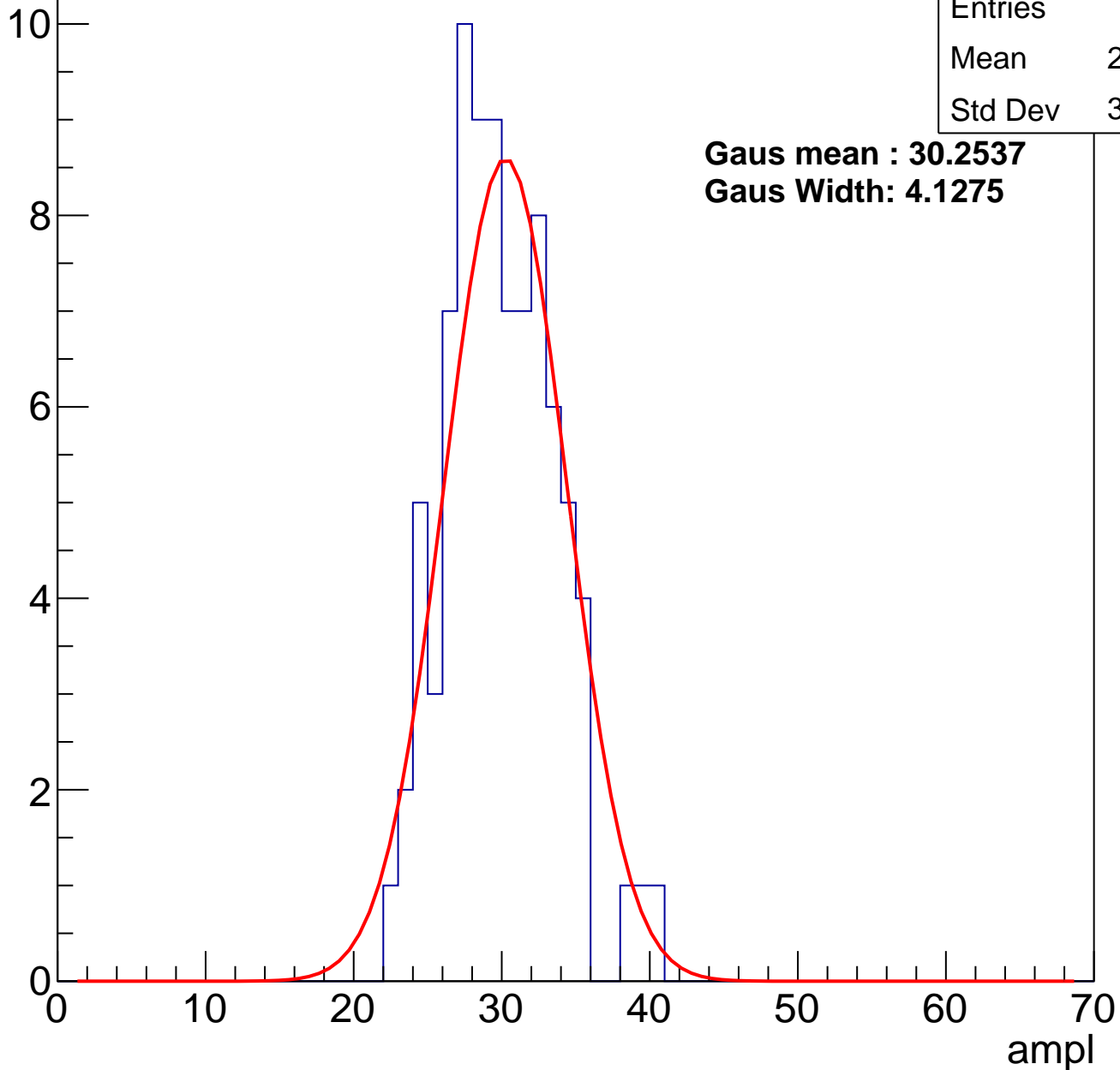
B1L102S, U6-ch24, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	29.49
Std Dev	3.672

Gaus mean : 30.2537
Gaus Width: 4.1275

Entry



B1L102S, U6-ch24, adc1

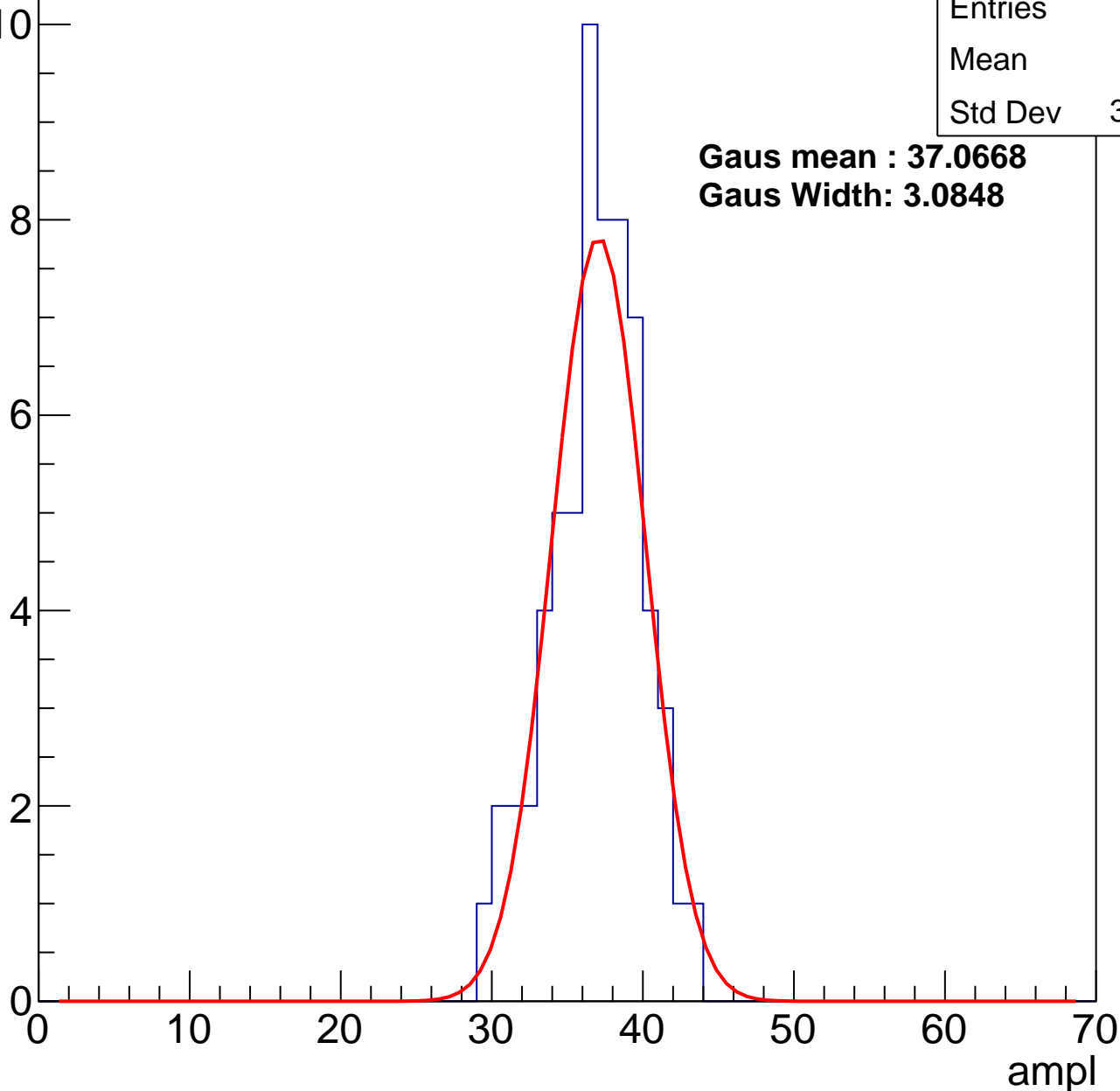
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.4
Std Dev	3.037

Gaus mean : 37.0668

Gaus Width: 3.0848



B1L102S, U6-ch24, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

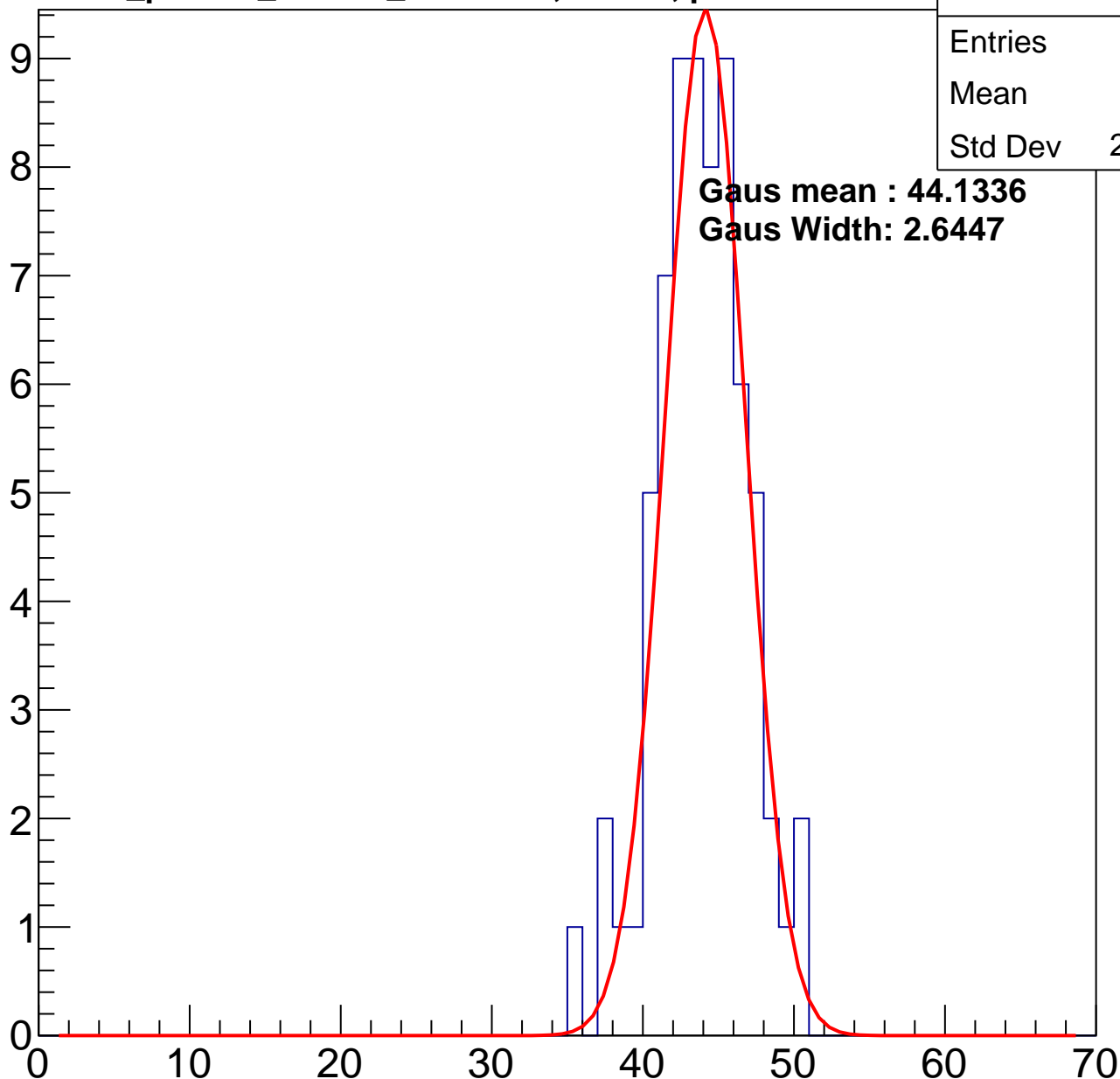
9
8
7
6
5
4
3
2
1
0

Entries	68
Mean	43.4
Std Dev	2.996

Gaus mean : 44.1336

Gaus Width: 2.6447

ampl

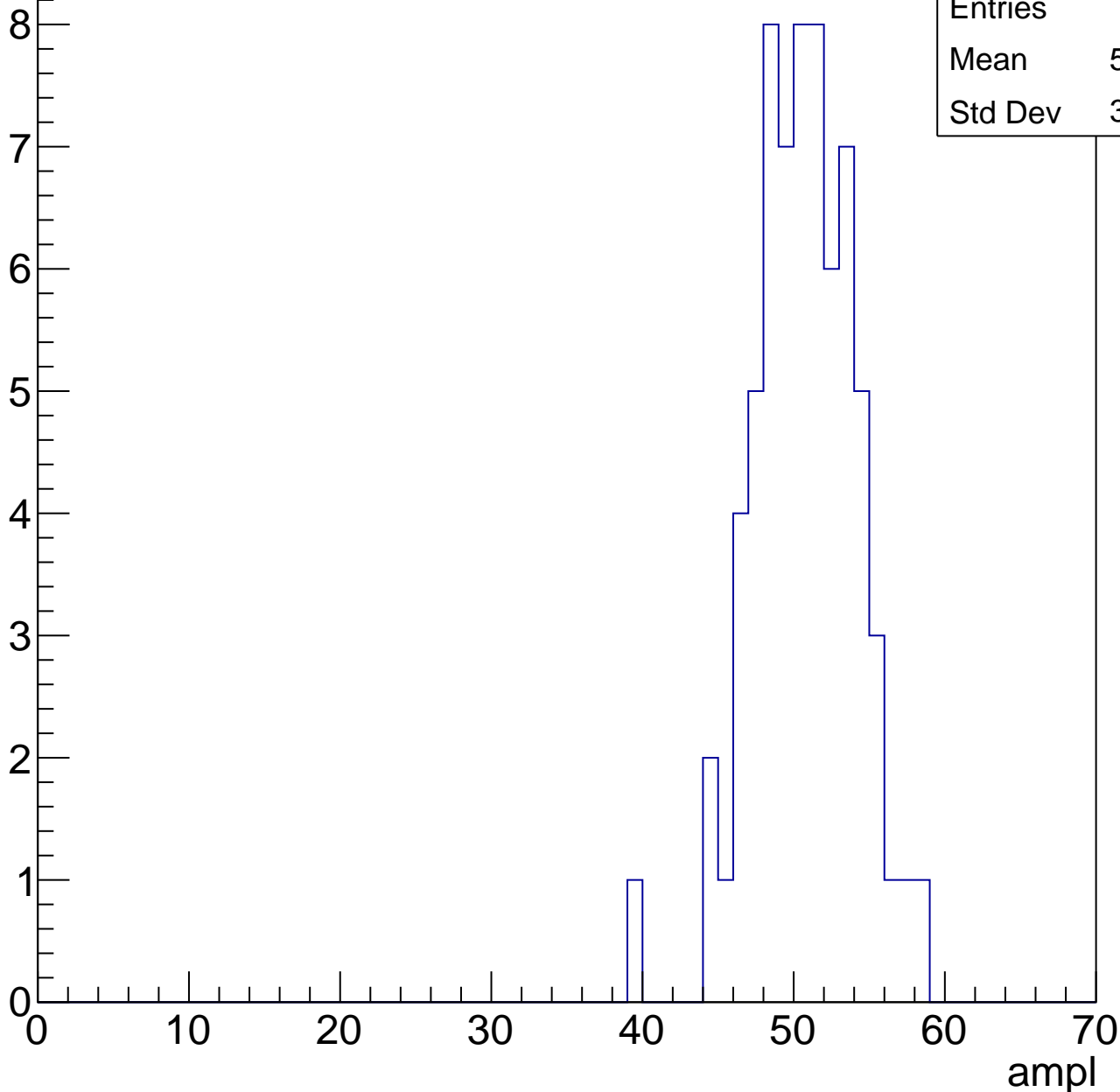


B1L102S, U6-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	50.22
Std Dev	3.347

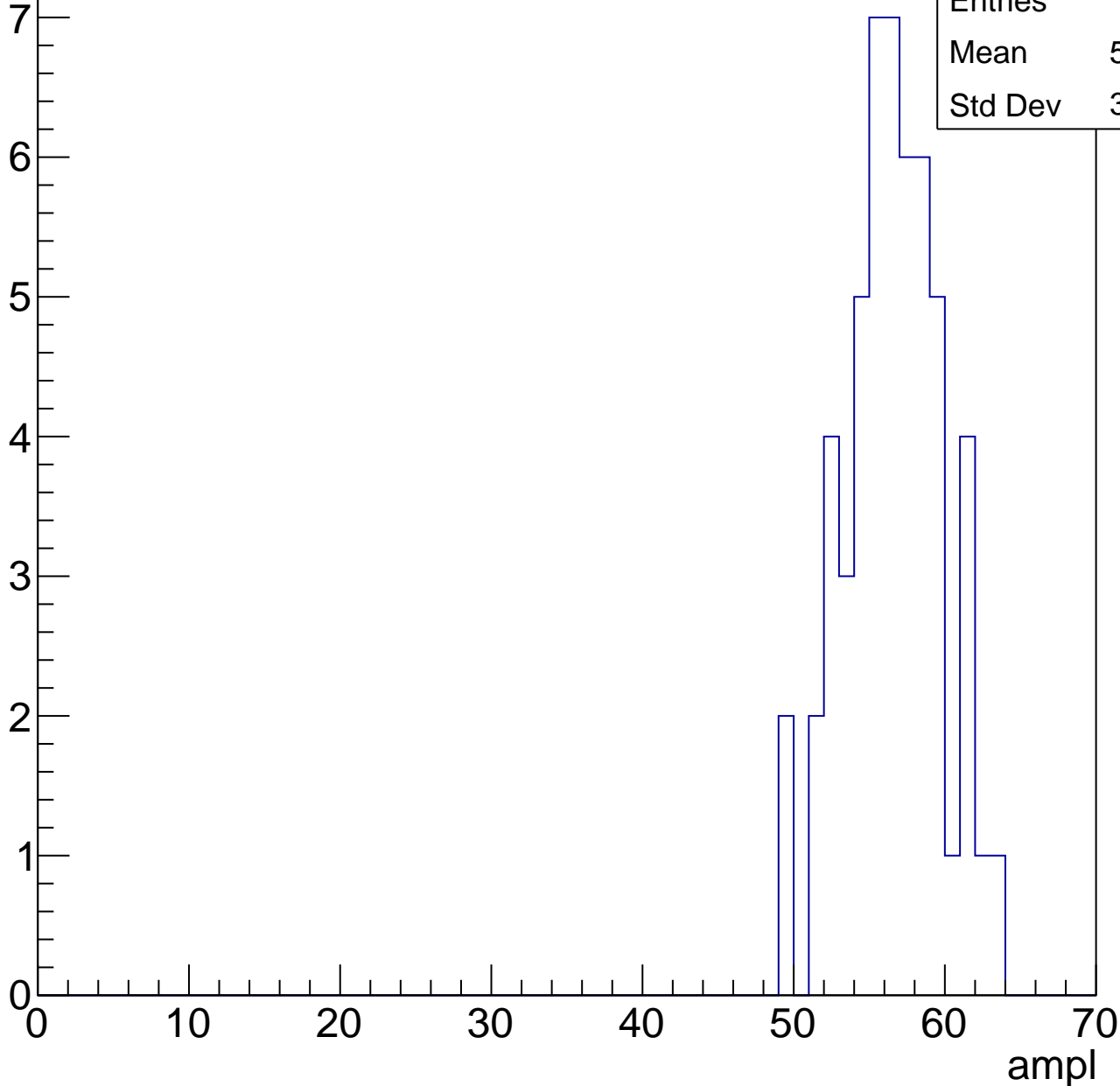


B1L102S, U6-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.07
Std Dev	3.167



B1L102S, U6-ch24, adc5

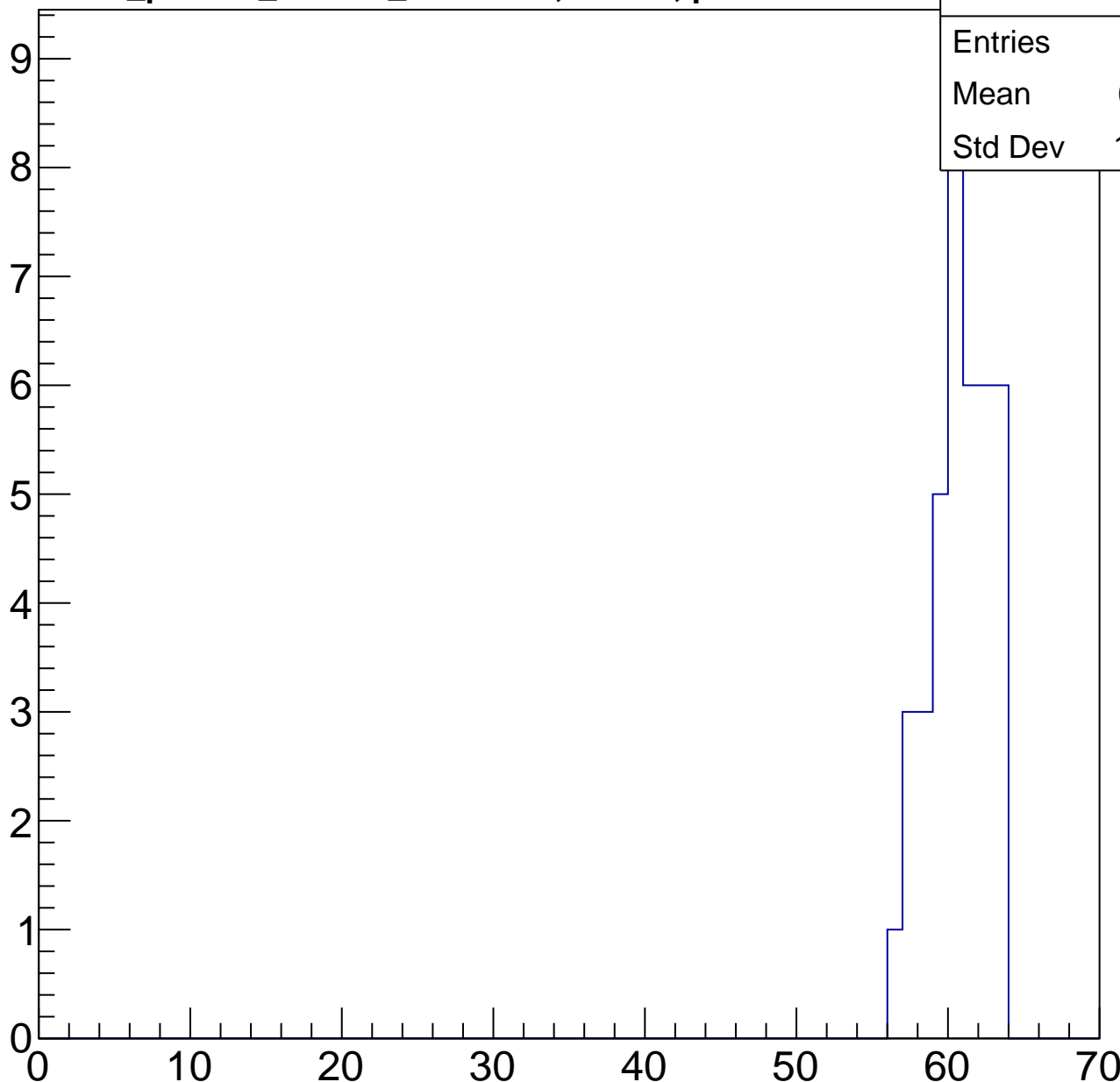
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.31
Std Dev	1.897

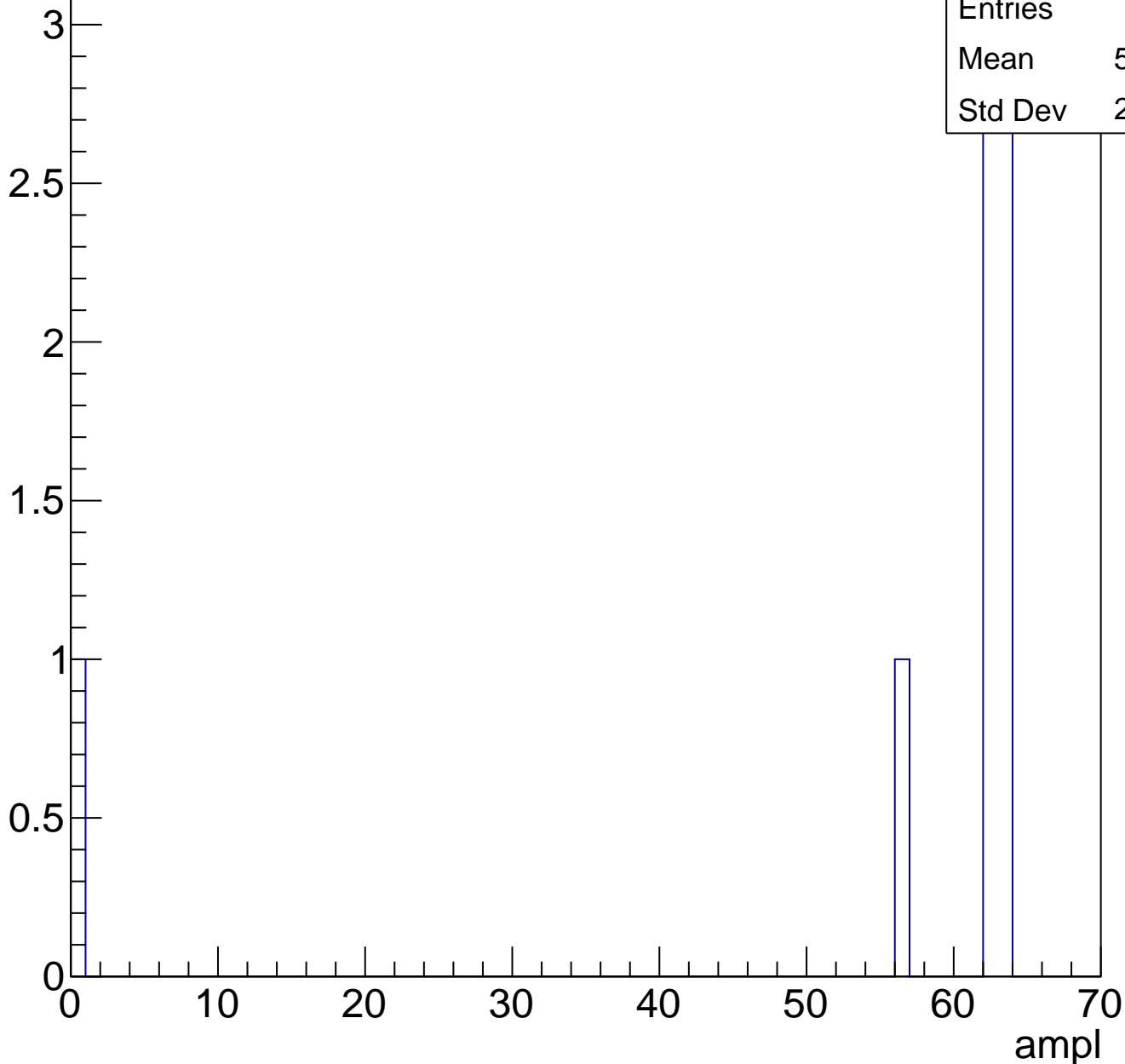
ampl



B1L102S, U6-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch25, adc0

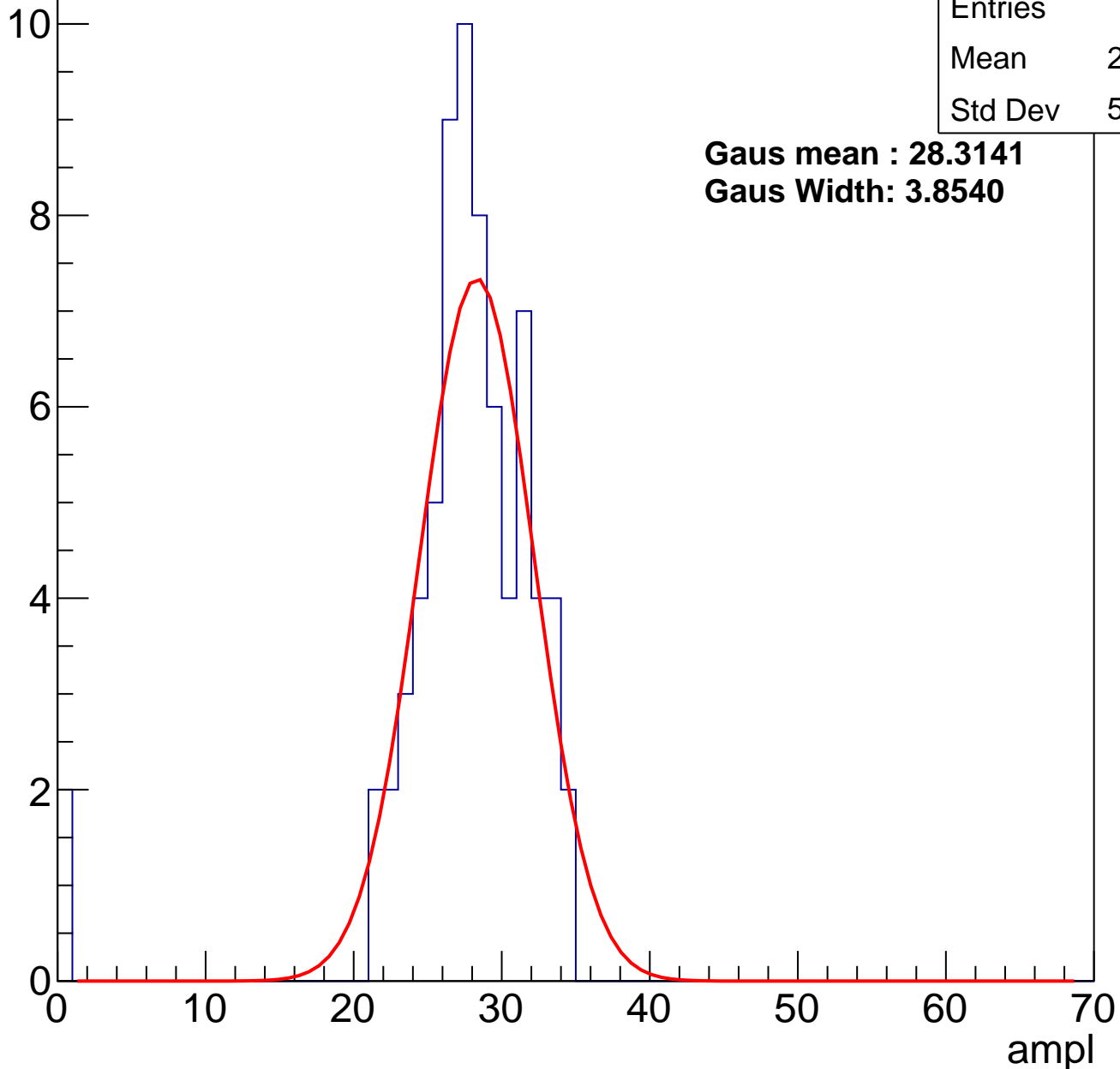
calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	26.99
Std Dev	5.547

Gaus mean : 28.3141

Gaus Width: 3.8540

Entry



B1L102S, U6-ch25, adc1

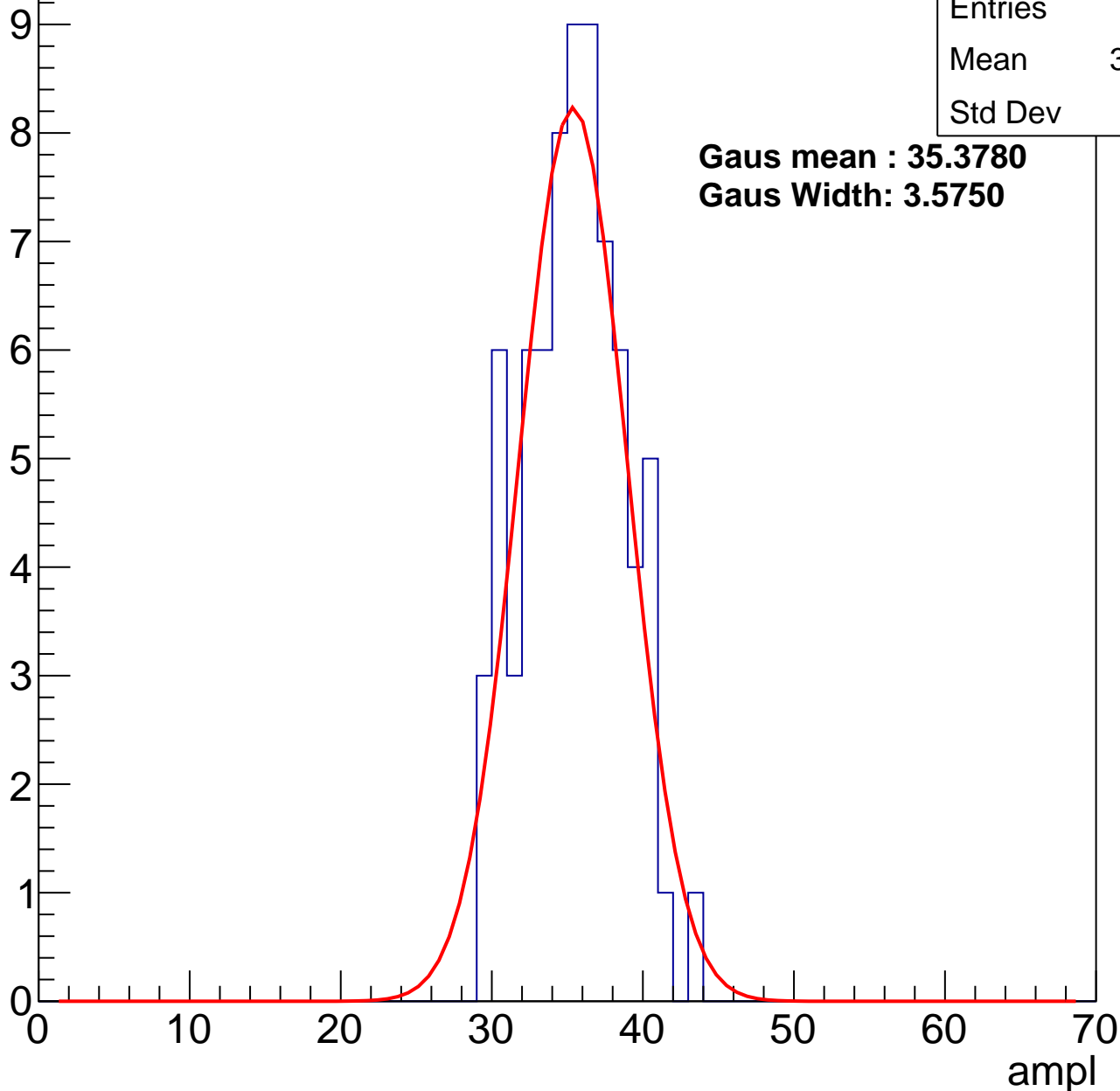
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.97
Std Dev	3.23

Gaus mean : 35.3780

Gaus Width: 3.5750



B1L102S, U6-ch25, adc2

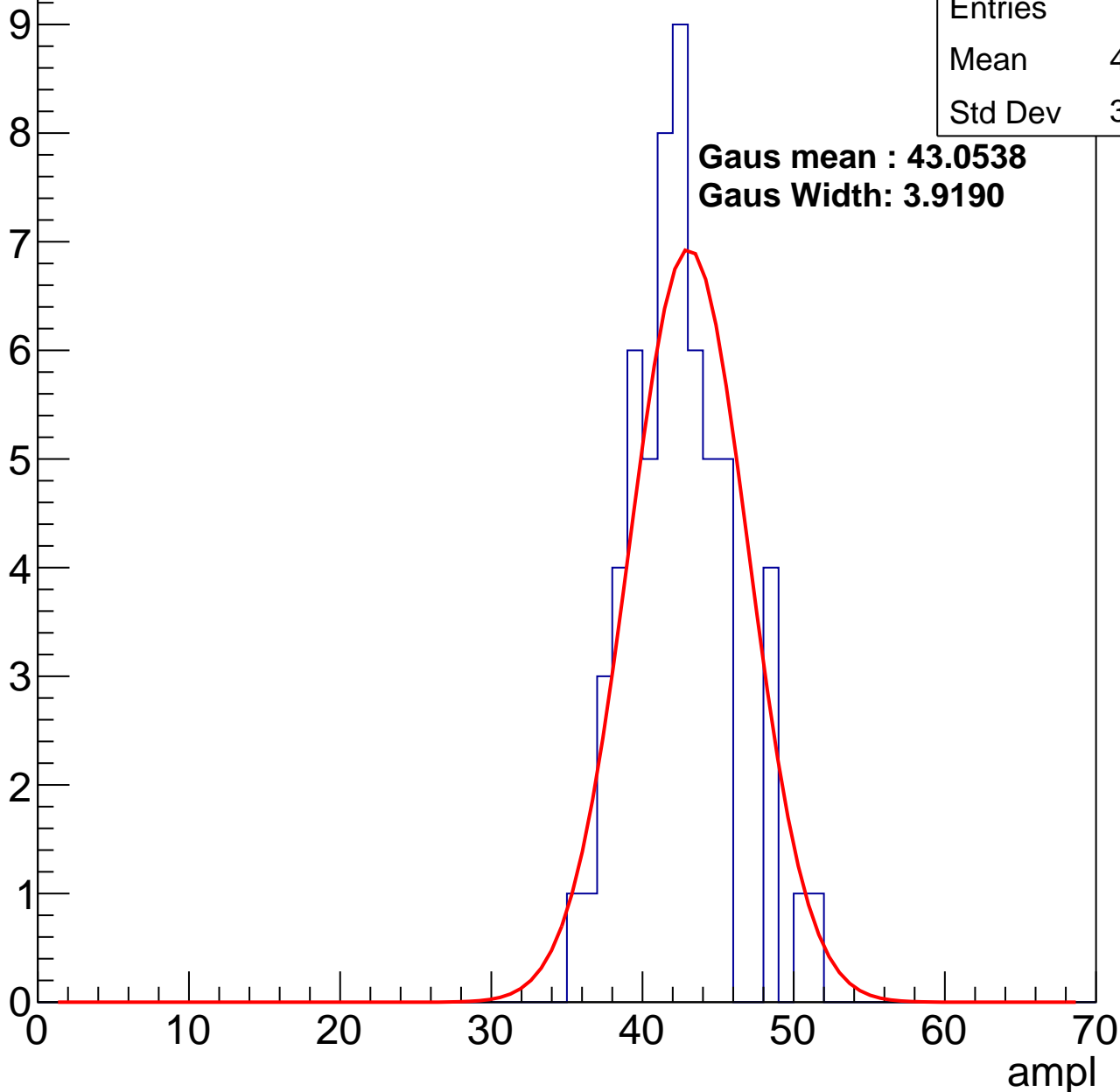
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.86
Std Dev	3.357

Gaus mean : 43.0538

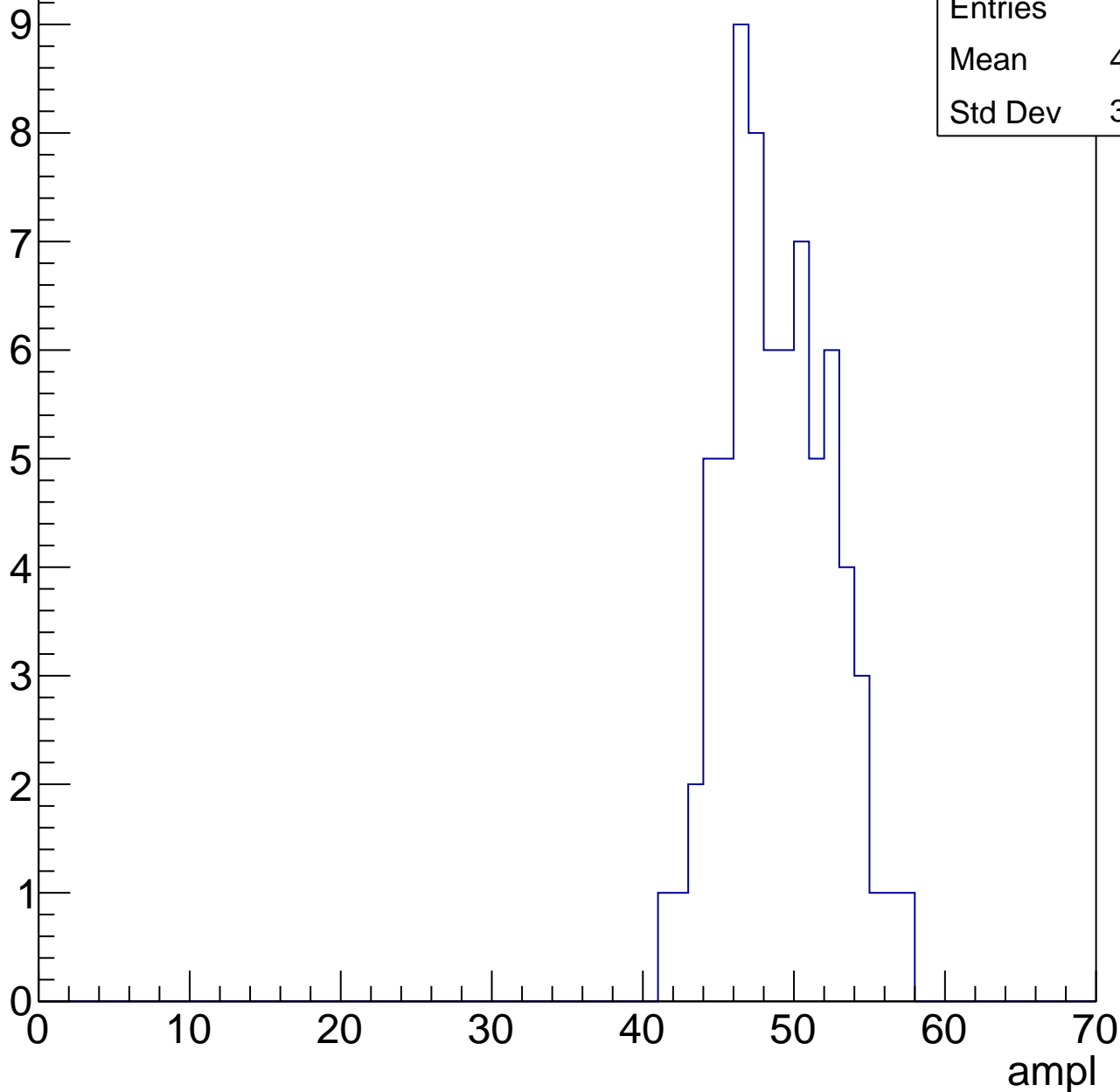
Gaus Width: 3.9190



B1L102S, U6-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



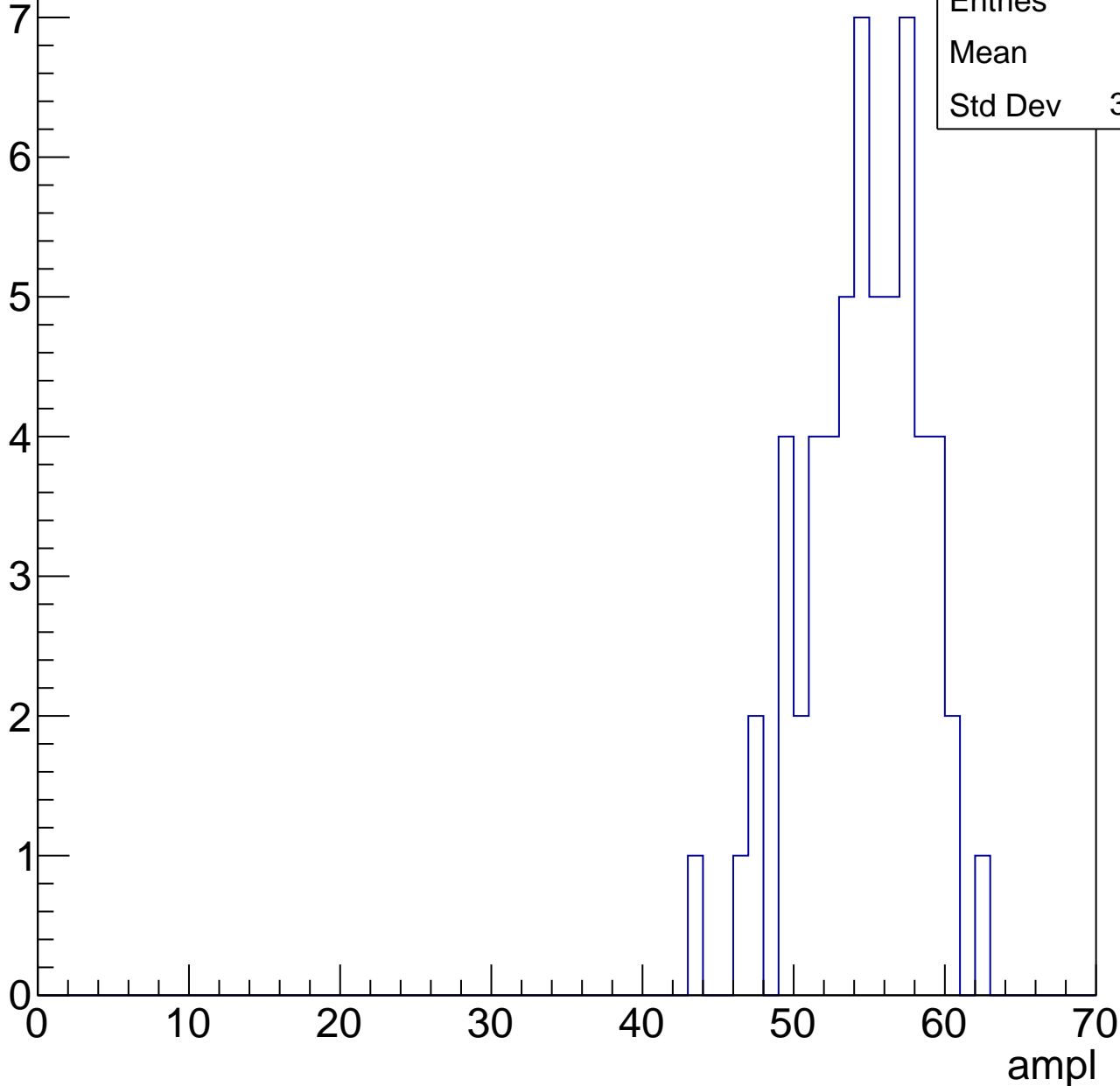
Entries	71
Mean	48.52
Std Dev	3.484

B1L102S, U6-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.1
Std Dev	3.849

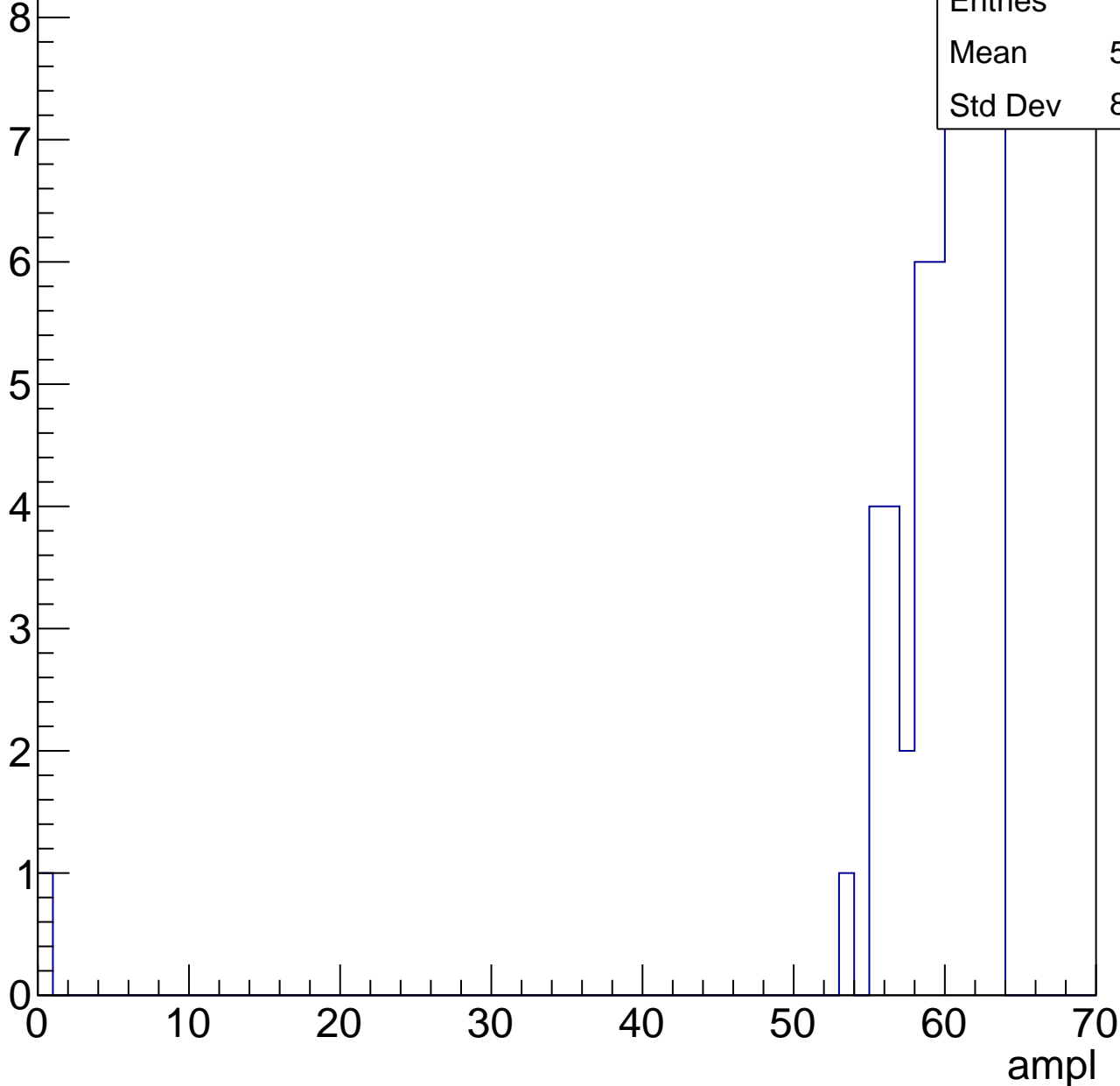


B1L102S, U6-ch25, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	58.59
Std Dev	8.304



B1L102S, U6-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

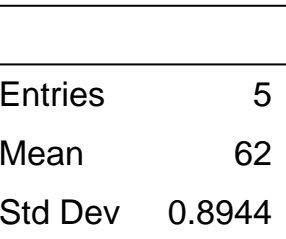
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch26, adc0

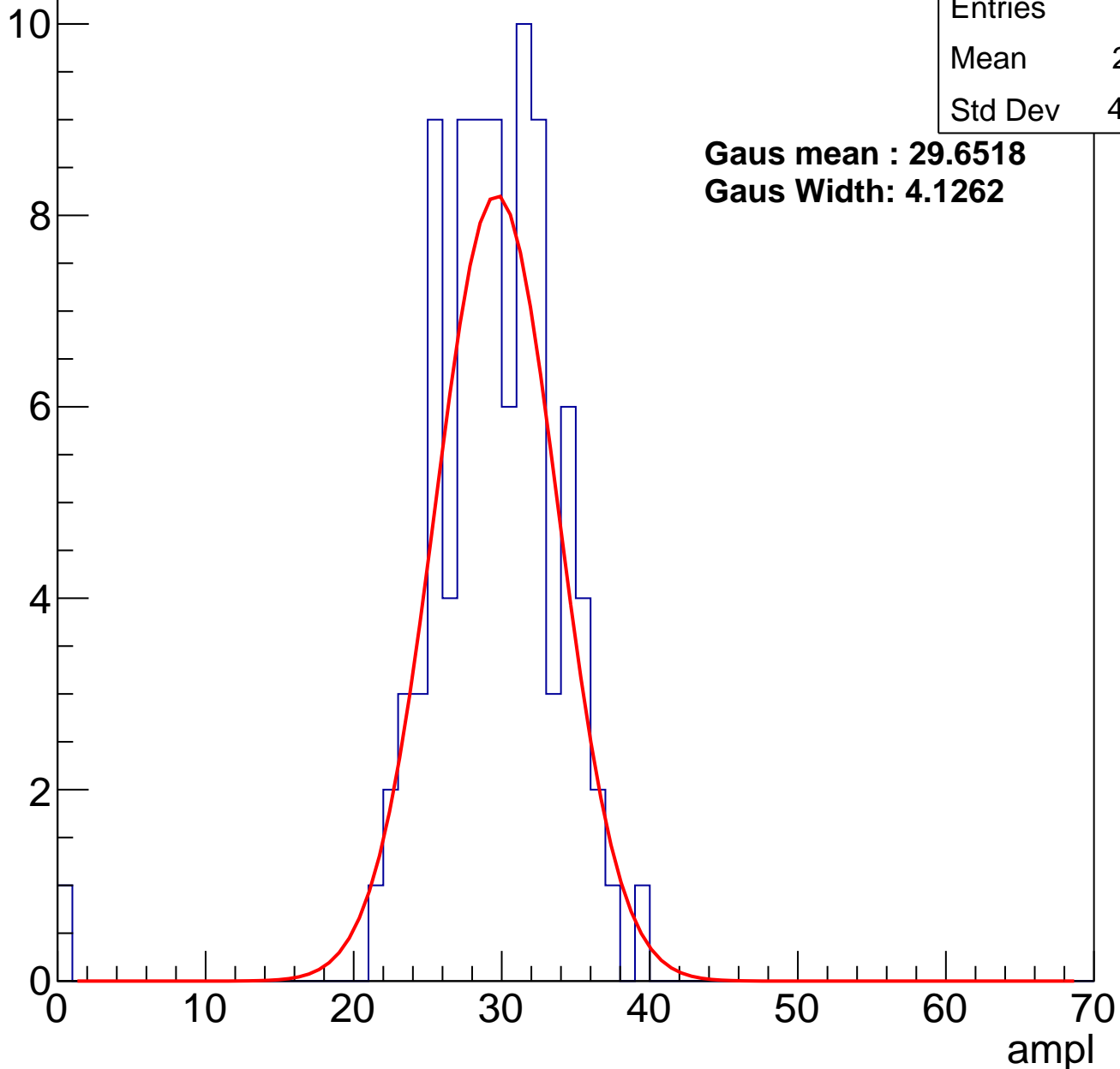
calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	28.91
Std Dev	4.824

Gaus mean : 29.6518

Gaus Width: 4.1262

Entry



B1L102S, U6-ch26, adc1

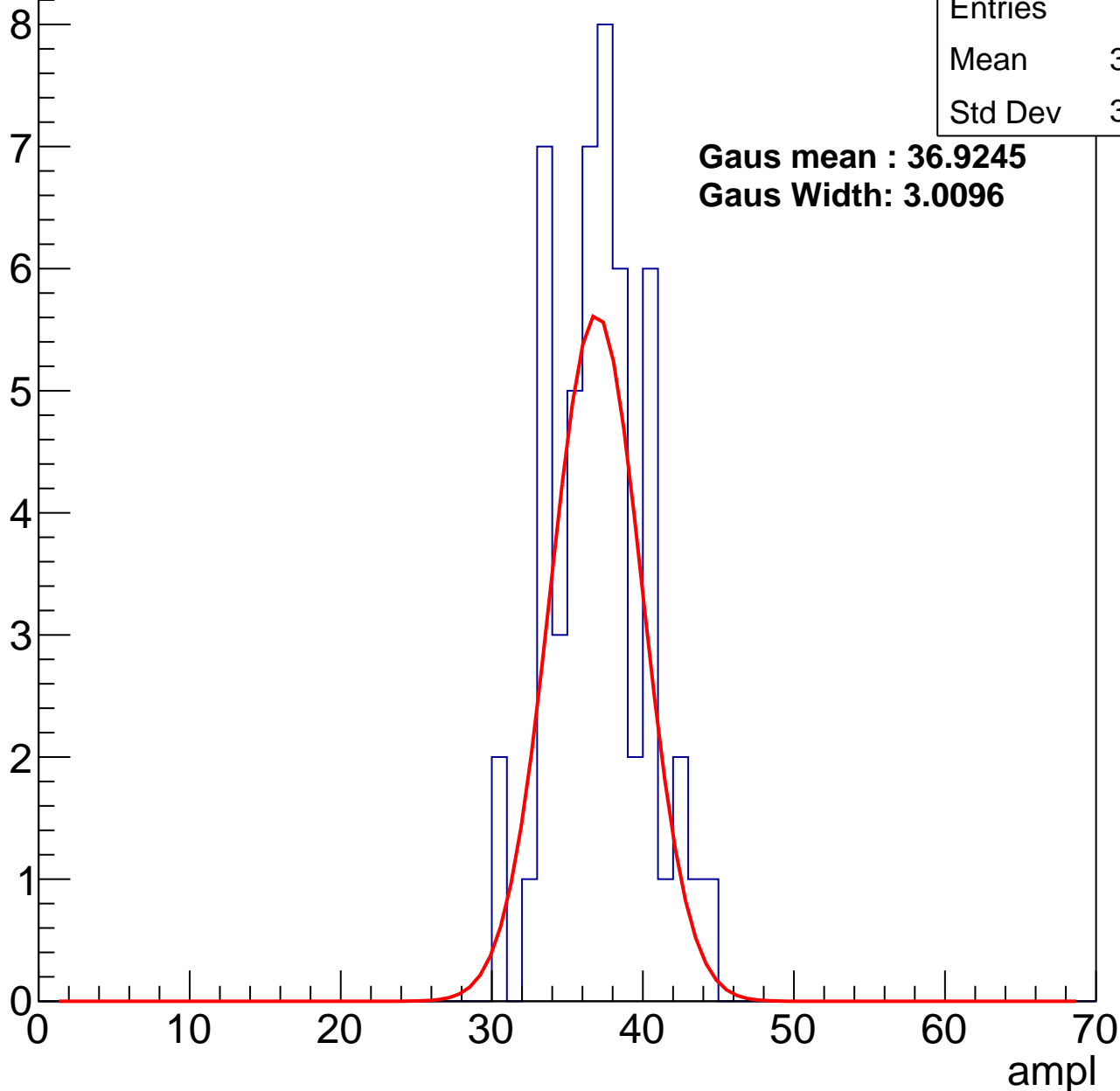
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	36.65
Std Dev	3.106

Gaus mean : 36.9245

Gaus Width: 3.0096



B1L102S, U6-ch26, adc2

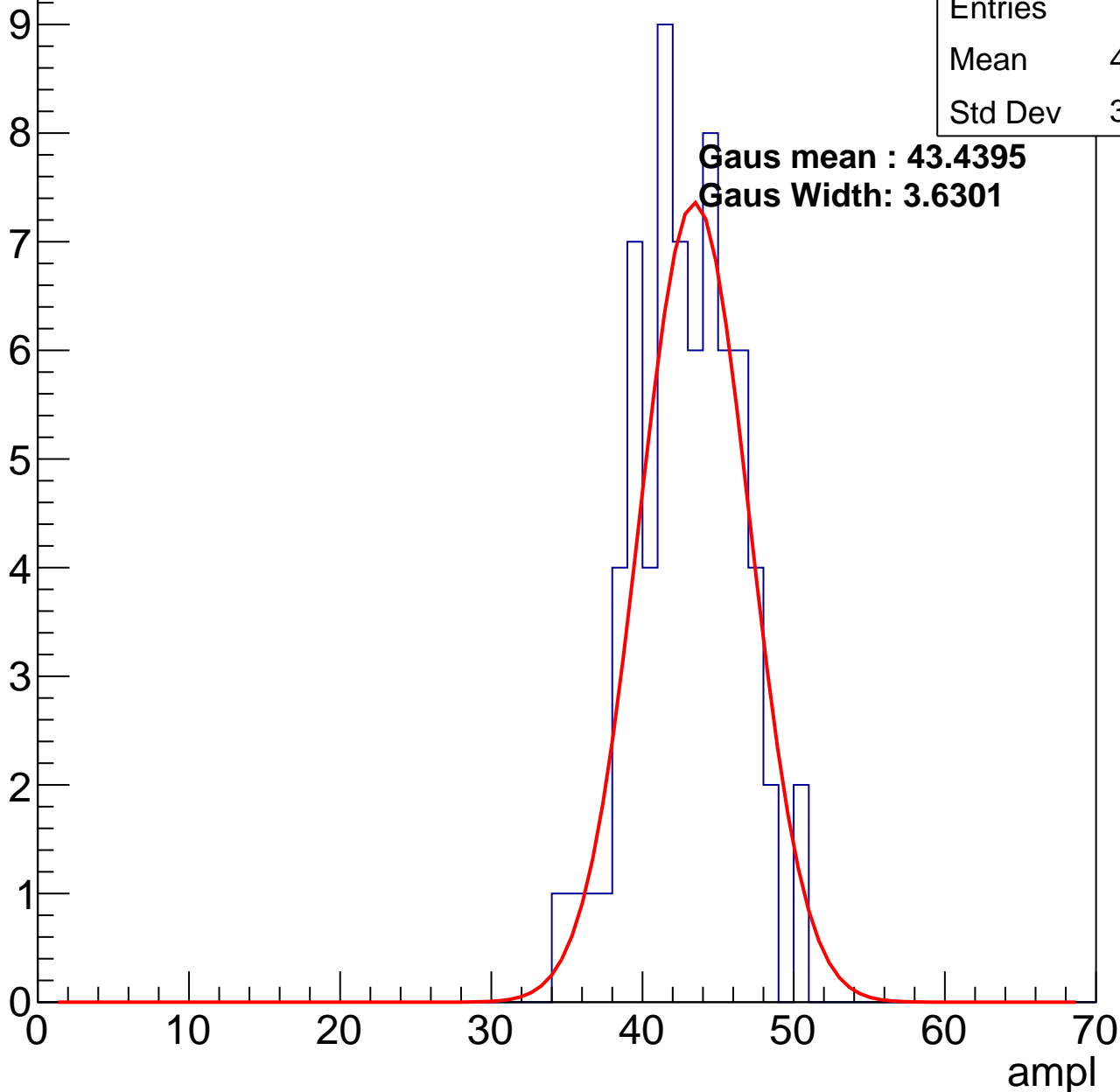
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.46
Std Dev	3.403

Gaus mean : 43.4395

Gaus Width: 3.6301

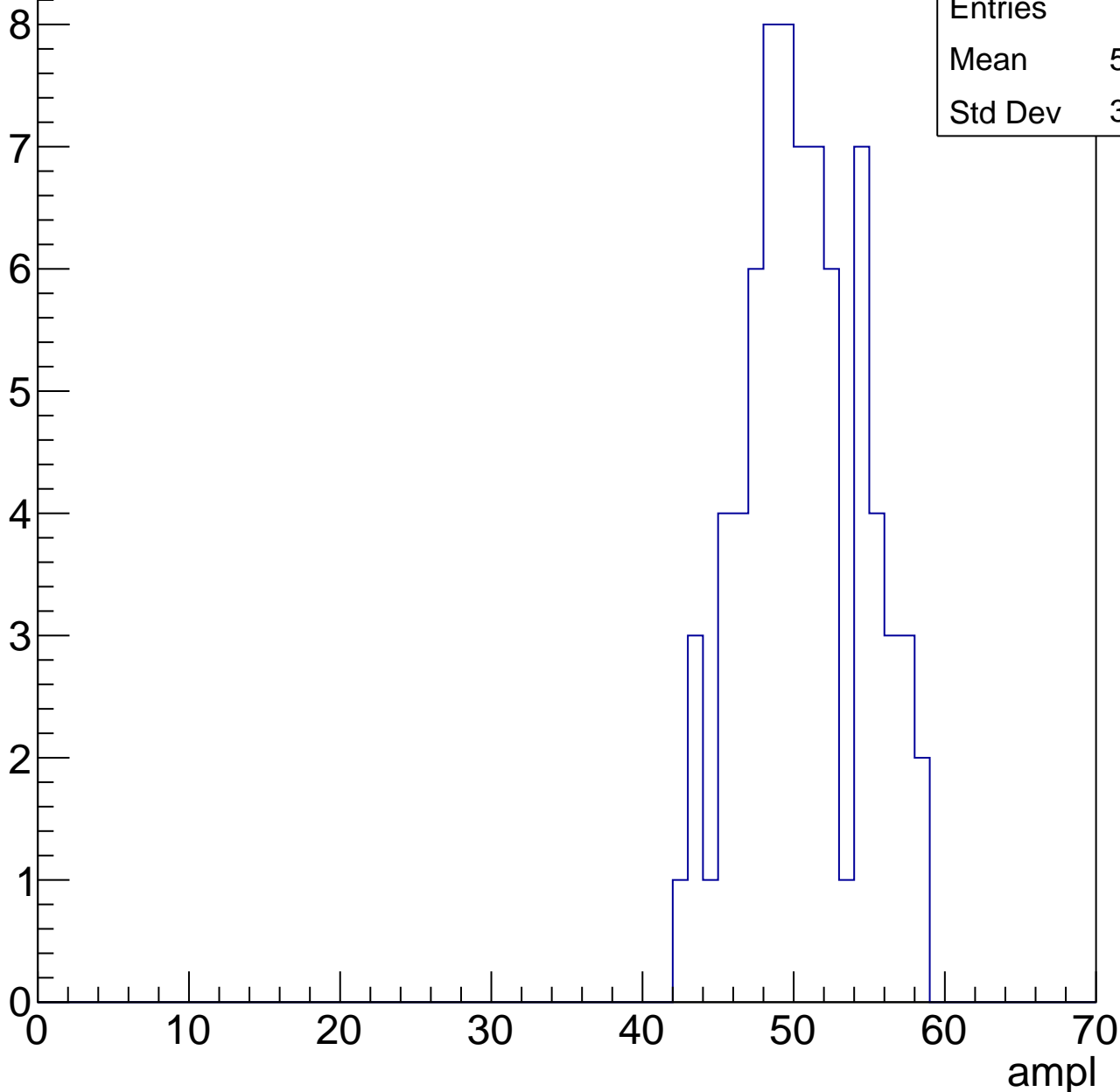


B1L102S, U6-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	50.16
Std Dev	3.895

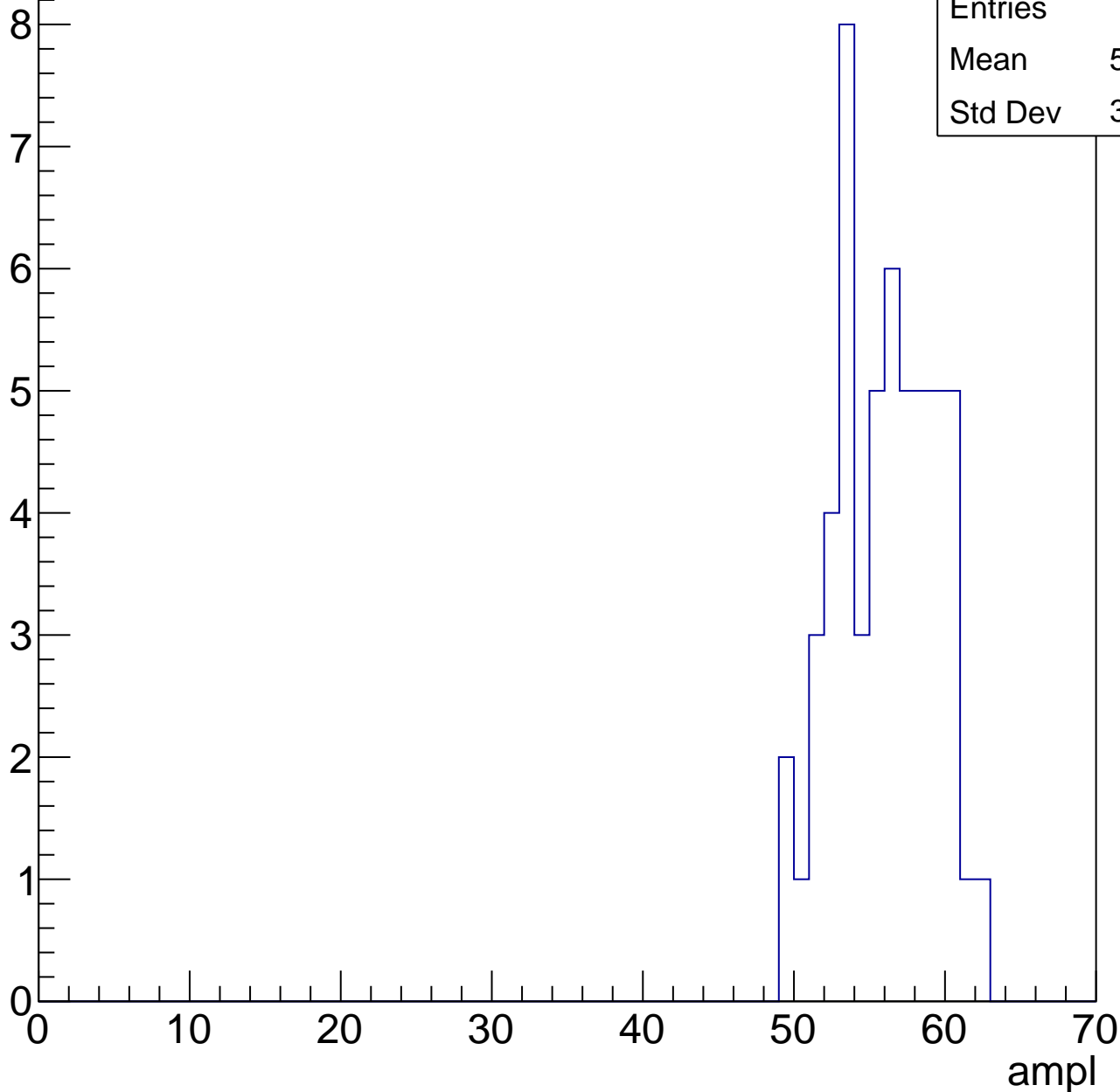


B1L102S, U6-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.54
Std Dev	3.224

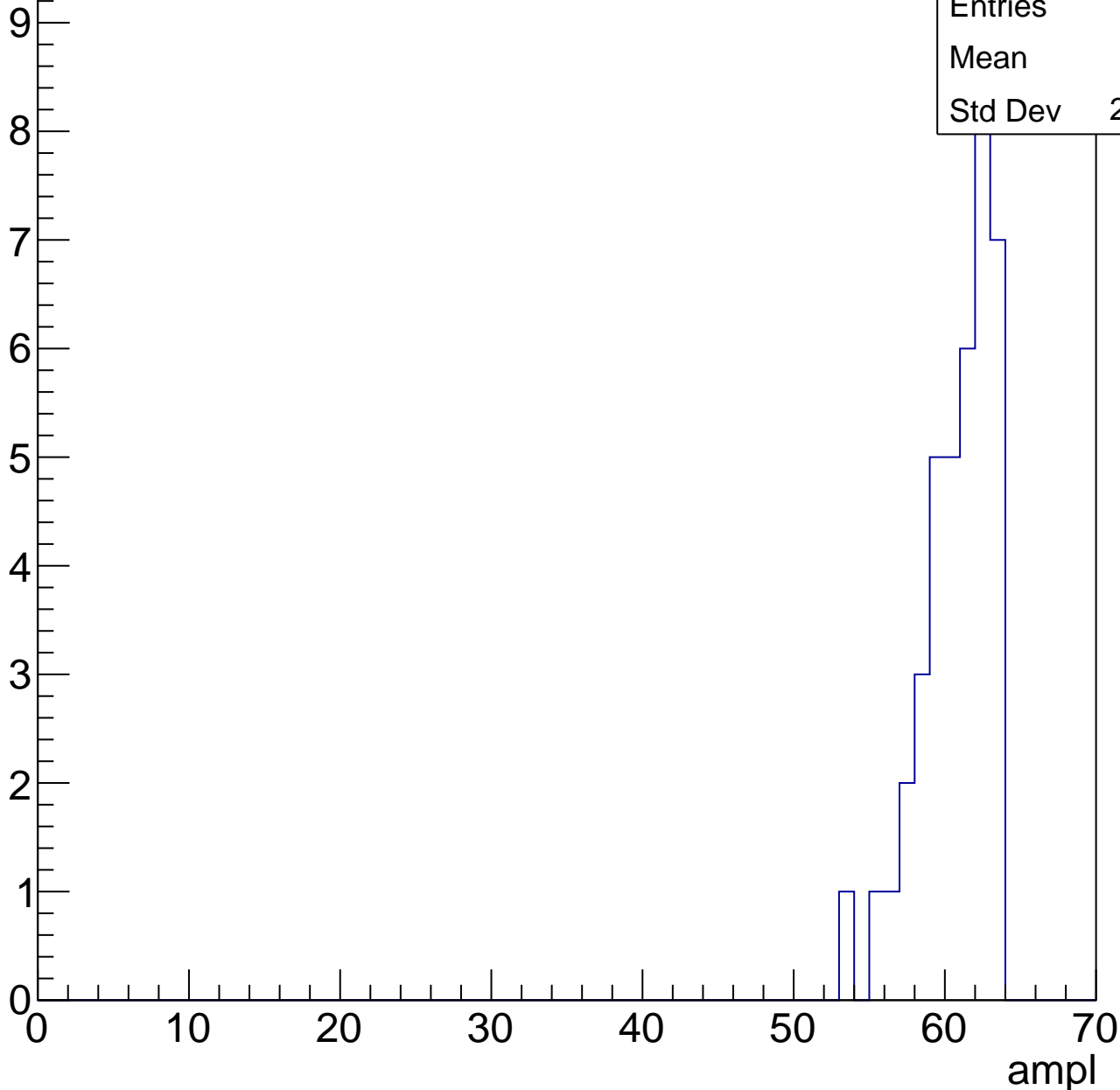


B1L102S, U6-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

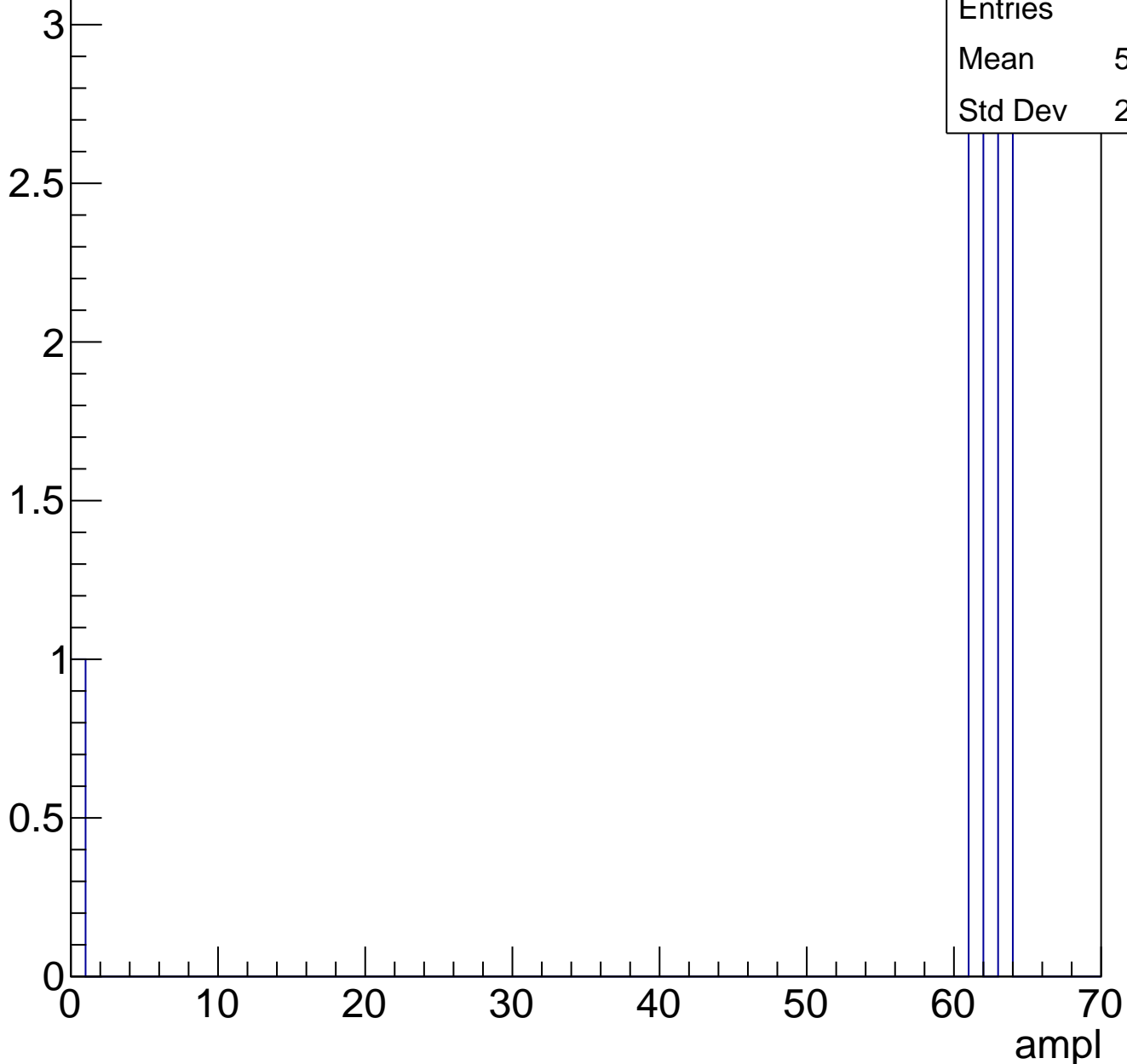
Entries	40
Mean	60.3
Std Dev	2.379



B1L102S, U6-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch27, adc0

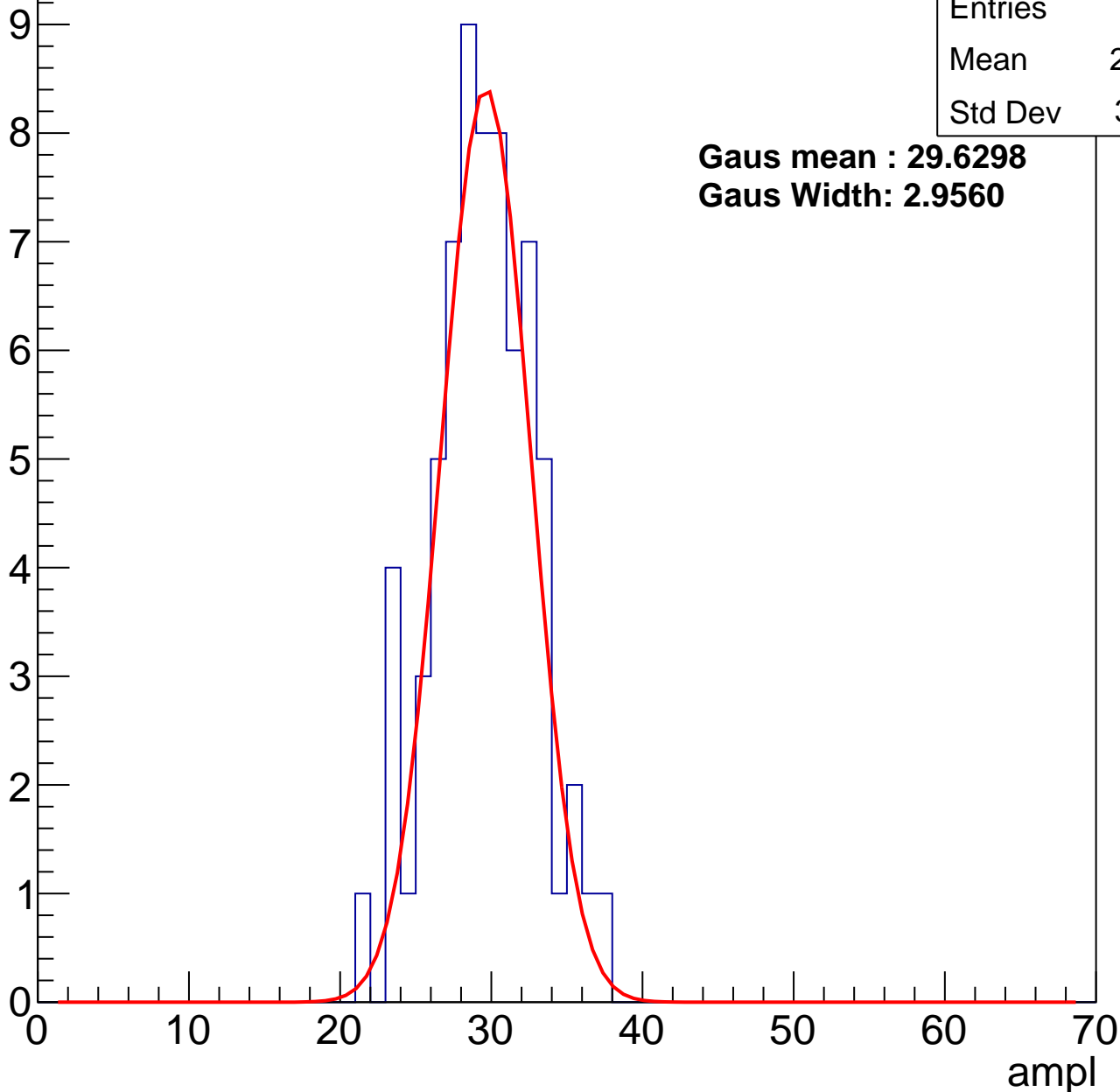
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.09
Std Dev	3.291

Gaus mean : 29.6298

Gaus Width: 2.9560



B1L102S, U6-ch27, adc1

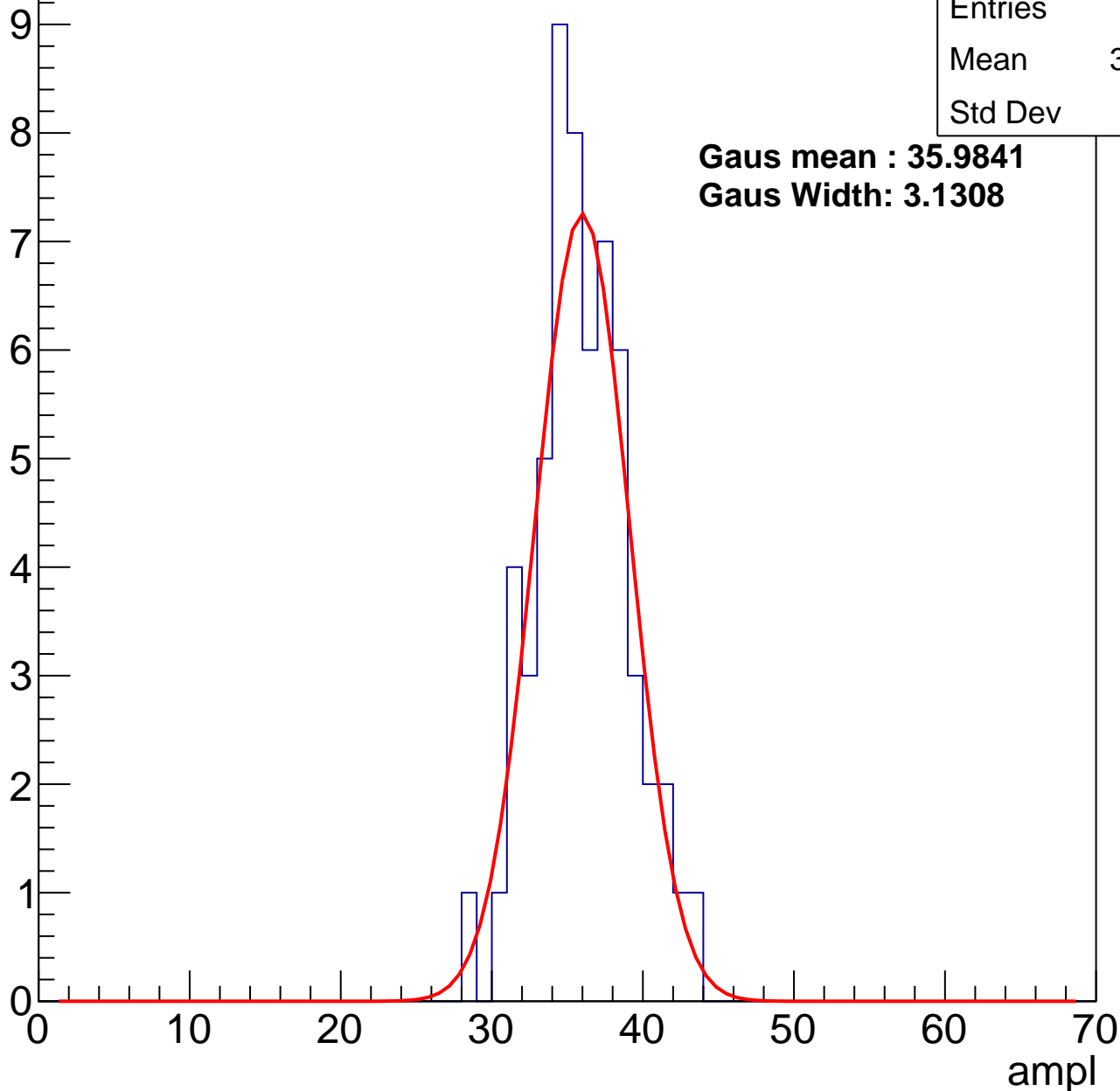
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.53
Std Dev	3.05

Gaus mean : 35.9841

Gaus Width: 3.1308



B1L102S, U6-ch27, adc2

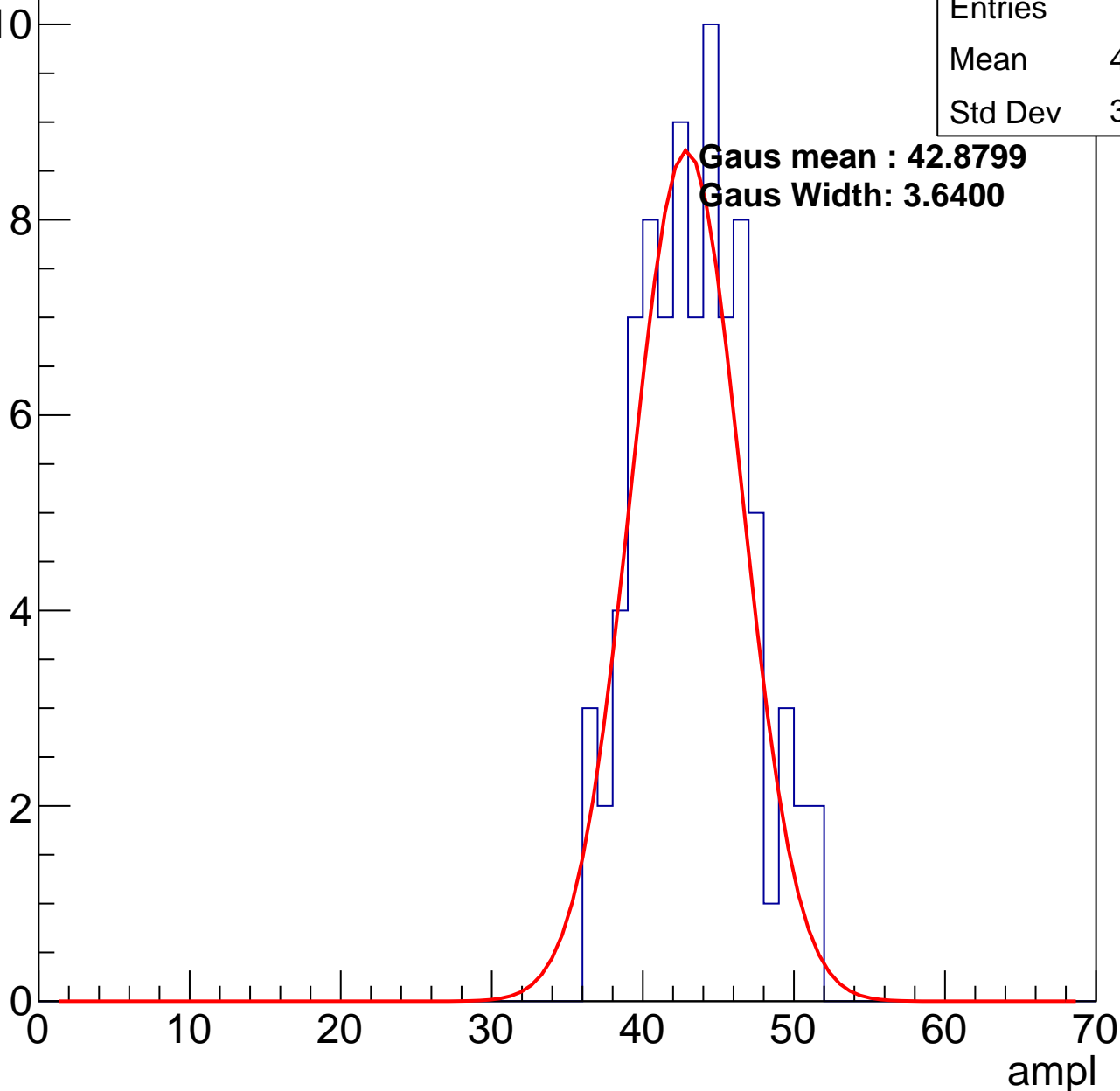
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	42.92
Std Dev	3.578

Gaus mean : 42.8799

Gaus Width: 3.6400

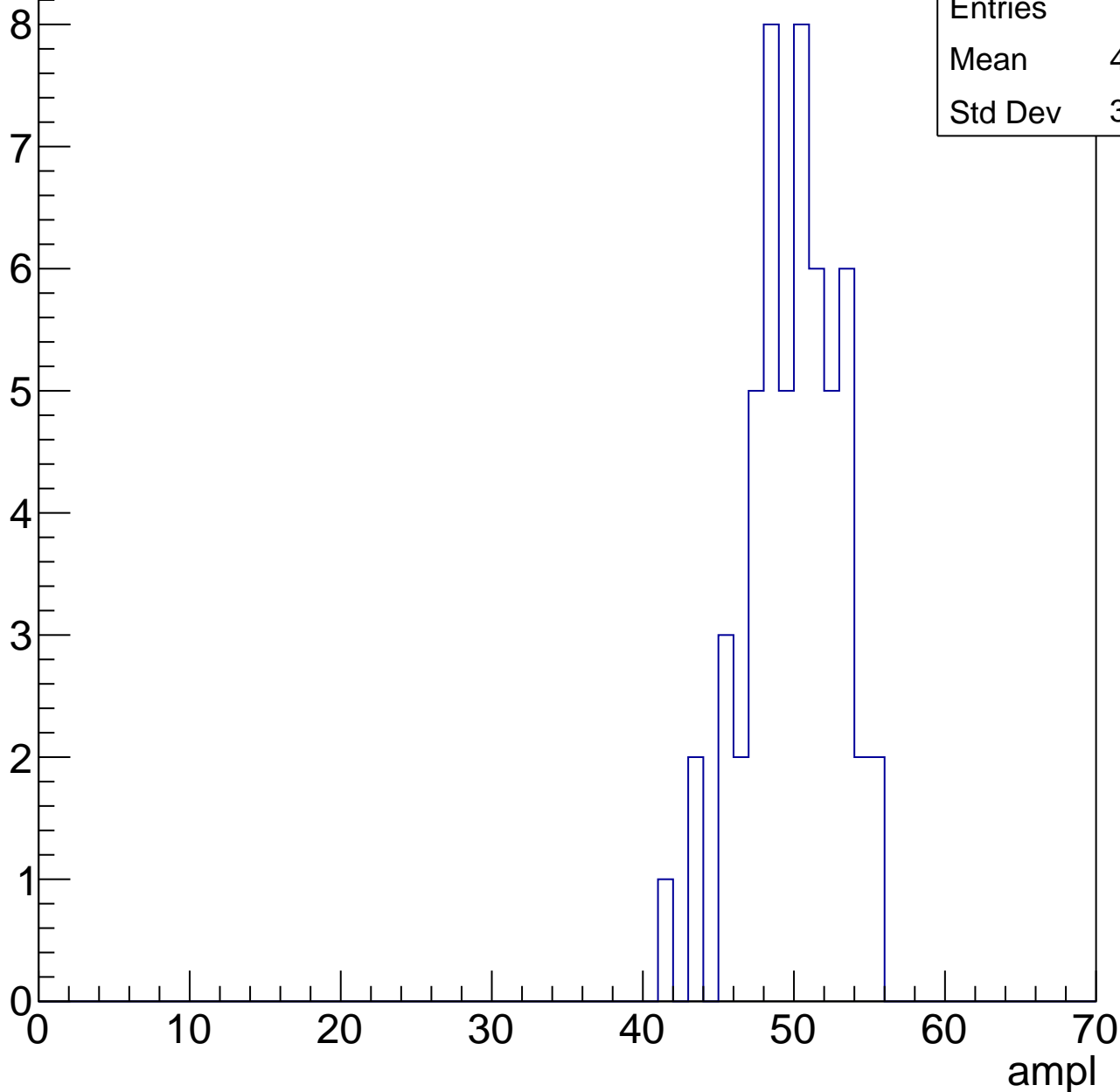


B1L102S, U6-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	49.45
Std Dev	3.056

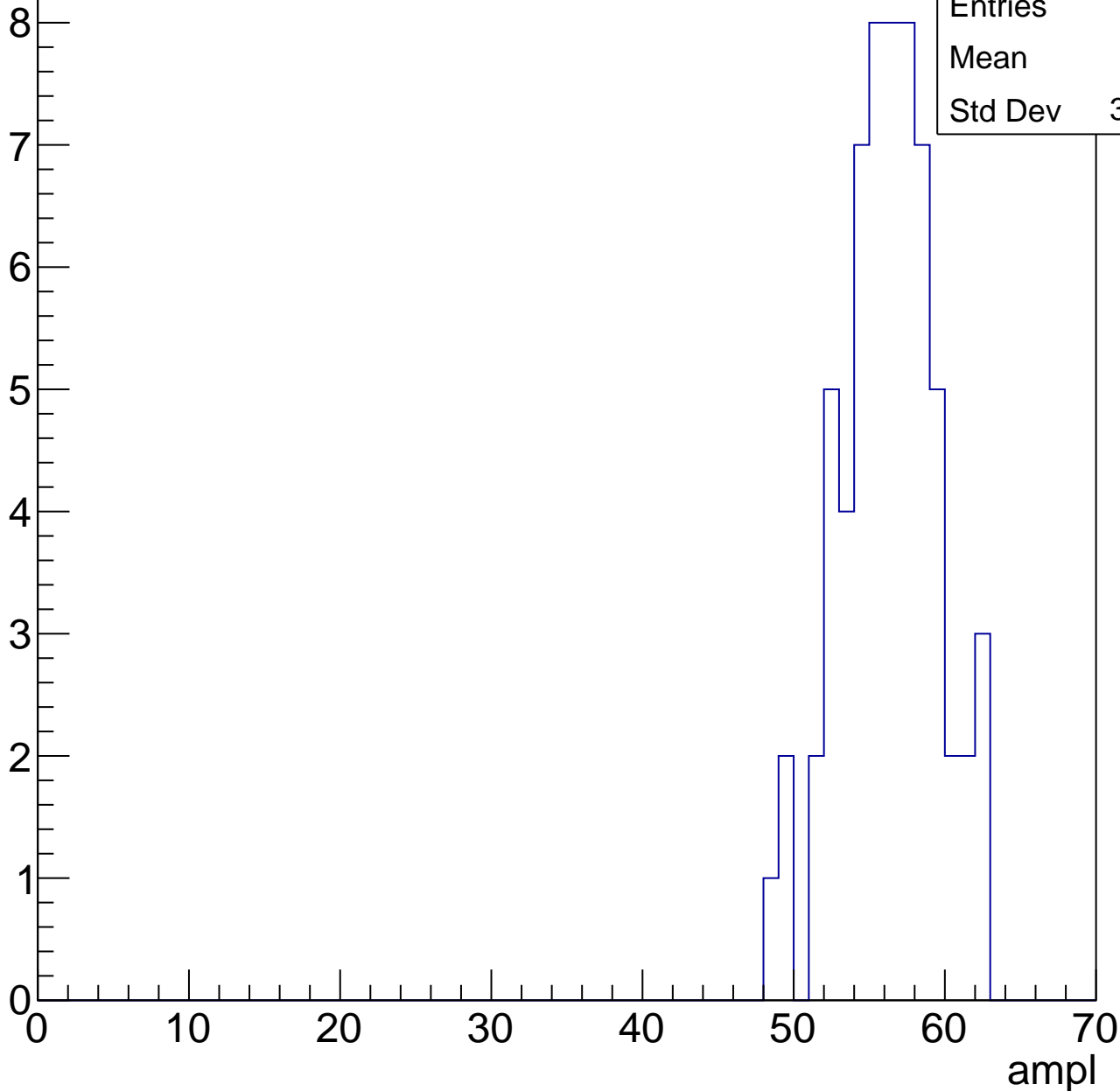


B1L102S, U6-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.8
Std Dev	3.143

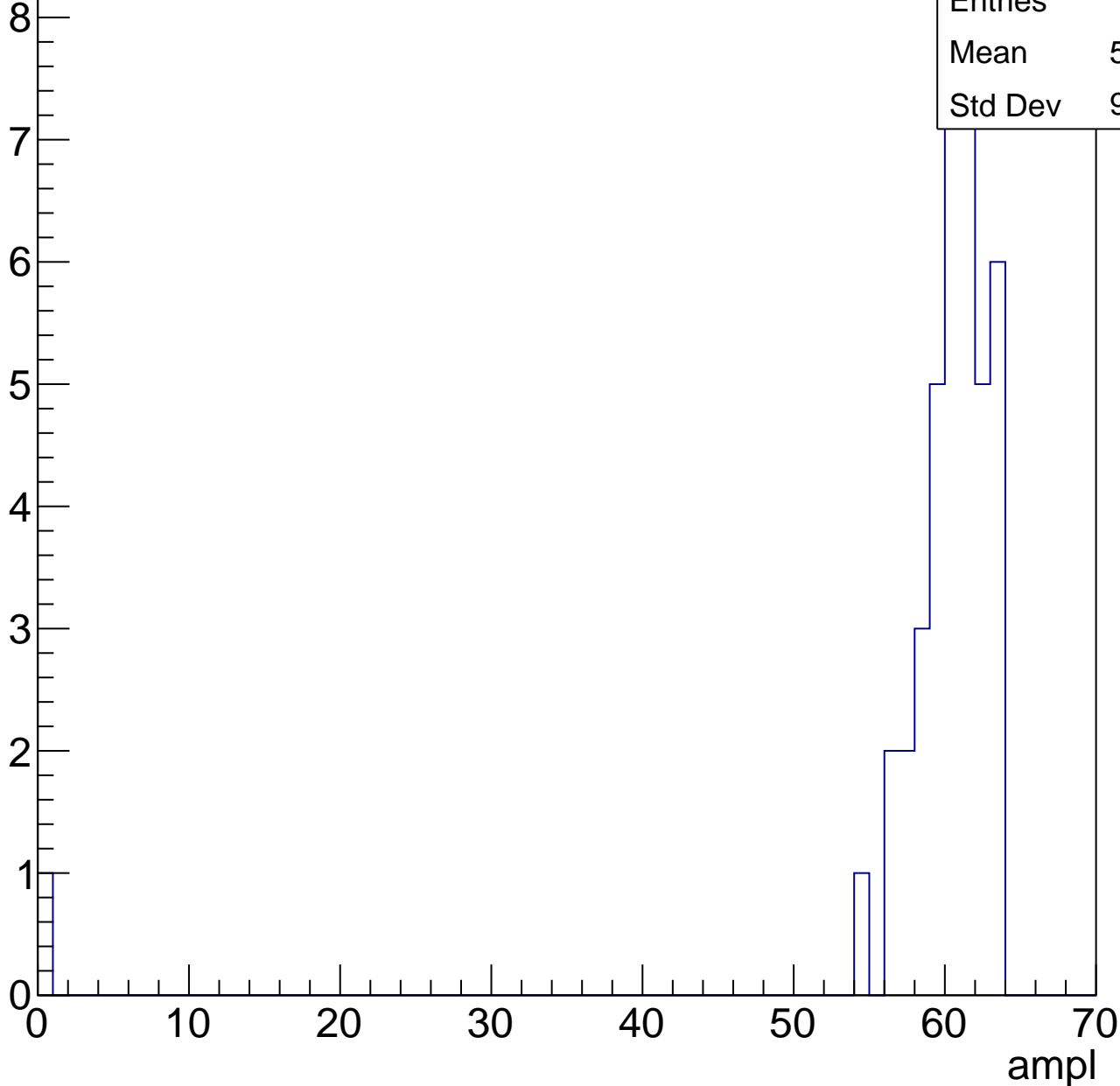


B1L102S, U6-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

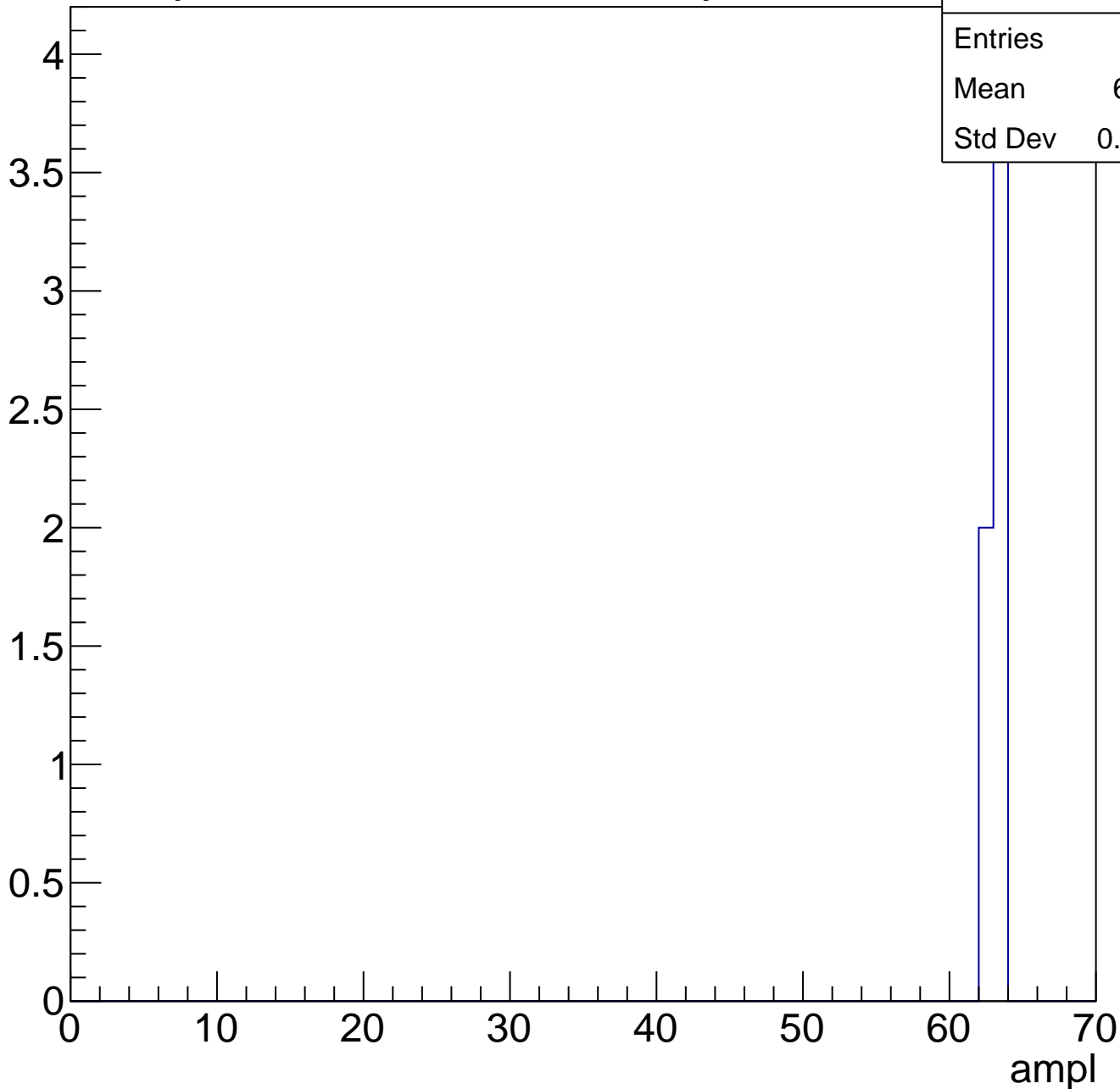
Entries	41
Mean	58.66
Std Dev	9.514



B1L102S, U6-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch28, adc0

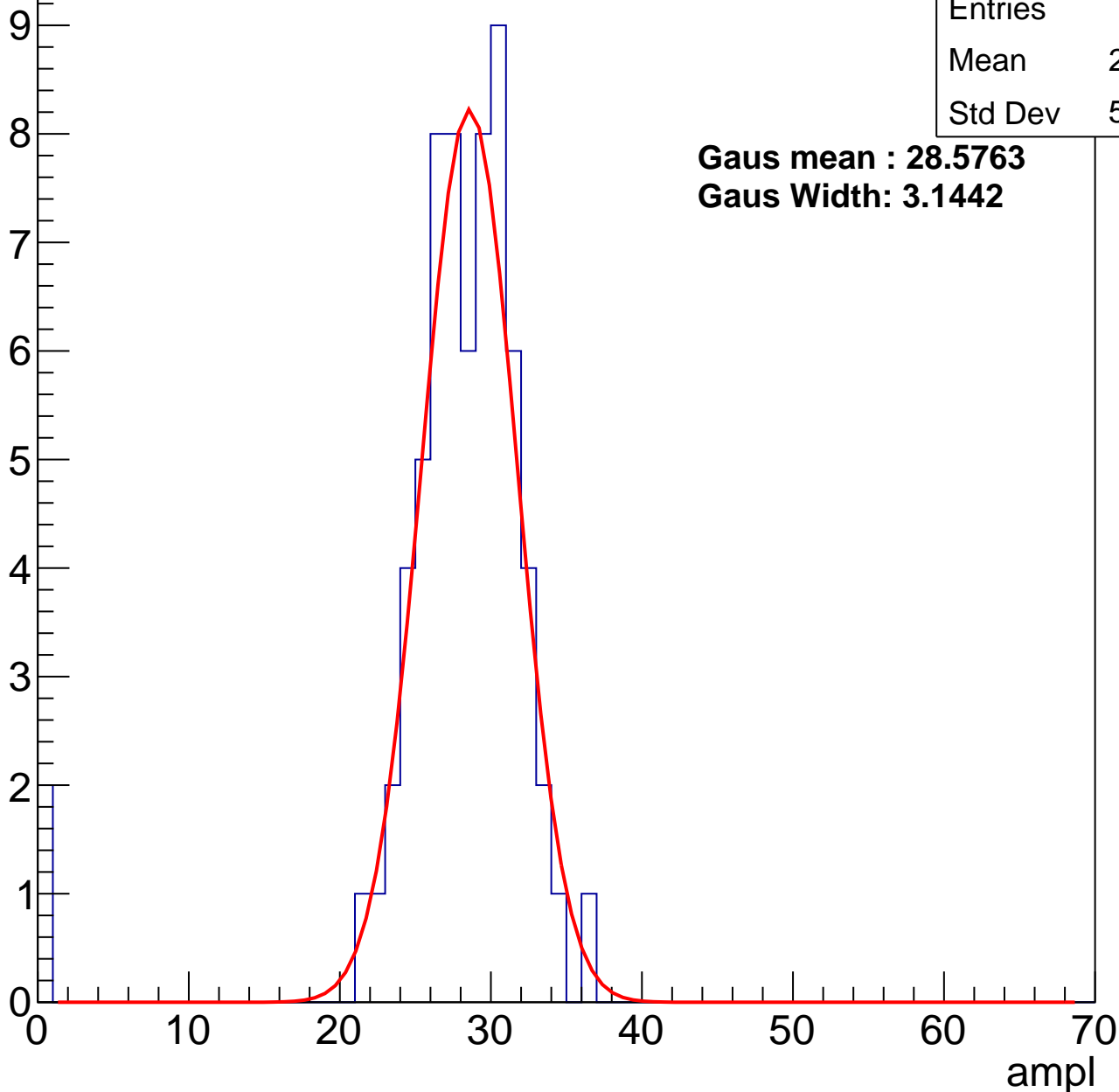
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.26
Std Dev	5.593

Gaus mean : 28.5763

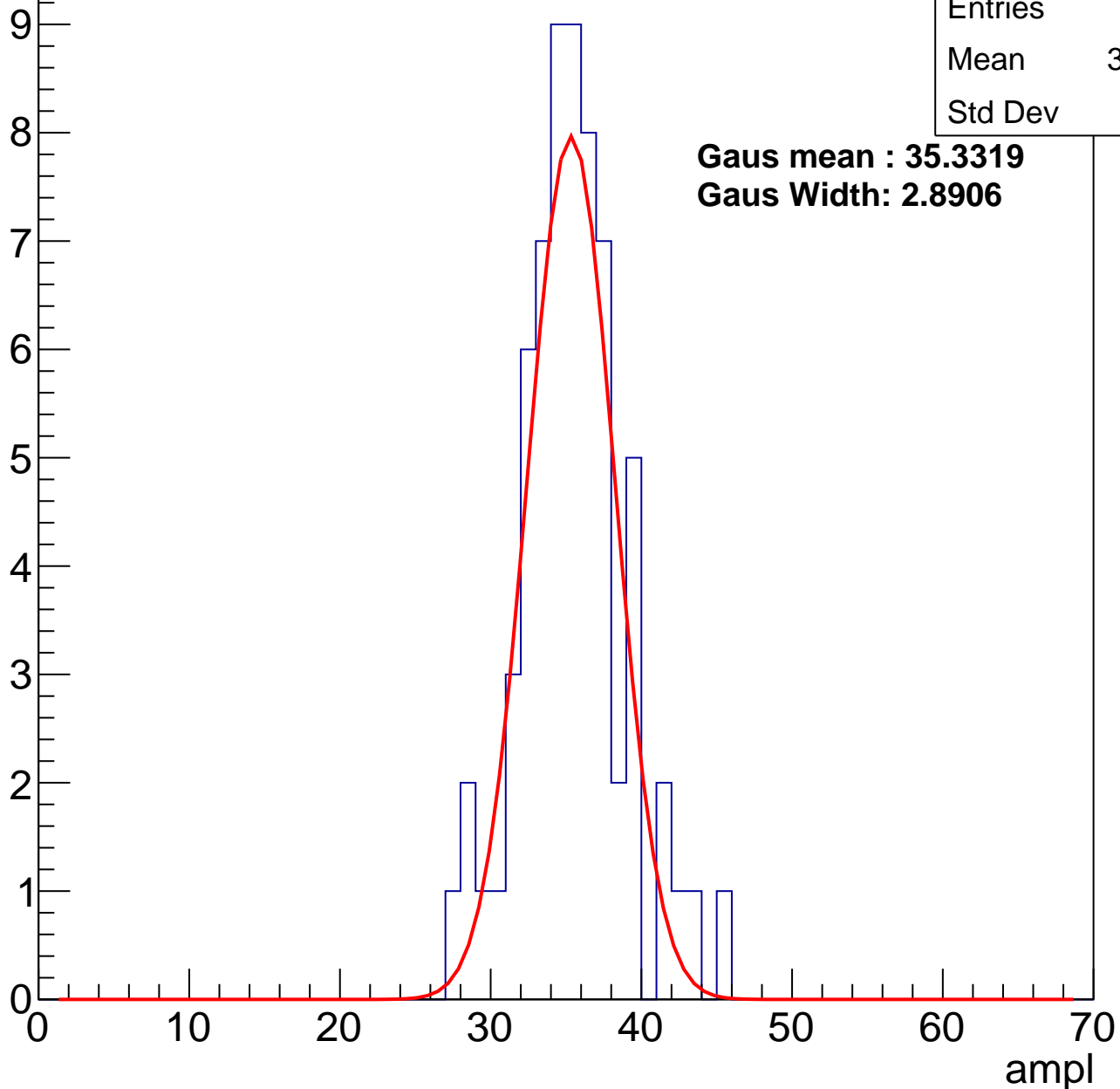
Gaus Width: 3.1442



B1L102S, U6-ch28, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch28, adc2

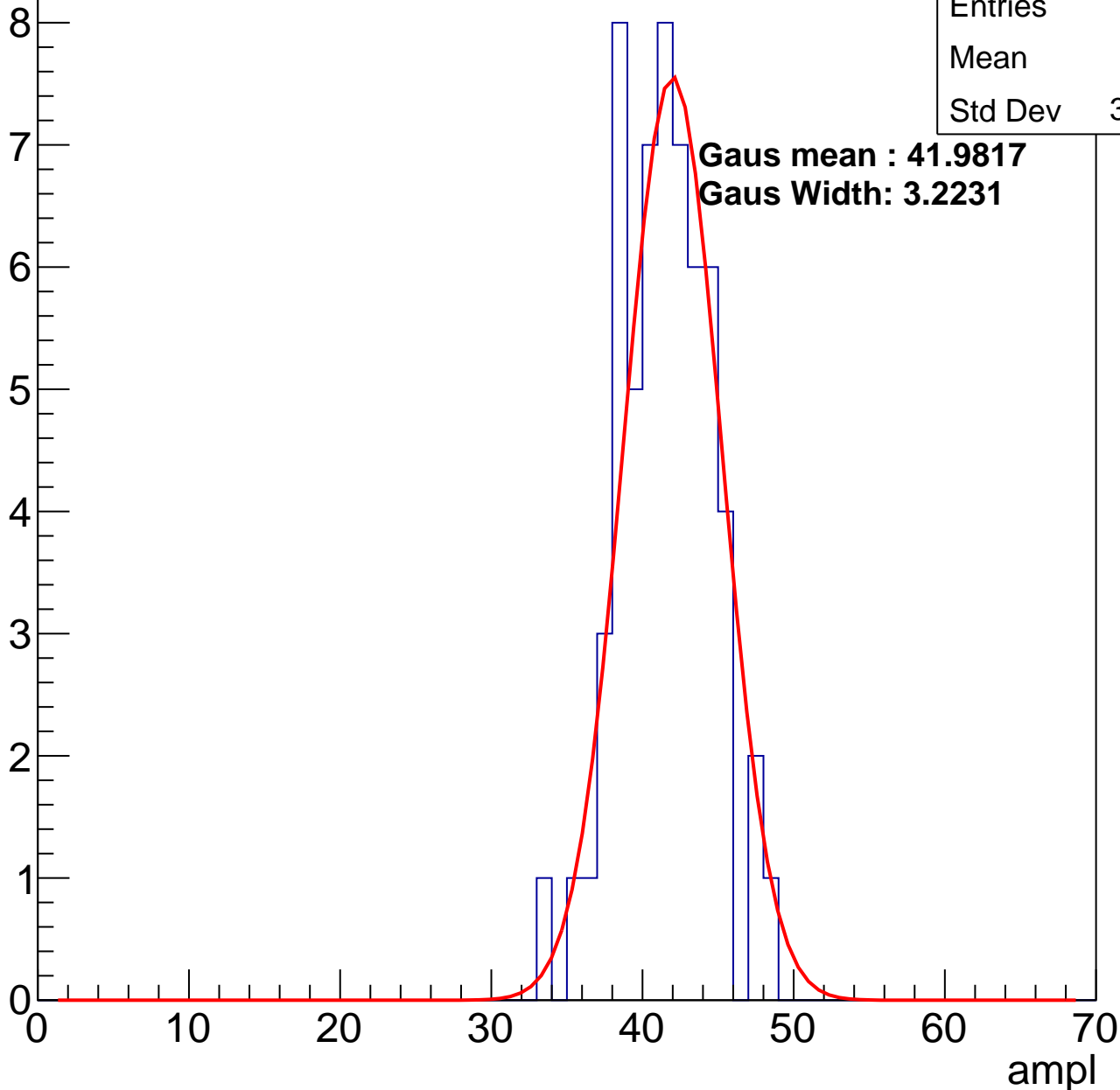
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41
Std Dev	3.006

Gaus mean : 41.9817

Gaus Width: 3.2231

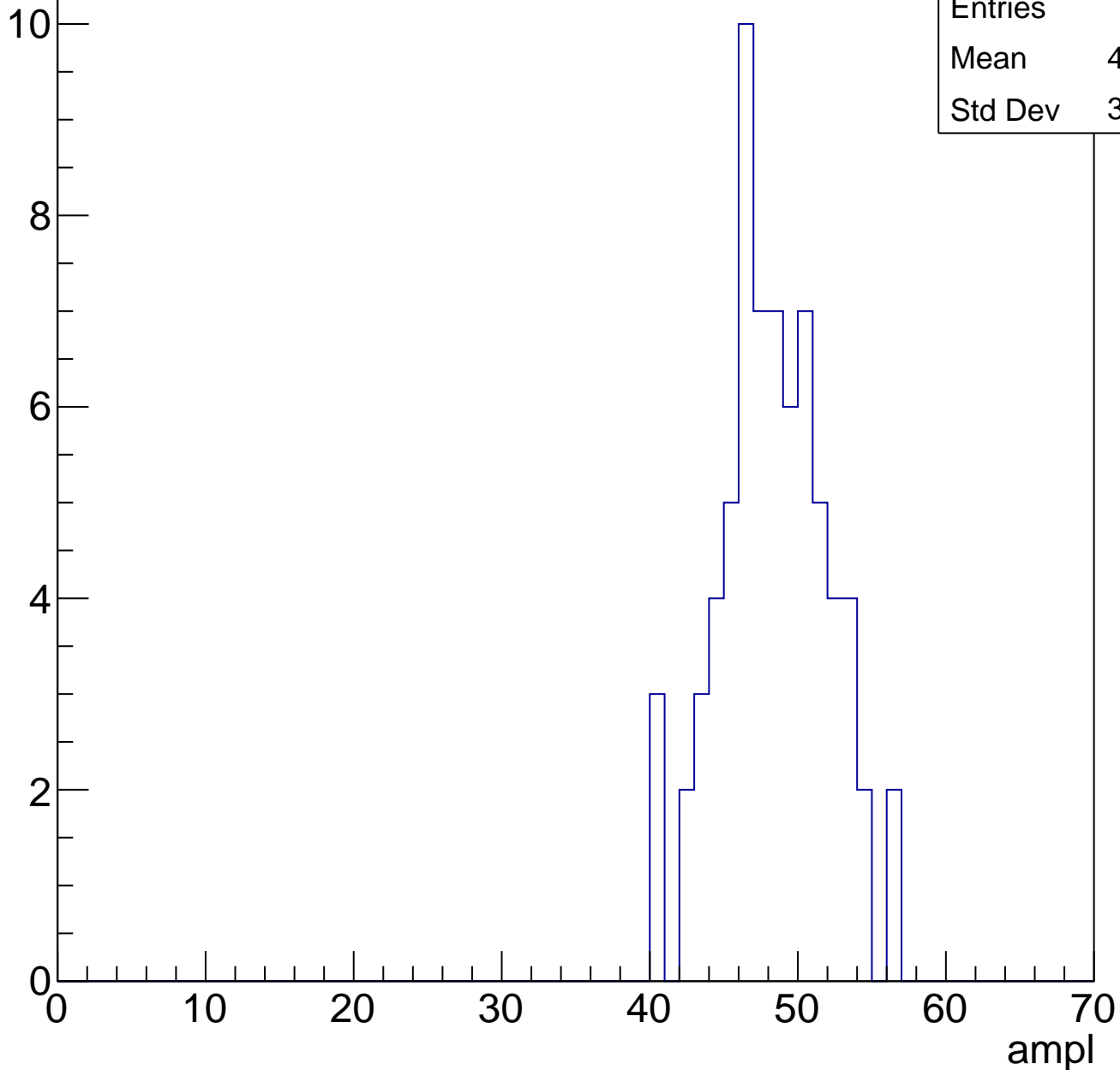


B1L102S, U6-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

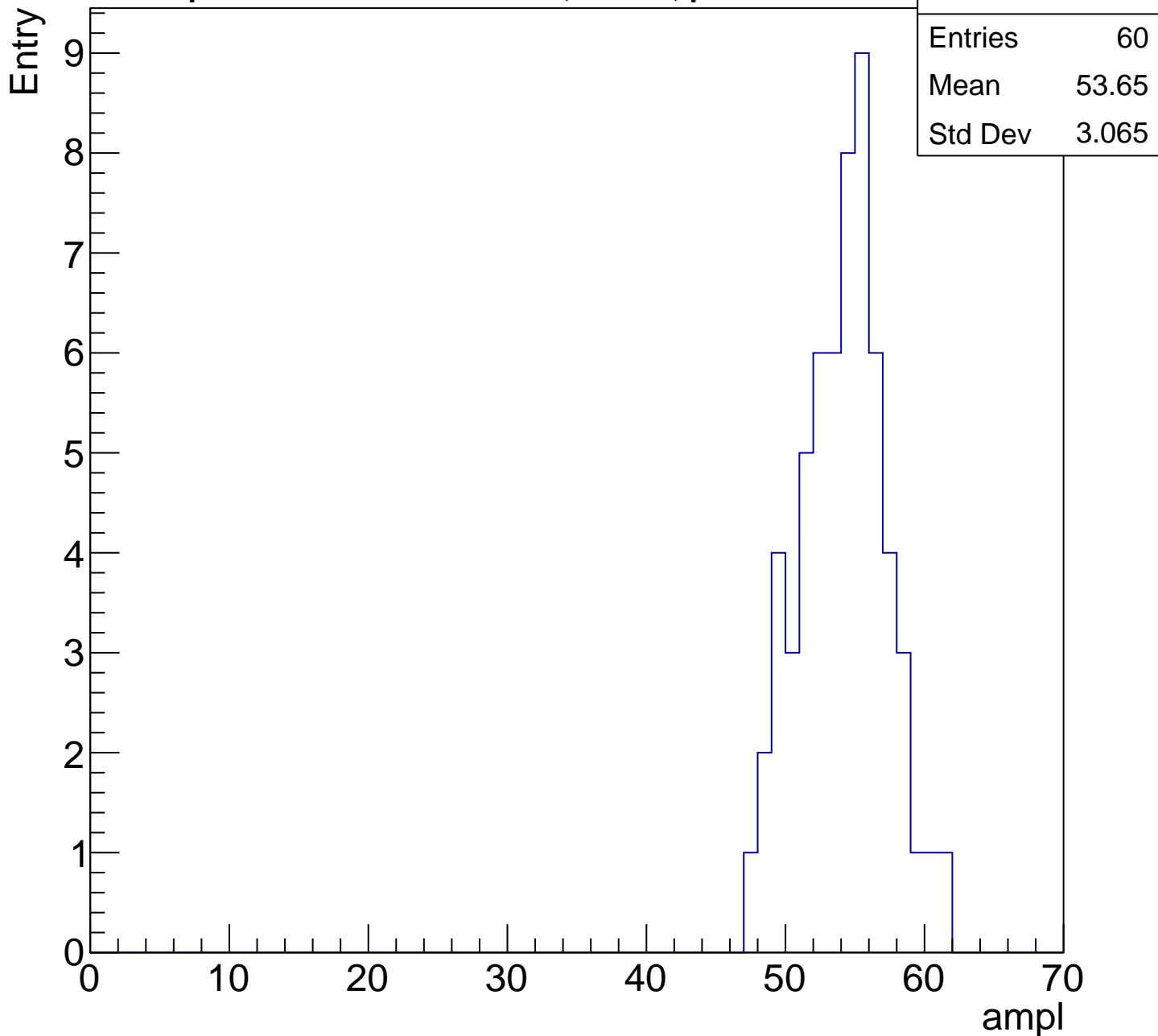
Entries	71
Mean	47.86
Std Dev	3.632

Entry



B1L102S, U6-ch28, adc4

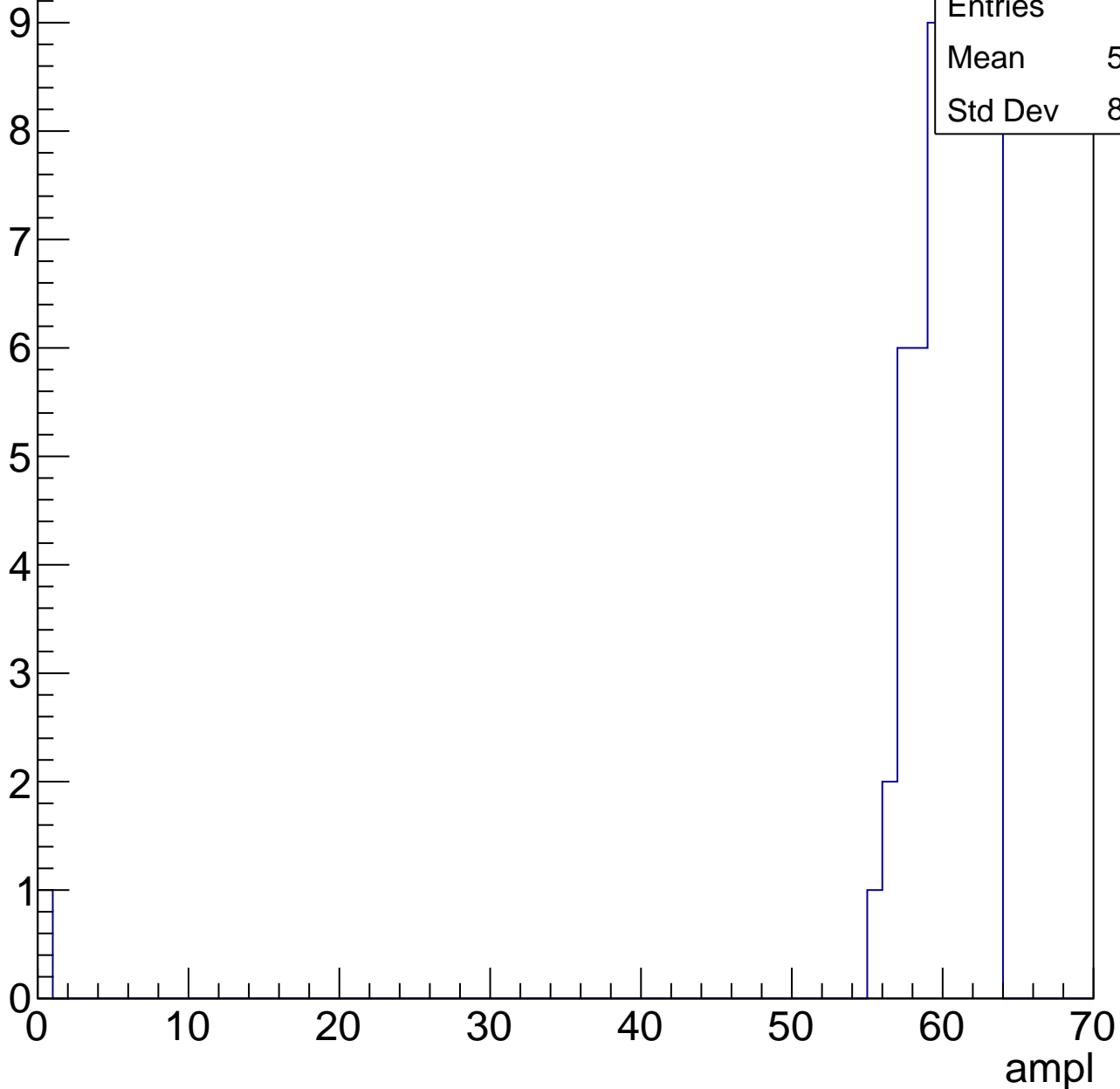
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

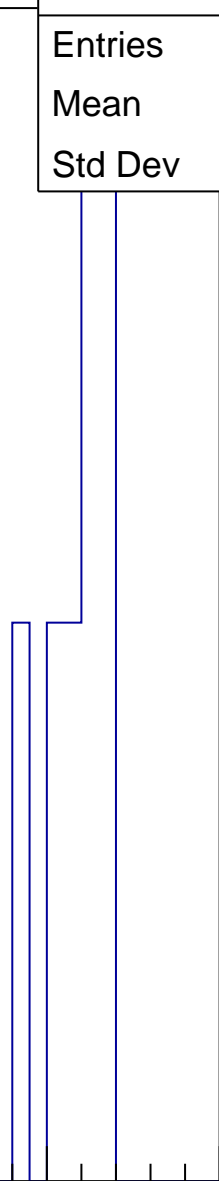
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.666

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch29, adc0

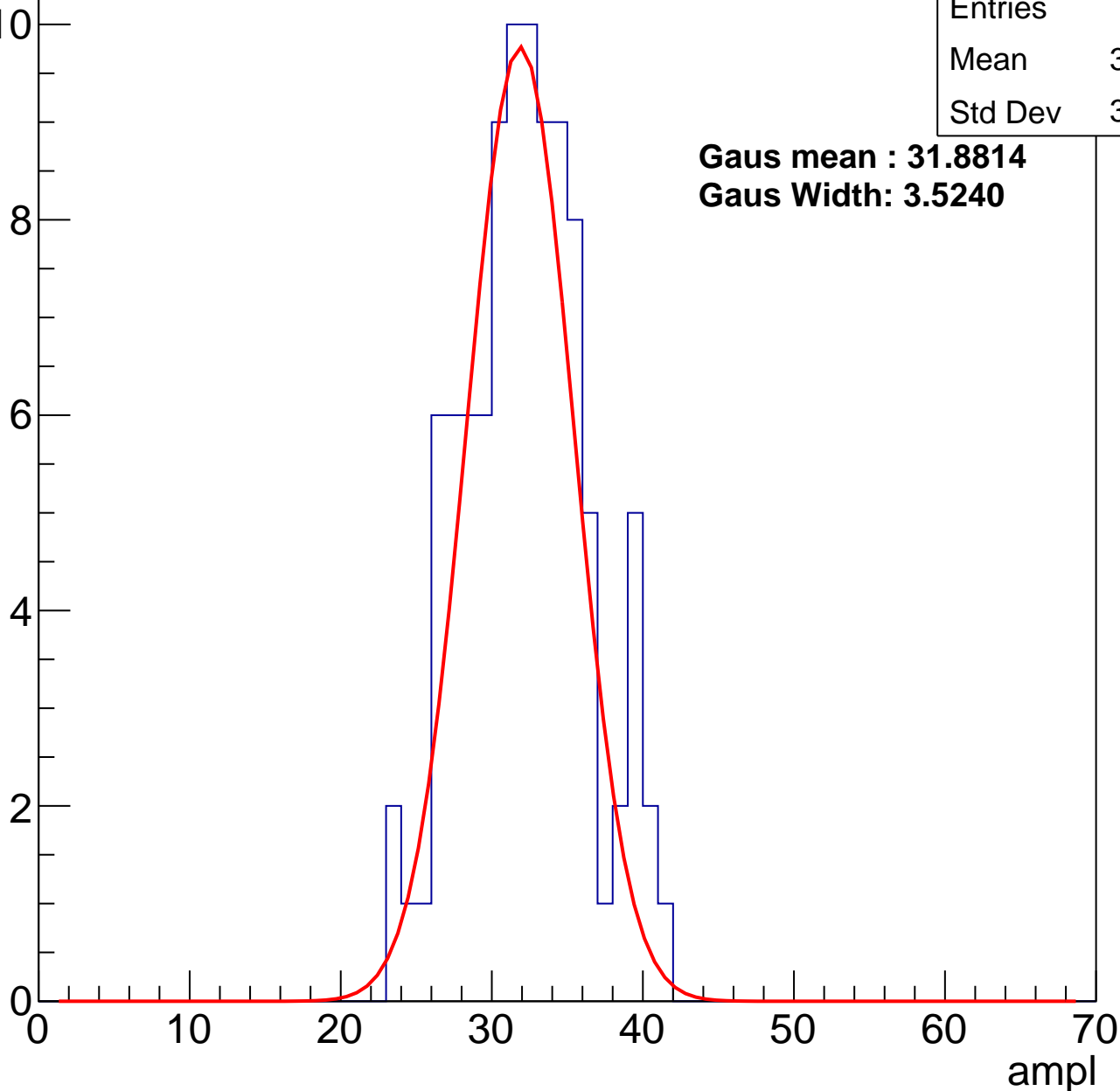
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	99
Mean	31.79
Std Dev	3.996

Gaus mean : 31.8814

Gaus Width: 3.5240



B1L102S, U6-ch29, adc1

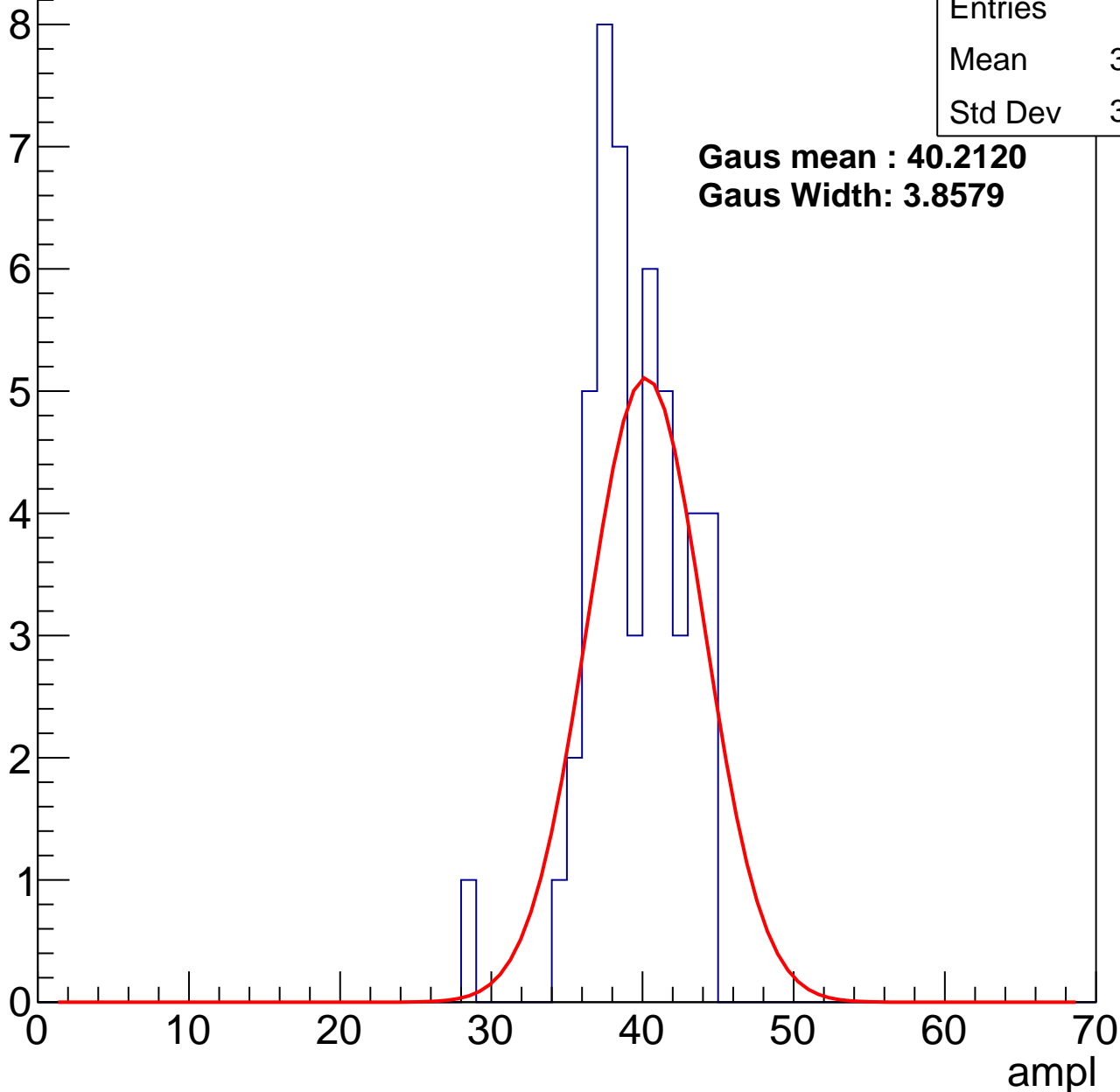
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	38.98
Std Dev	3.127

Gaus mean : 40.2120

Gaus Width: 3.8579



B1L102S, U6-ch29, adc2

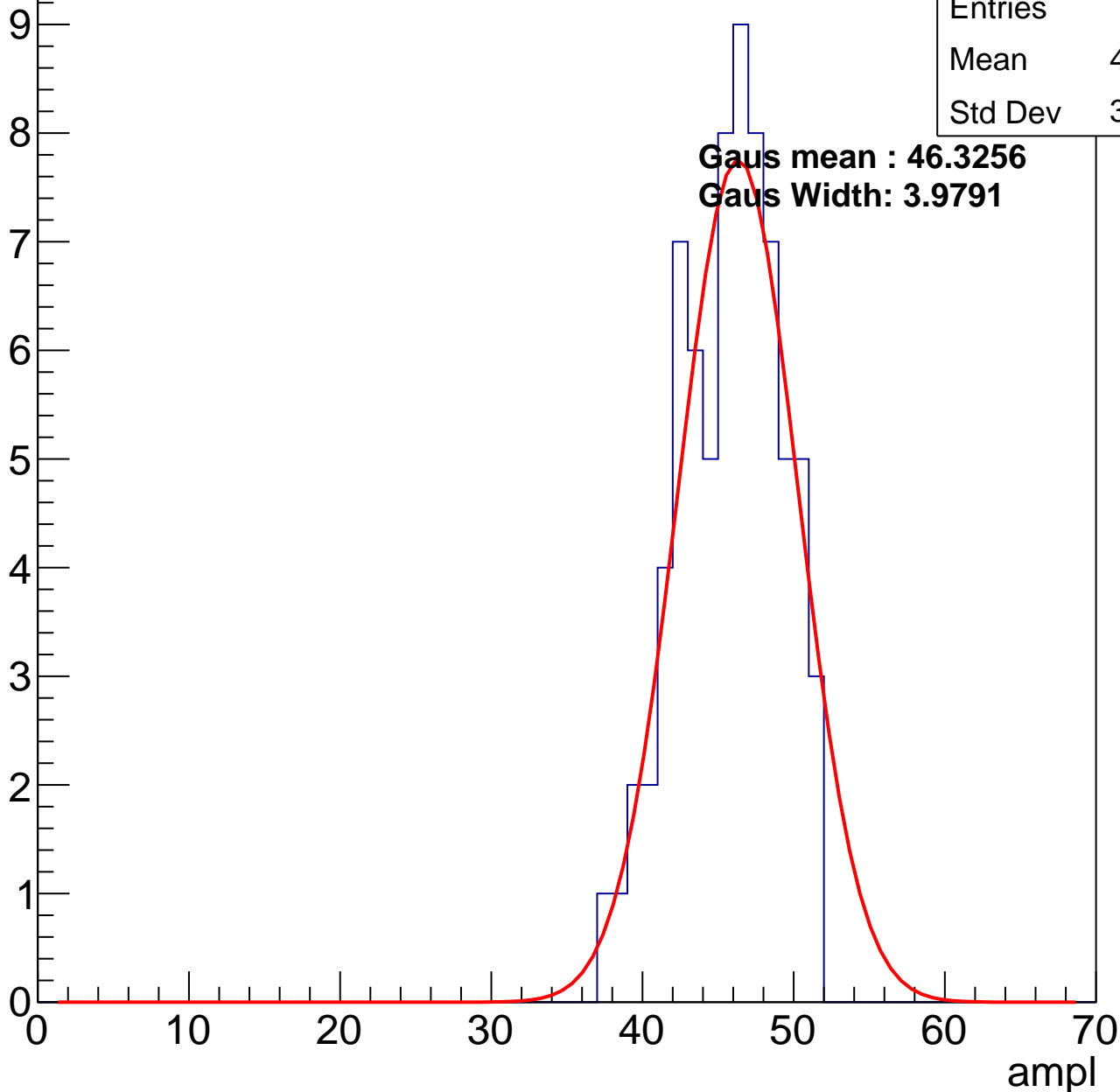
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	45.25
Std Dev	3.318

Gaus mean : 46.3256

Gaus Width: 3.9791

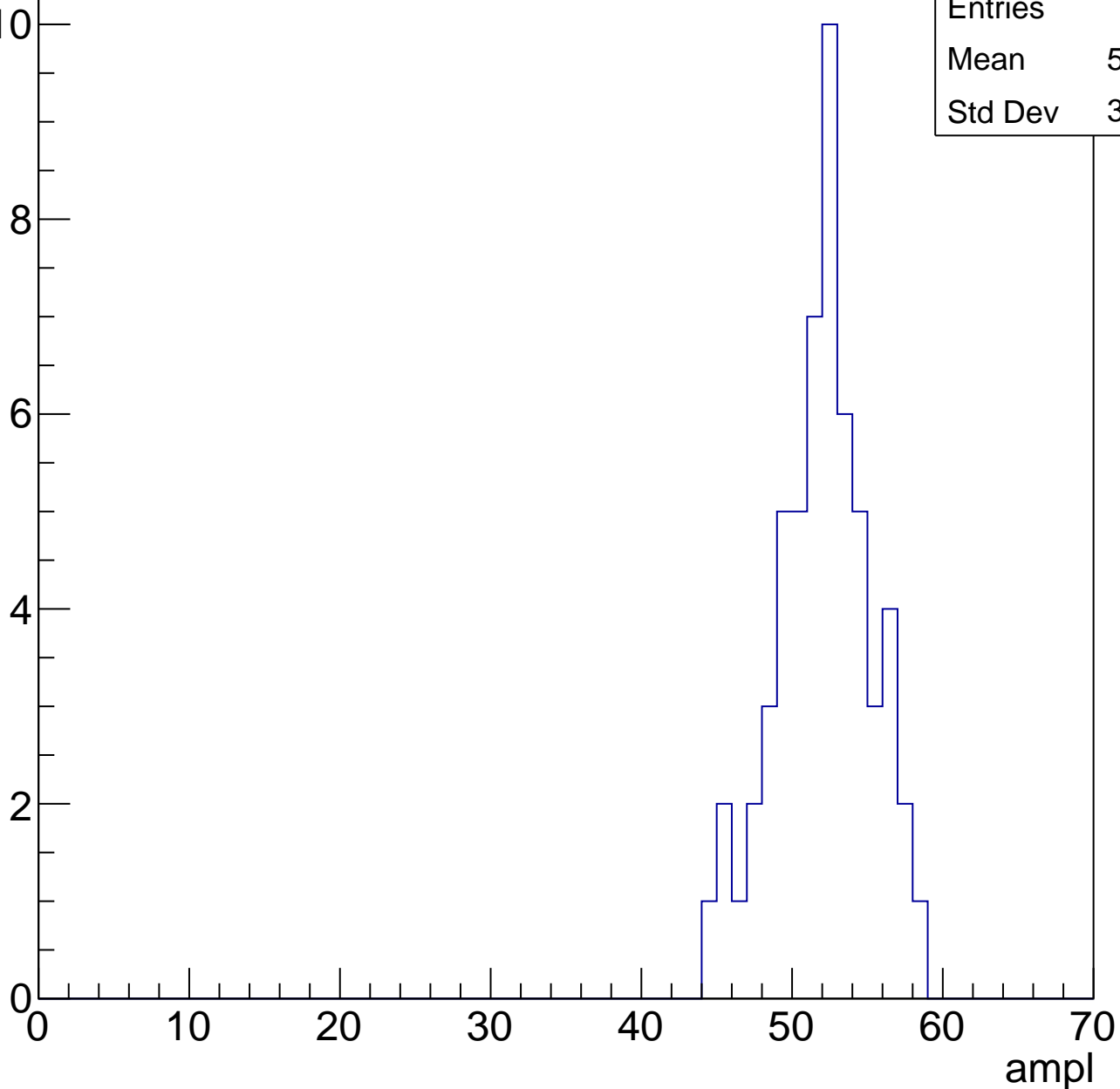


B1L102S, U6-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	51.56
Std Dev	3.135

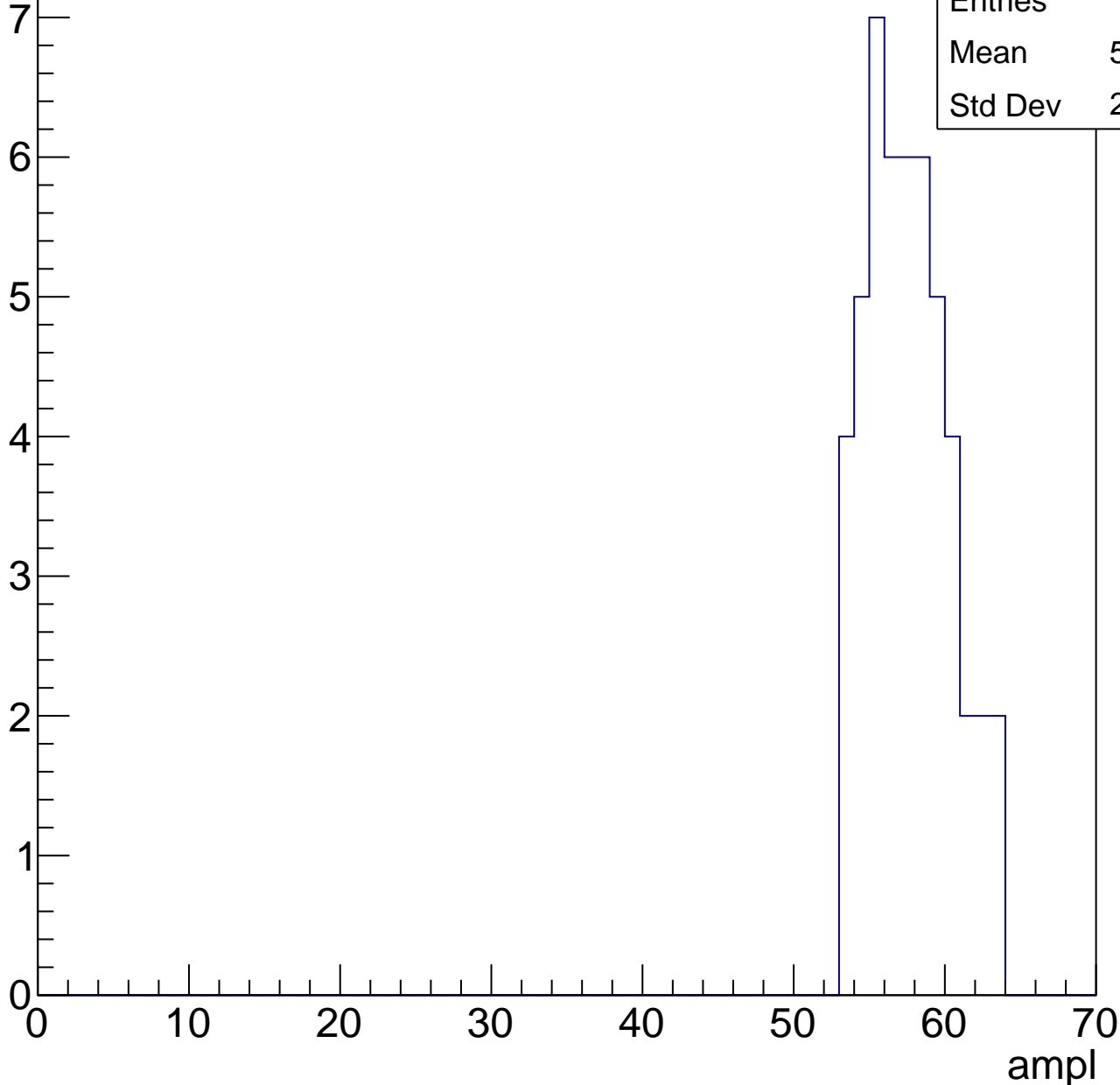


B1L102S, U6-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	57.14
Std Dev	2.703



B1L102S, U6-ch29, adc5

calib_packv5_042523_0143.root, FC#11, port A2

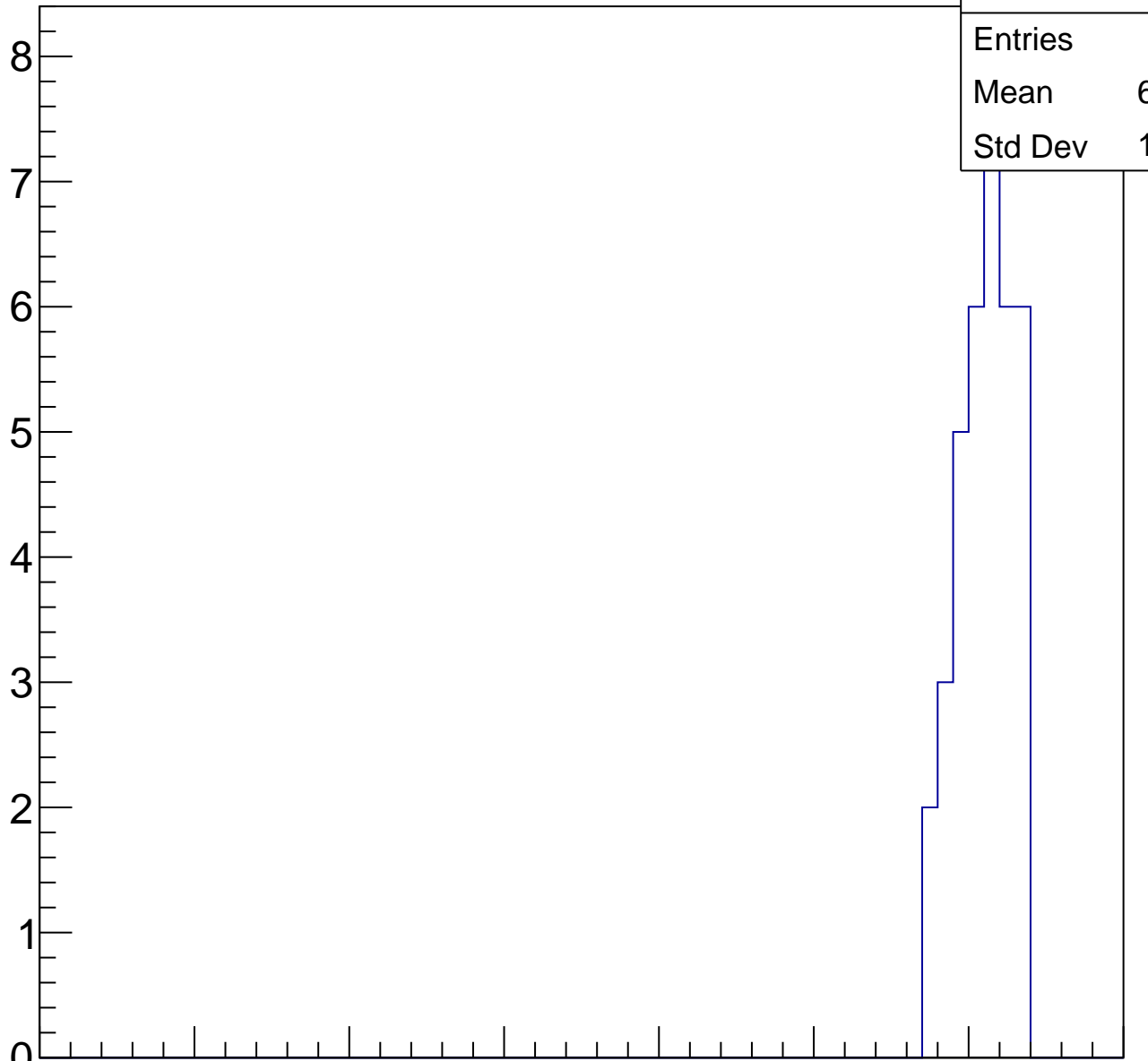
Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.58
Std Dev	1.738

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch30, adc0

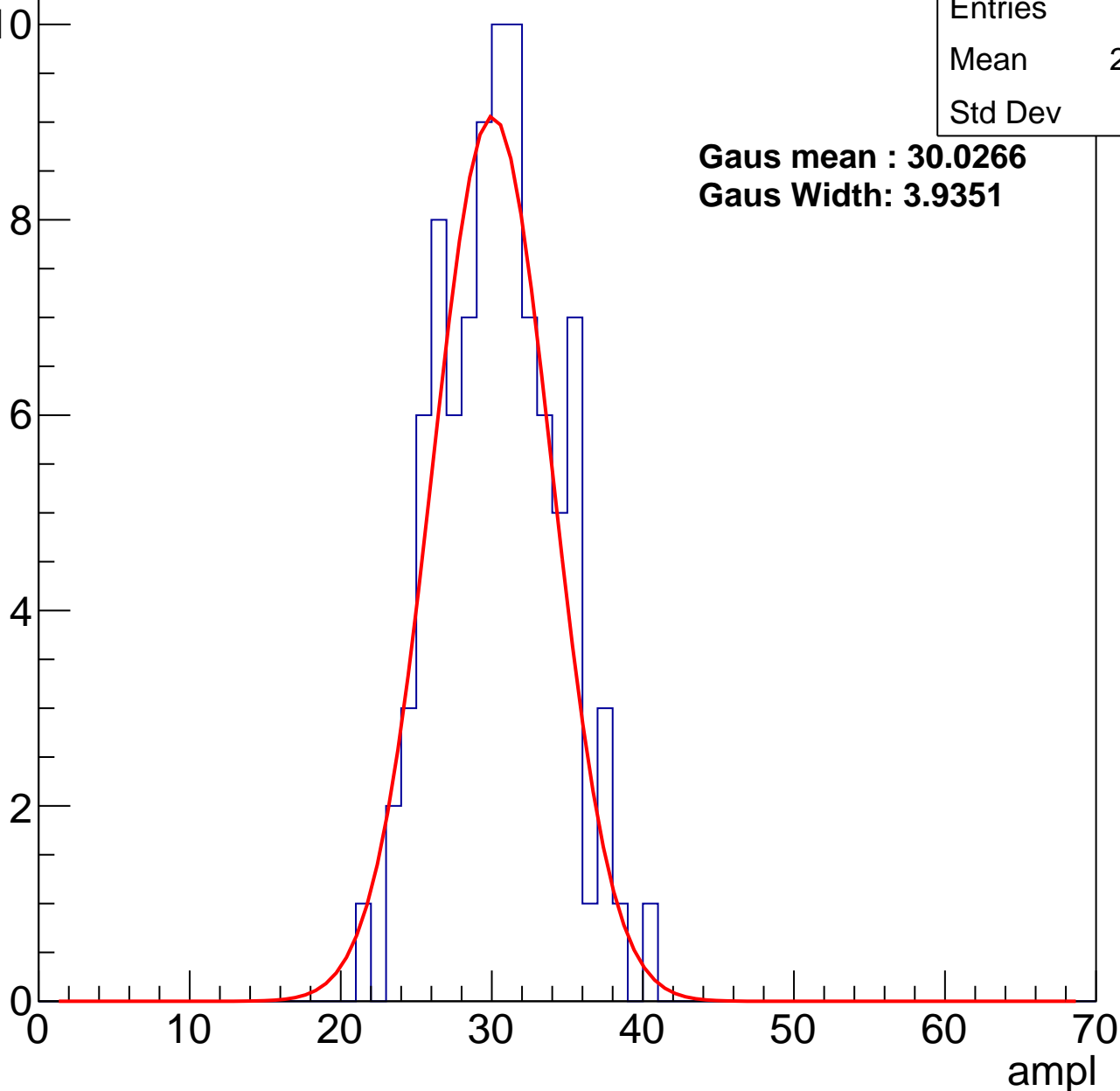
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	29.98
Std Dev	3.81

Gaus mean : 30.0266

Gaus Width: 3.9351



B1L102S, U6-ch30, adc1

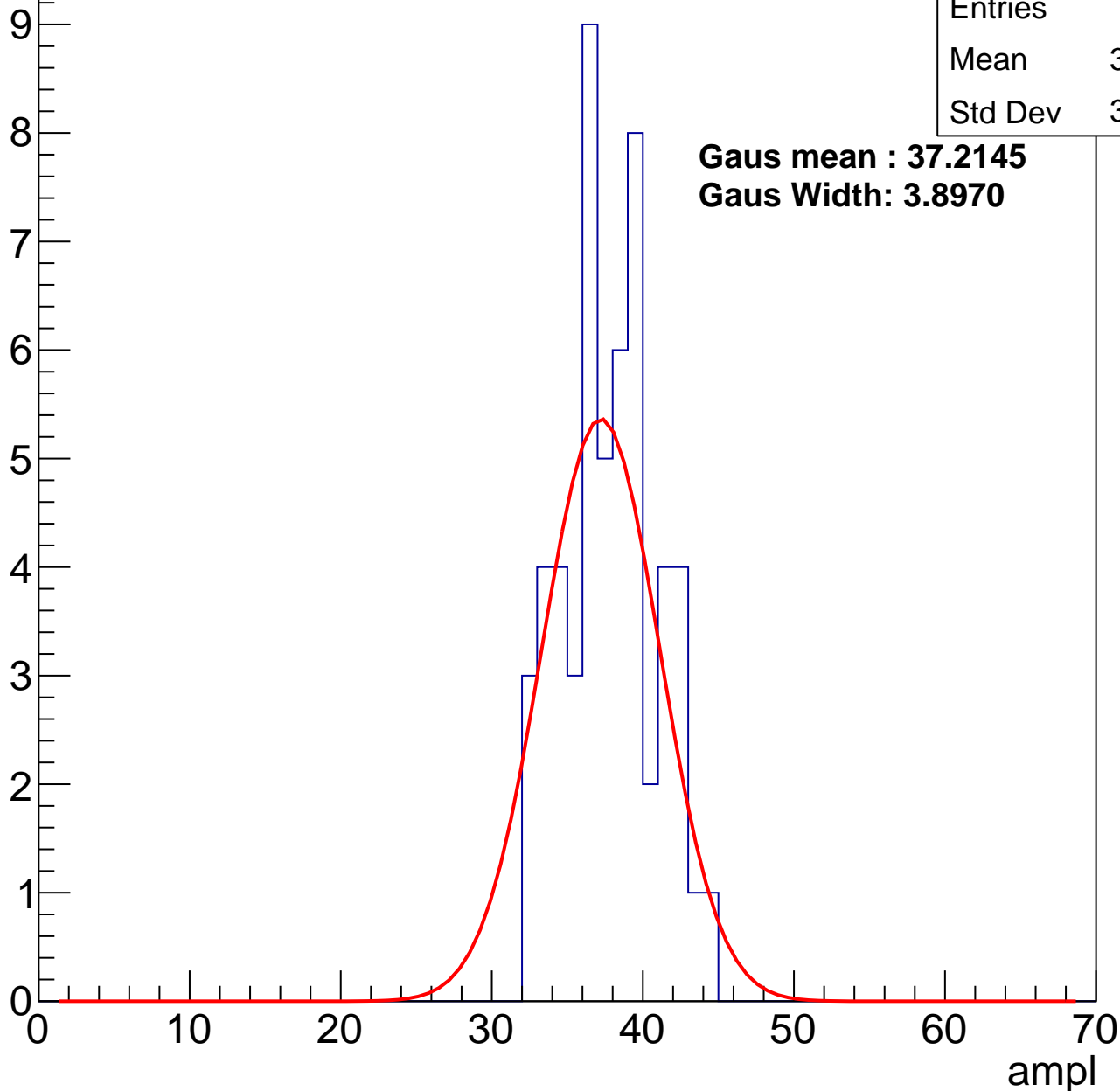
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.35
Std Dev	3.026

Gaus mean : 37.2145

Gaus Width: 3.8970



B1L102S, U6-ch30, adc2

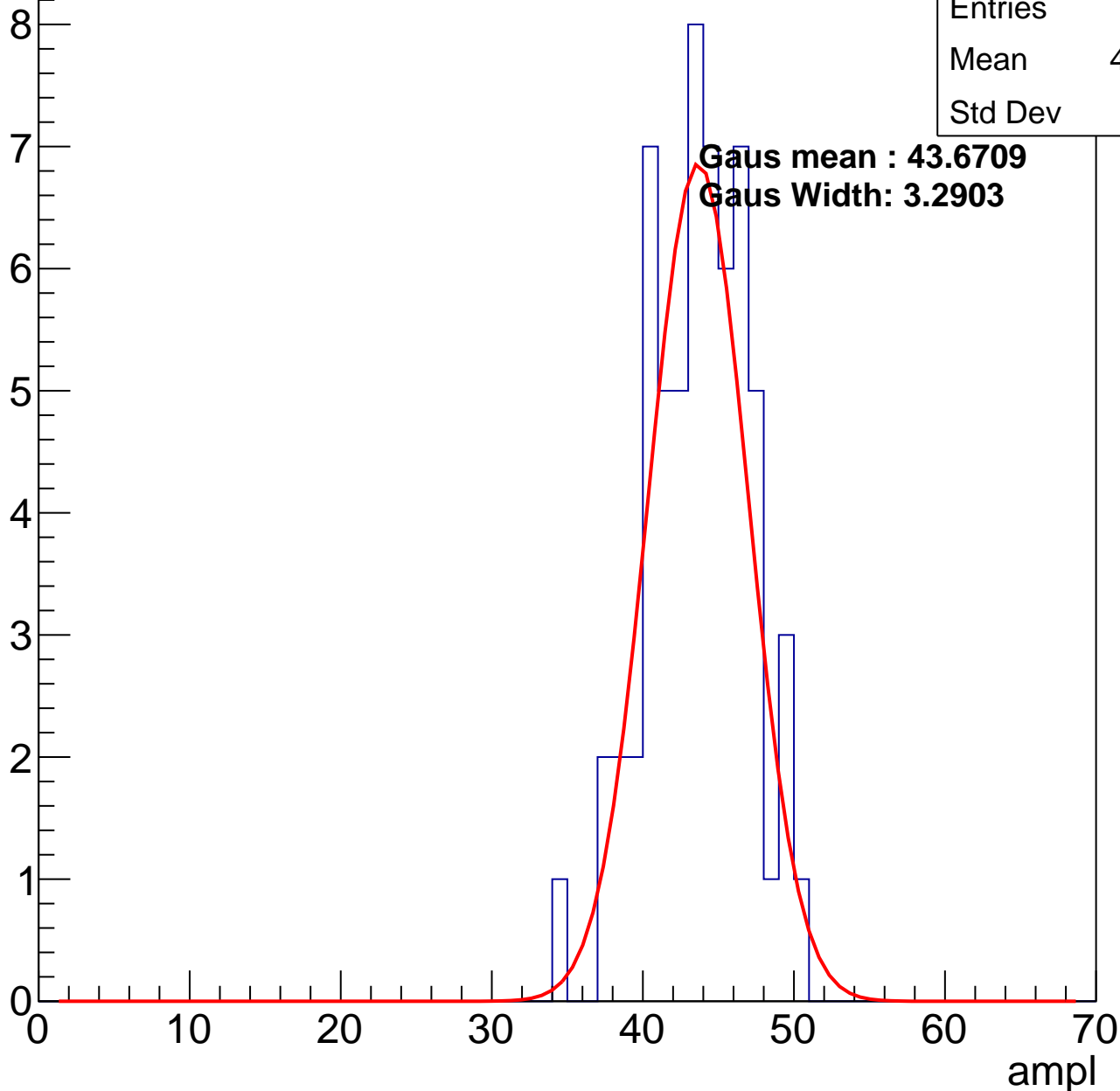
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	43.24
Std Dev	3.3

Gaus mean : 43.6709

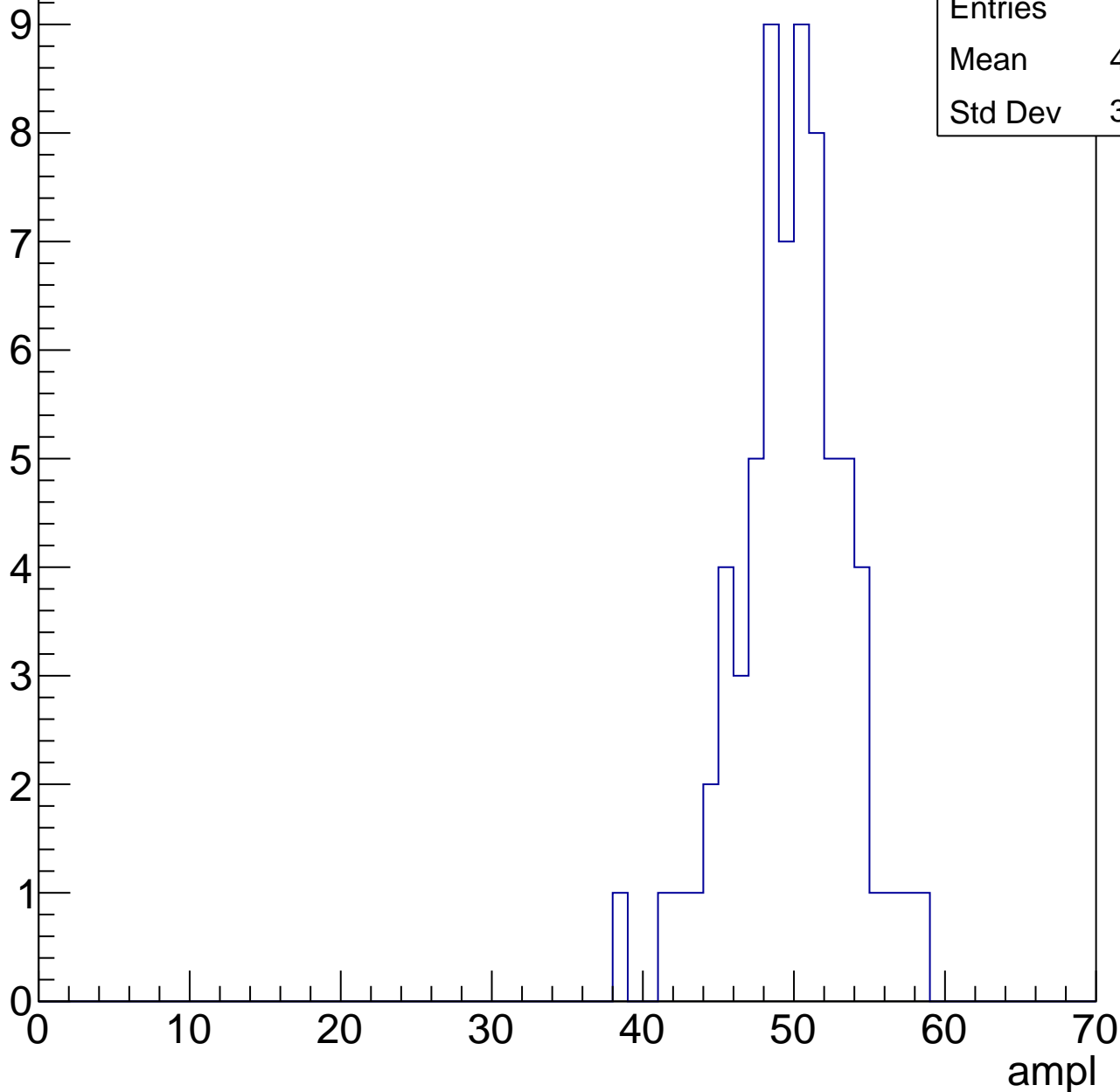
Gaus Width: 3.2903



B1L102S, U6-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

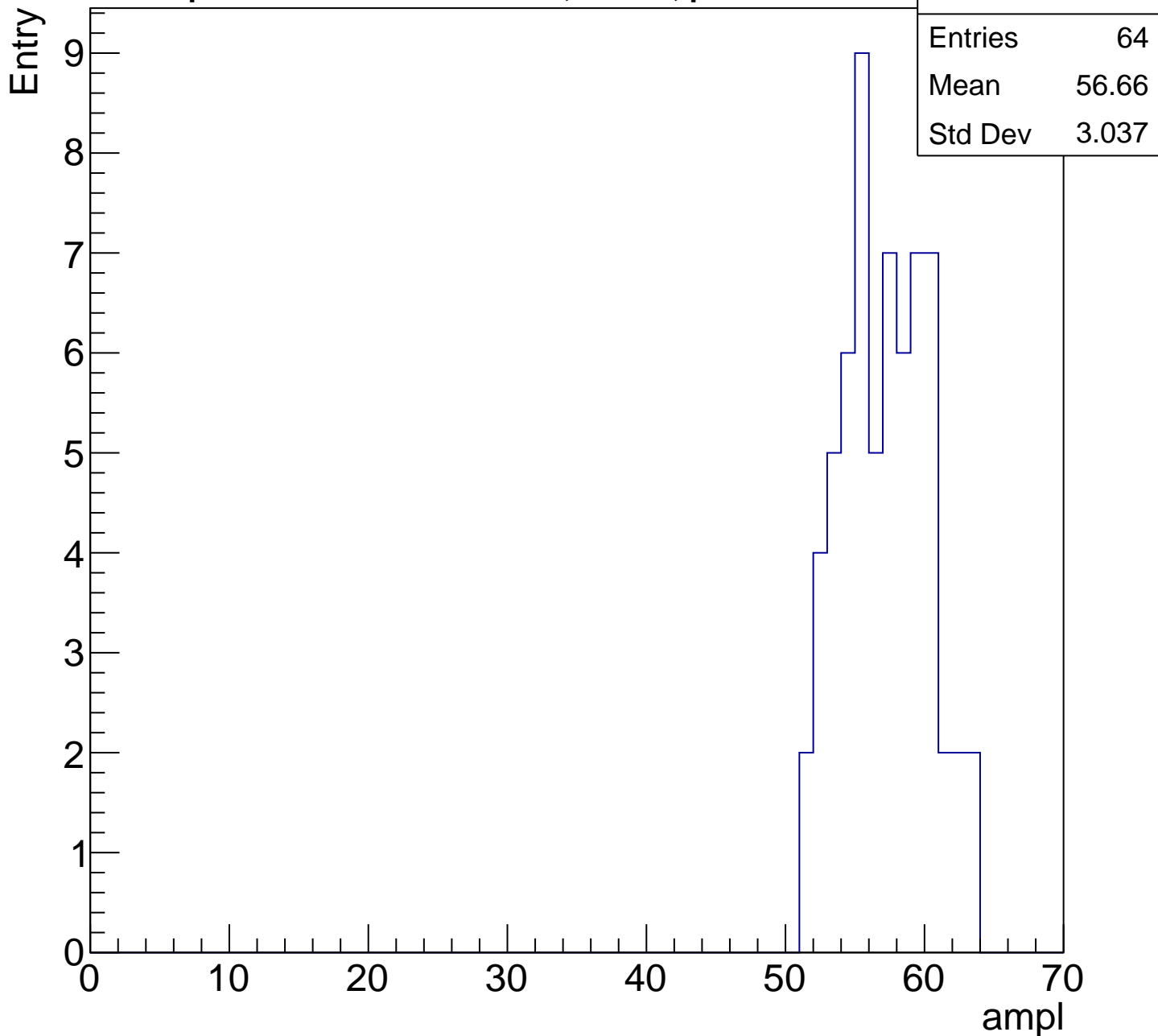
Entry



Entries	69
Mean	49.35
Std Dev	3.667

B1L102S, U6-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

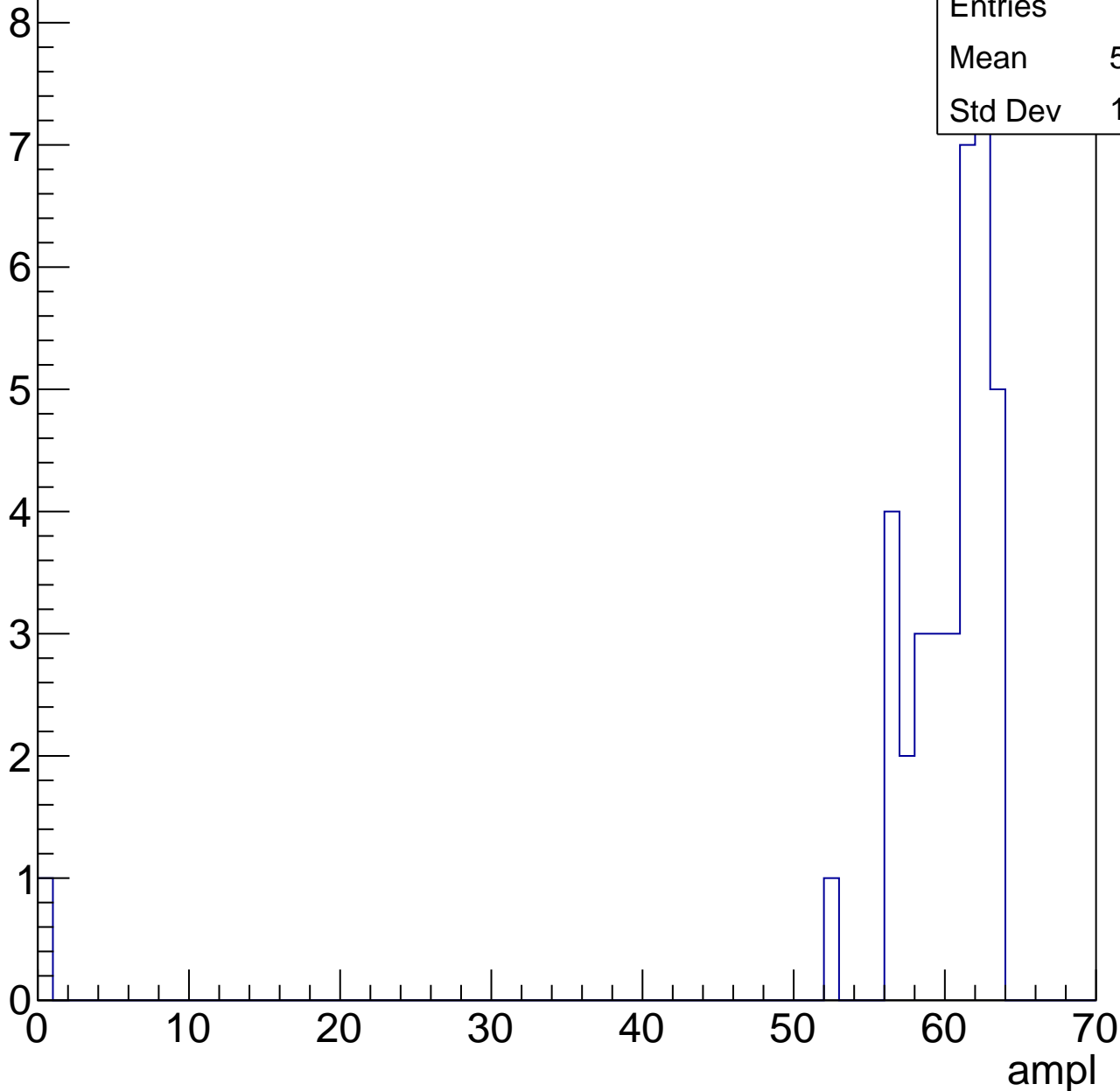


B1L102S, U6-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	58.35
Std Dev	10.06



B1L102S, U6-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U6-ch31, adc0

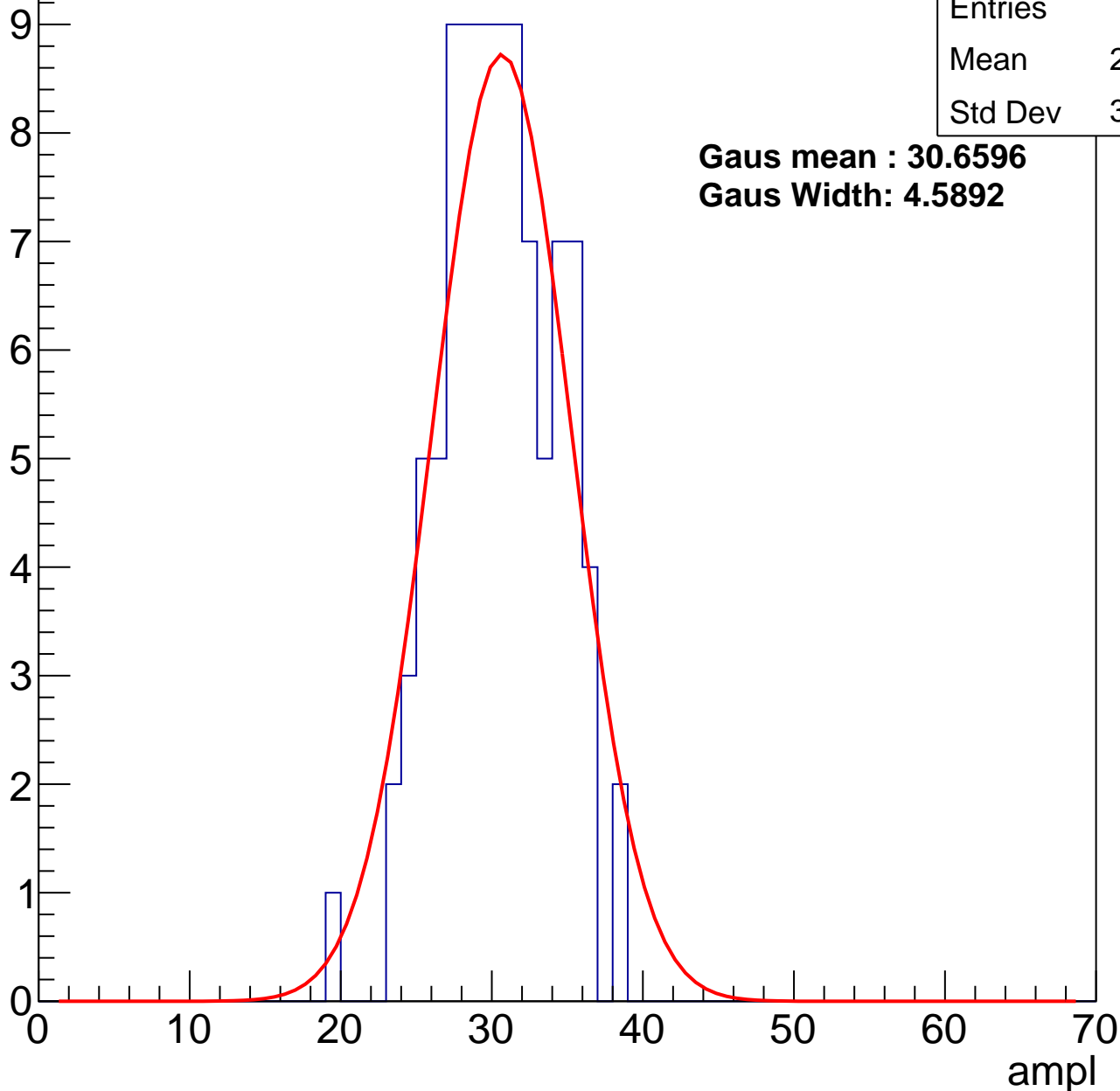
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	93
Mean	29.99
Std Dev	3.737

Gaus mean : 30.6596

Gaus Width: 4.5892



B1L102S, U6-ch31, adc1

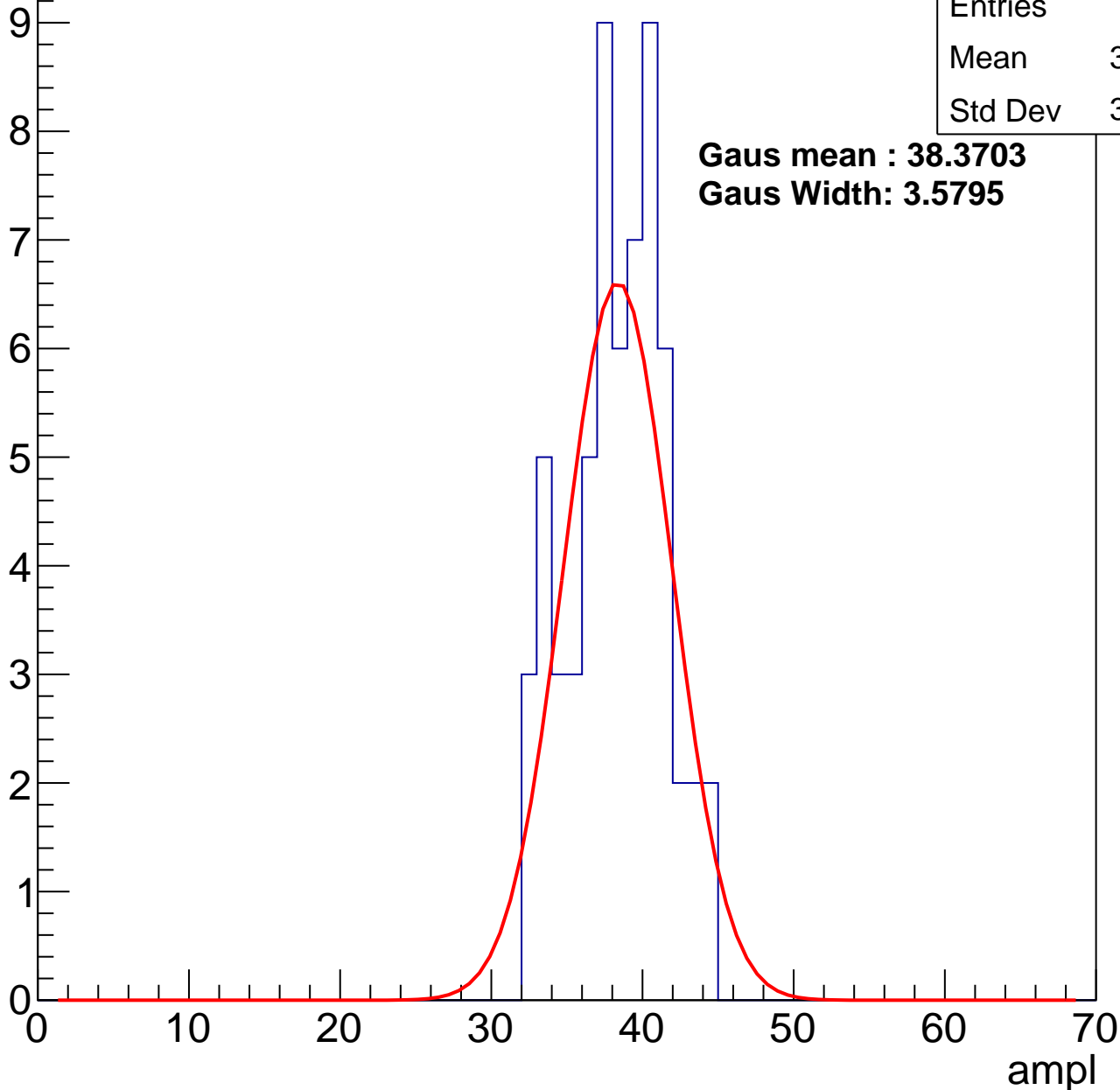
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.84
Std Dev	3.075

Gaus mean : 38.3703

Gaus Width: 3.5795



B1L102S, U6-ch31, adc2

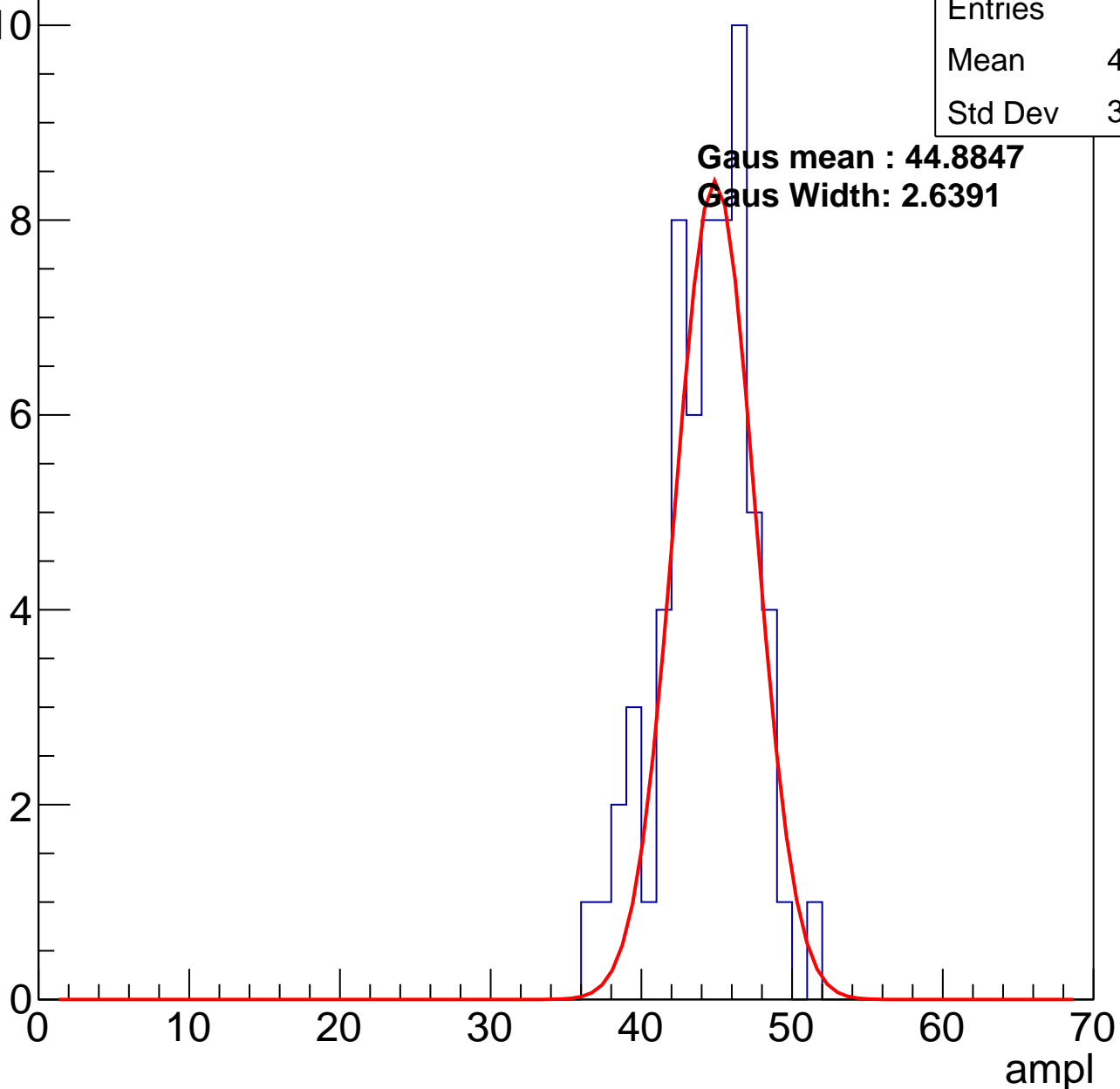
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.86
Std Dev	3.033

Gaus mean : 44.8847

Gaus Width: 2.6391

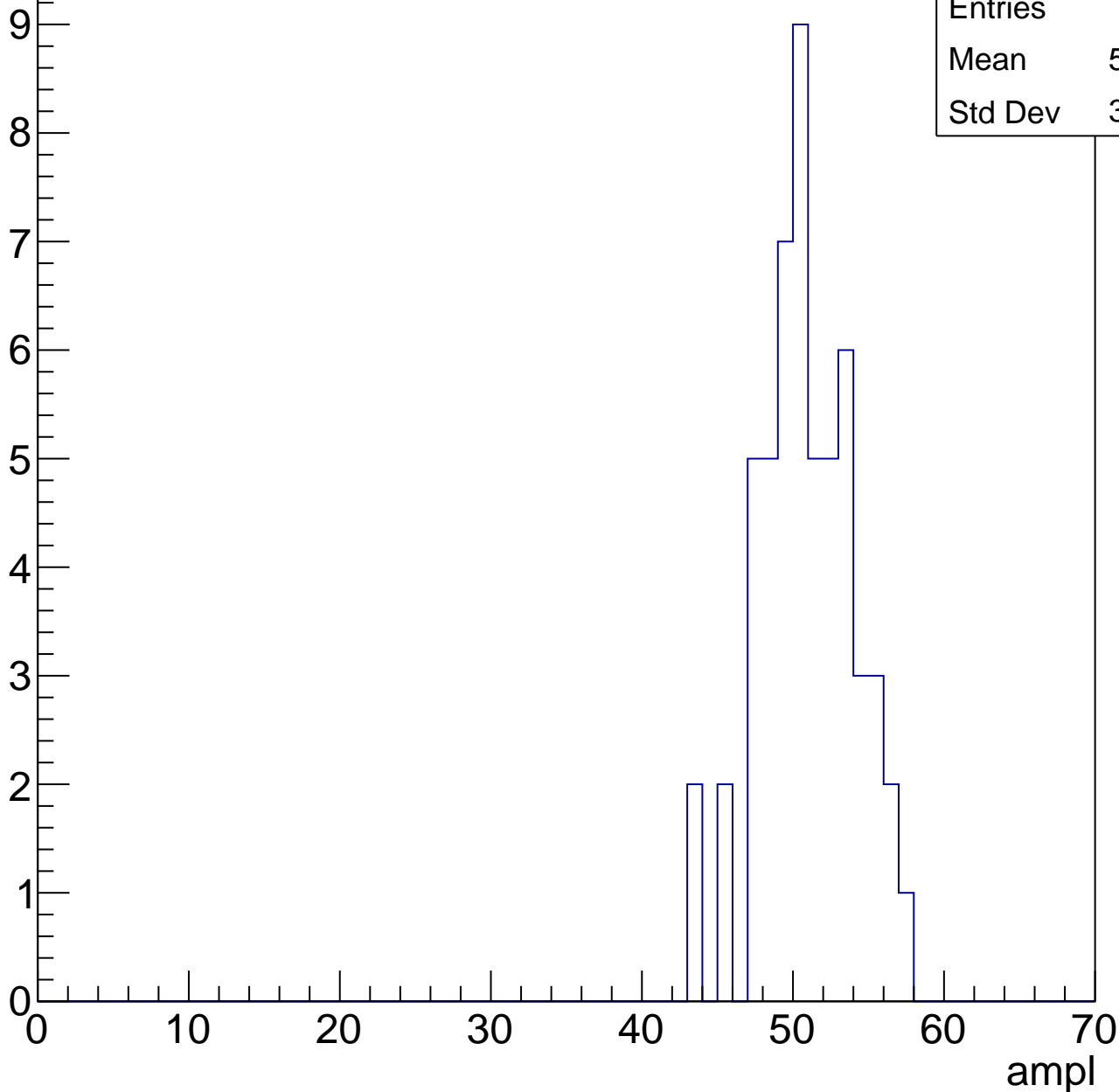


B1L102S, U6-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	50.42
Std Dev	3.114

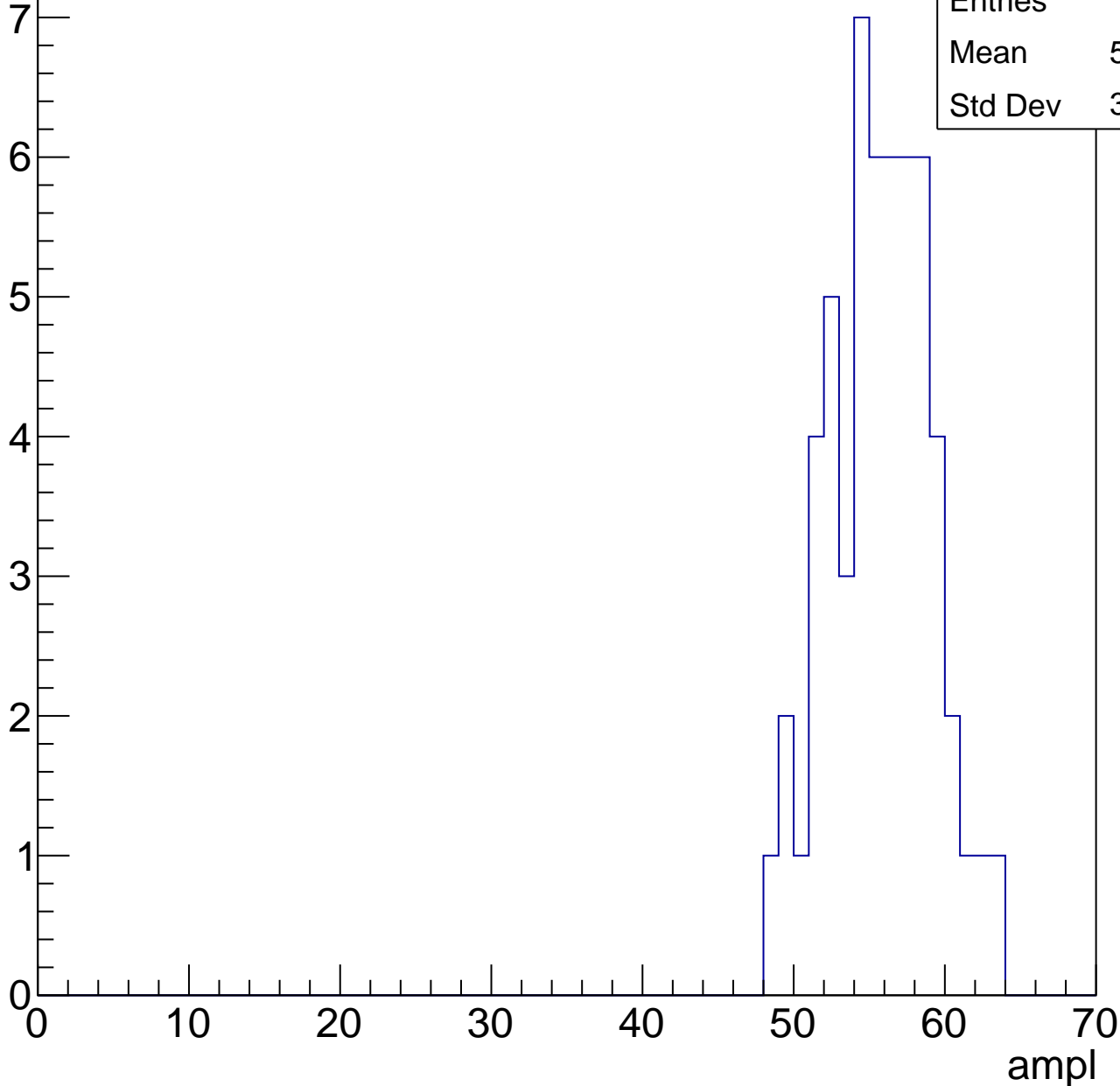


B1L102S, U6-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

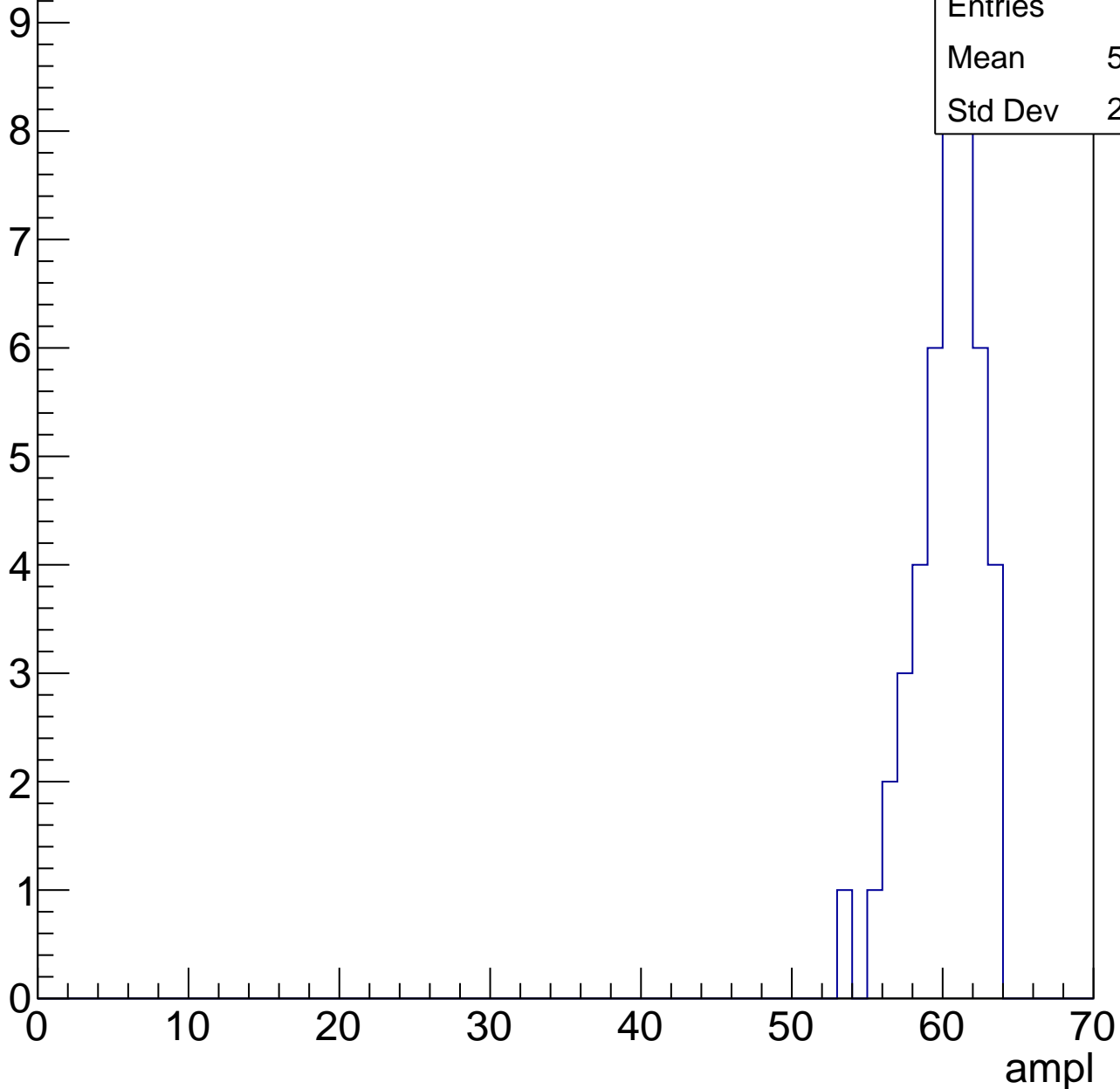
Entries	56
Mean	55.27
Std Dev	3.319



B1L102S, U6-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

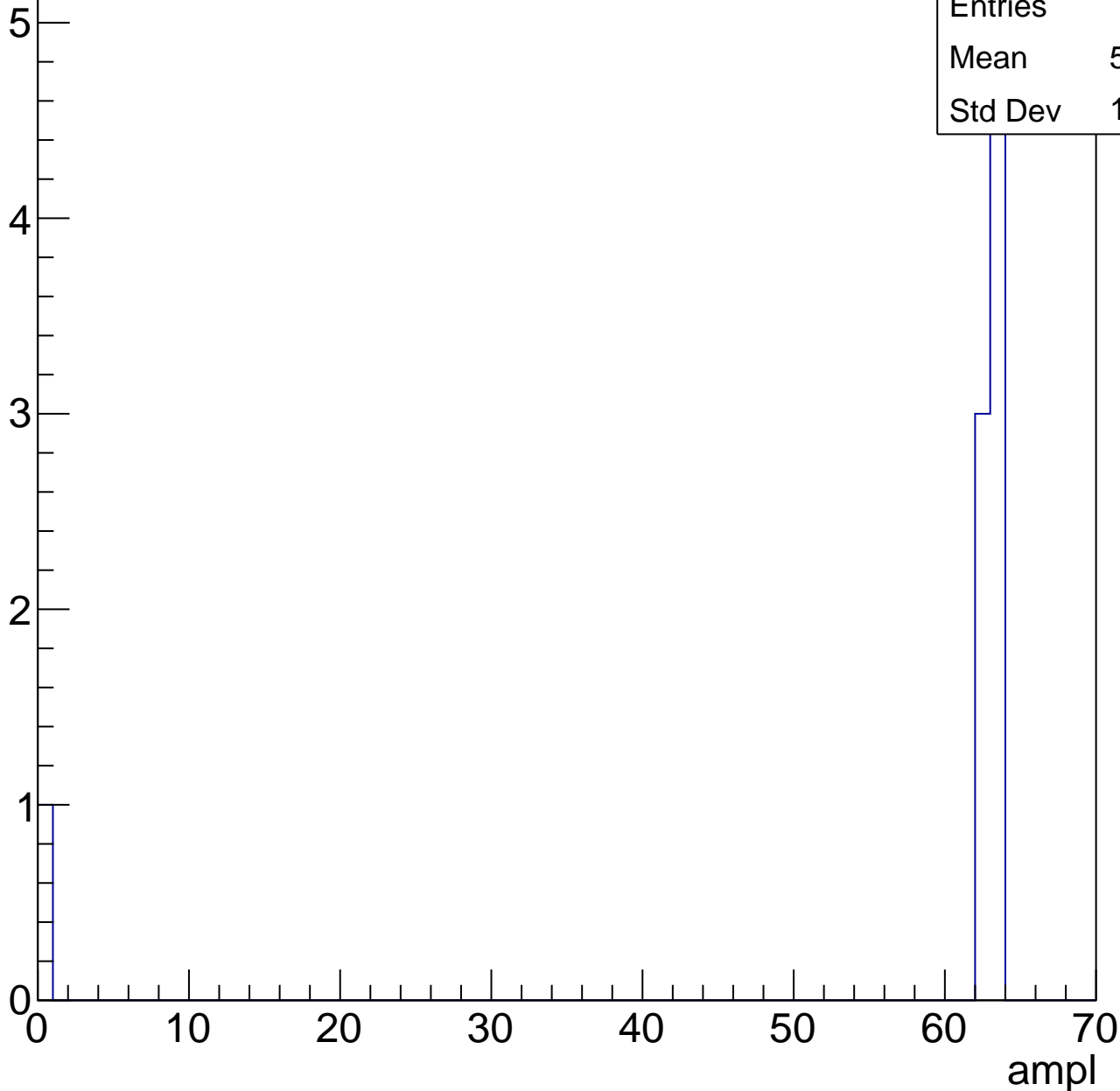


B1L102S, U6-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	55.67
Std Dev	19.69



B1L102S, U6-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch32, adc0

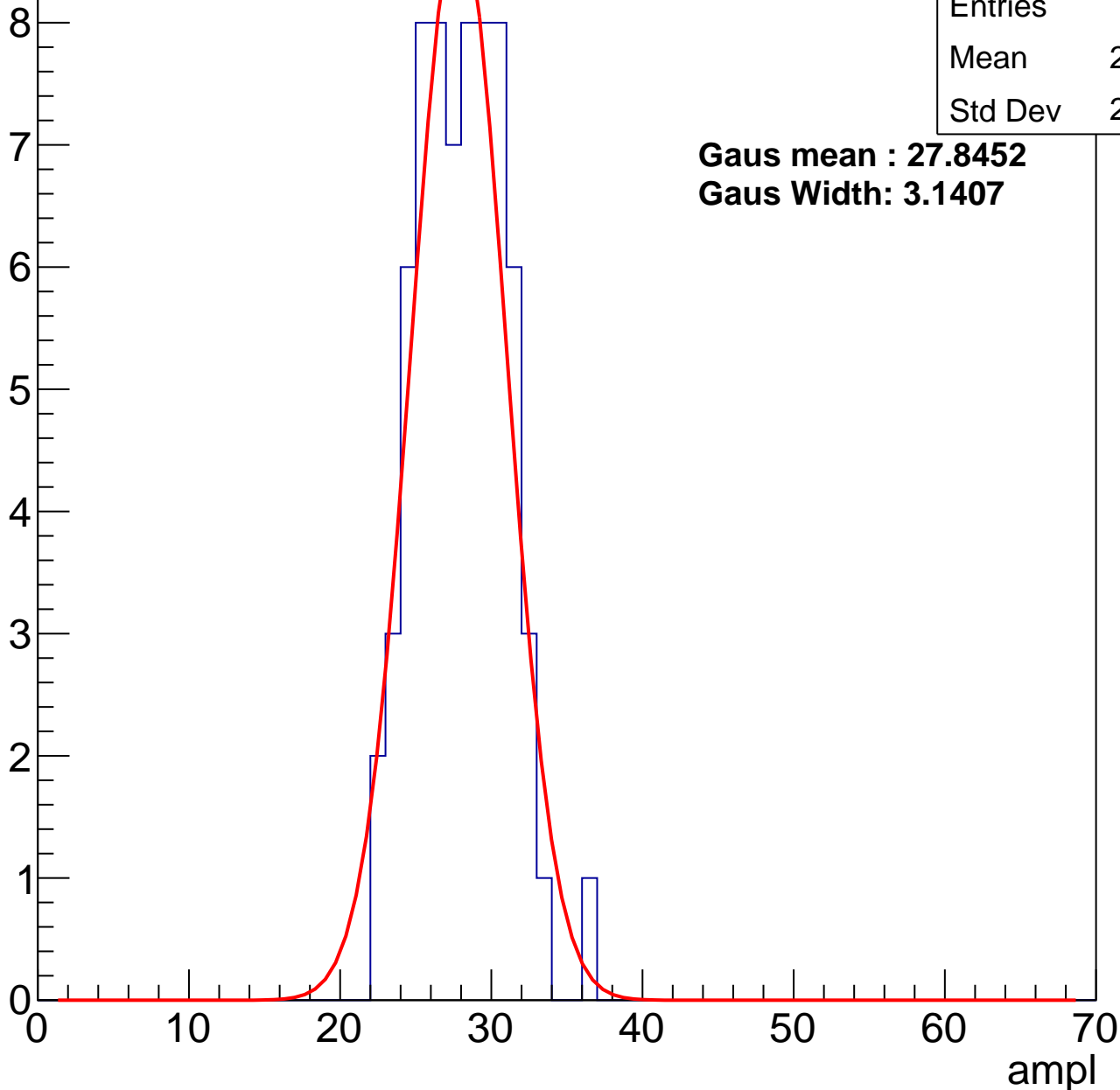
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	27.55
Std Dev	2.877

Gaus mean : 27.8452

Gaus Width: 3.1407



B1L102S, U6-ch32, adc1

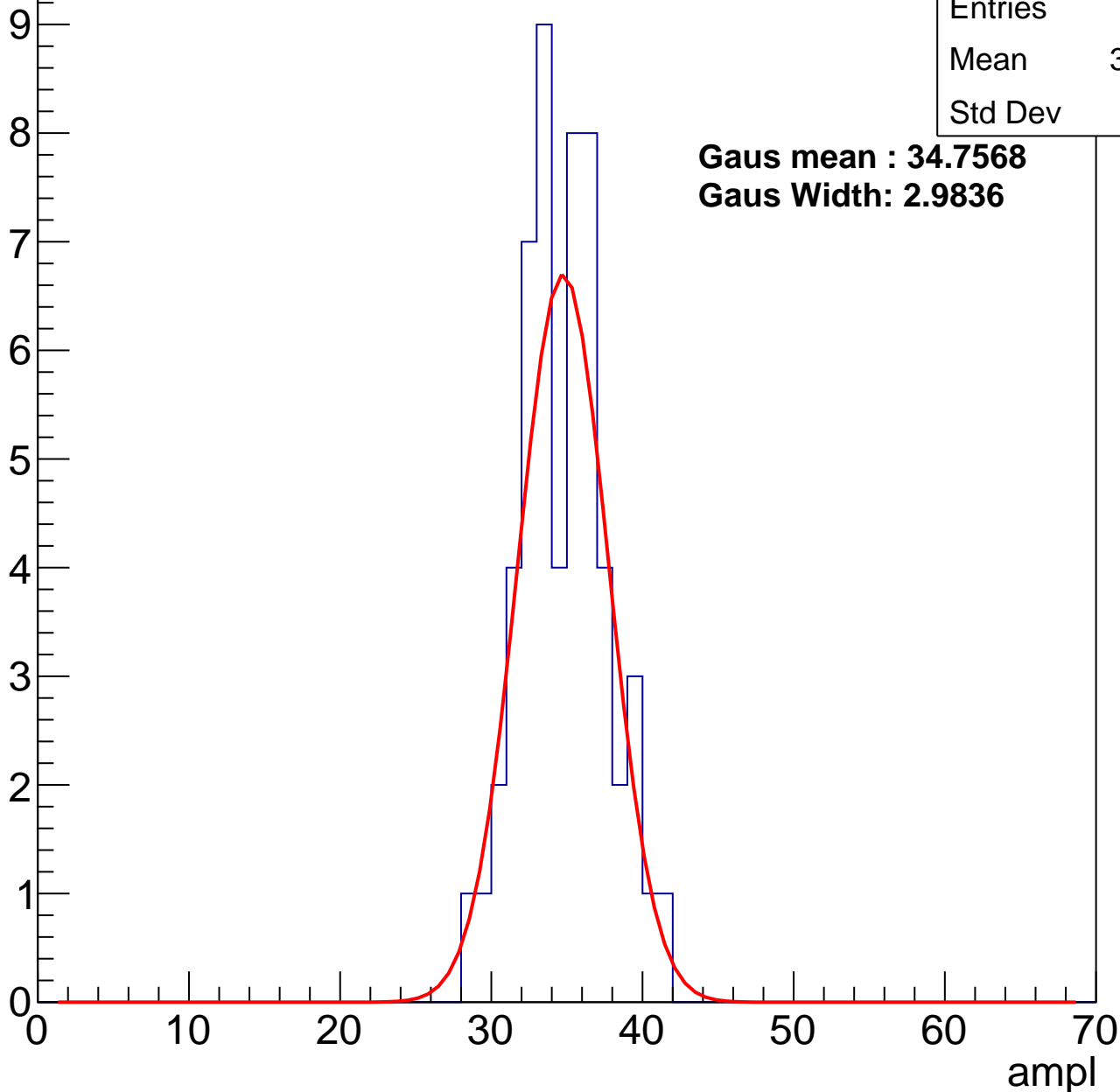
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	34.33
Std Dev	2.79

Gaus mean : 34.7568

Gaus Width: 2.9836



B1L102S, U6-ch32, adc2

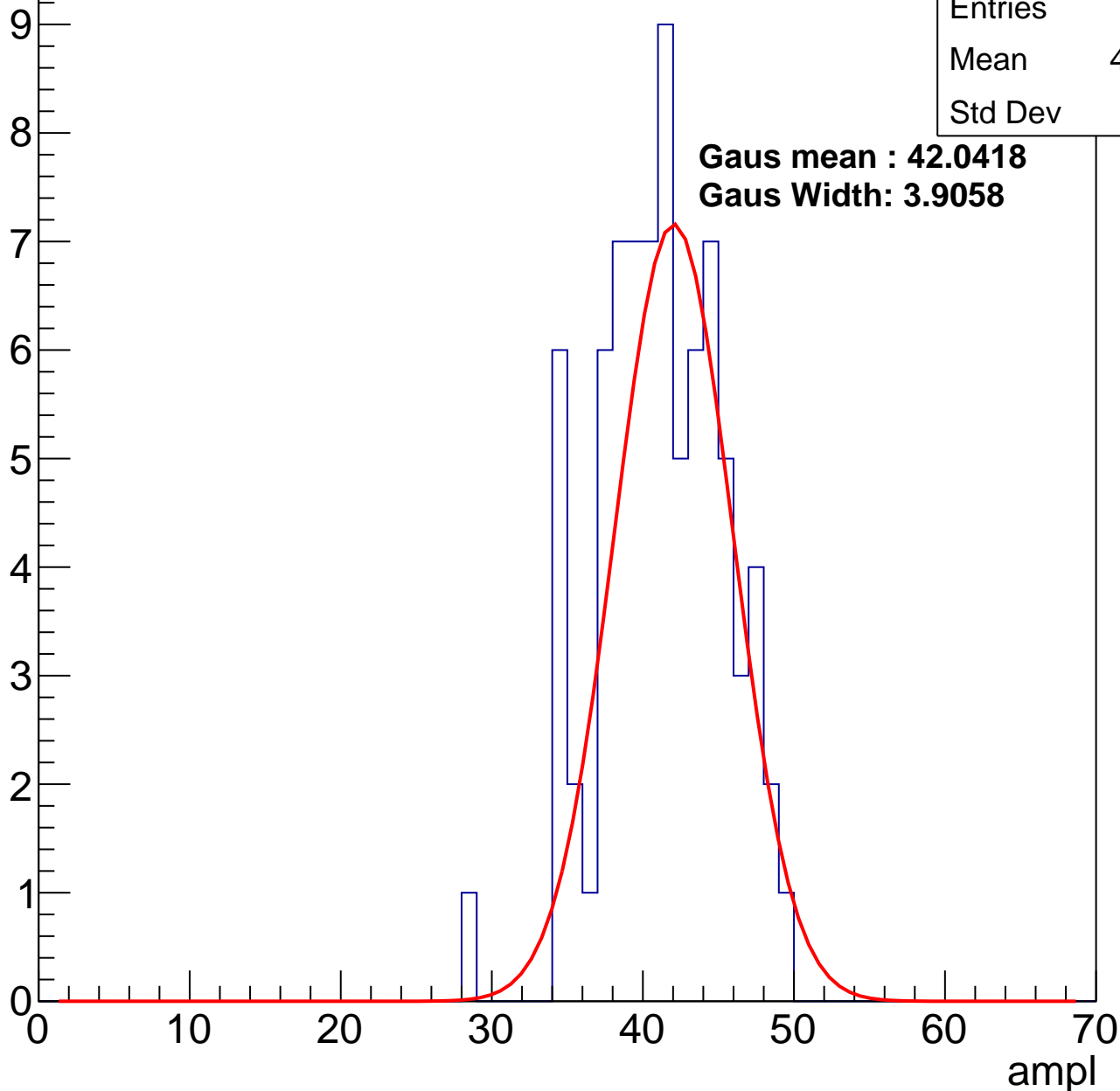
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	40.76
Std Dev	4.06

Gaus mean : 42.0418

Gaus Width: 3.9058

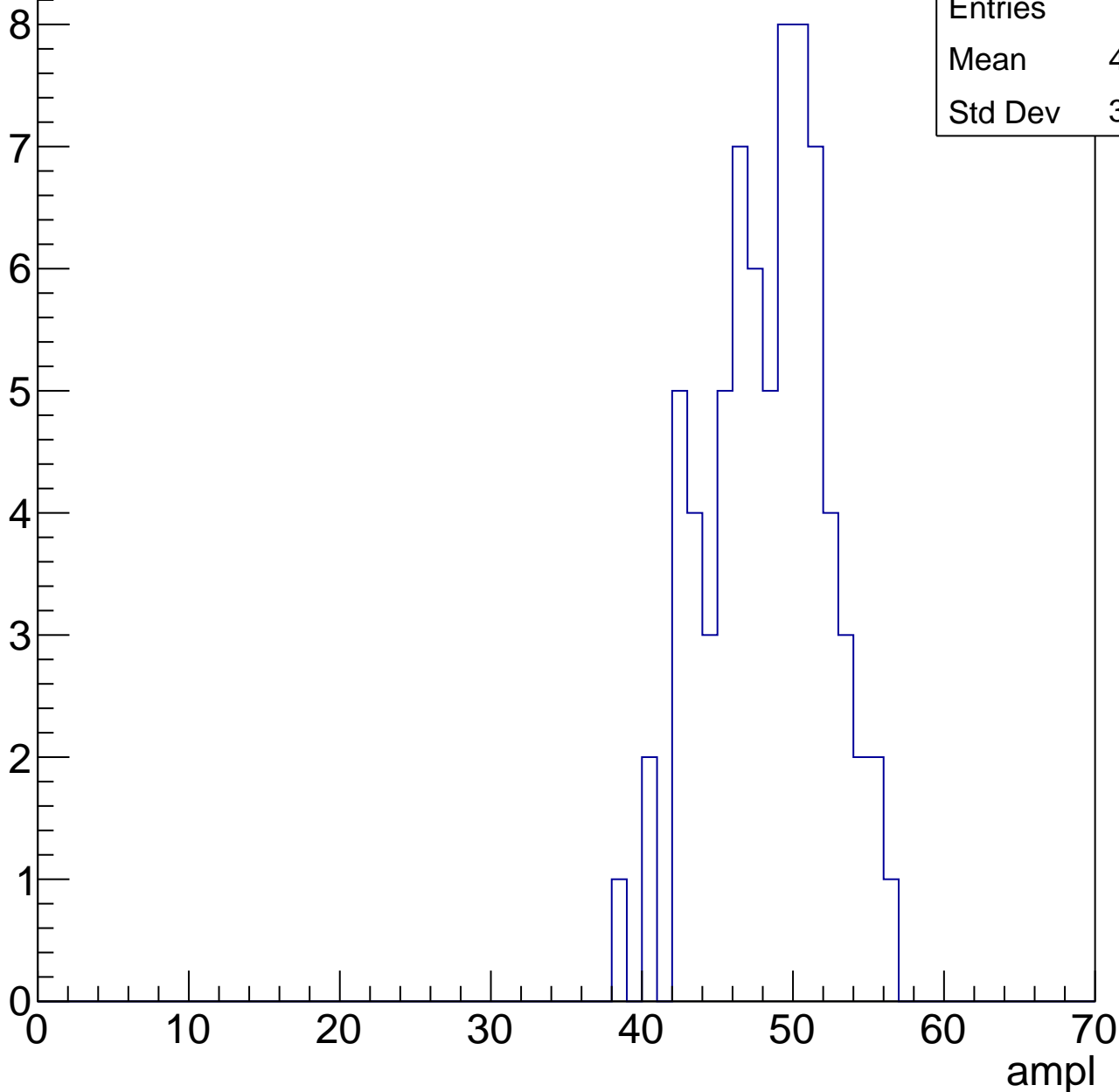


B1L102S, U6-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	47.82
Std Dev	3.897

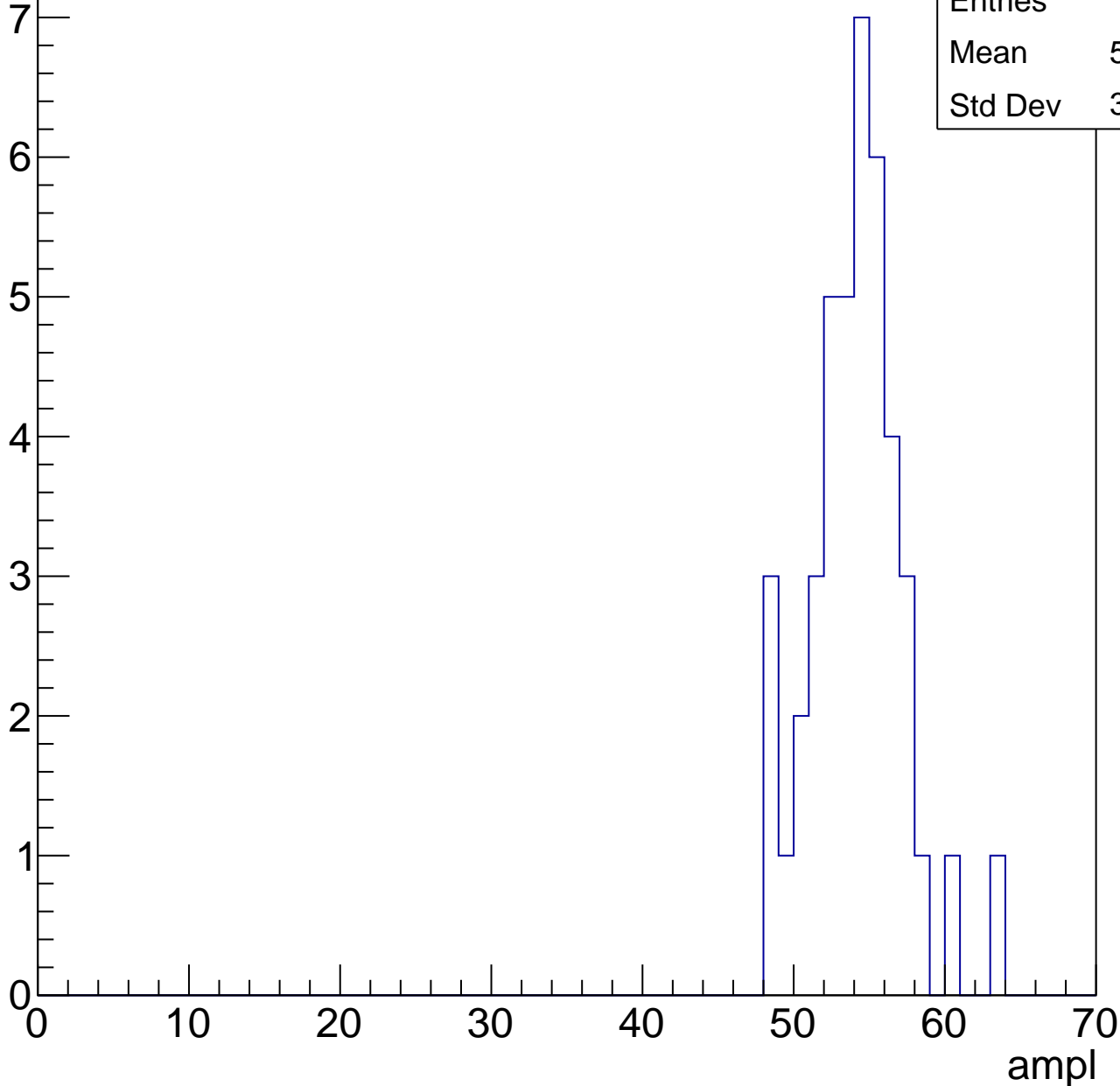


B1L102S, U6-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	53.69
Std Dev	3.067

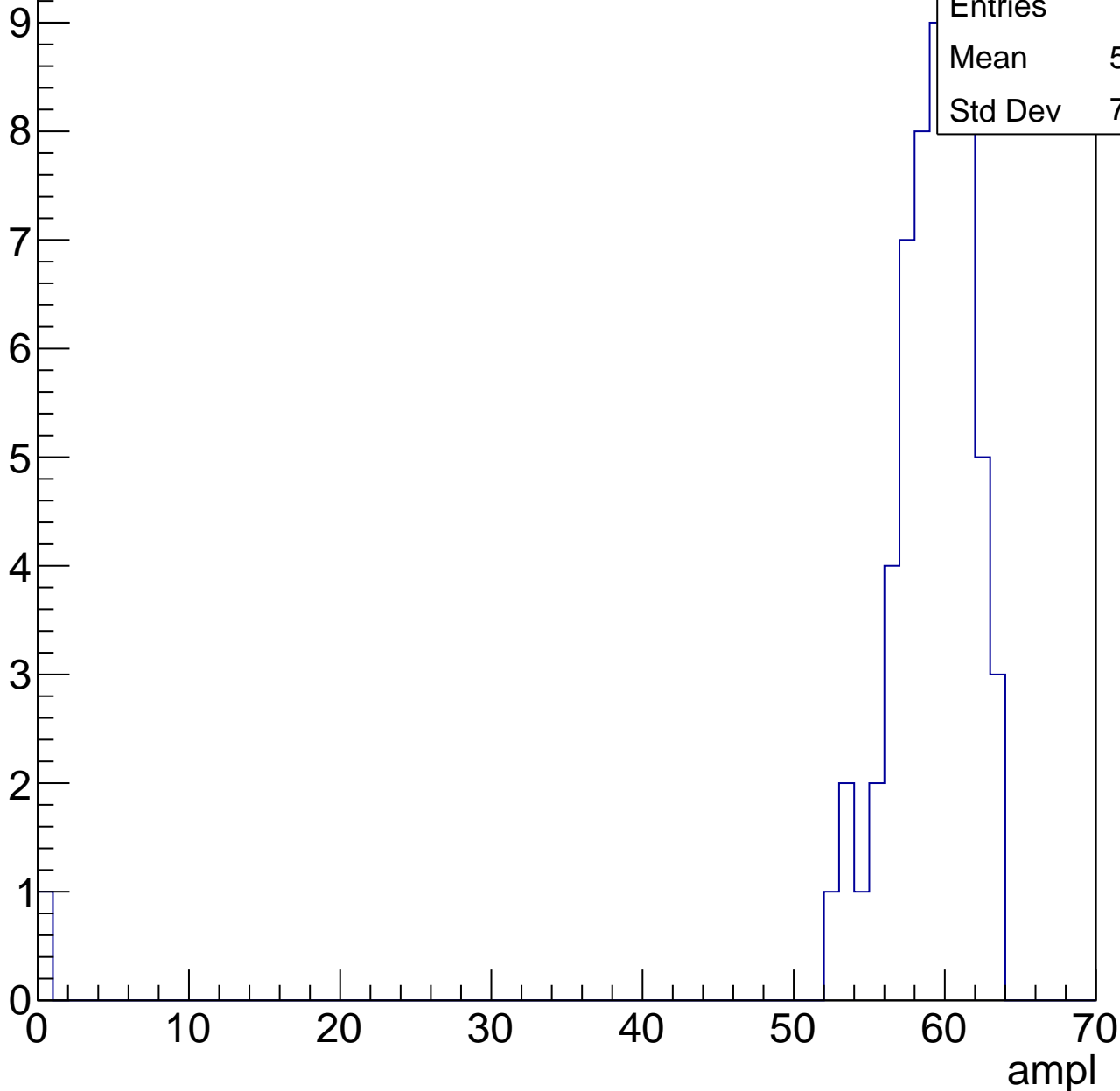


B1L102S, U6-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	57.84
Std Dev	7.879

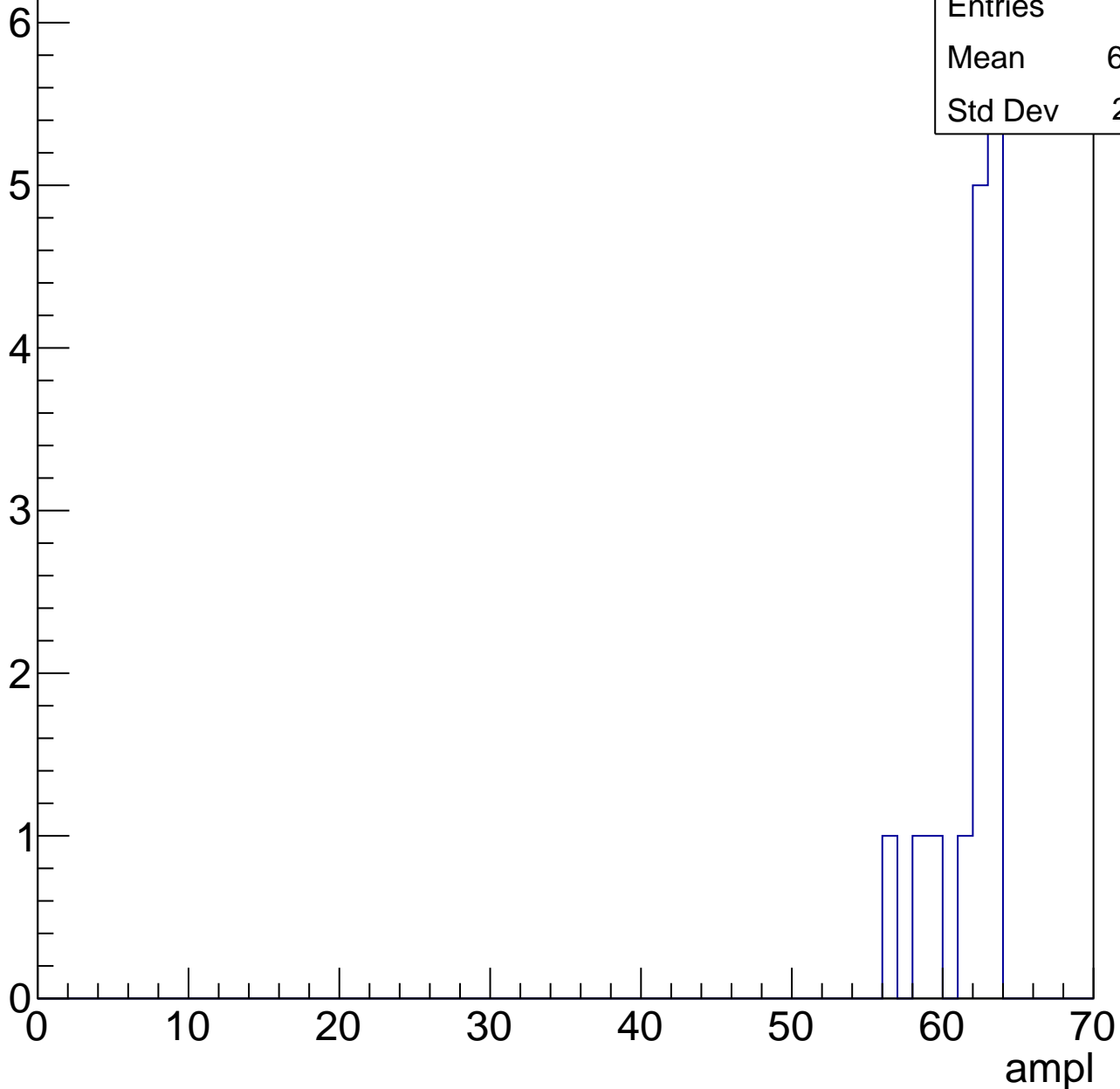


B1L102S, U6-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.47
Std Dev	2.061



B1L102S, U6-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch33, adc0

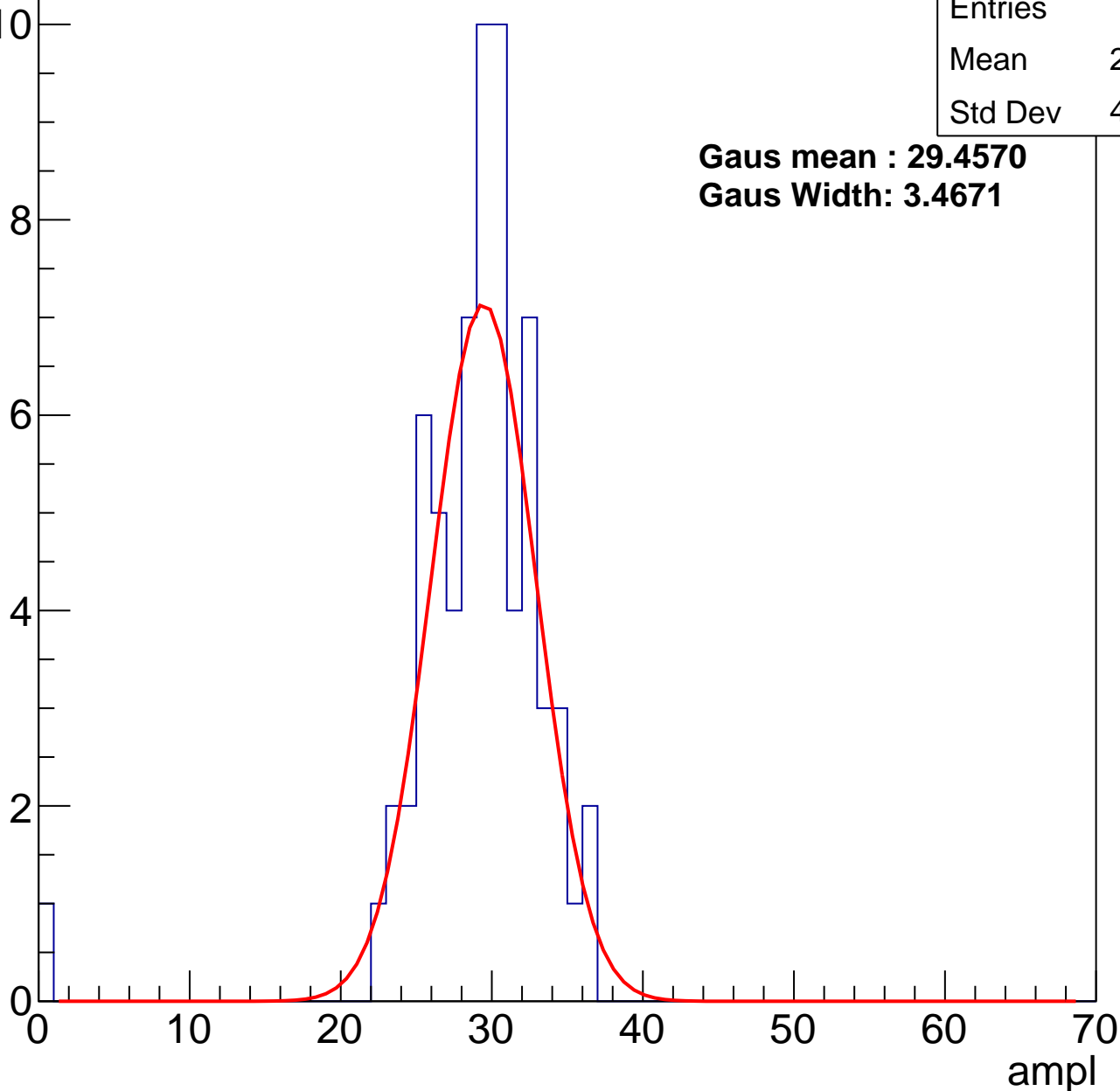
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.62
Std Dev	4.716

Gaus mean : 29.4570

Gaus Width: 3.4671



B1L102S, U6-ch33, adc1

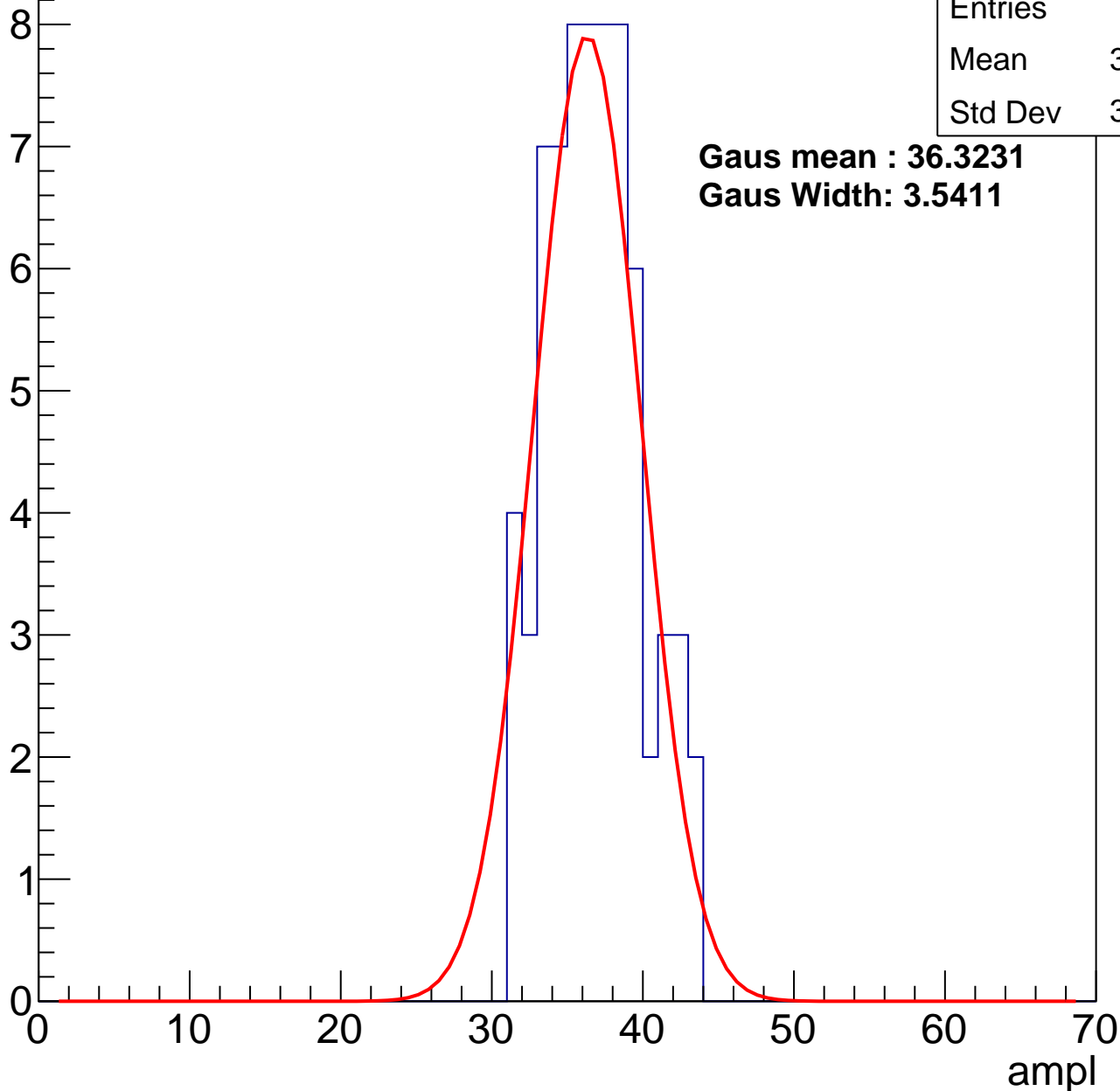
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.32
Std Dev	3.062

Gaus mean : 36.3231

Gaus Width: 3.5411



B1L102S, U6-ch33, adc2

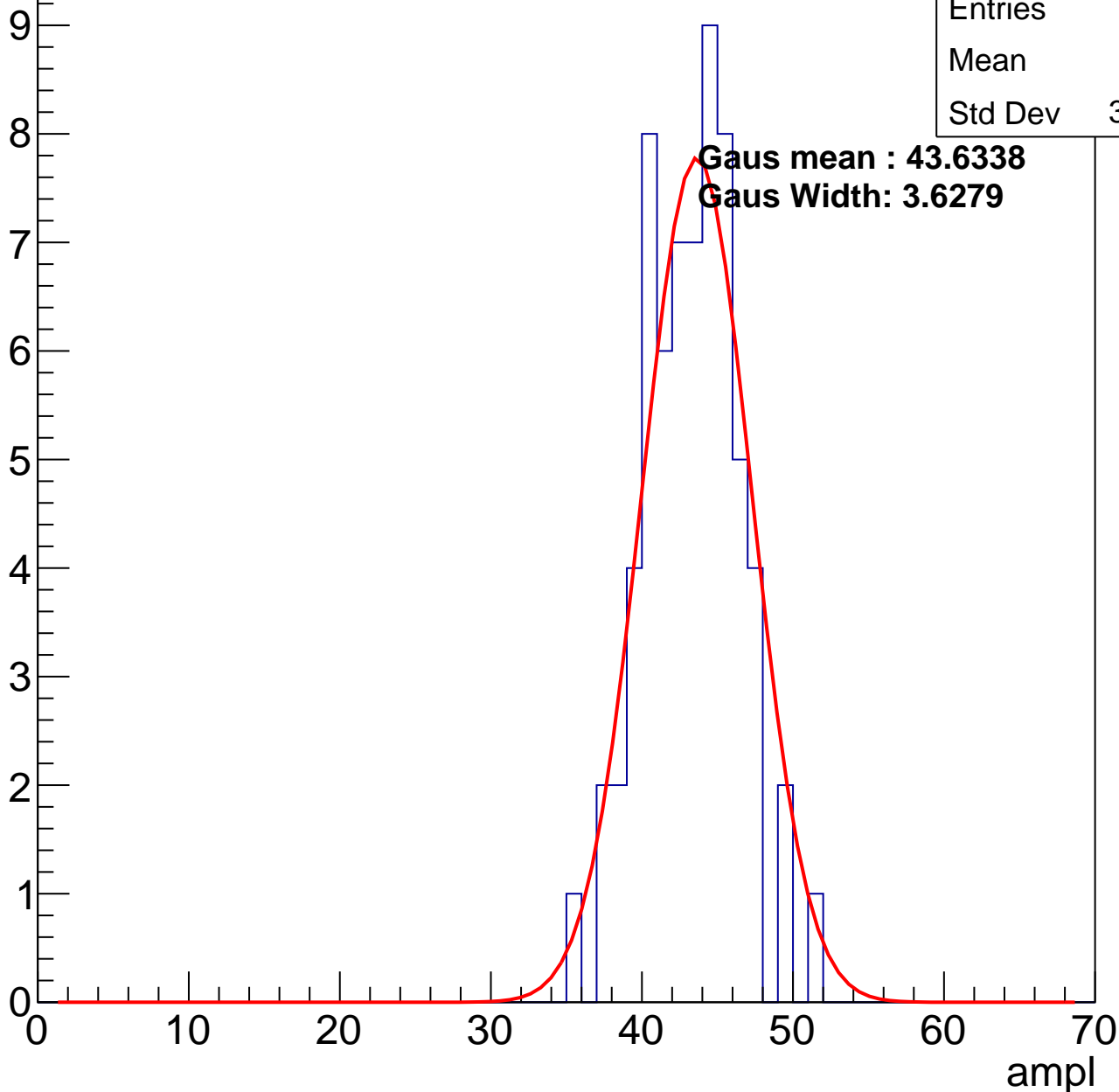
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.8
Std Dev	3.105

Gaus mean : 43.6338

Gaus Width: 3.6279

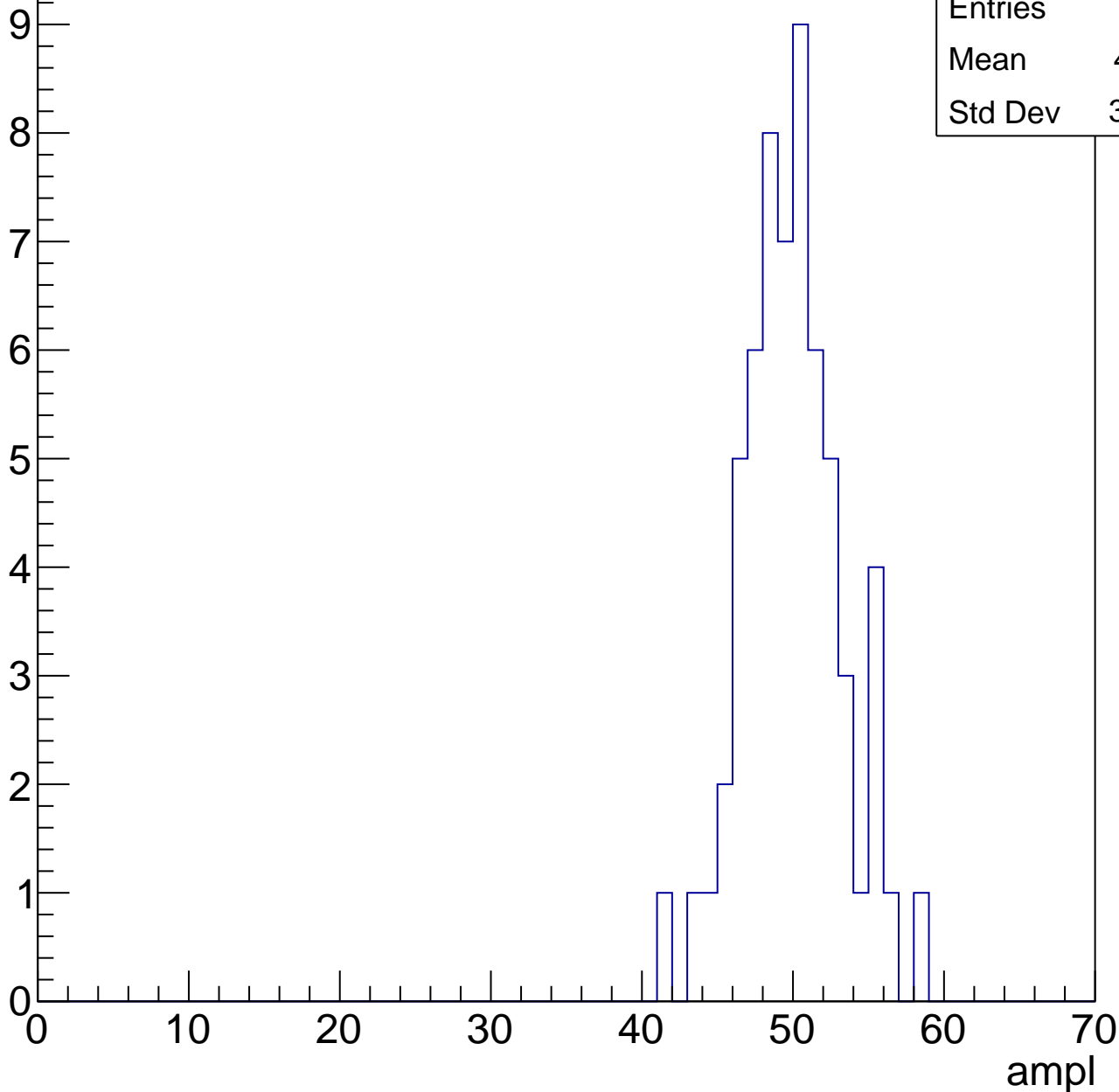


B1L102S, U6-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.51
Std Dev	3.247

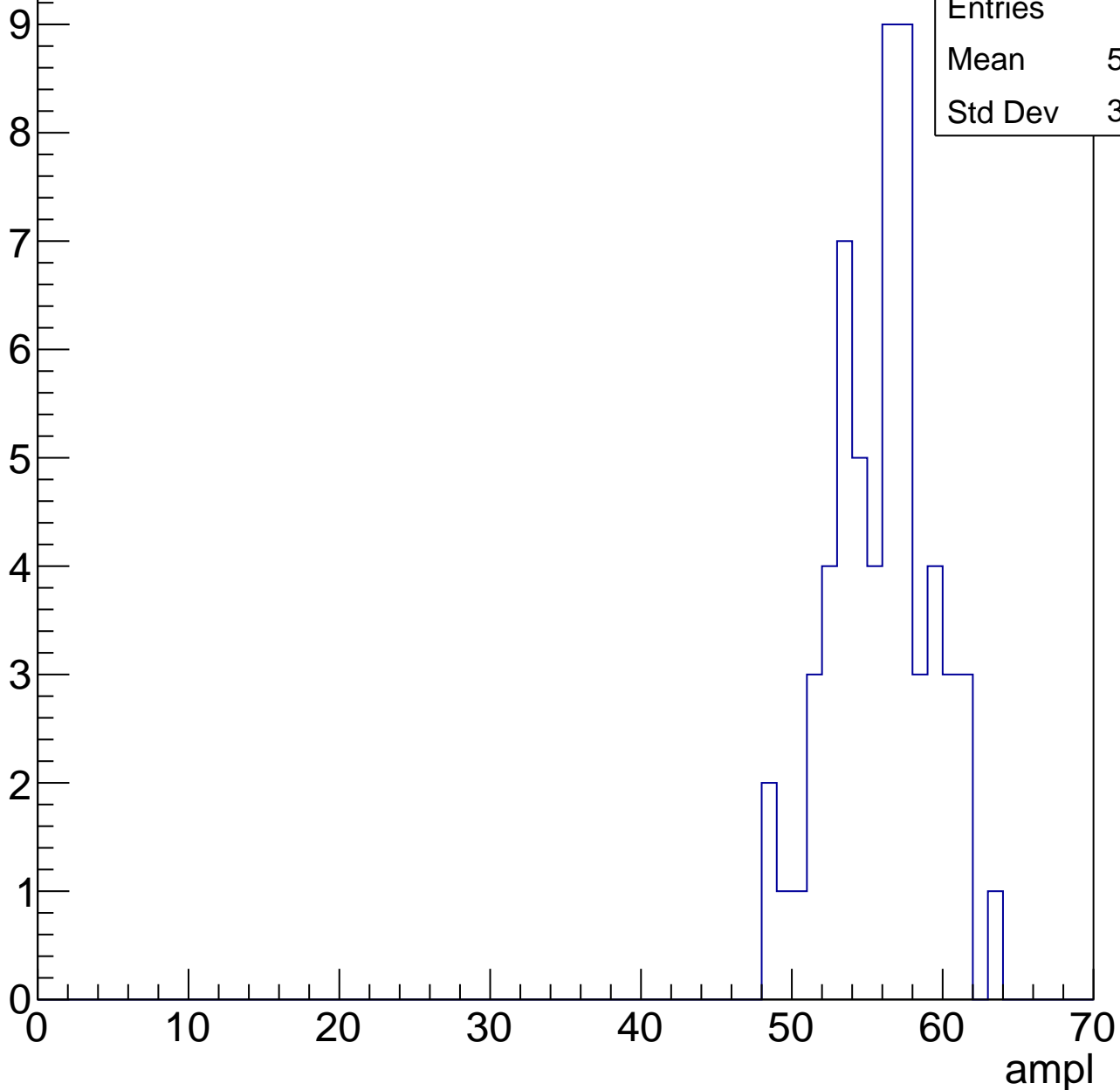


B1L102S, U6-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	55.42
Std Dev	3.315

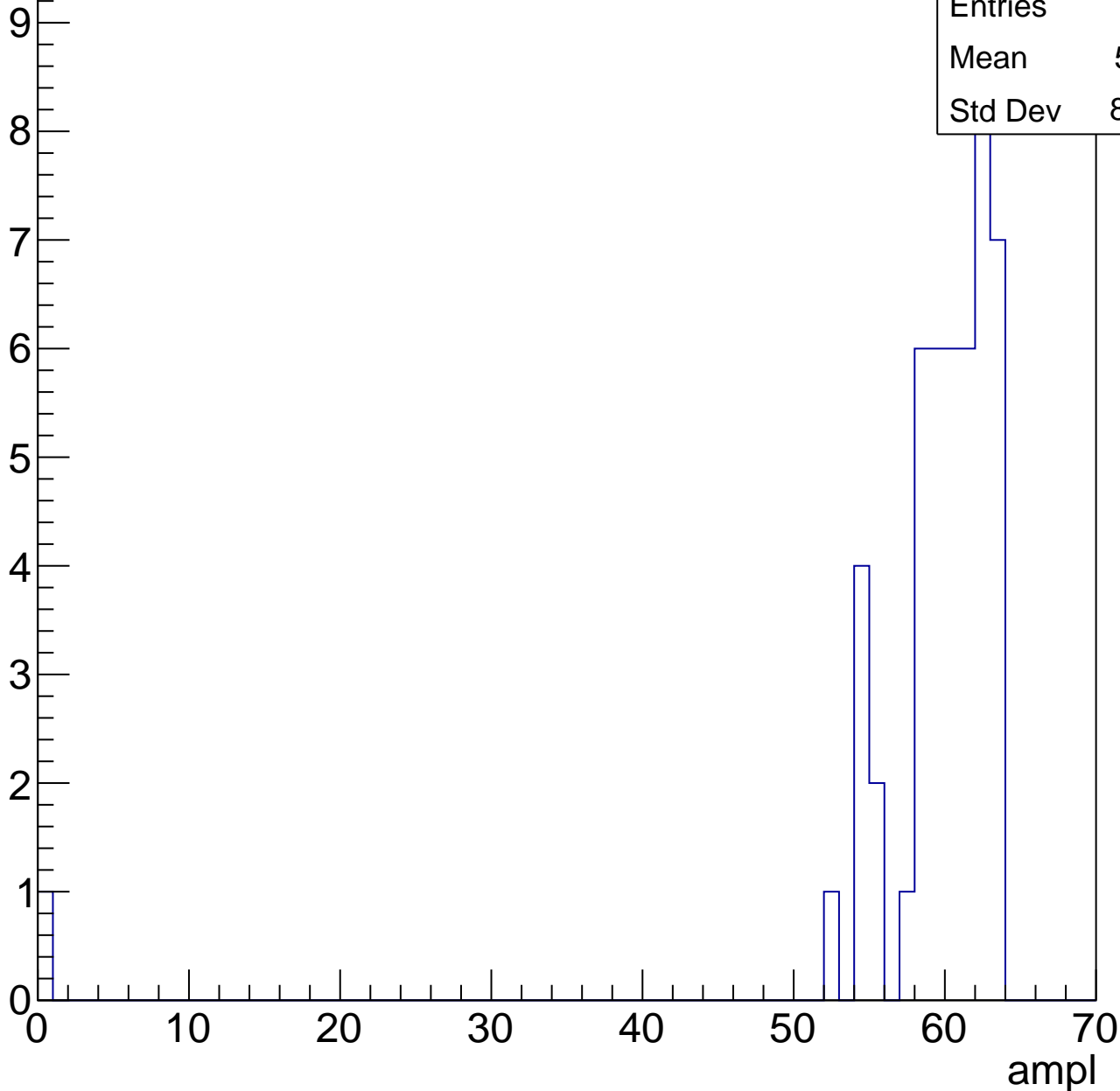


B1L102S, U6-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.41
Std Dev	8.896



B1L102S, U6-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U6-ch34, adc0

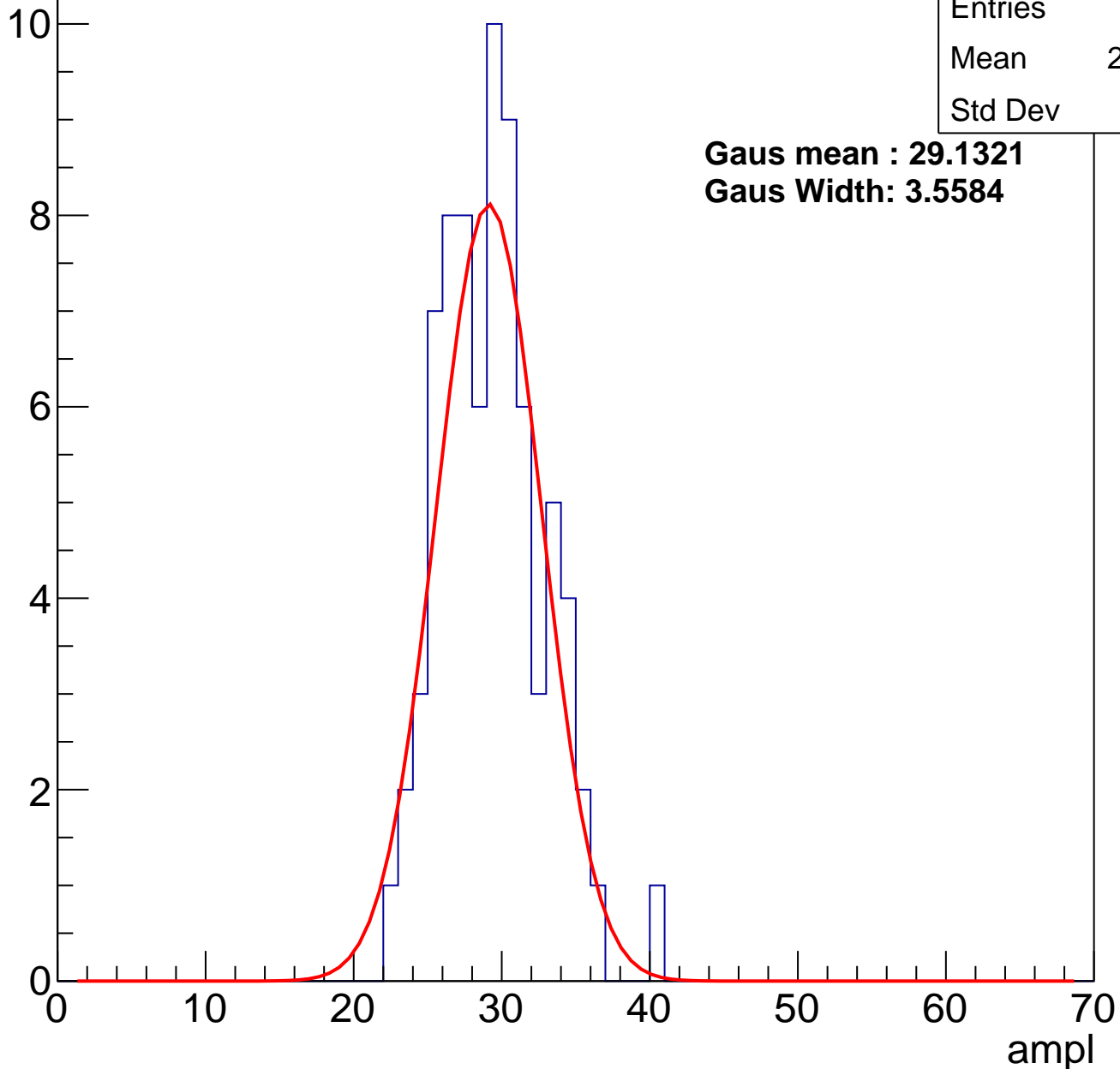
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	28.89
Std Dev	3.44

Gaus mean : 29.1321

Gaus Width: 3.5584

Entry



B1L102S, U6-ch34, adc1

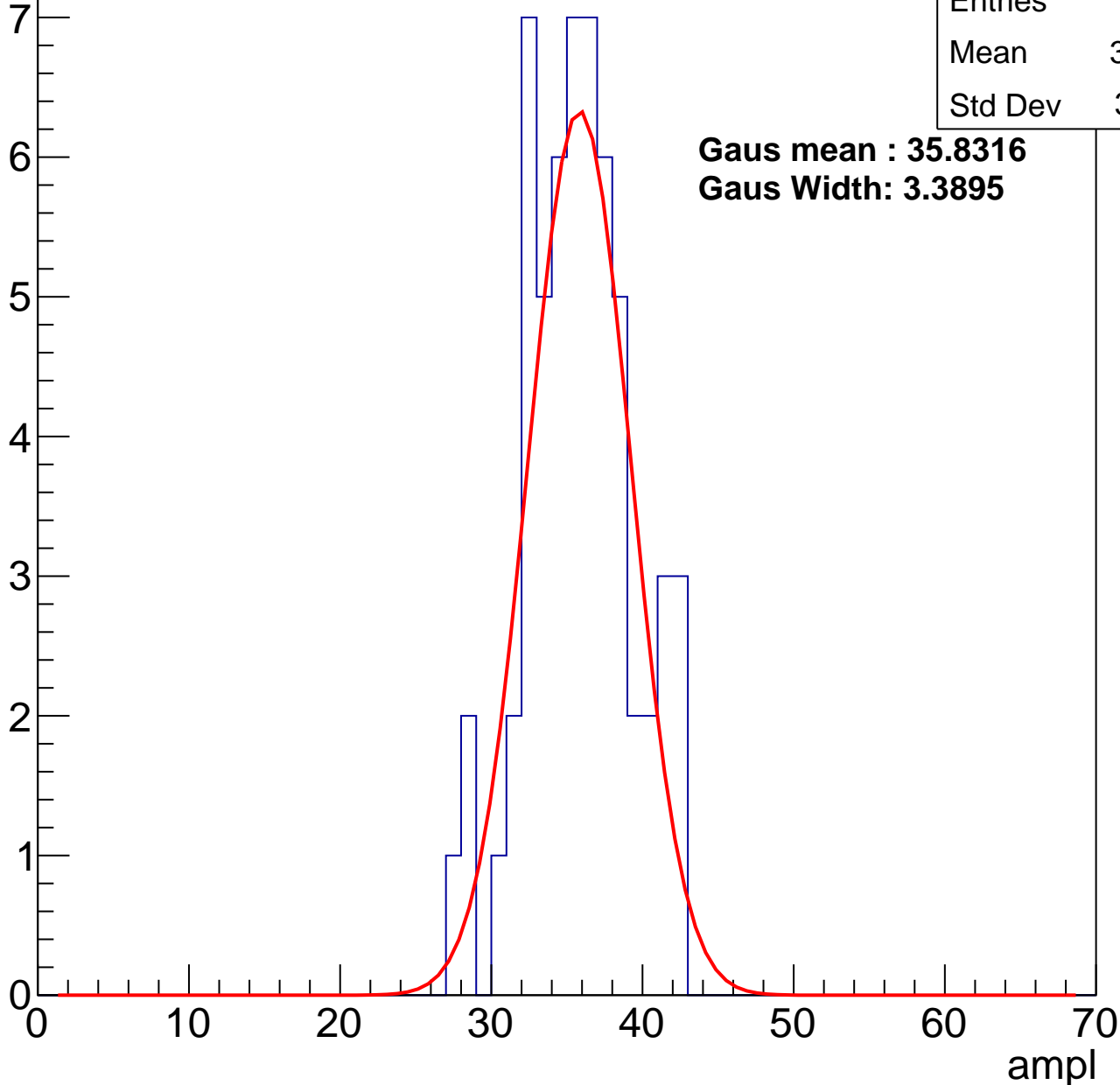
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.32
Std Dev	3.481

Gaus mean : 35.8316

Gaus Width: 3.3895

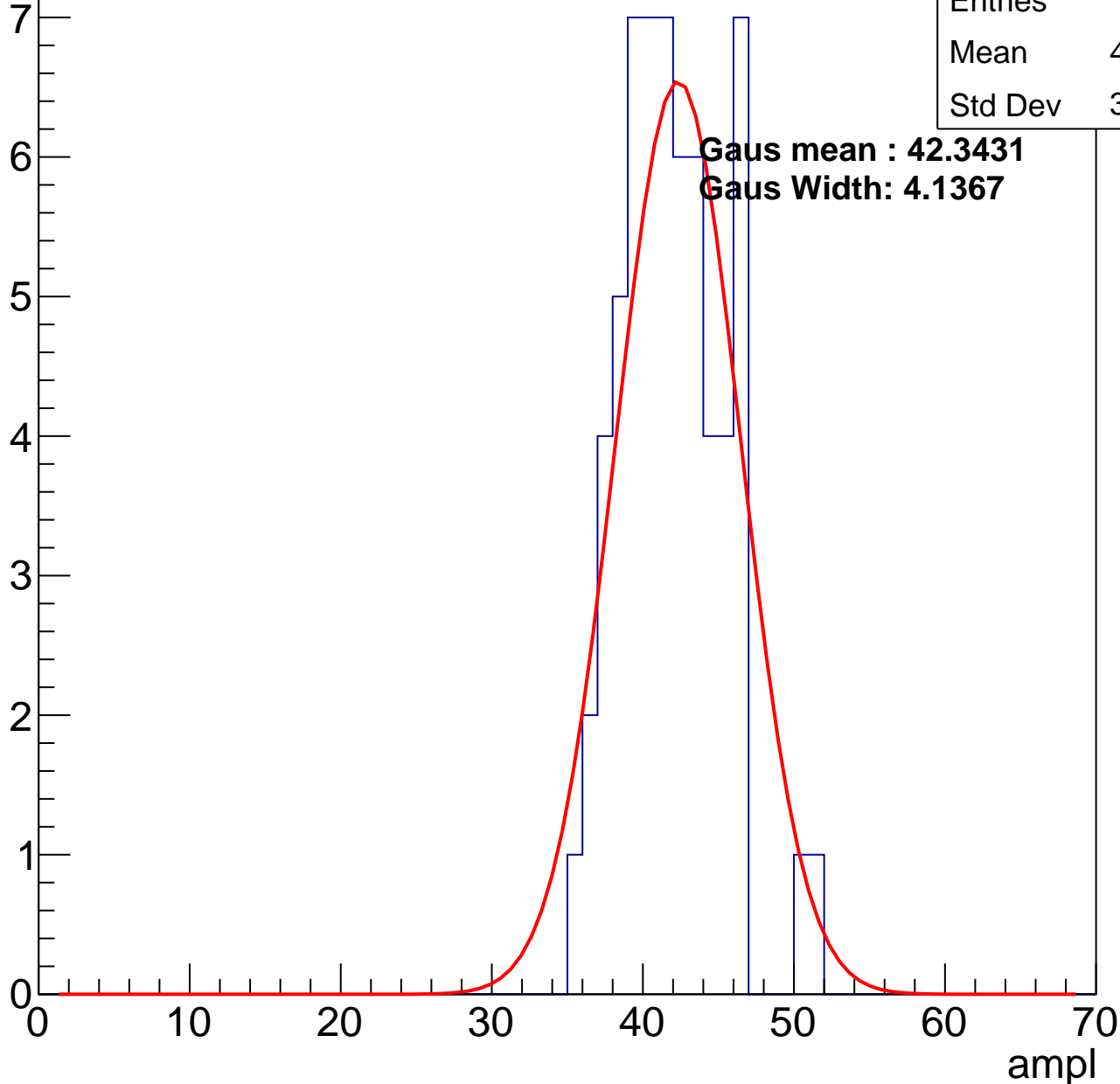


B1L102S, U6-ch34, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.52
Std Dev	3.359

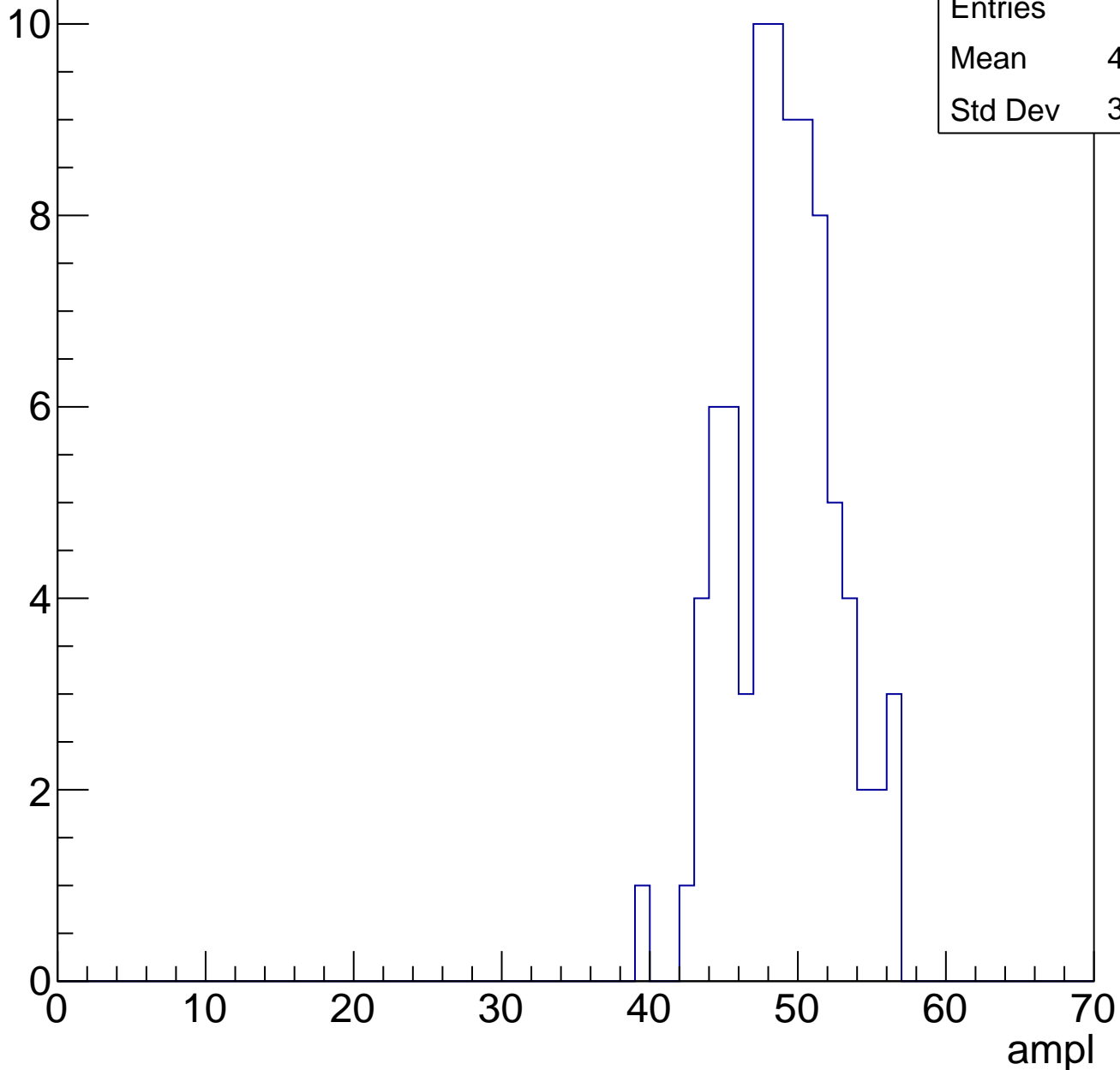


B1L102S, U6-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	83
Mean	48.58
Std Dev	3.506

Entry

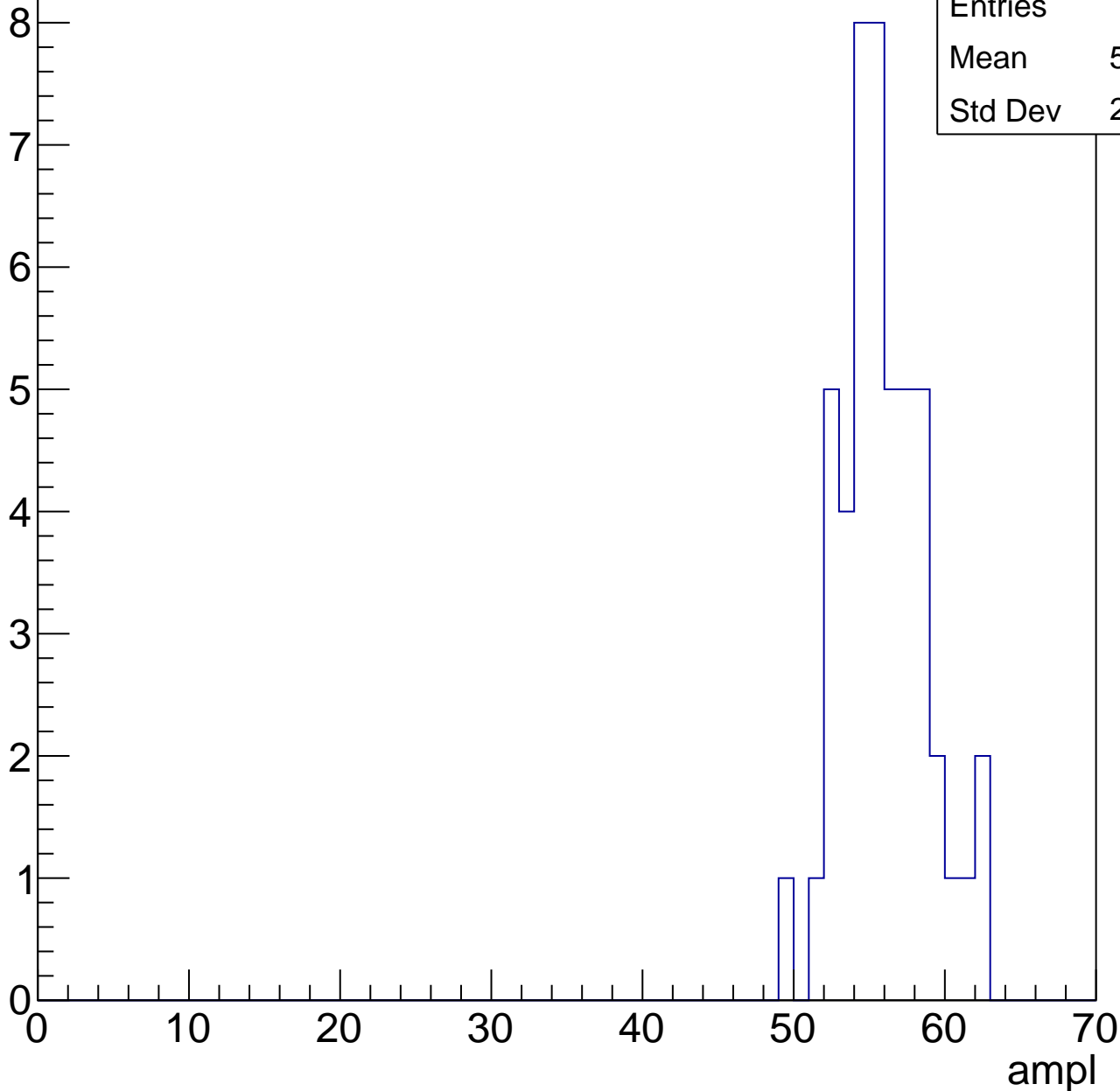


B1L102S, U6-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	55.46
Std Dev	2.784

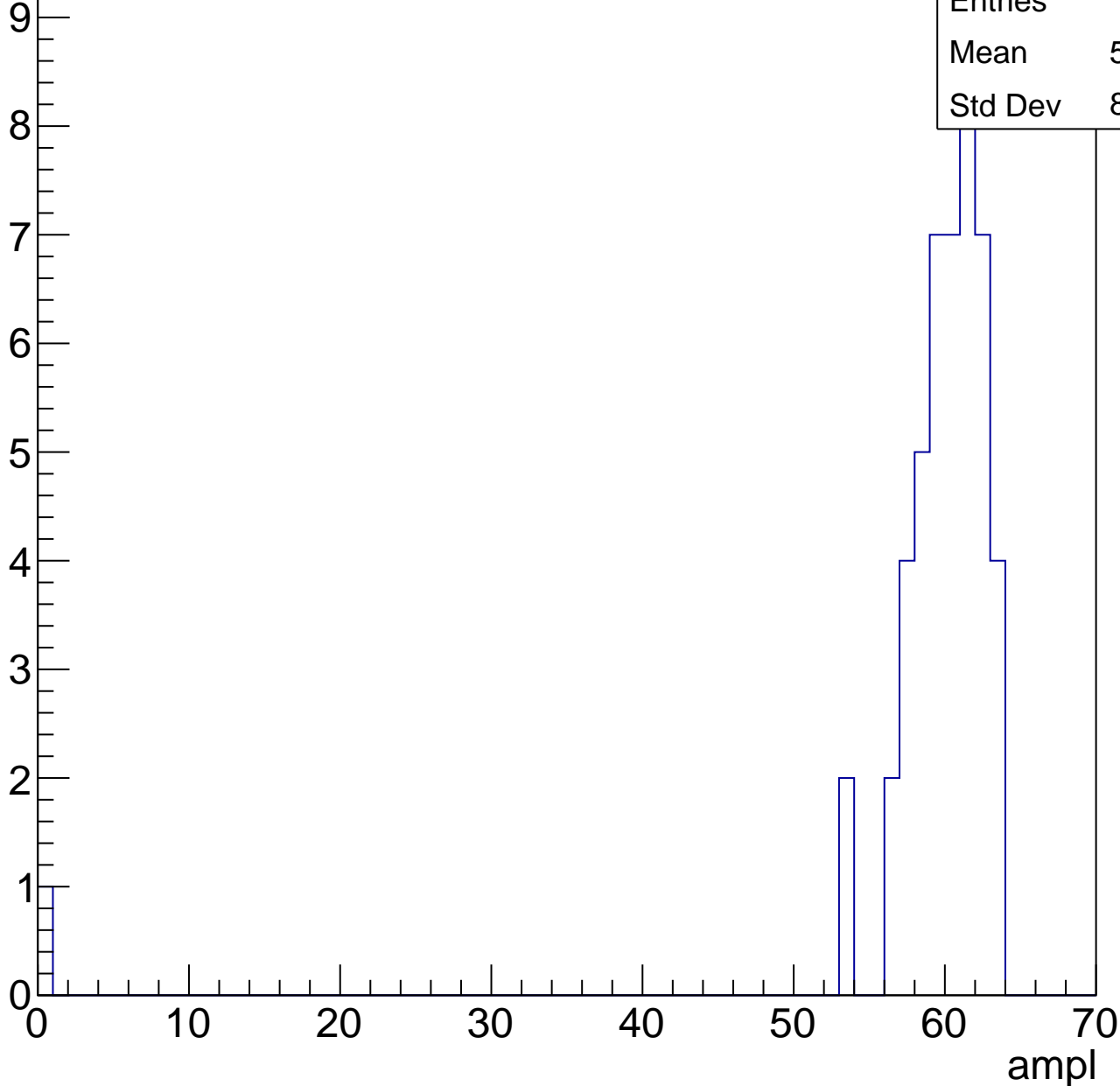


B1L102S, U6-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.42
Std Dev	8.834

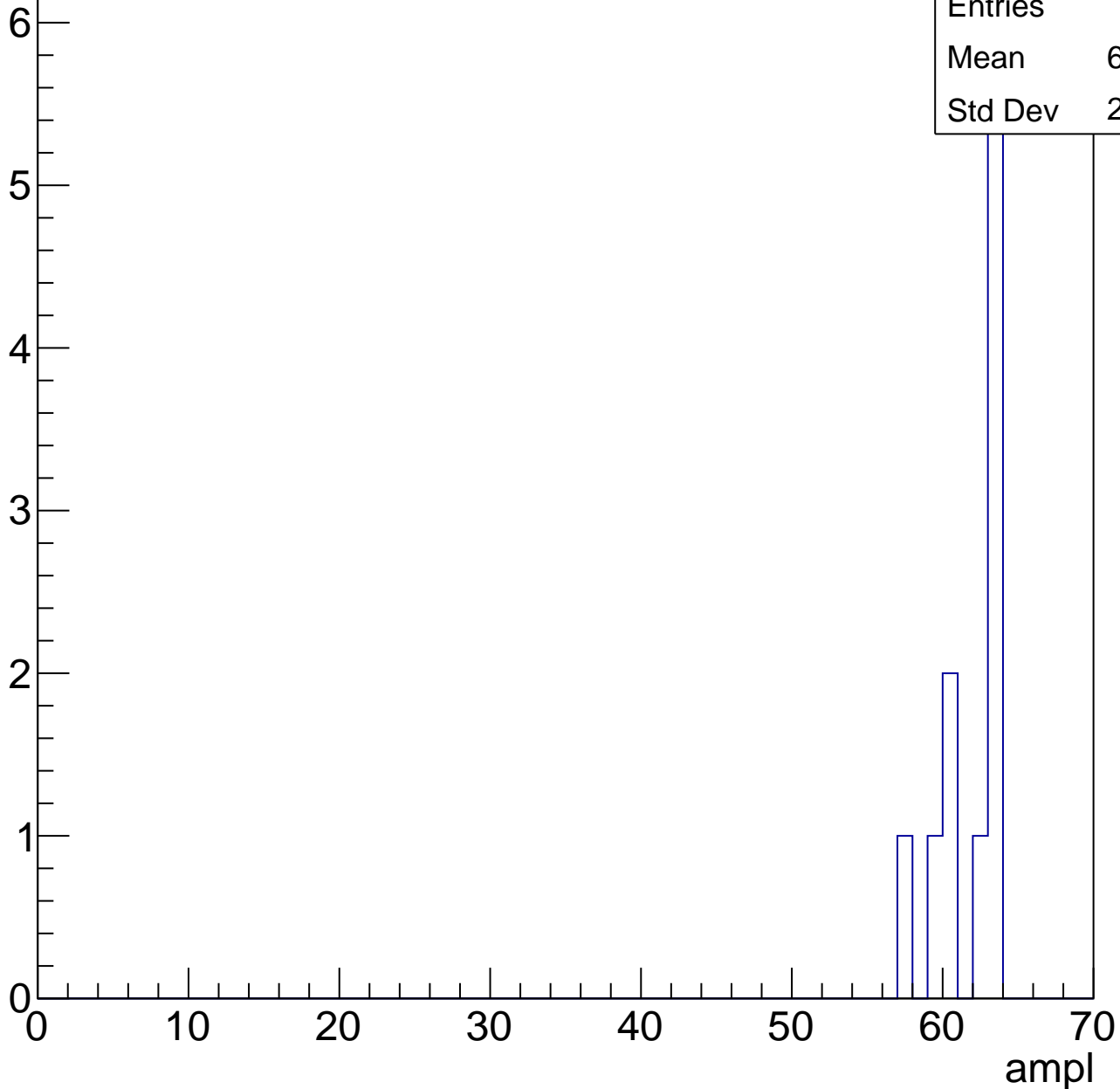


B1L102S, U6-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.45
Std Dev	2.016



B1L102S, U6-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch35, adc0

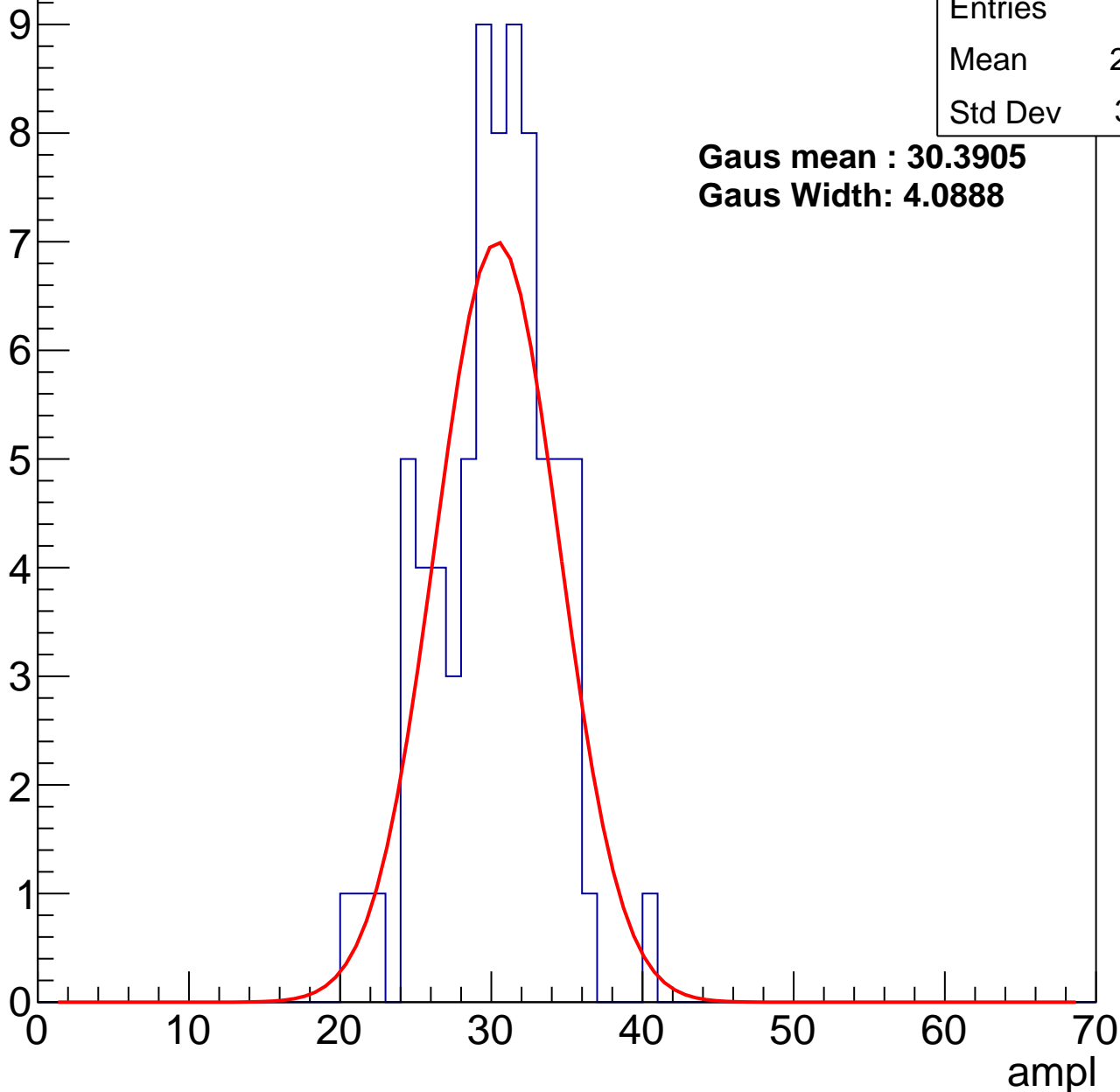
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.73
Std Dev	3.771

Gaus mean : 30.3905

Gaus Width: 4.0888



B1L102S, U6-ch35, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	60
Mean	36.57
Std Dev	3.456

Gaus mean : 37.3964

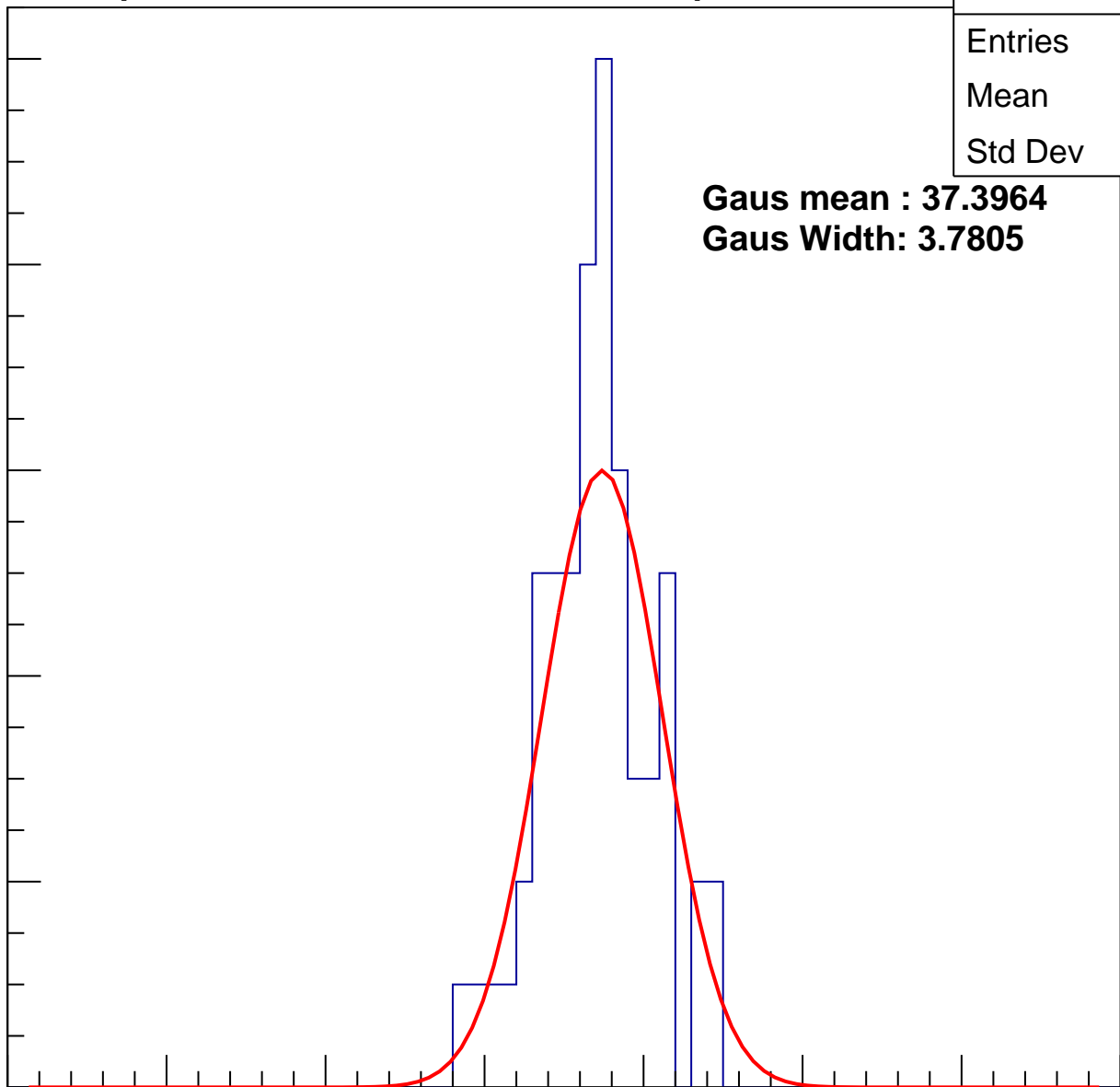
Gaus Width: 3.7805

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch35, adc2

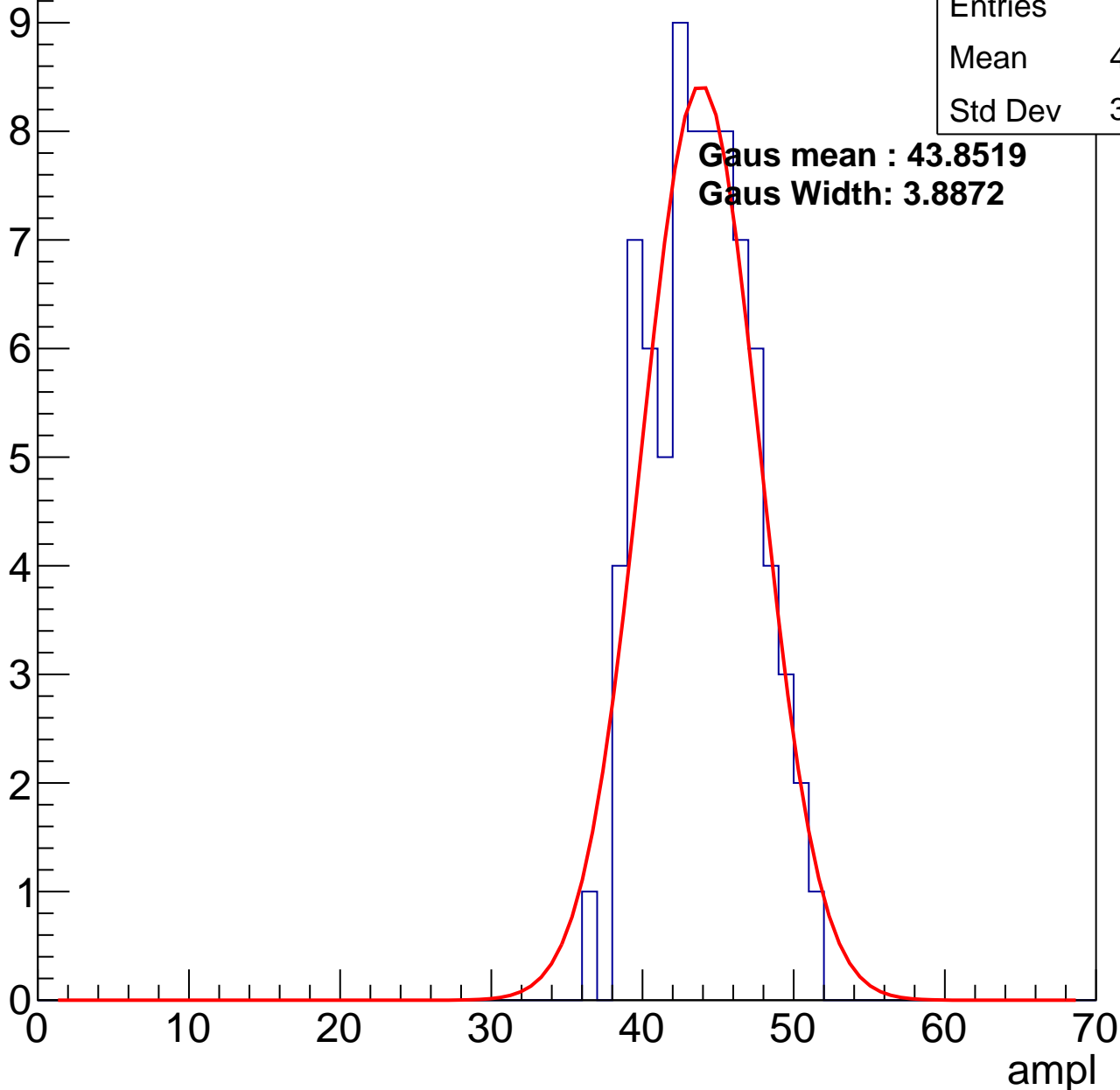
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	43.47
Std Dev	3.367

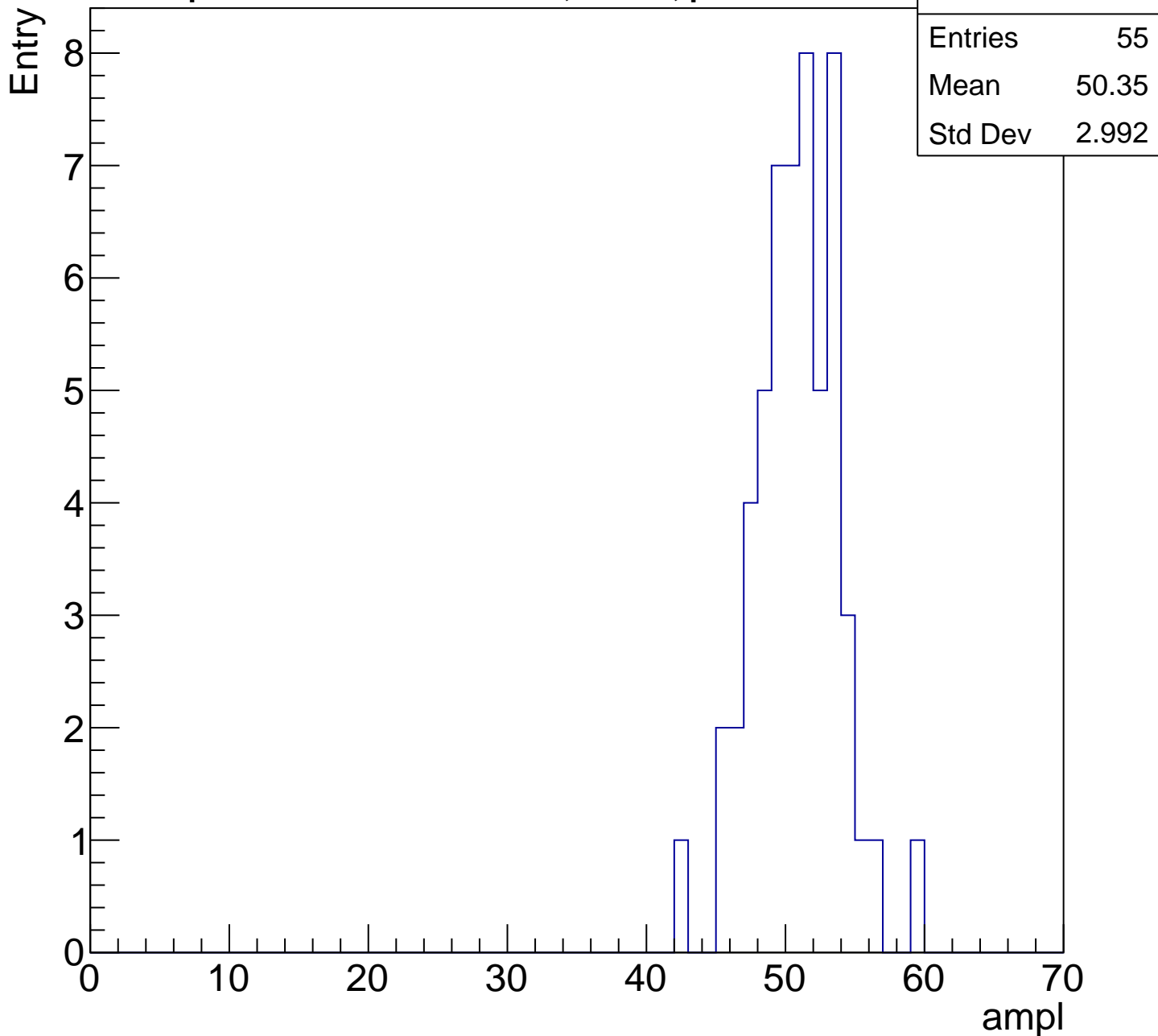
Gaus mean : 43.8519

Gaus Width: 3.8872



B1L102S, U6-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

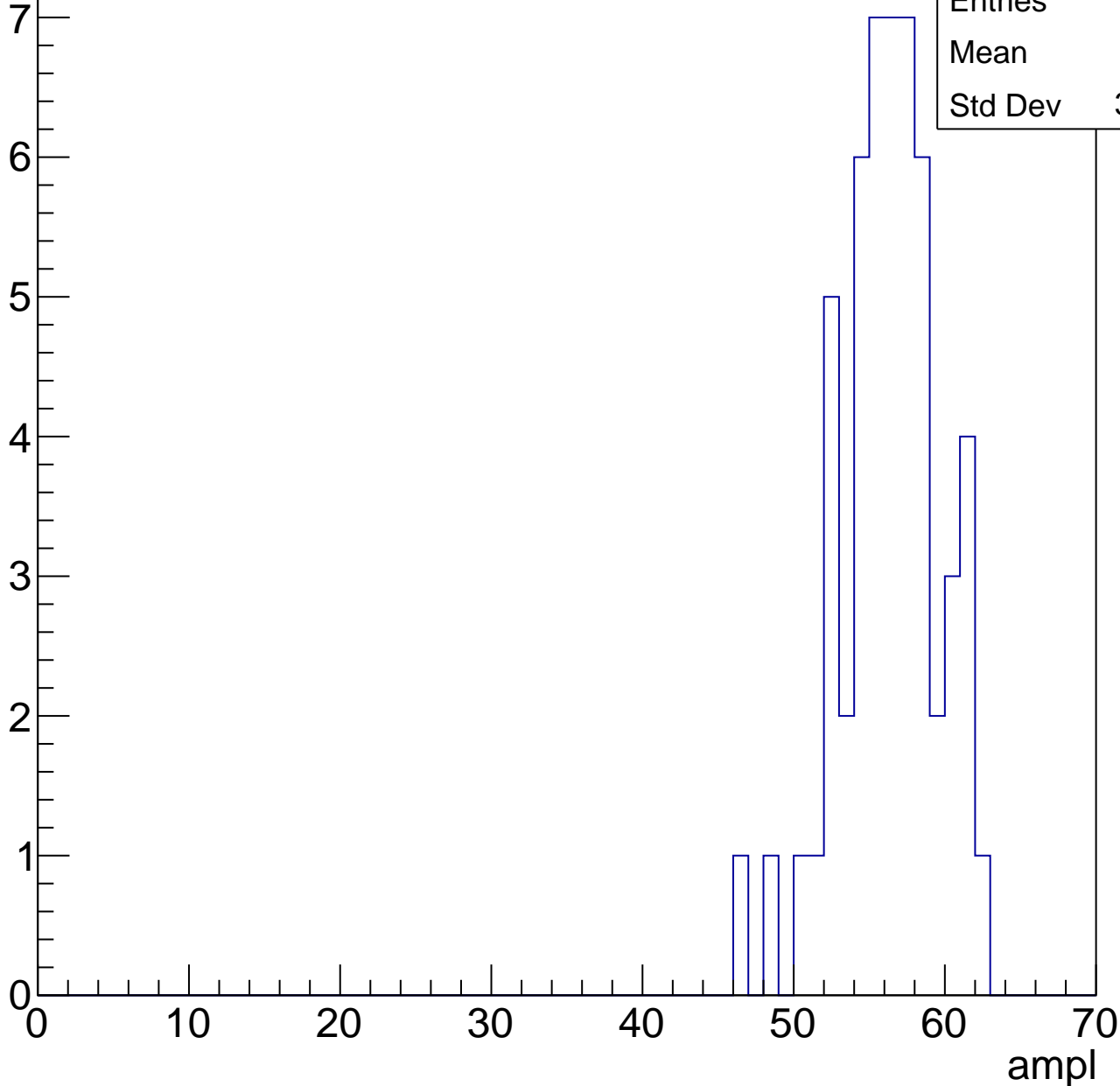


B1L102S, U6-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.8
Std Dev	3.291

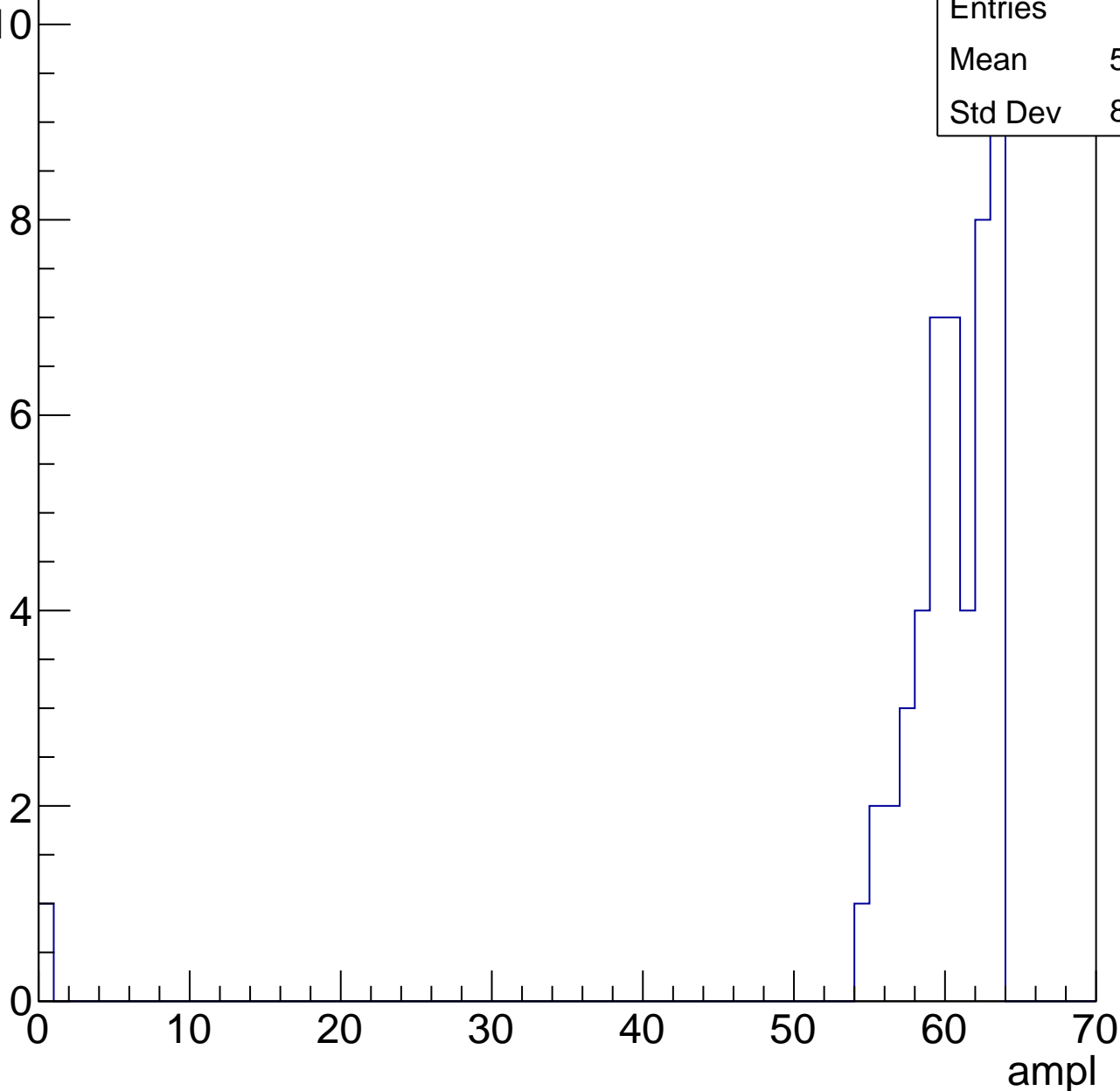


B1L102S, U6-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.82
Std Dev	8.836



B1L102S, U6-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U6-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch36, adc0

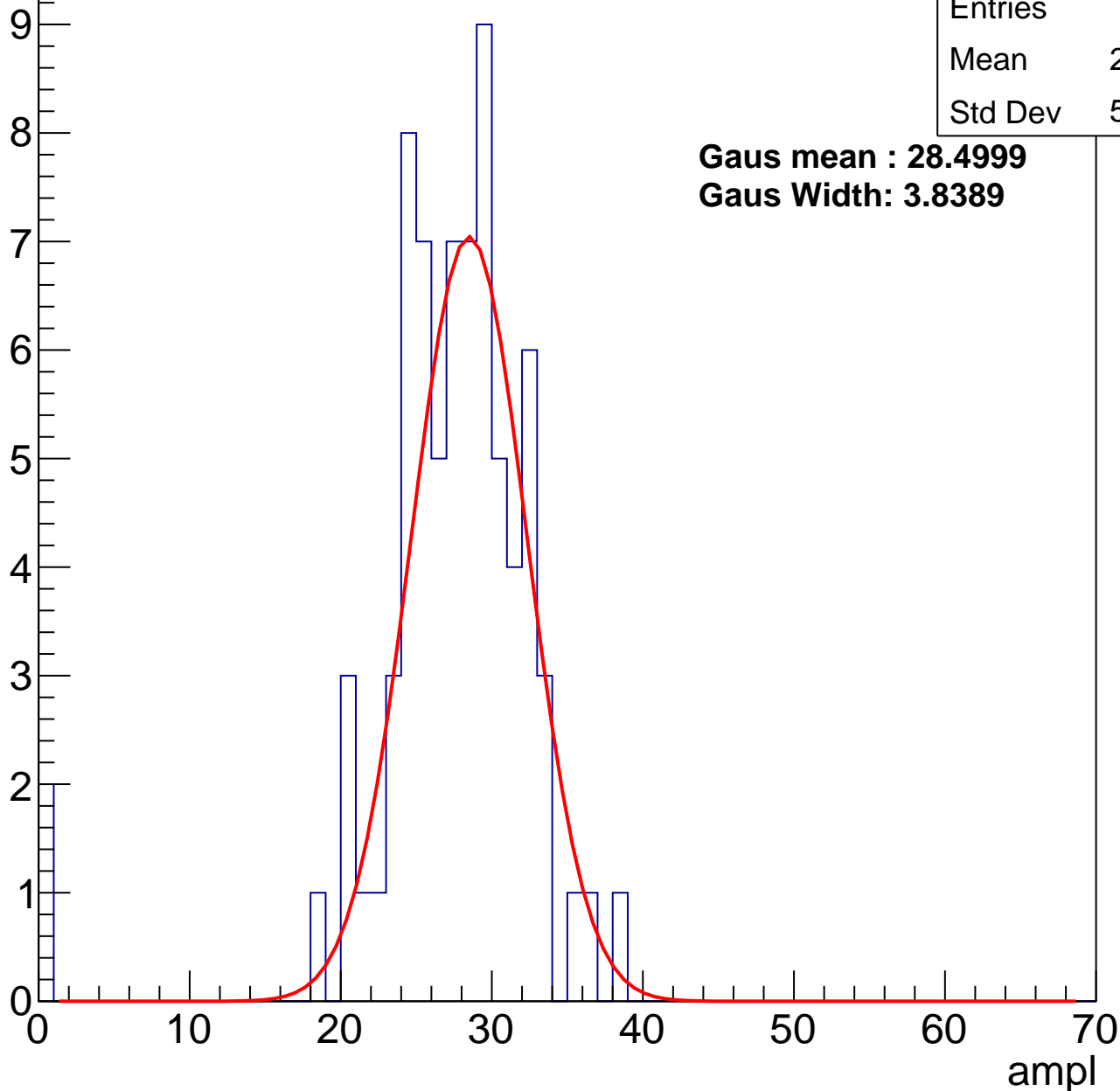
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	26.76
Std Dev	5.856

Gaus mean : 28.4999

Gaus Width: 3.8389



B1L102S, U6-ch36, adc1

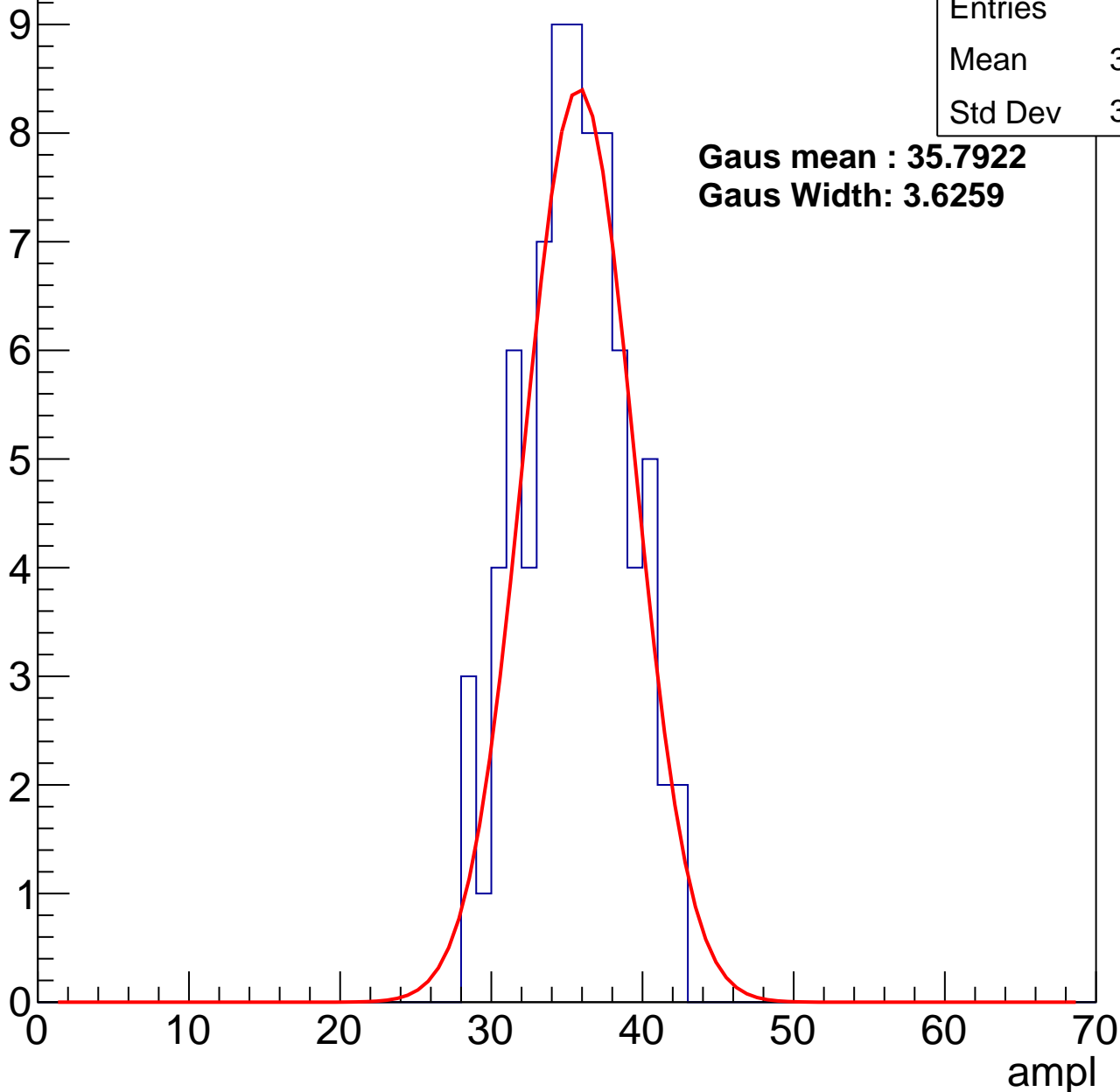
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.04
Std Dev	3.406

Gaus mean : 35.7922

Gaus Width: 3.6259



B1L102S, U6-ch36, adc2

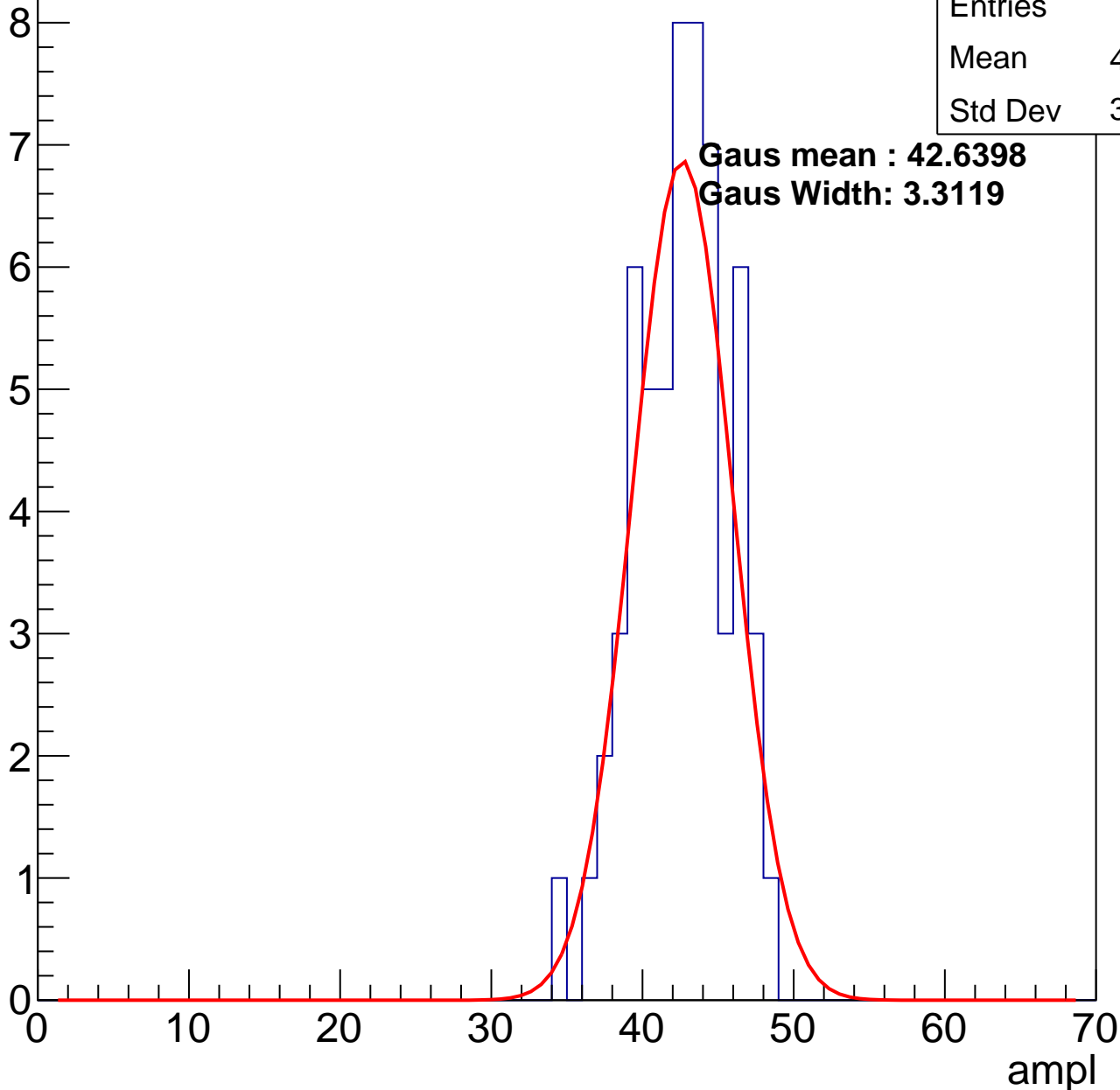
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.12
Std Dev	3.043

Gaus mean : 42.6398

Gaus Width: 3.3119

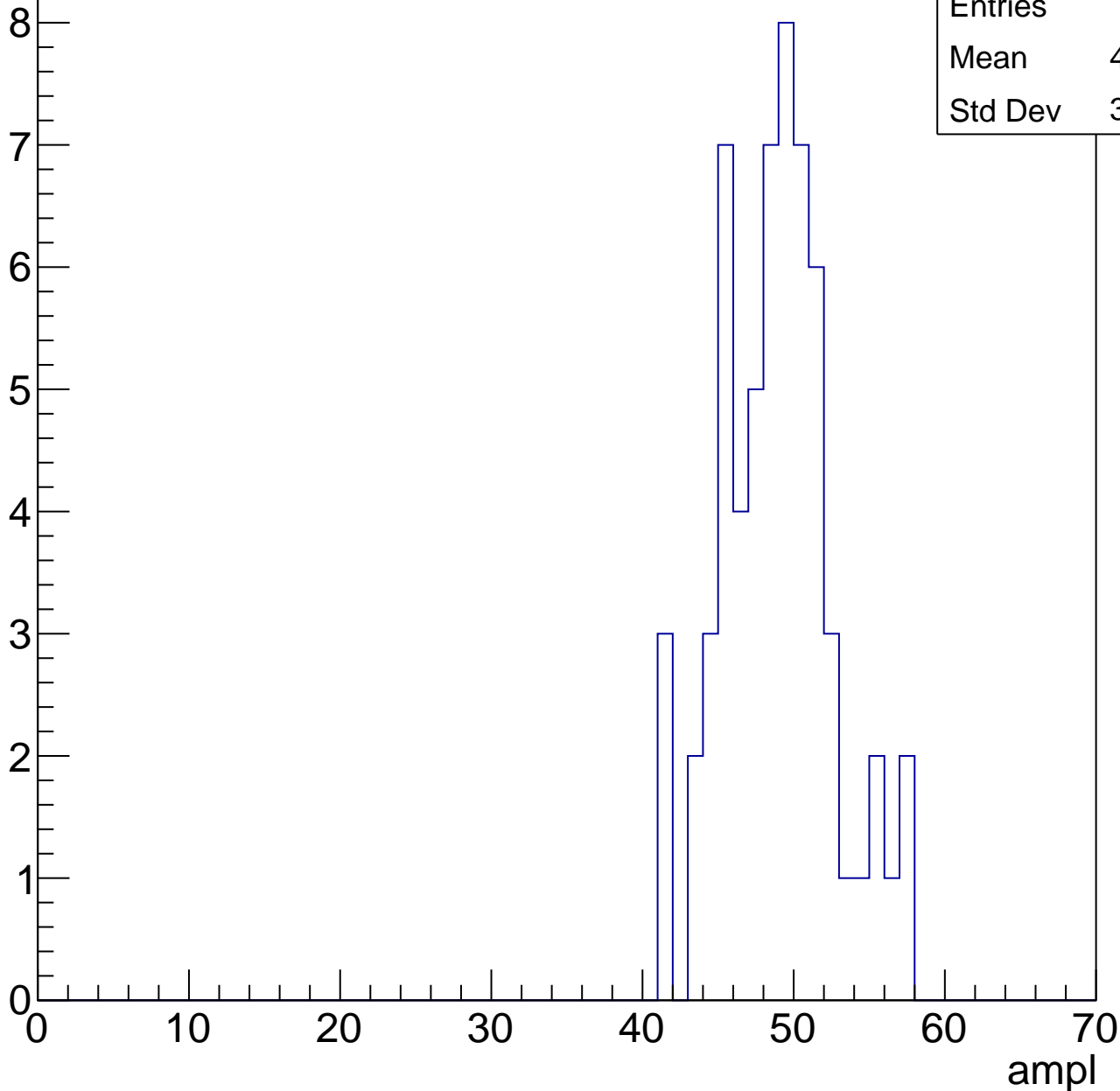


B1L102S, U6-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.42
Std Dev	3.683

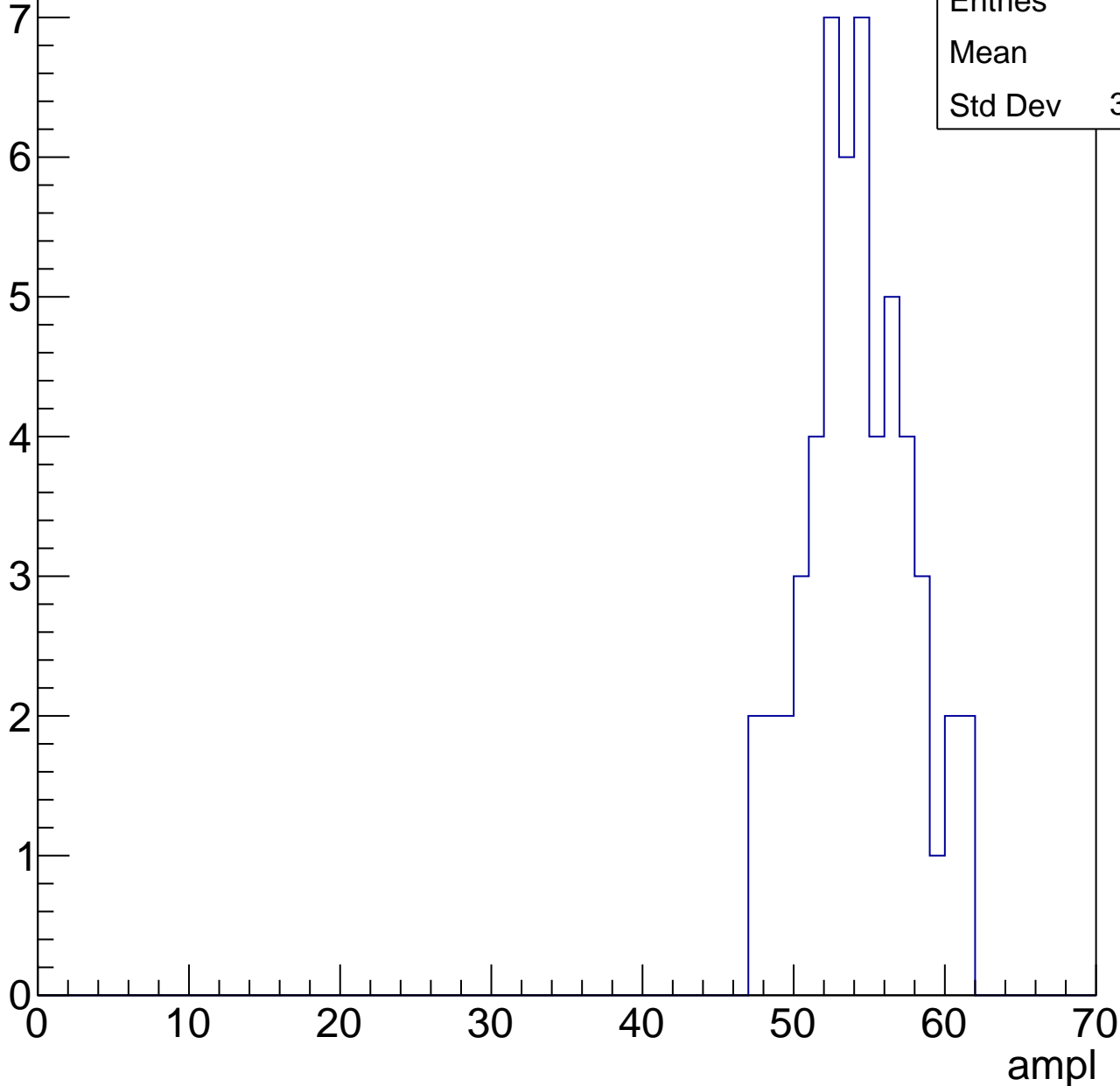


B1L102S, U6-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	53.8
Std Dev	3.439

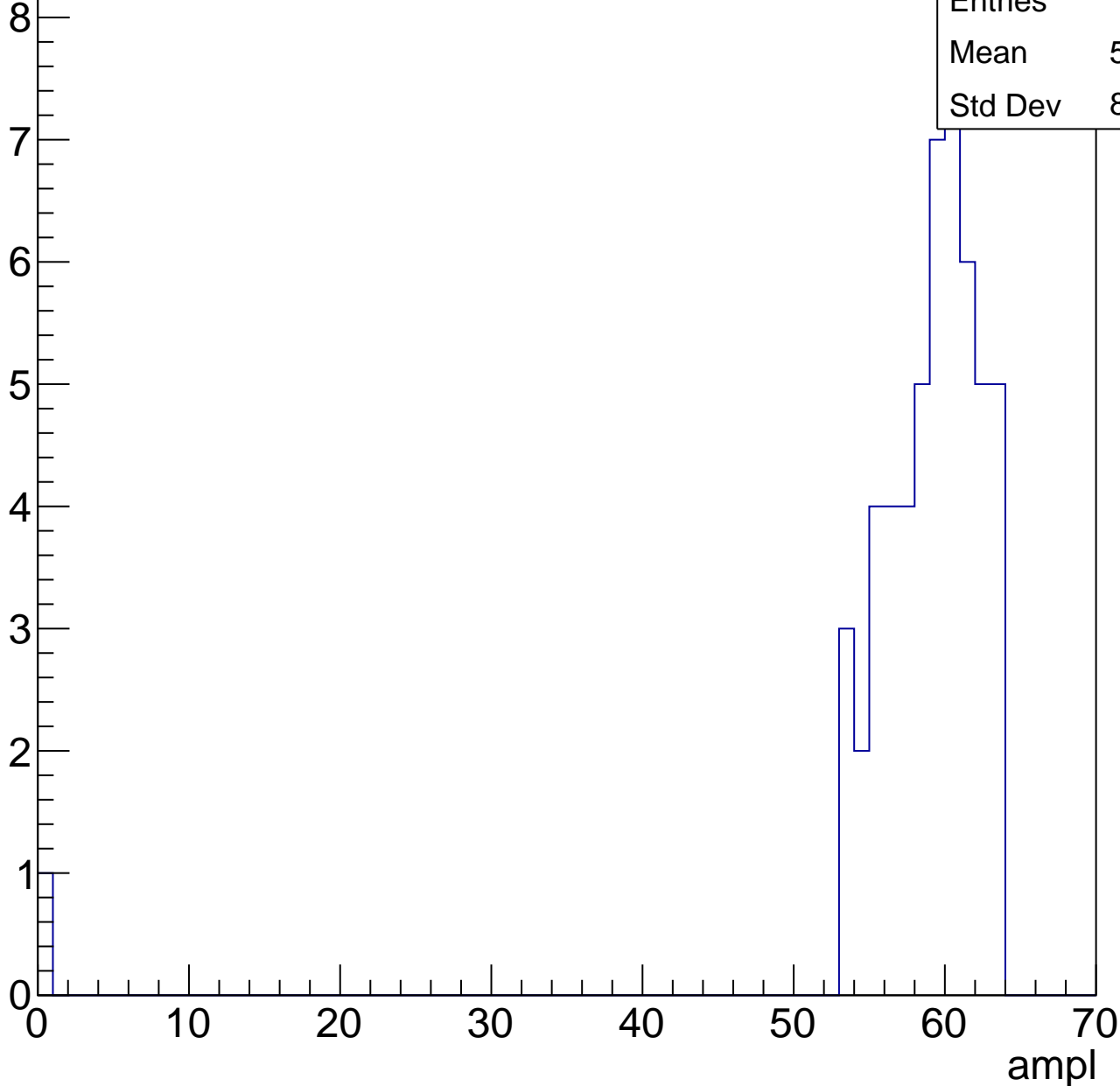


B1L102S, U6-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.65
Std Dev	8.409

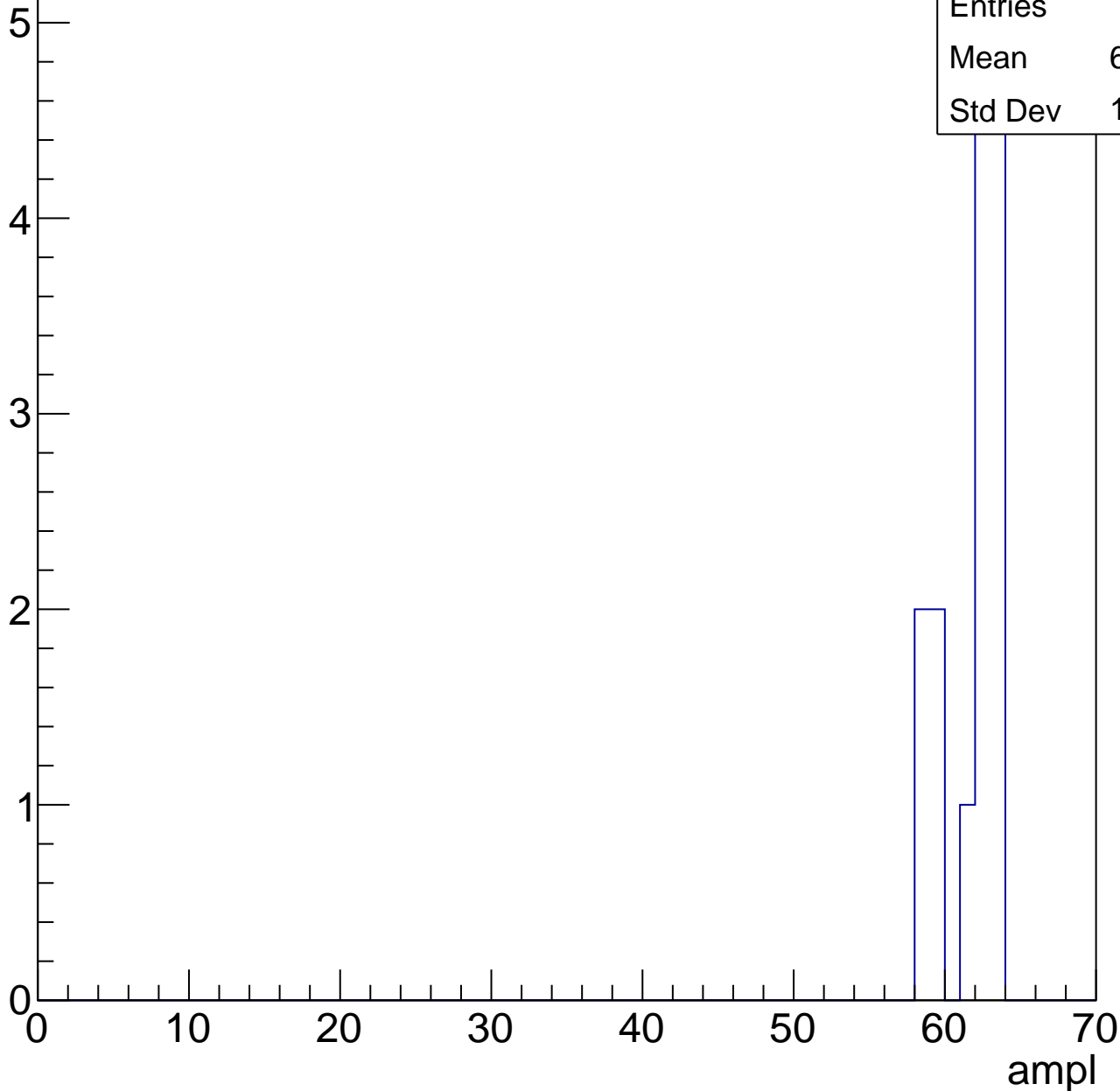


B1L102S, U6-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

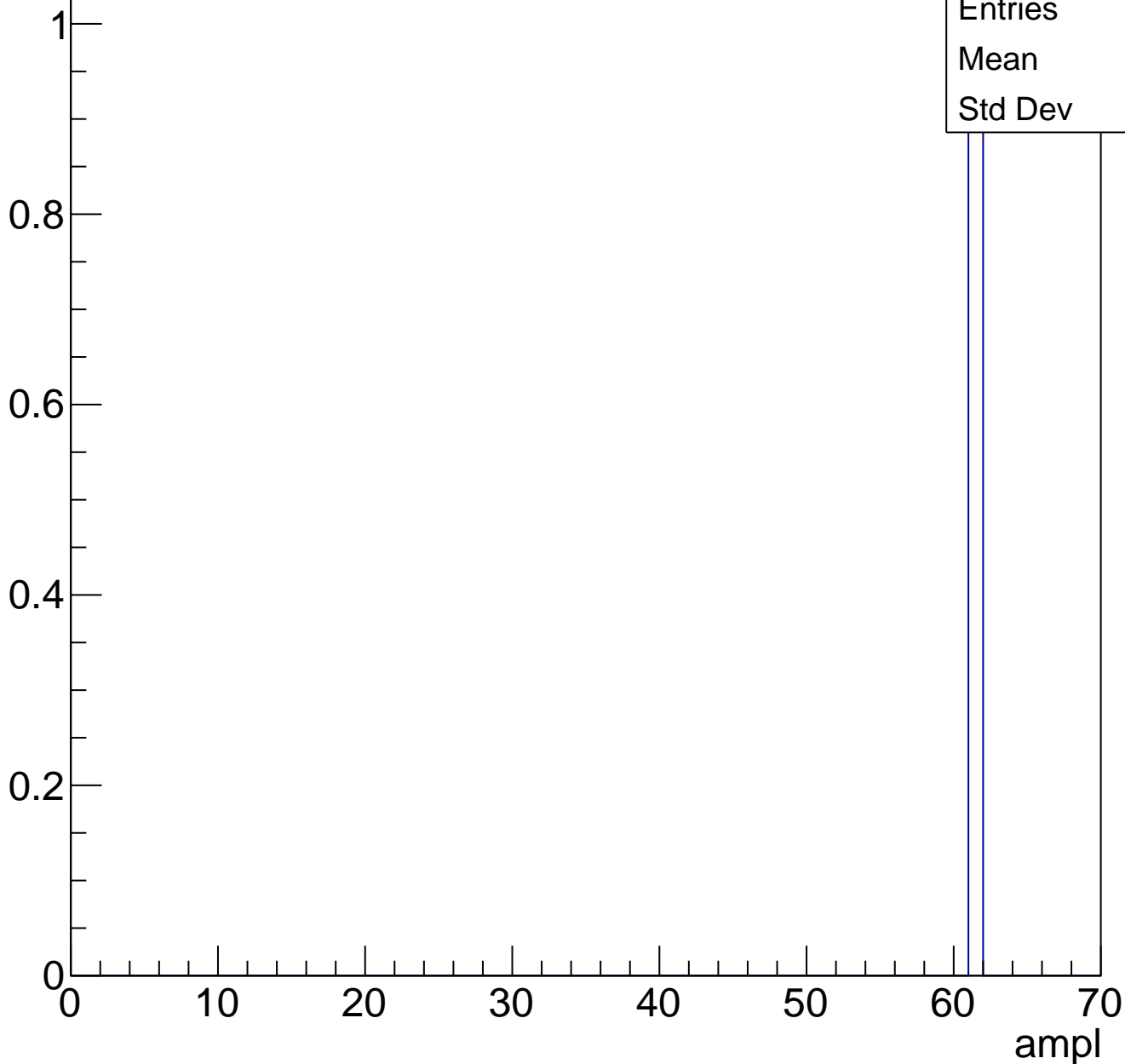
Entries	15
Mean	61.33
Std Dev	1.814



B1L102S, U6-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch37, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	28.95
Std Dev	3.203

Gaus mean : 29.8125

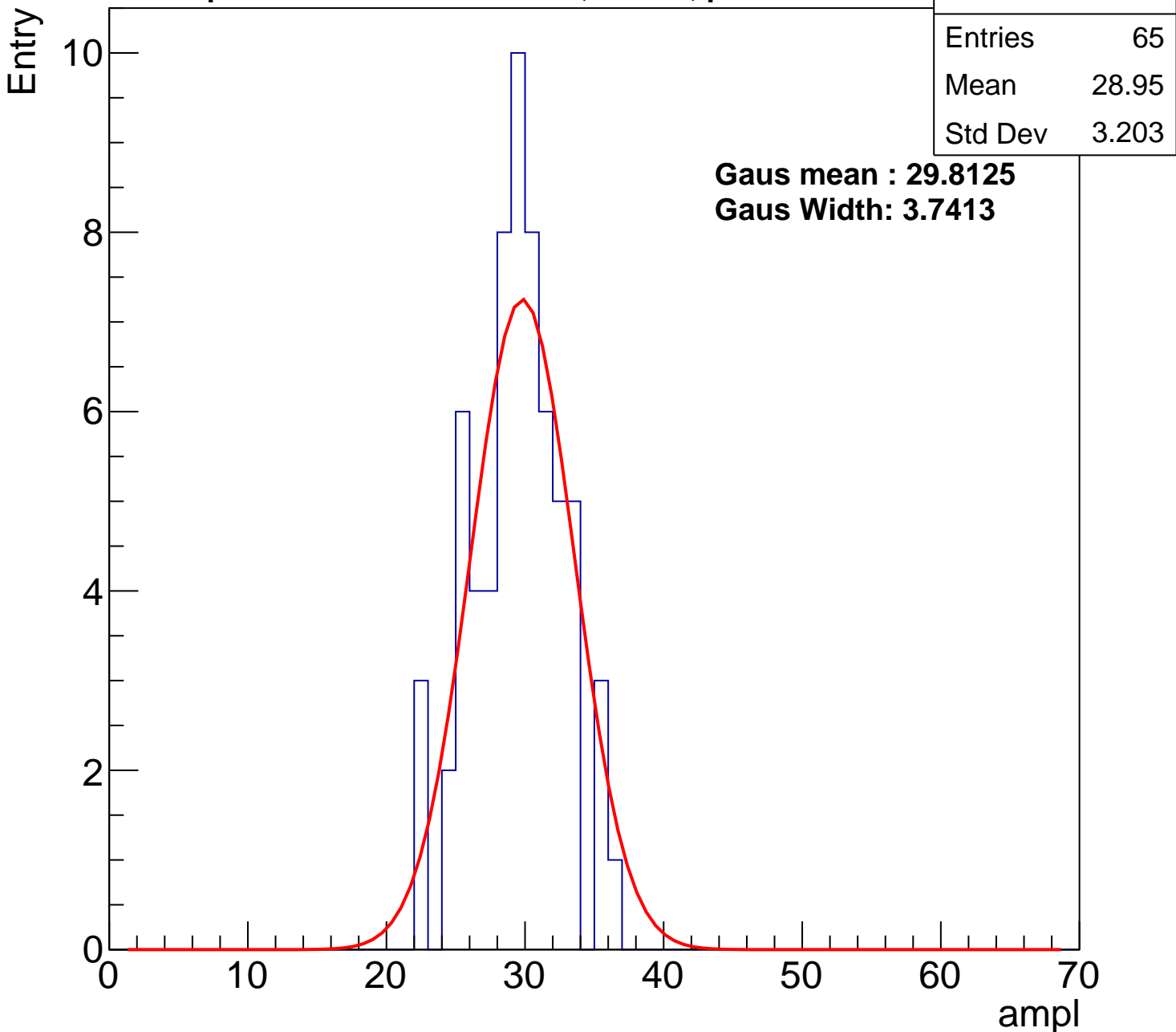
Gaus Width: 3.7413

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch37, adc1

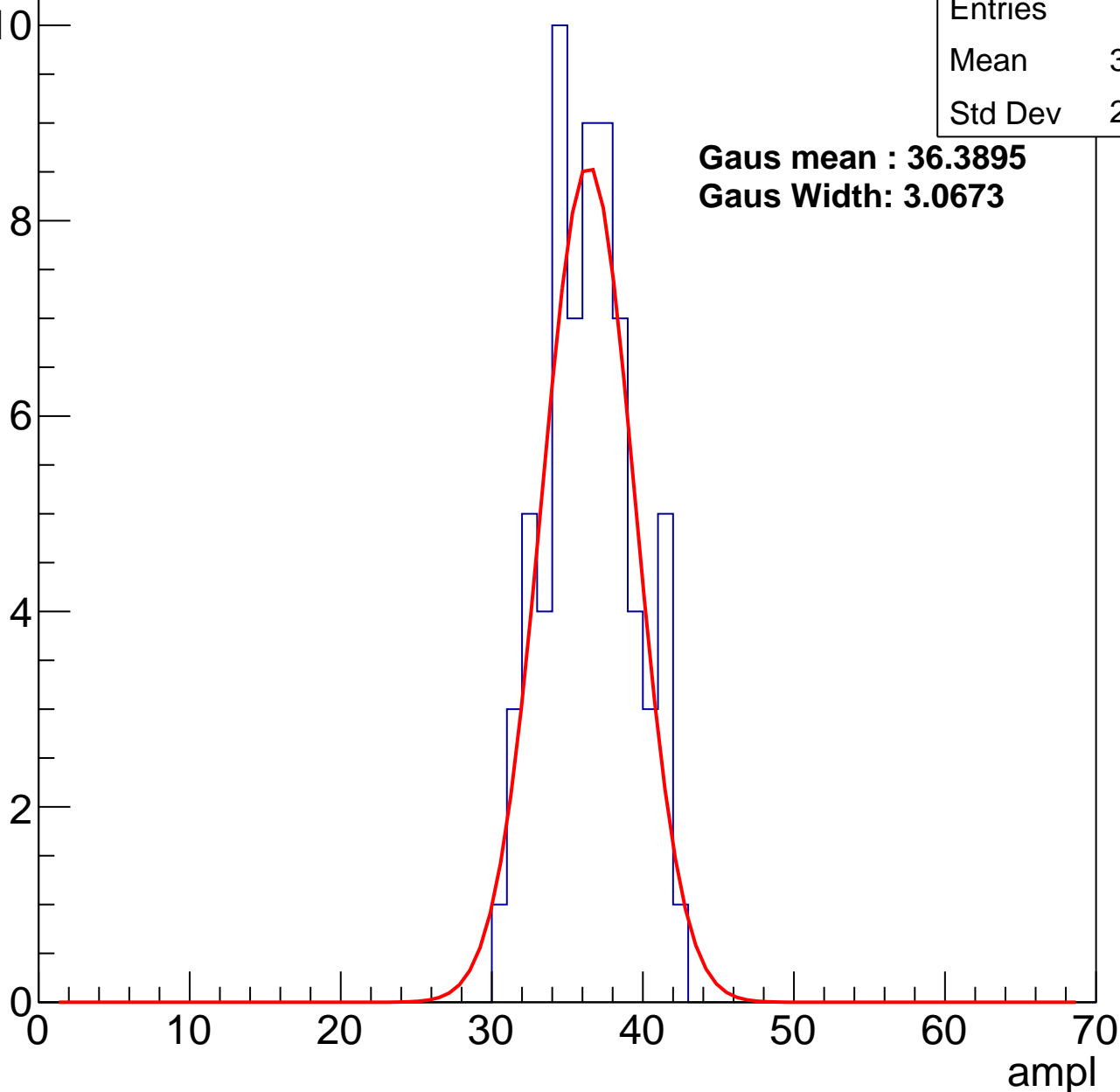
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.97
Std Dev	2.859

Gaus mean : 36.3895

Gaus Width: 3.0673



B1L102S, U6-ch37, adc2

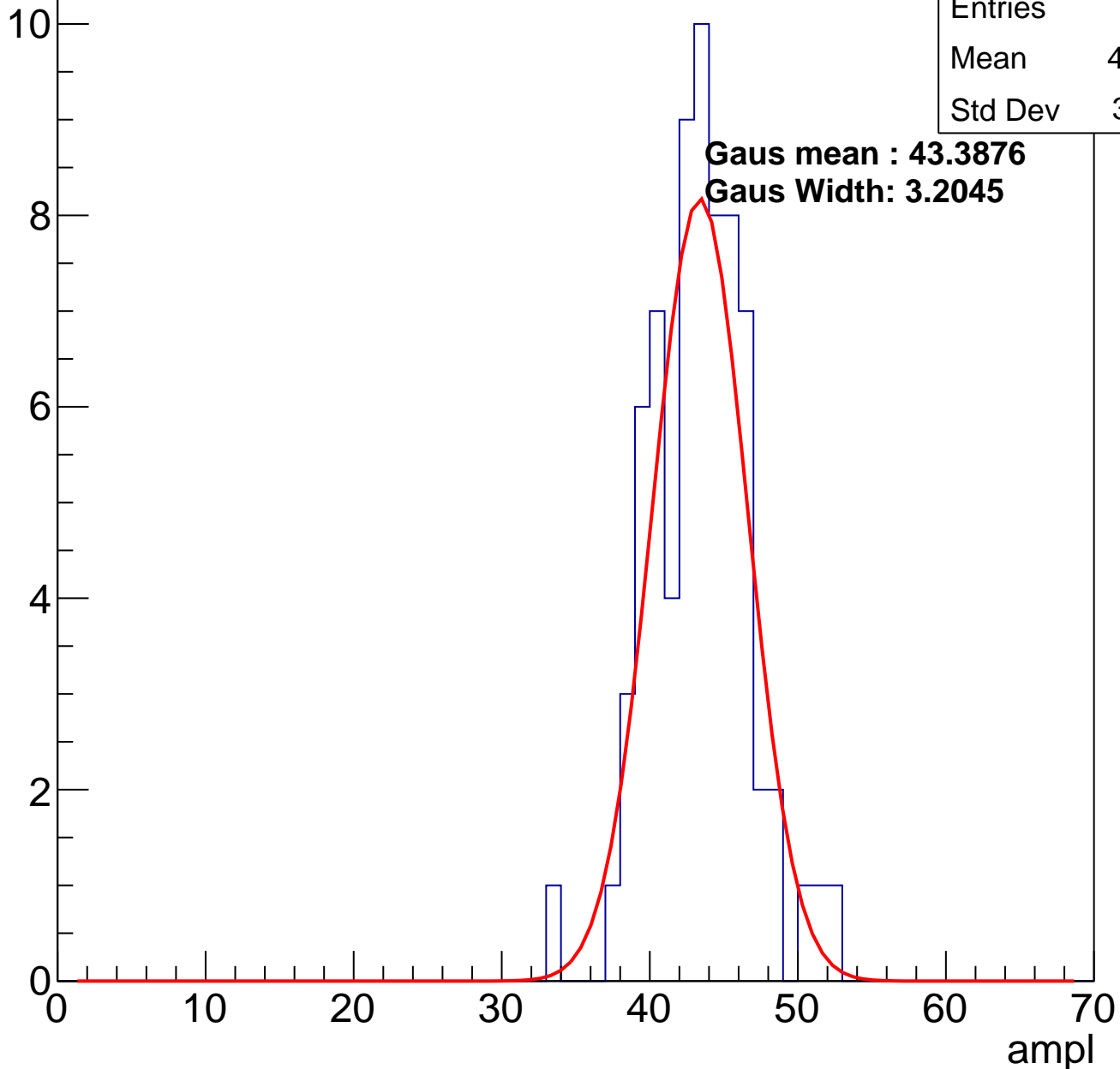
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	42.92
Std Dev	3.301

Gaus mean : 43.3876

Gaus Width: 3.2045

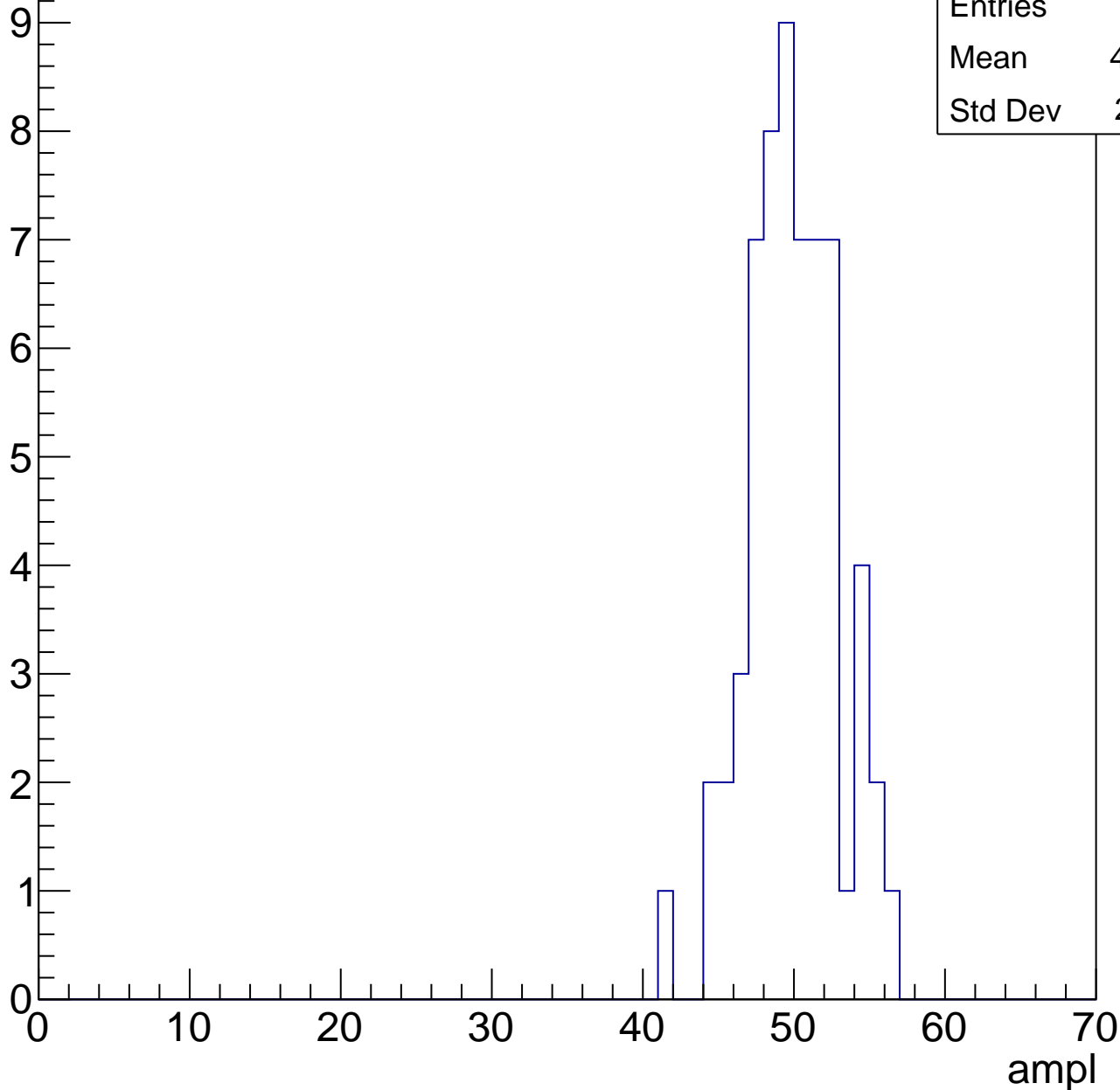
Entry



B1L102S, U6-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

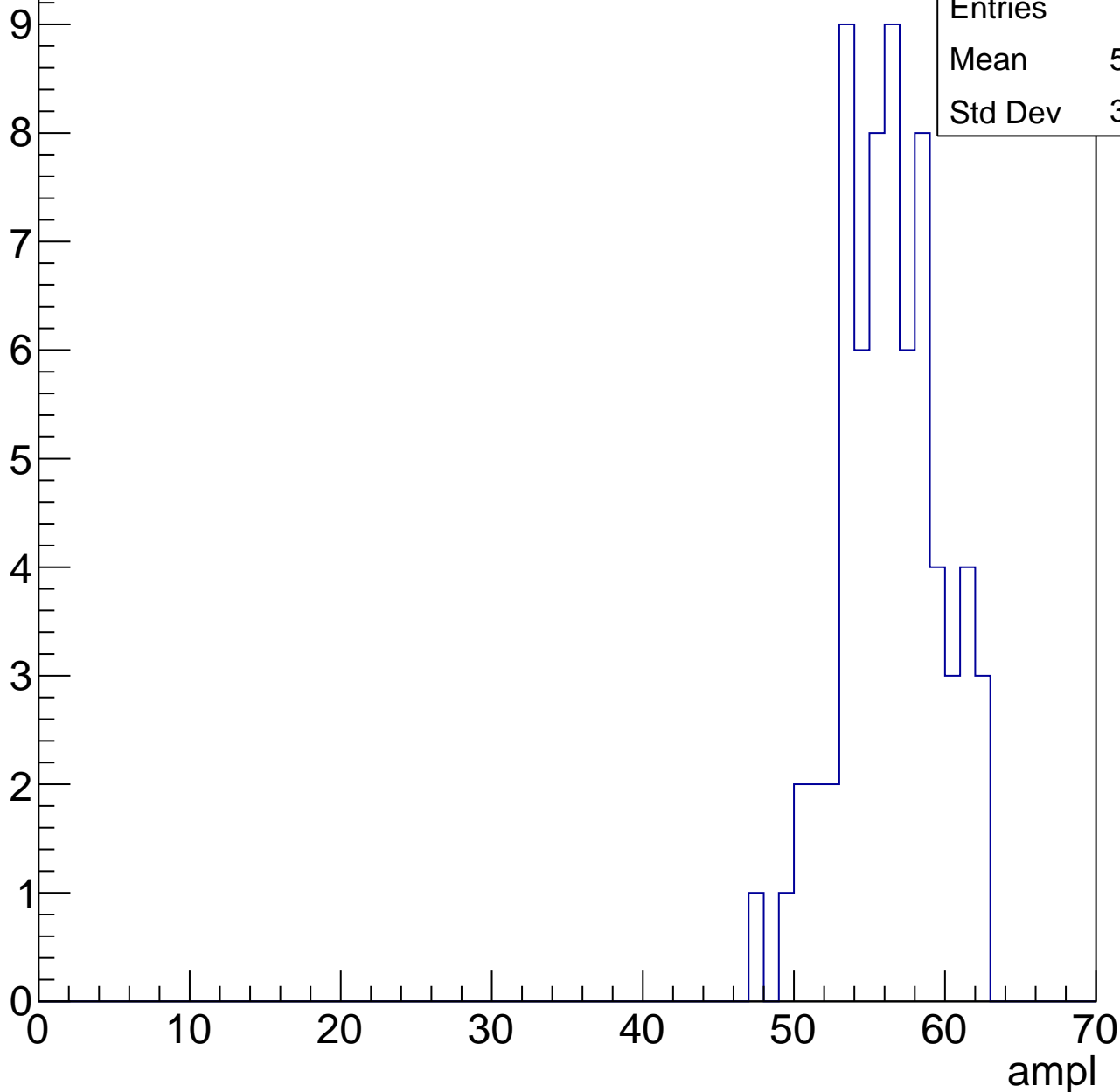
Entry



B1L102S, U6-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

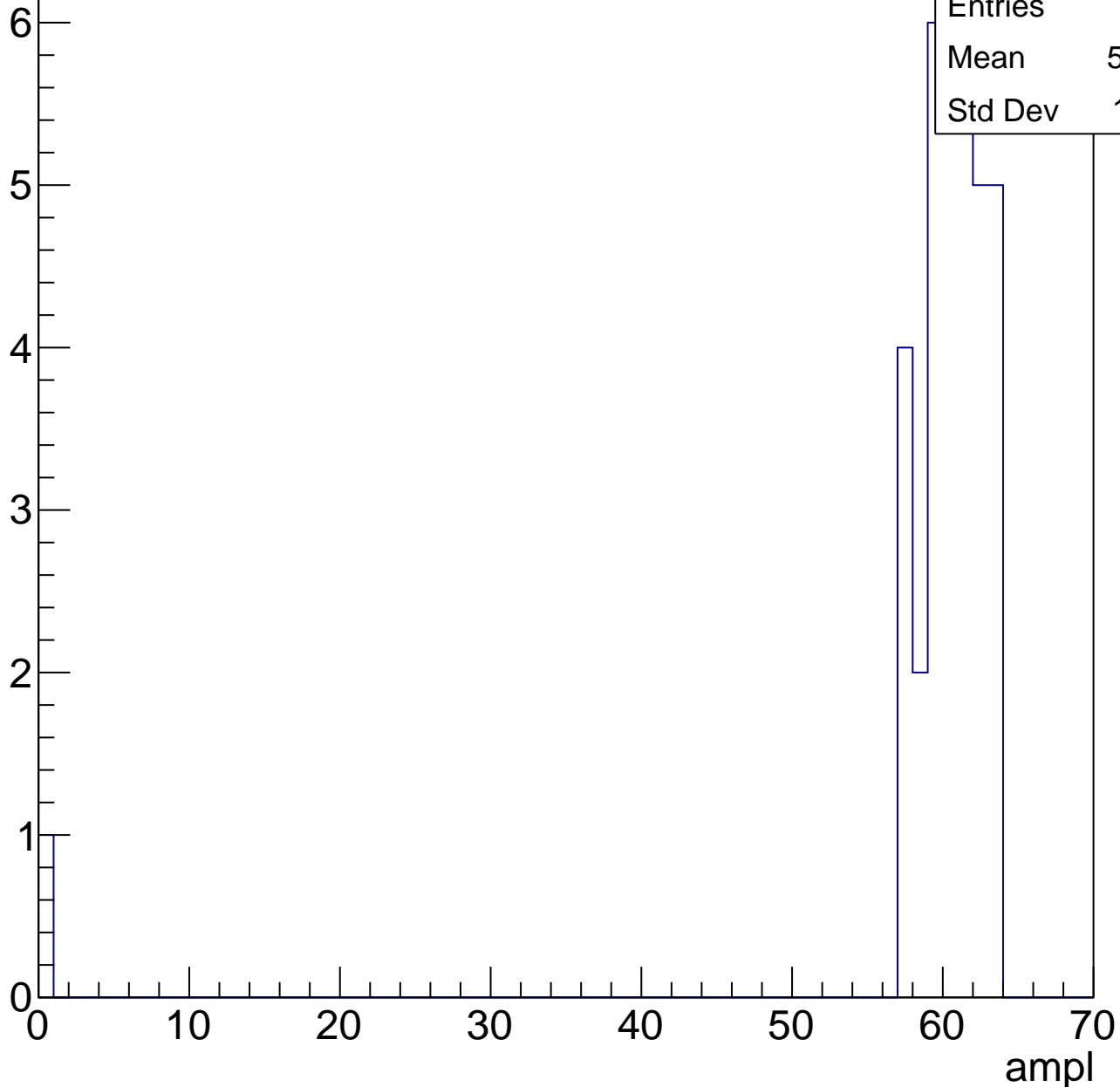
Entry



B1L102S, U6-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

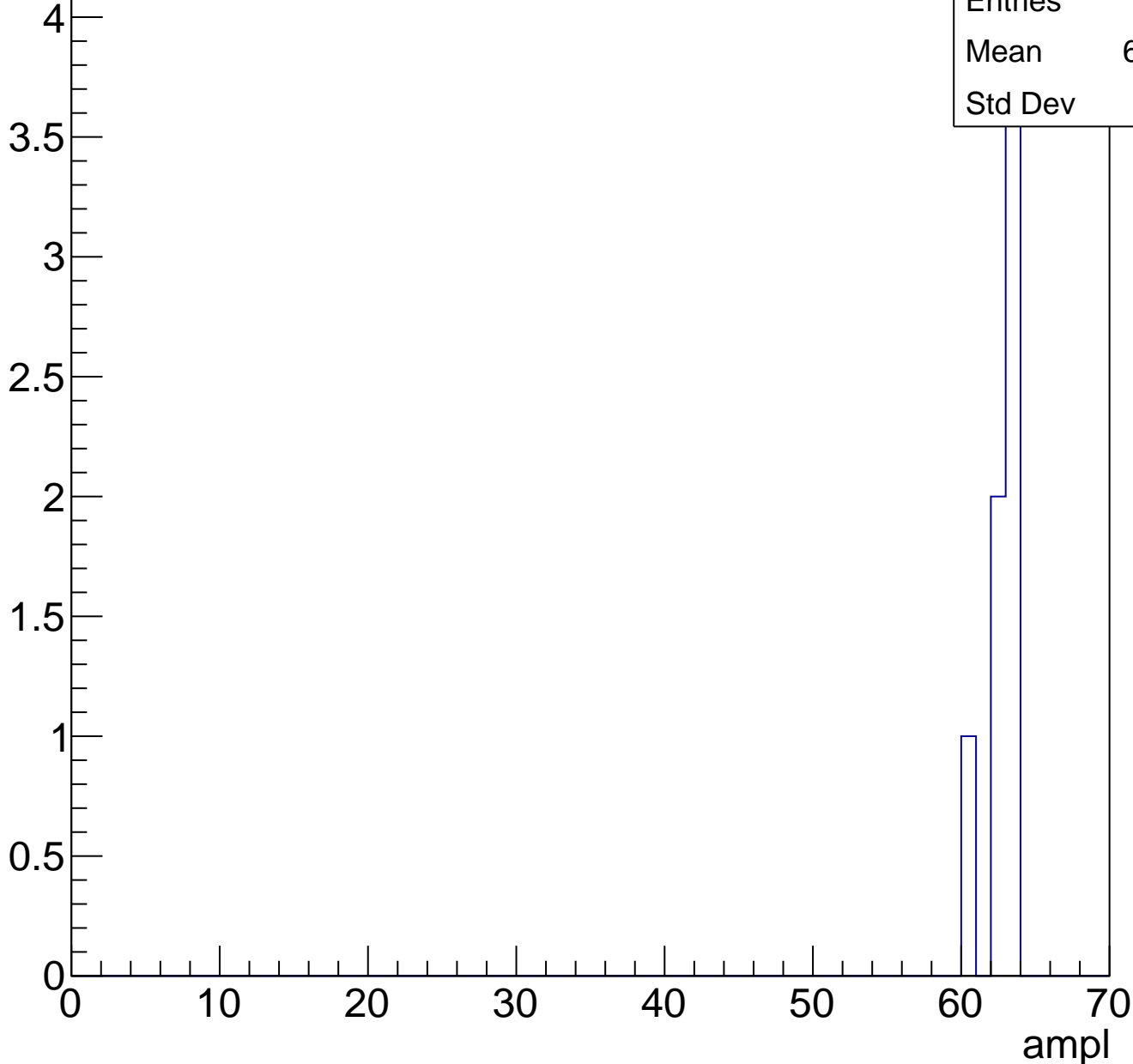
Entry



B1L102S, U6-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch38, adc0

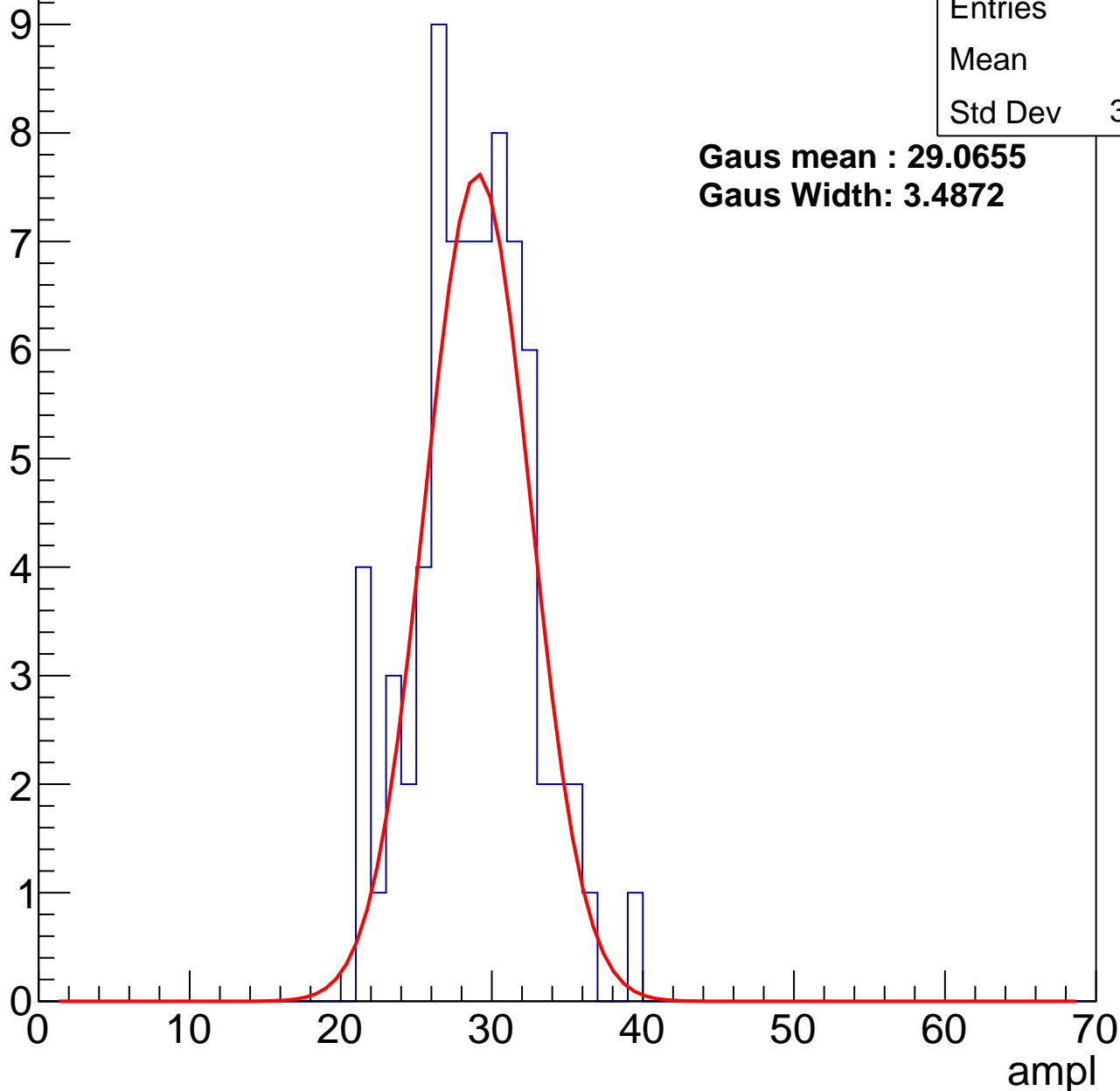
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.4
Std Dev	3.719

Gaus mean : 29.0655

Gaus Width: 3.4872



B1L102S, U6-ch38, adc1

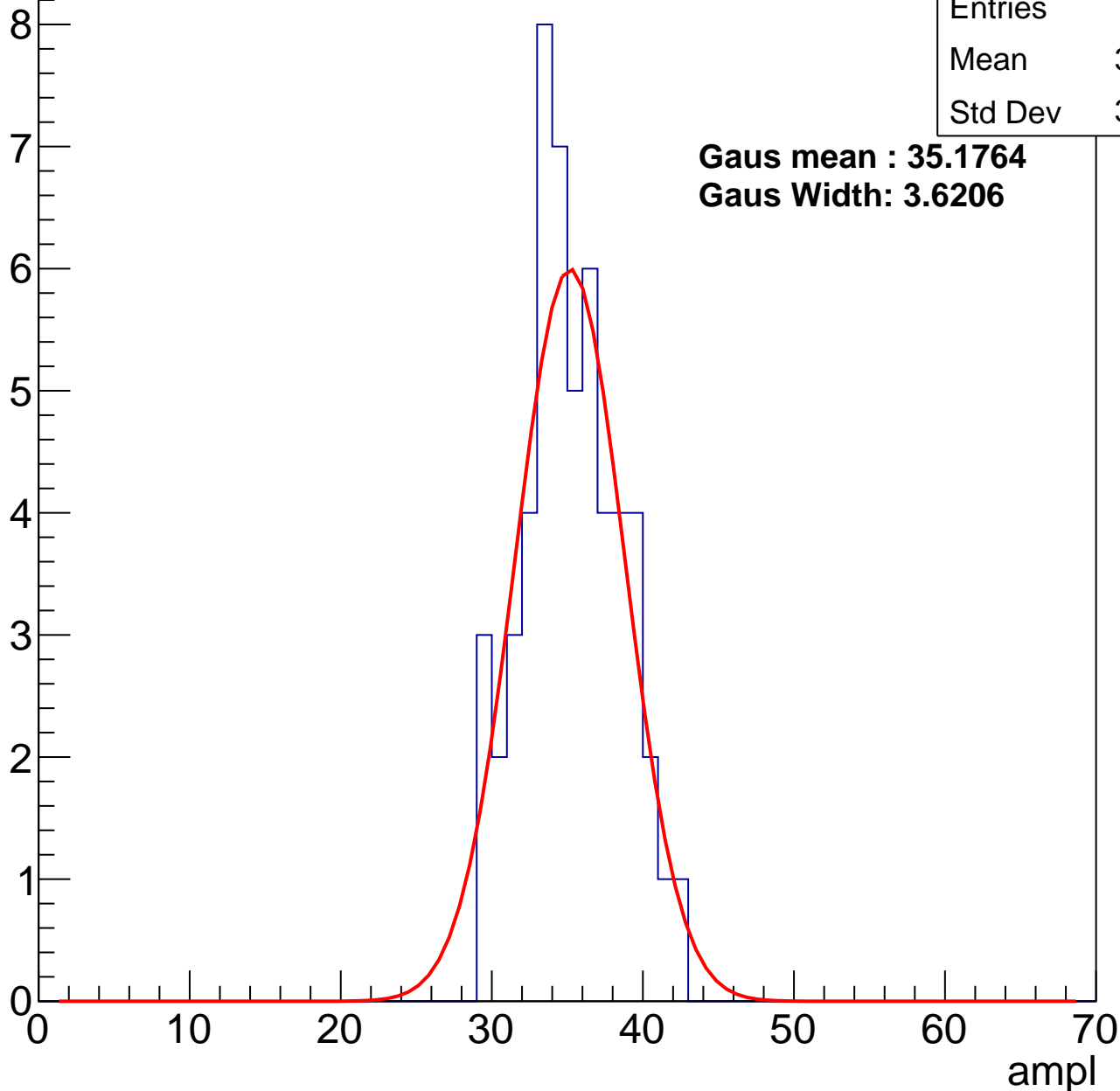
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.81
Std Dev	3.151

Gaus mean : 35.1764

Gaus Width: 3.6206



B1L102S, U6-ch38, adc2

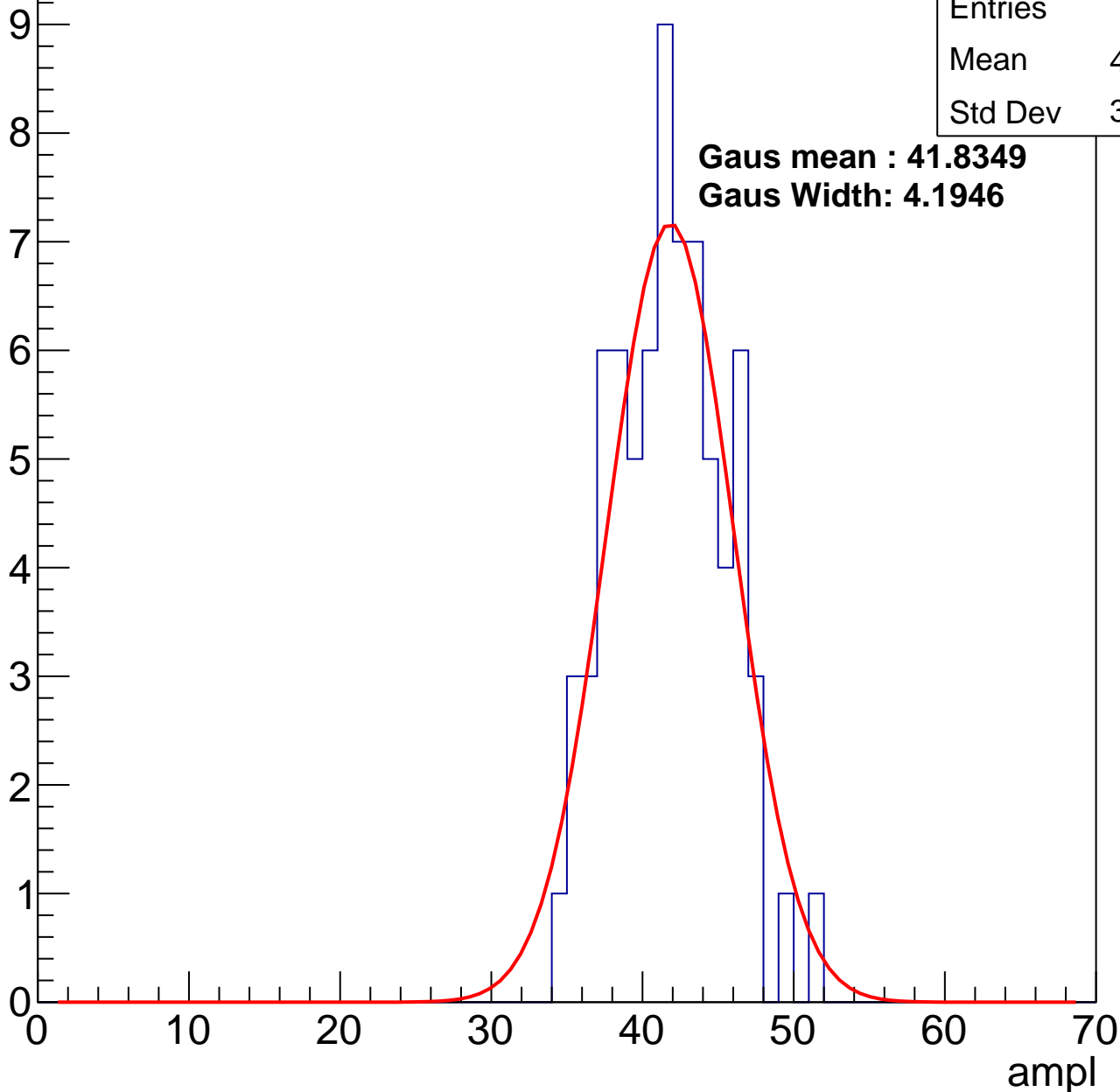
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.27
Std Dev	3.642

Gaus mean : 41.8349

Gaus Width: 4.1946

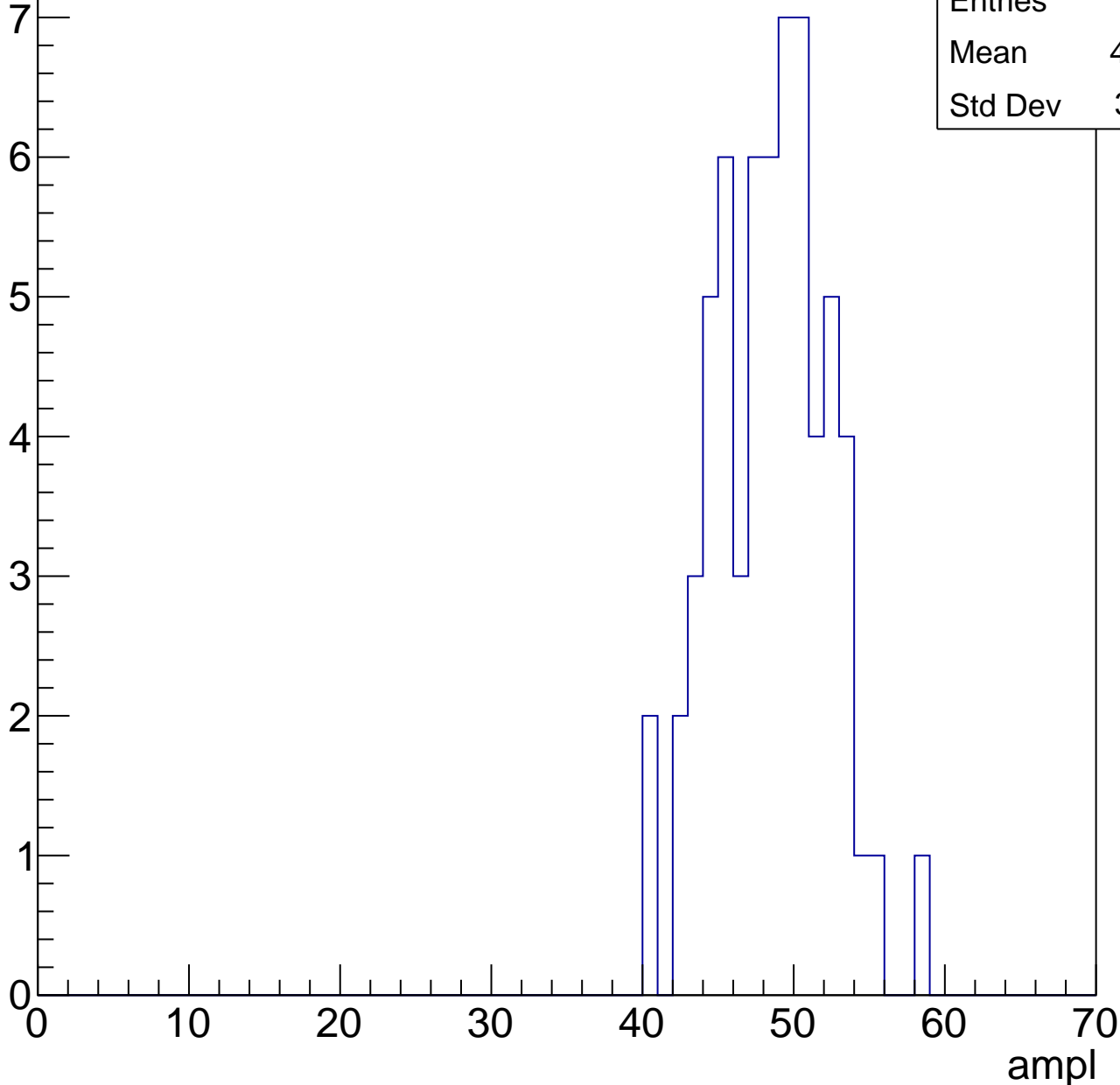


B1L102S, U6-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

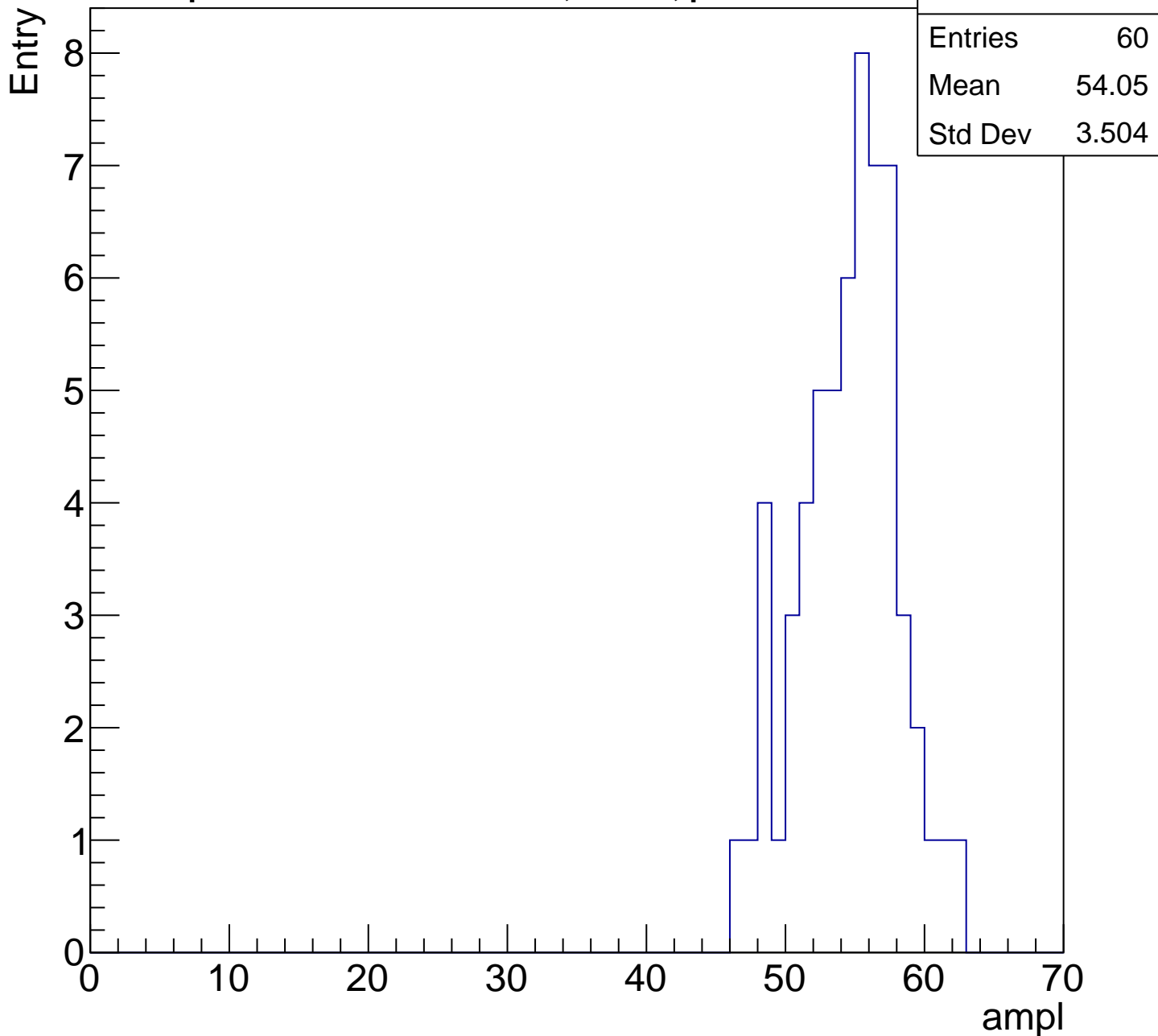
Entry

Entries	63
Mean	48.05
Std Dev	3.701



B1L102S, U6-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch38, adc5

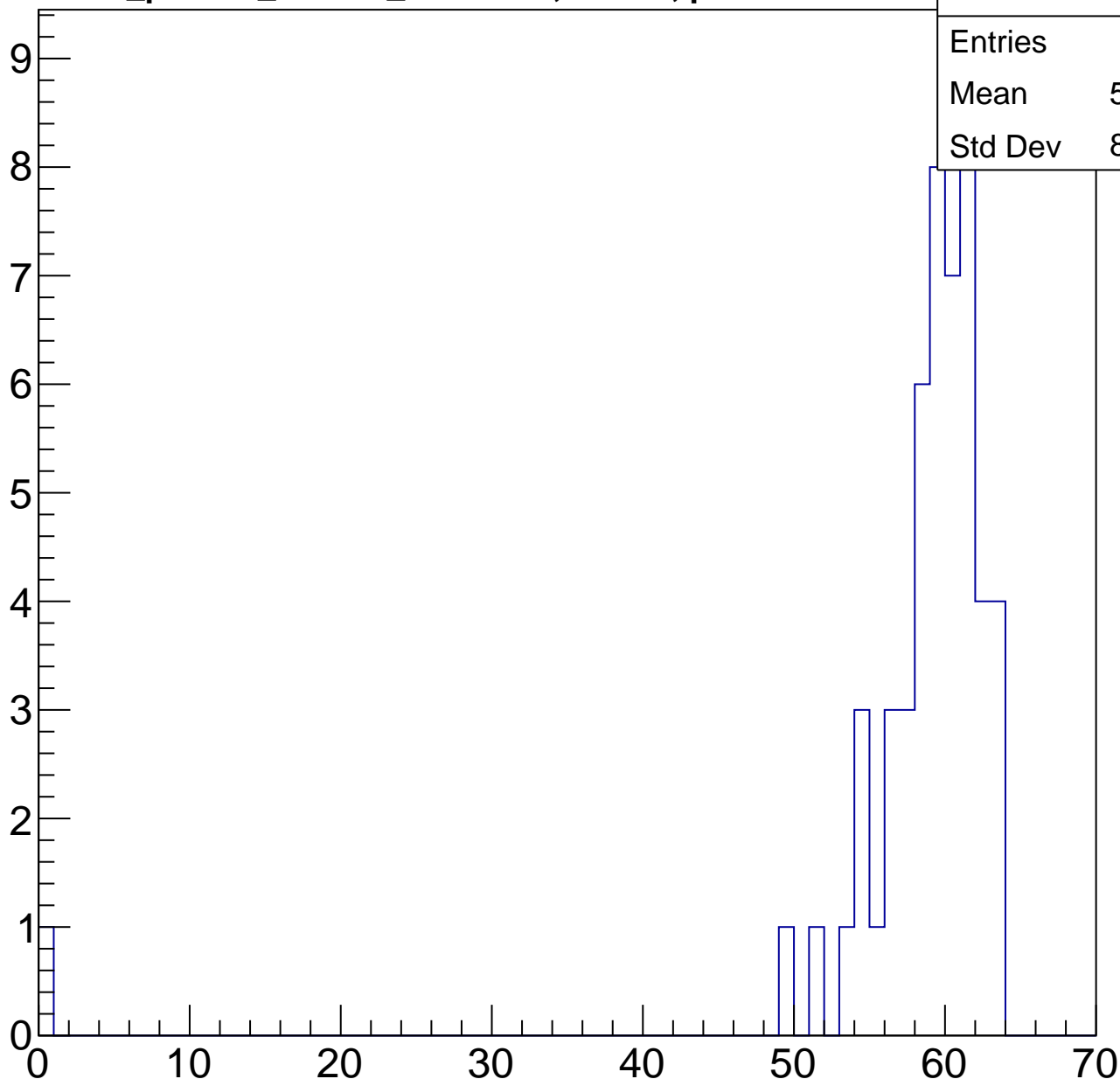
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	57.65
Std Dev	8.622

ampl

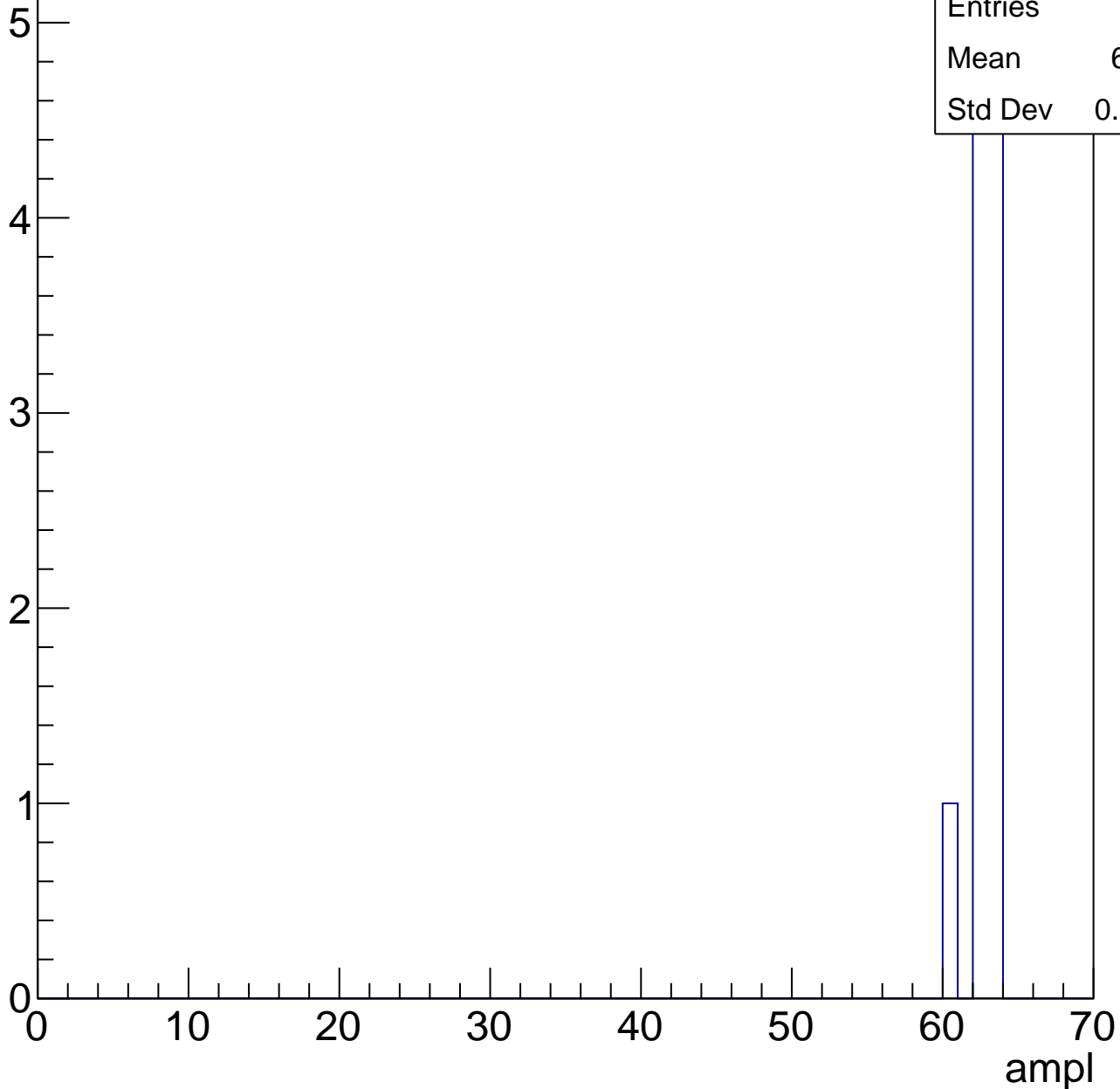


B1L102S, U6-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

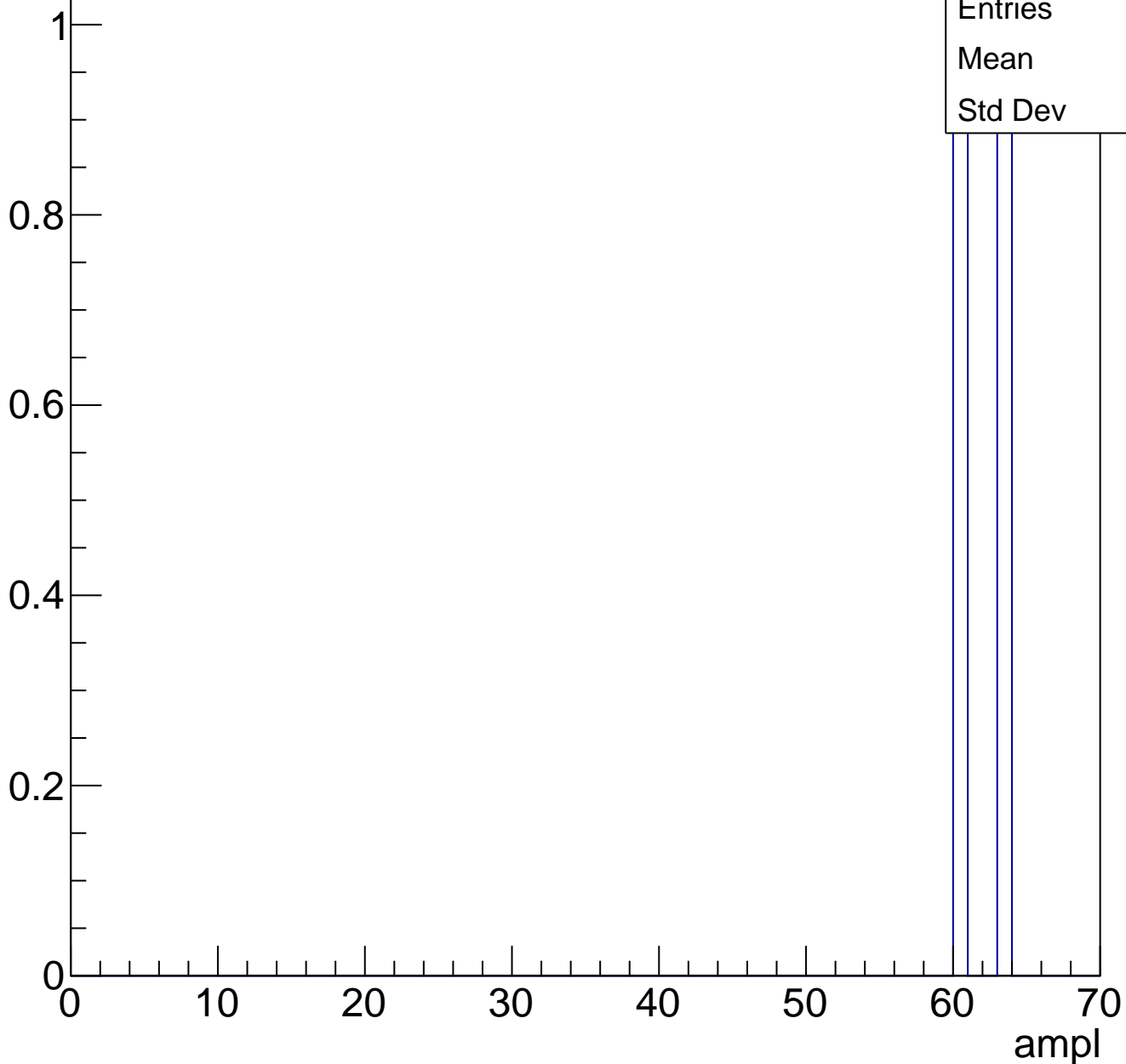
Entries	11
Mean	62.27
Std Dev	0.8624



B1L102S, U6-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch39, adc0

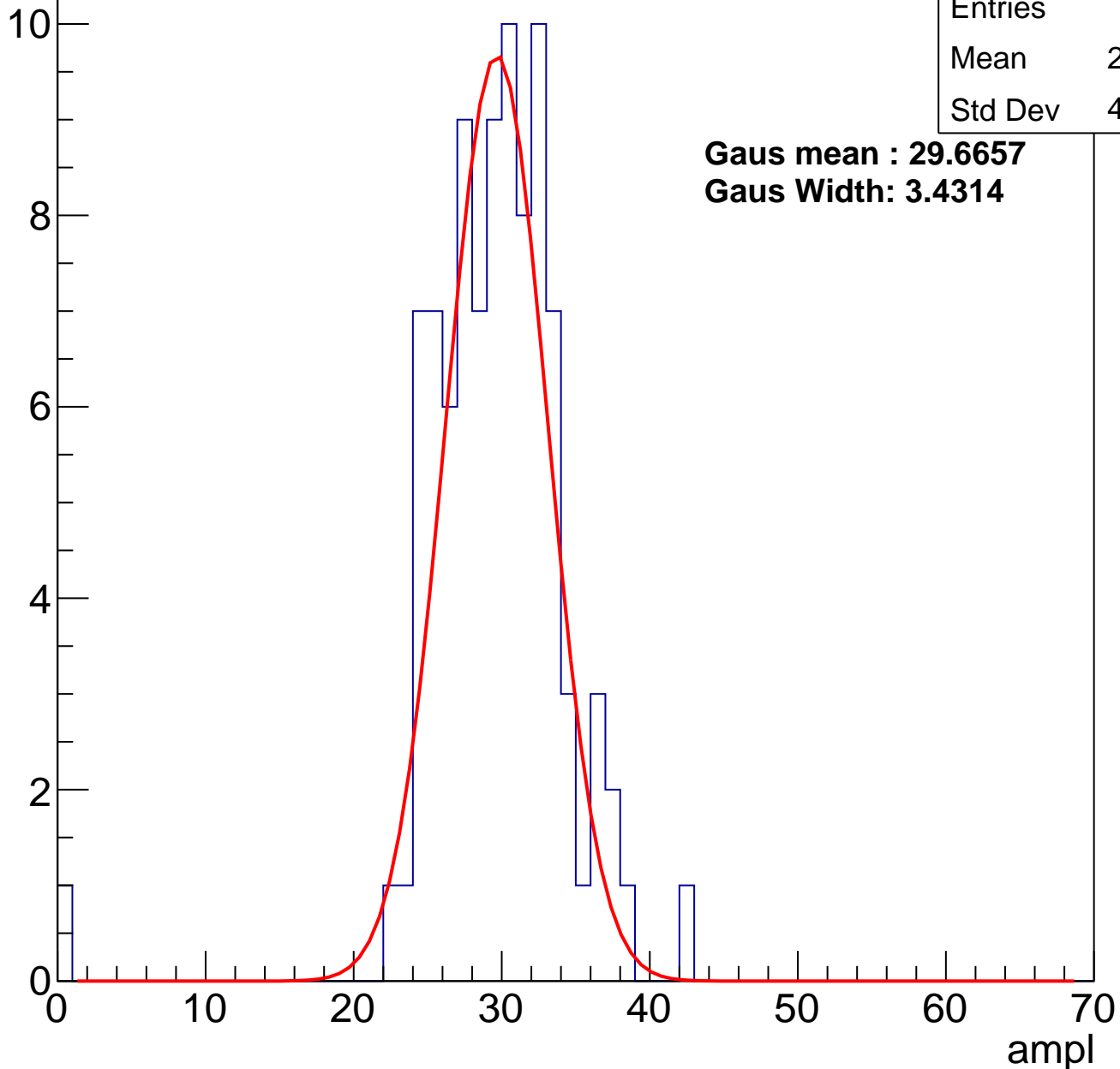
calib_packv5_042523_0143.root, FC#11, port A2

Entries	94
Mean	29.17
Std Dev	4.824

Gaus mean : 29.6657

Gaus Width: 3.4314

Entry



B1L102S, U6-ch39, adc1

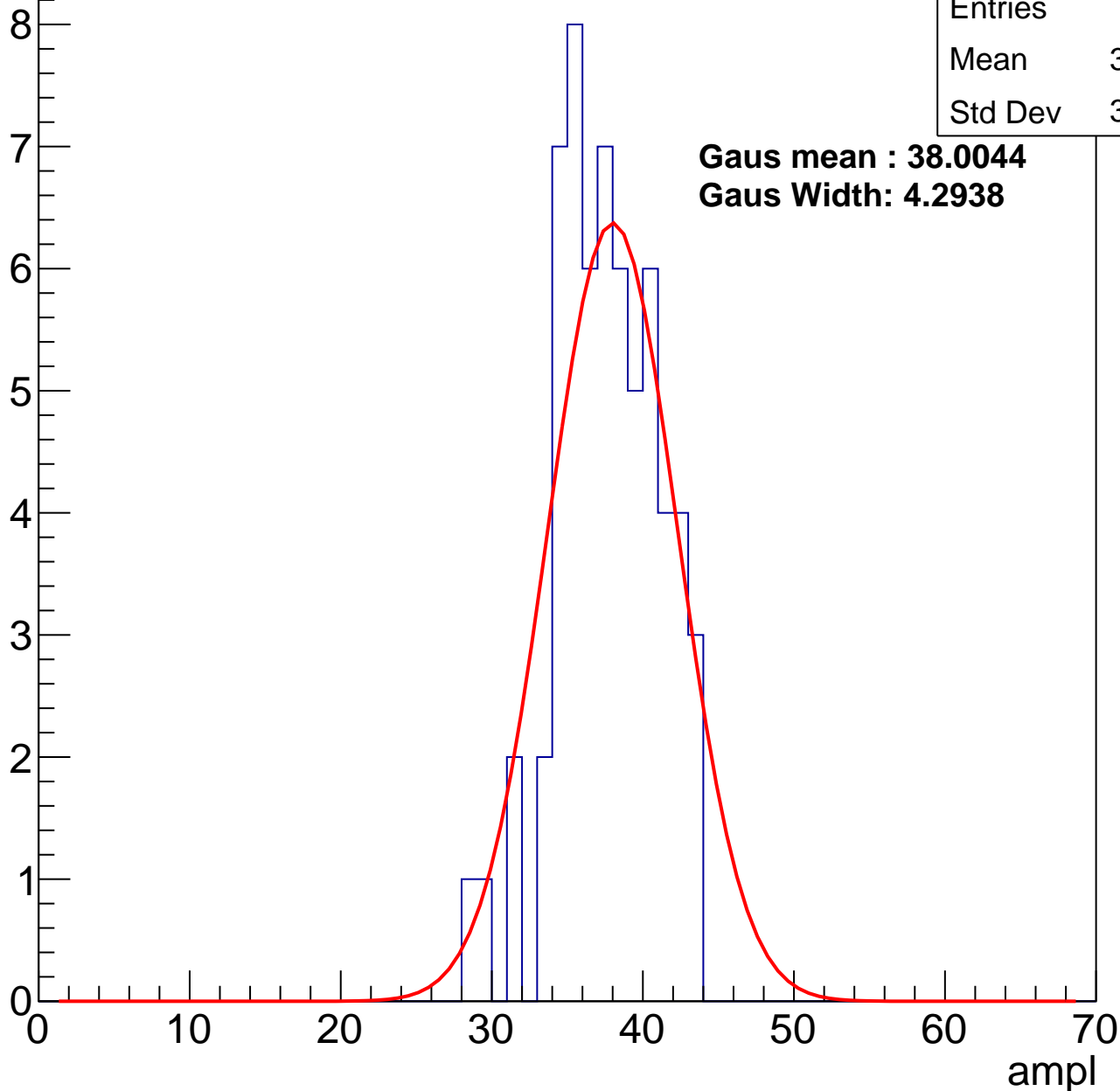
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	37.13
Std Dev	3.362

Gaus mean : 38.0044

Gaus Width: 4.2938



B1L102S, U6-ch39, adc2

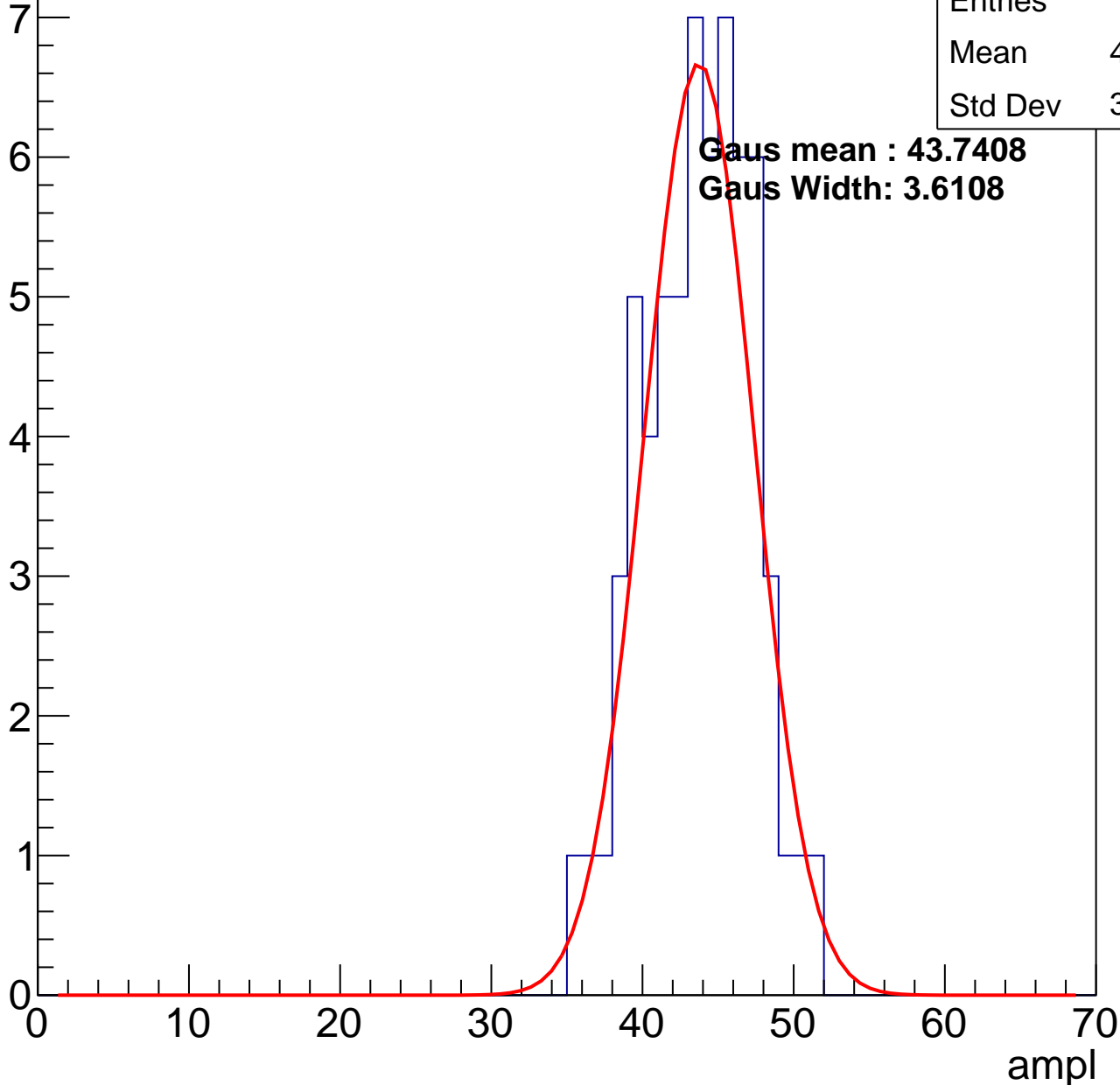
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.24
Std Dev	3.495

Gaus mean : 43.7408

Gaus Width: 3.6108

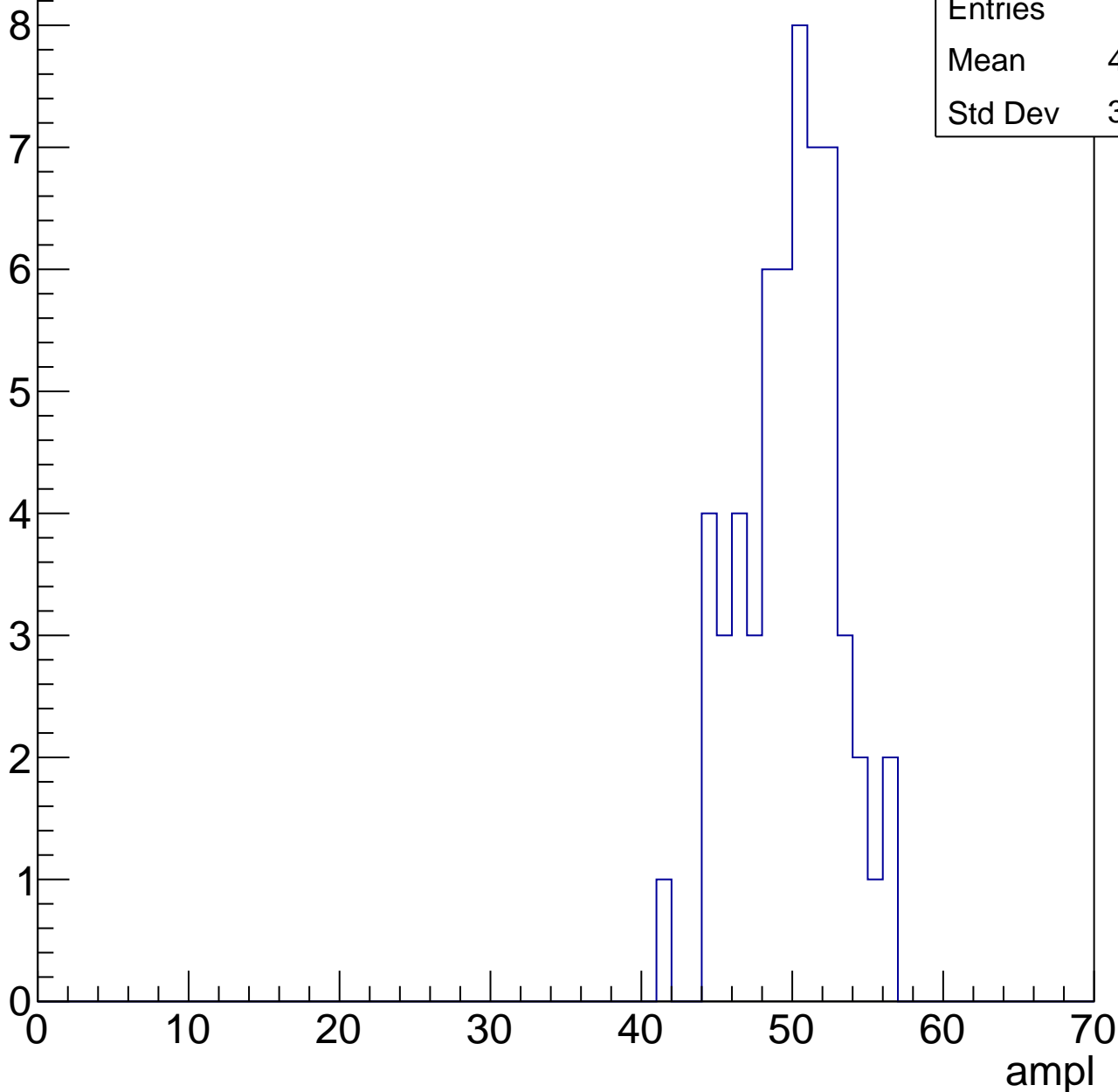


B1L102S, U6-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

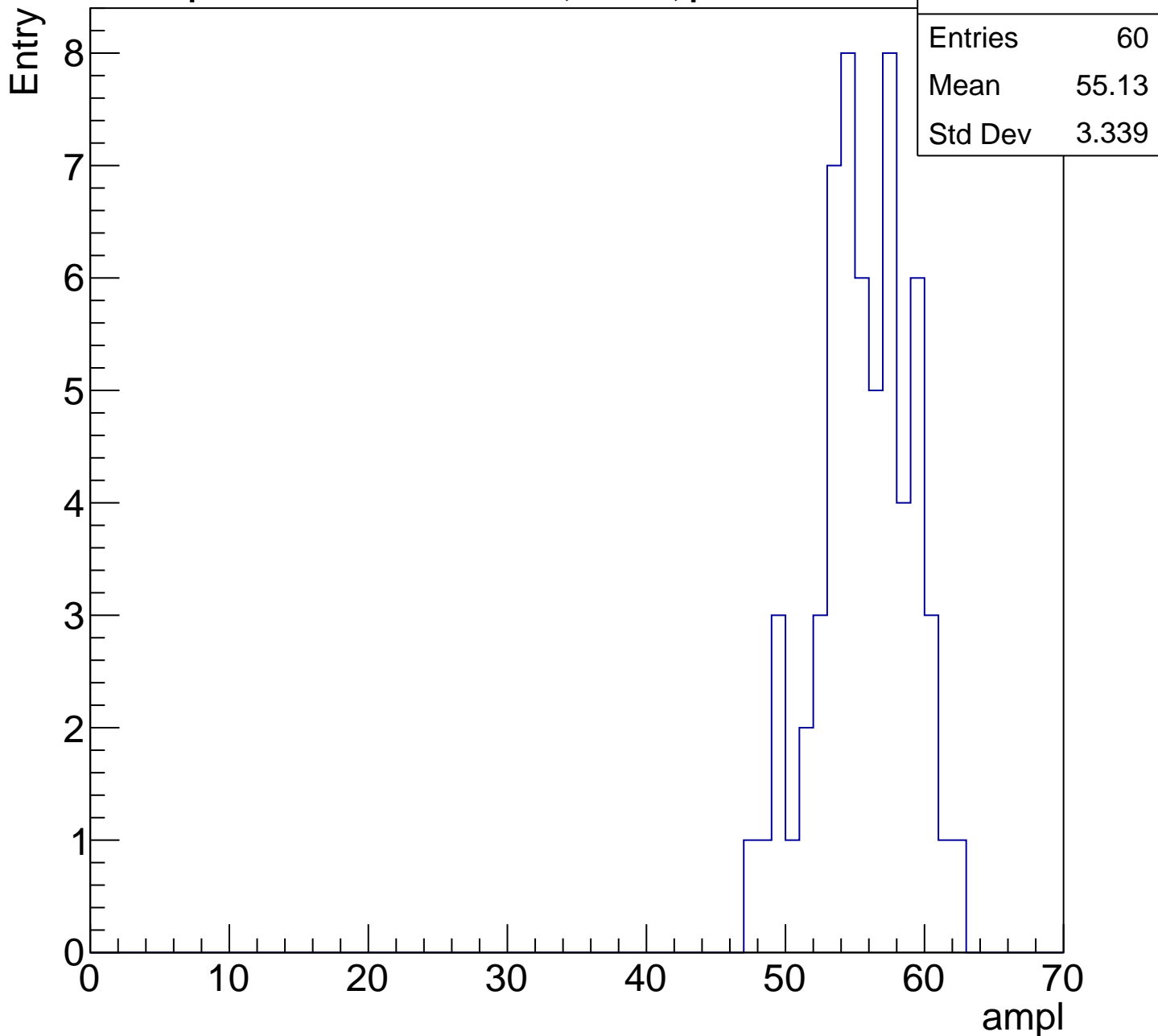
Entry

Entries	57
Mean	49.37
Std Dev	3.215



B1L102S, U6-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

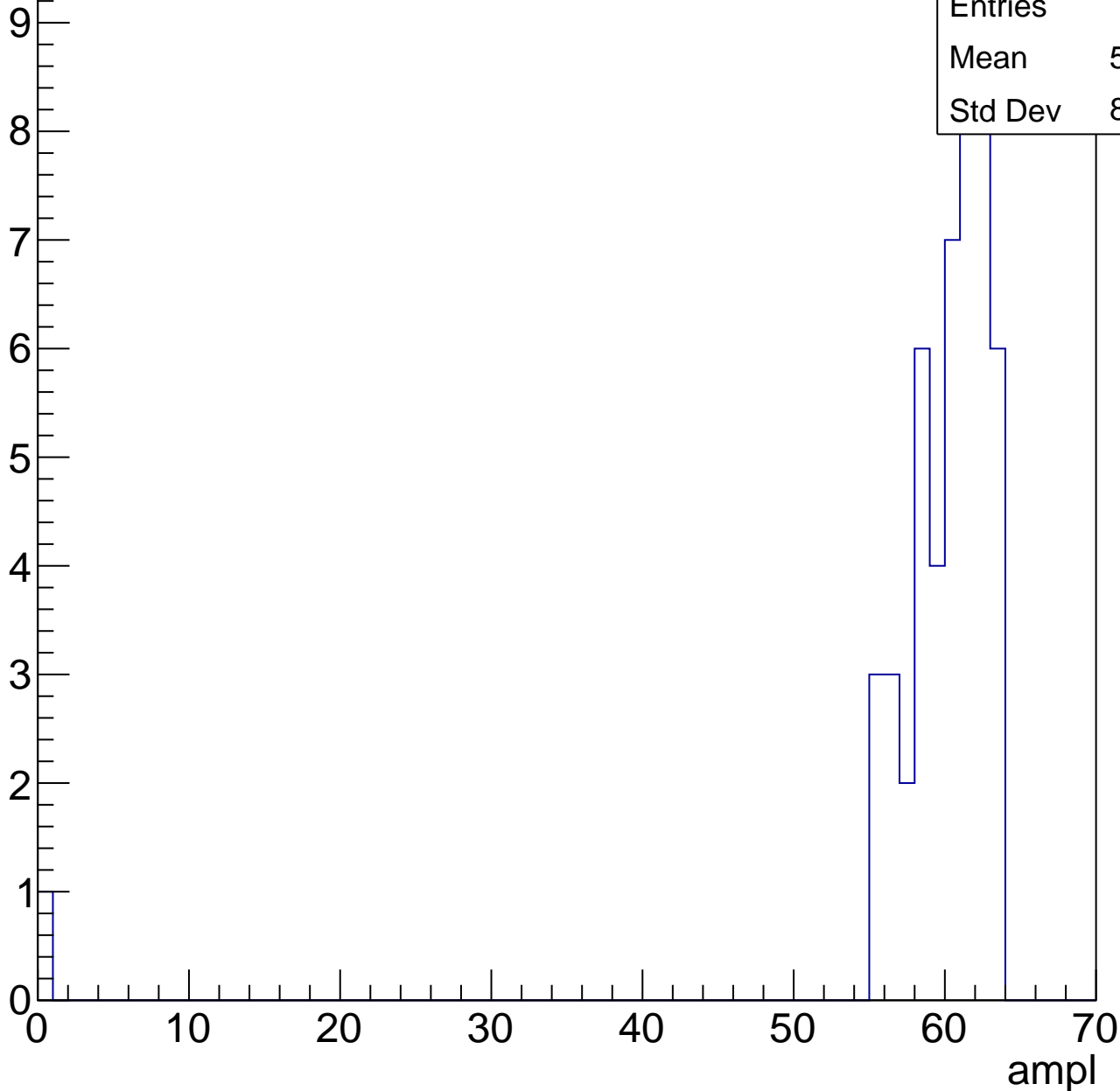


B1L102S, U6-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.67
Std Dev	8.784



B1L102S, U6-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U6-ch40, adc0

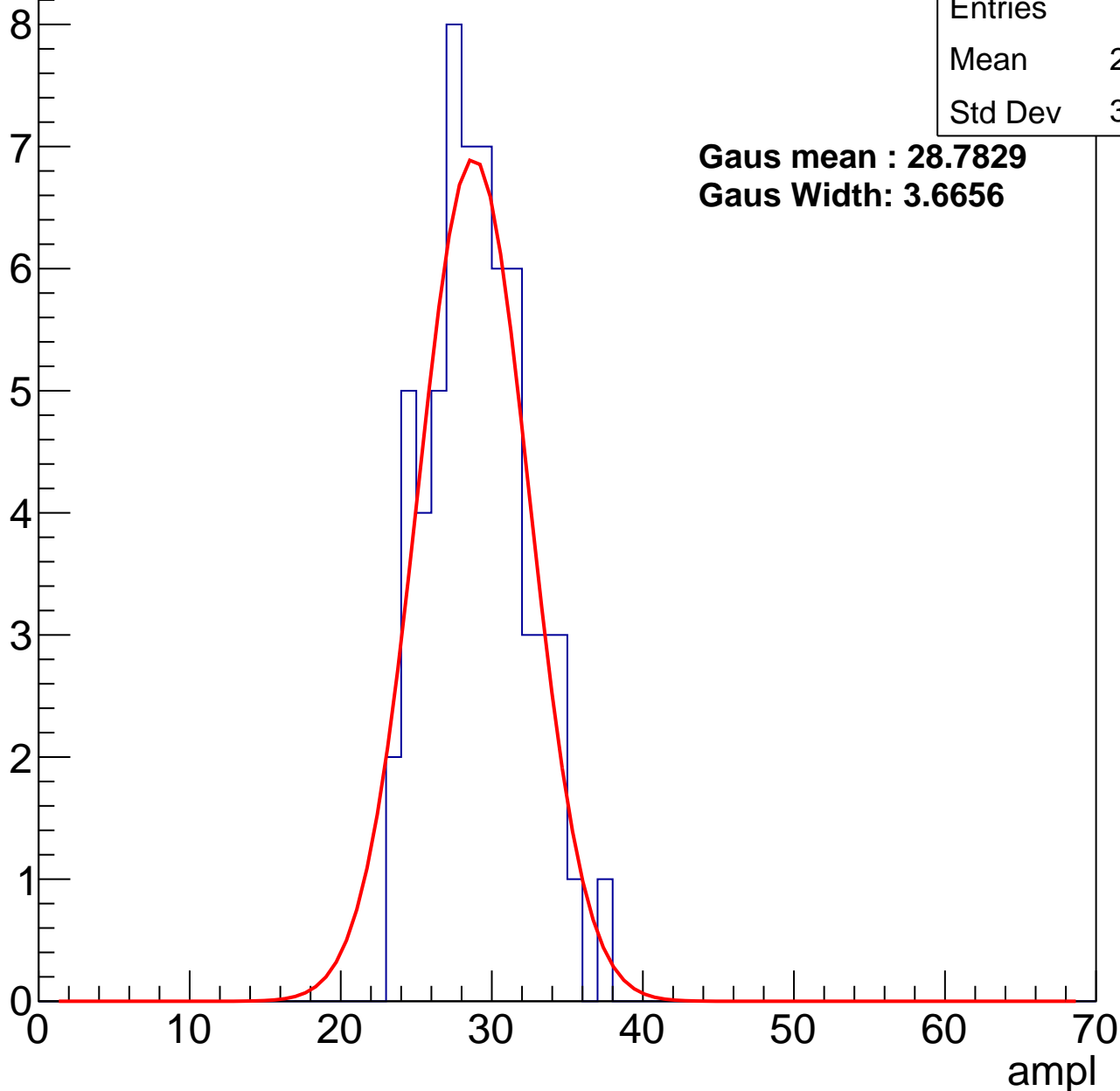
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.62
Std Dev	3.179

Gaus mean : 28.7829

Gaus Width: 3.6656



B1L102S, U6-ch40, adc1

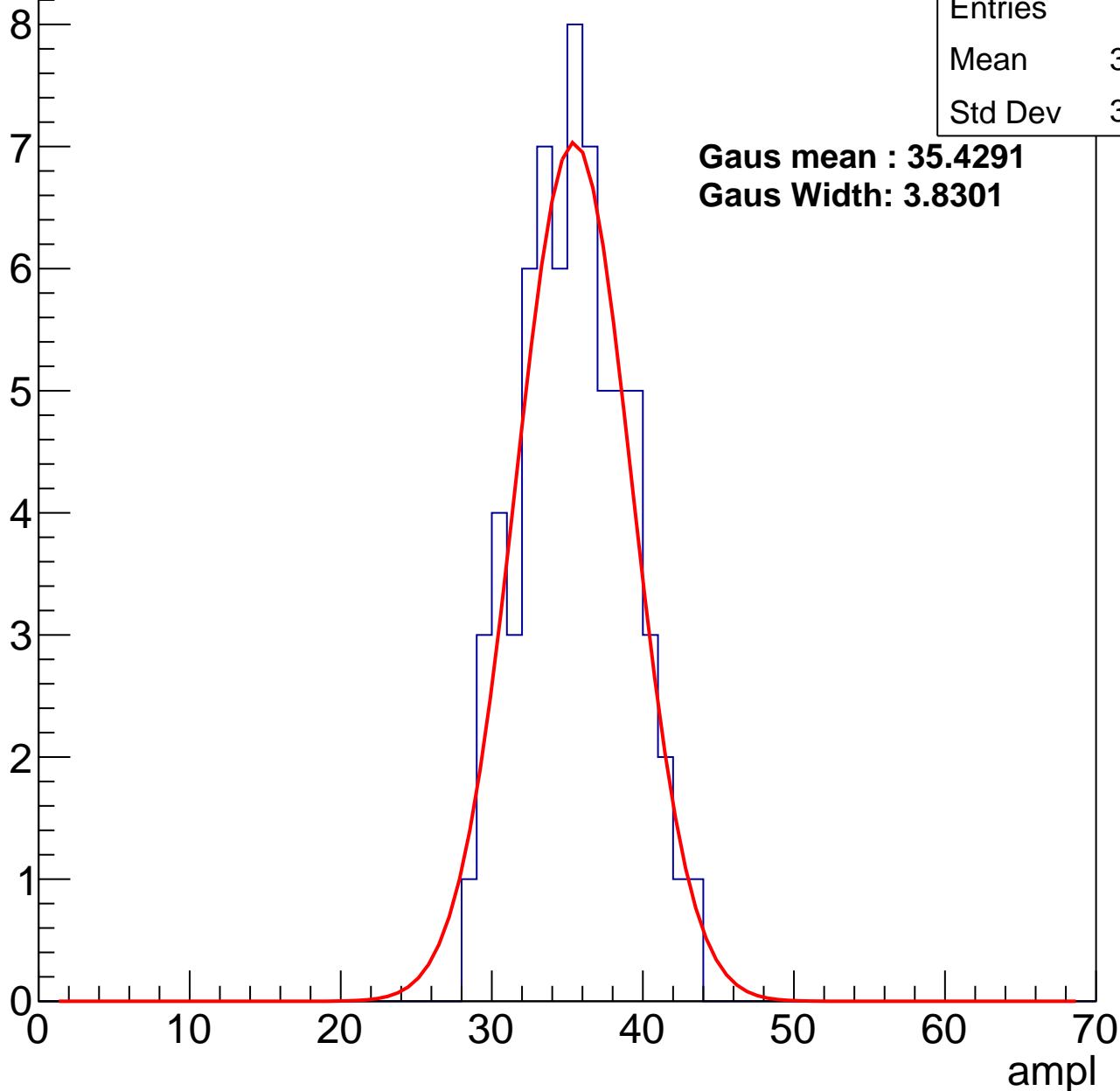
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.99
Std Dev	3.466

Gaus mean : 35.4291

Gaus Width: 3.8301



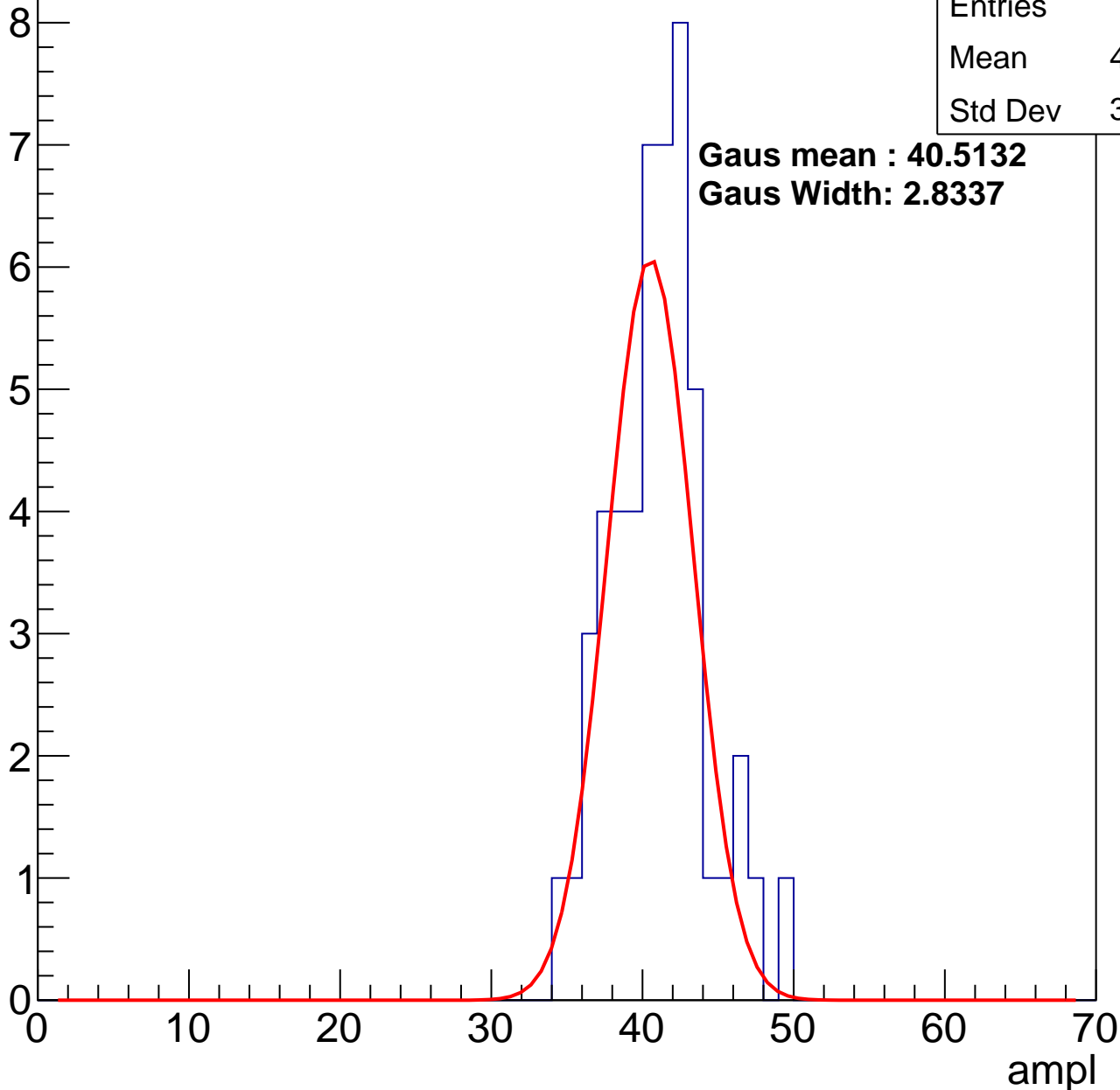
B1L102S, U6-ch40, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	40.56
Std Dev	3.086

Gaus mean : 40.5132
Gaus Width: 2.8337

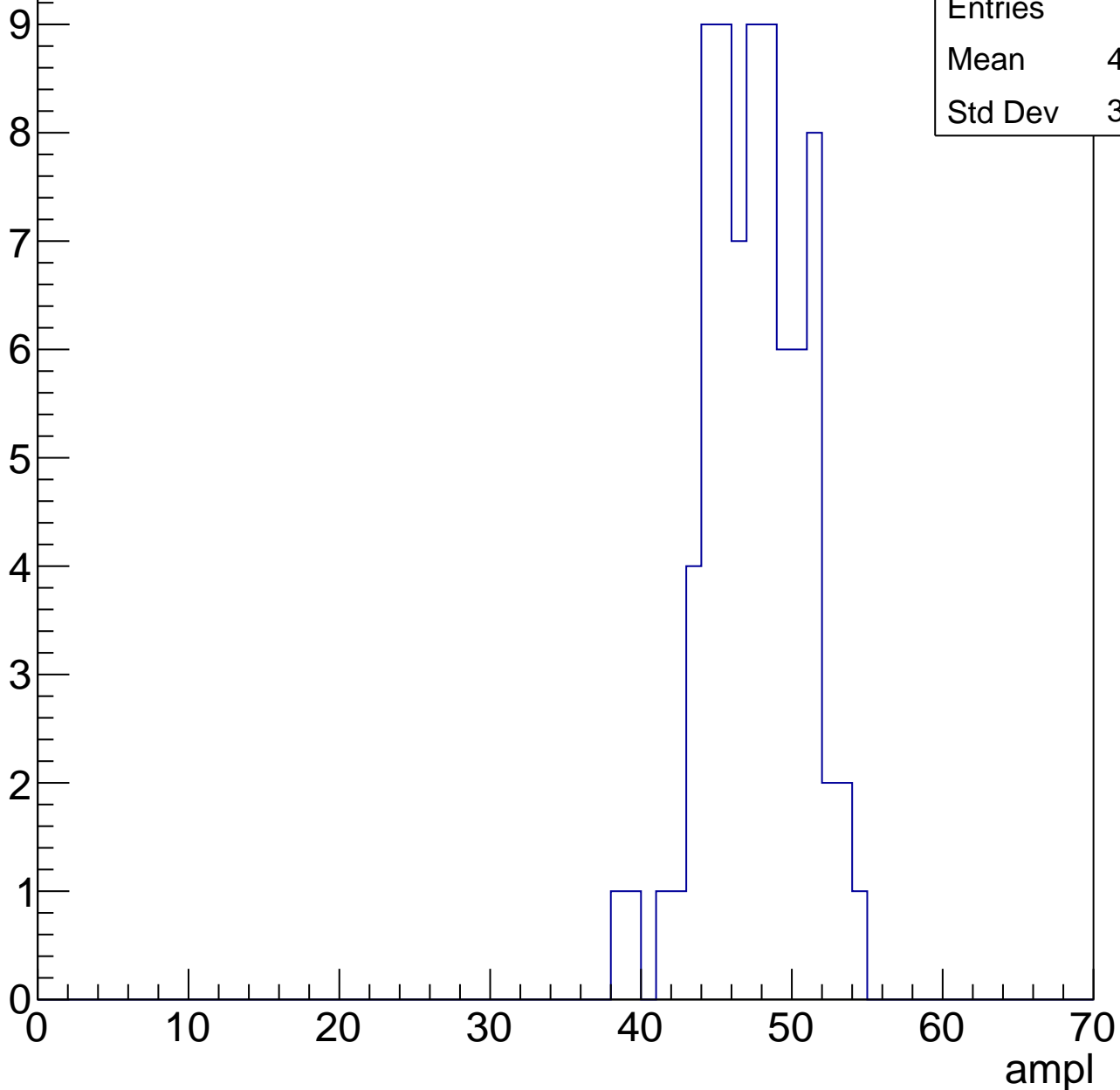


B1L102S, U6-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	47.05
Std Dev	3.203

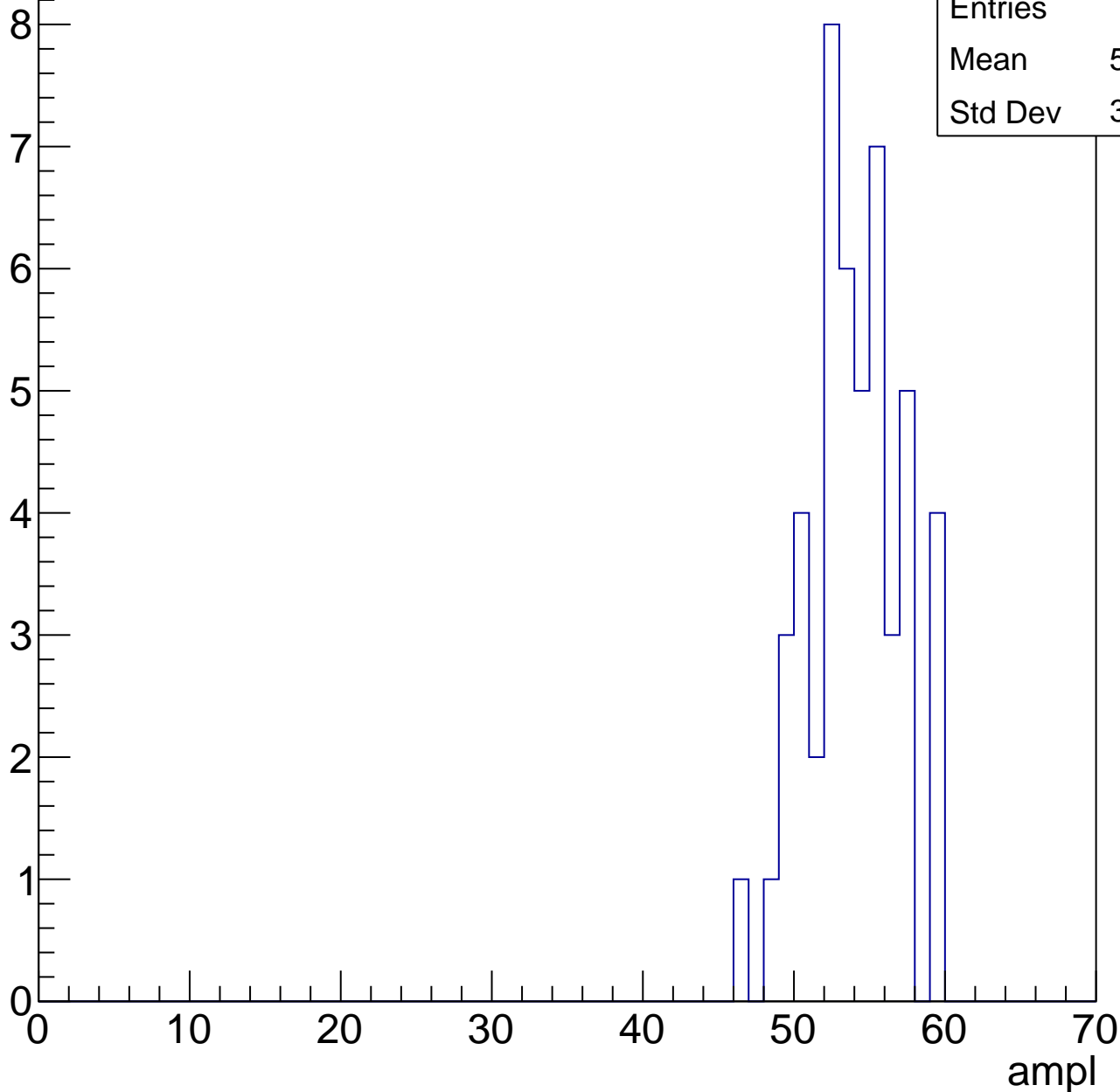


B1L102S, U6-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.49
Std Dev	3.018



B1L102S, U6-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

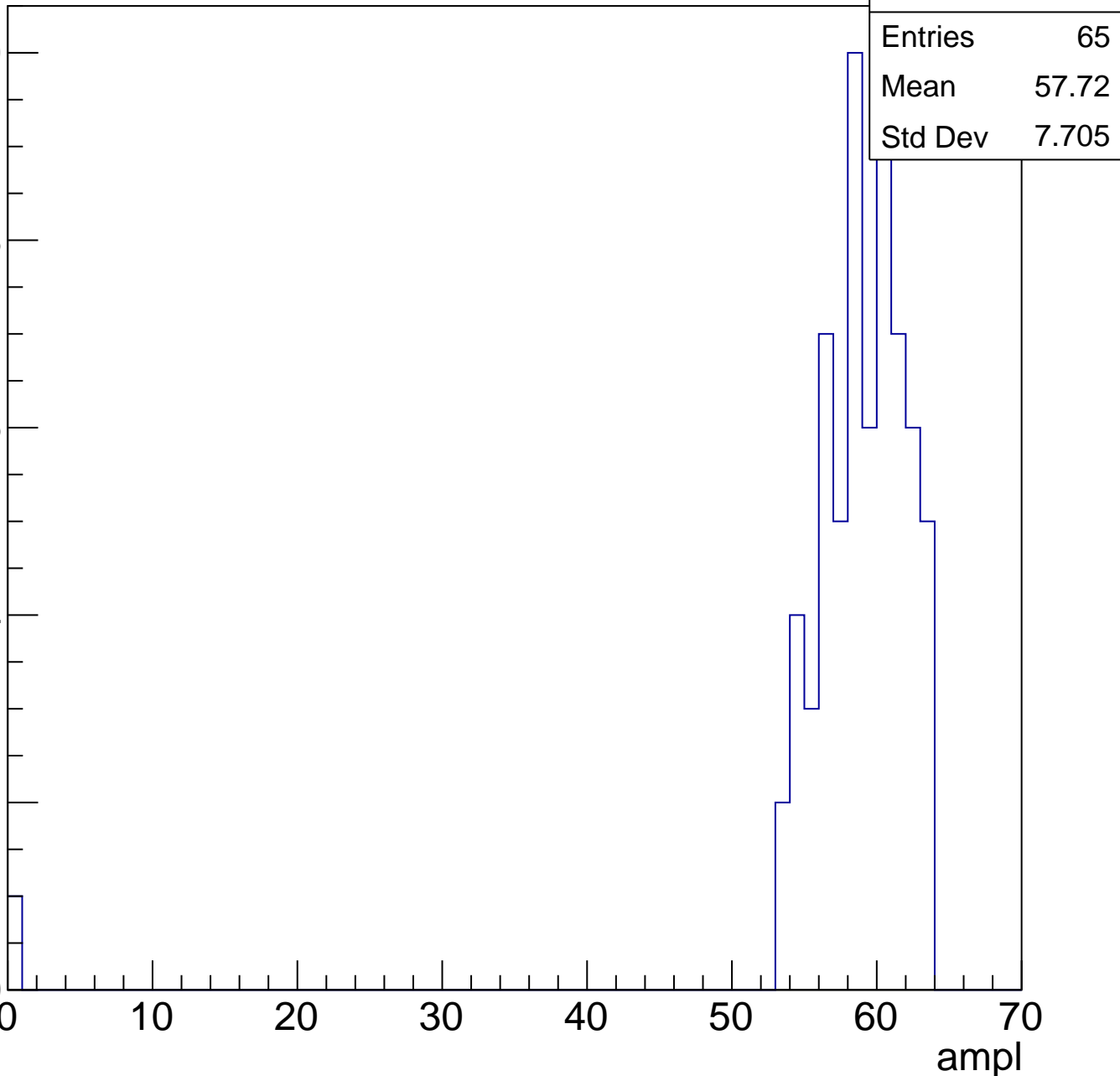
50

60

70

ampl

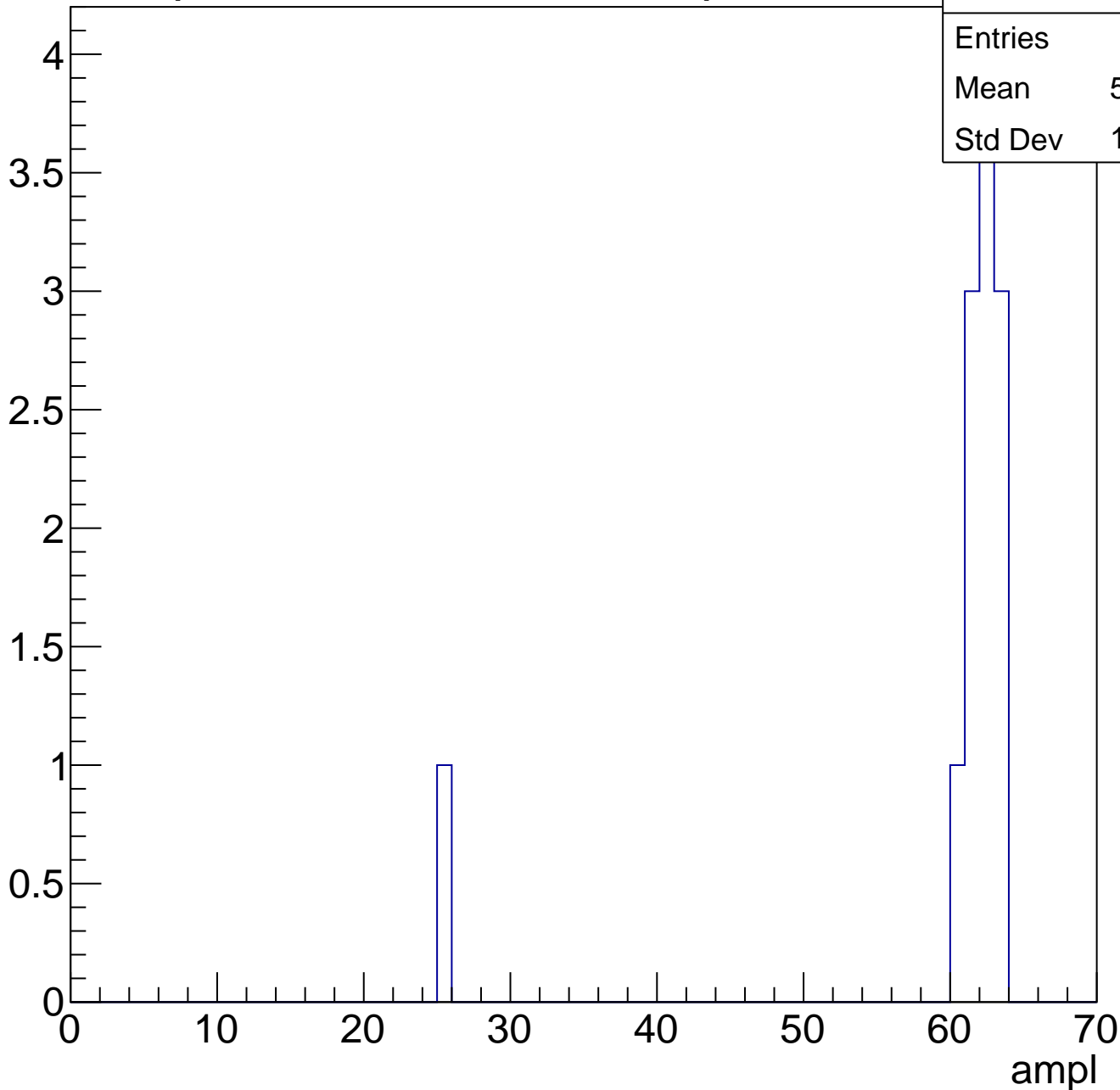
Entries	65
Mean	57.72
Std Dev	7.705



B1L102S, U6-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

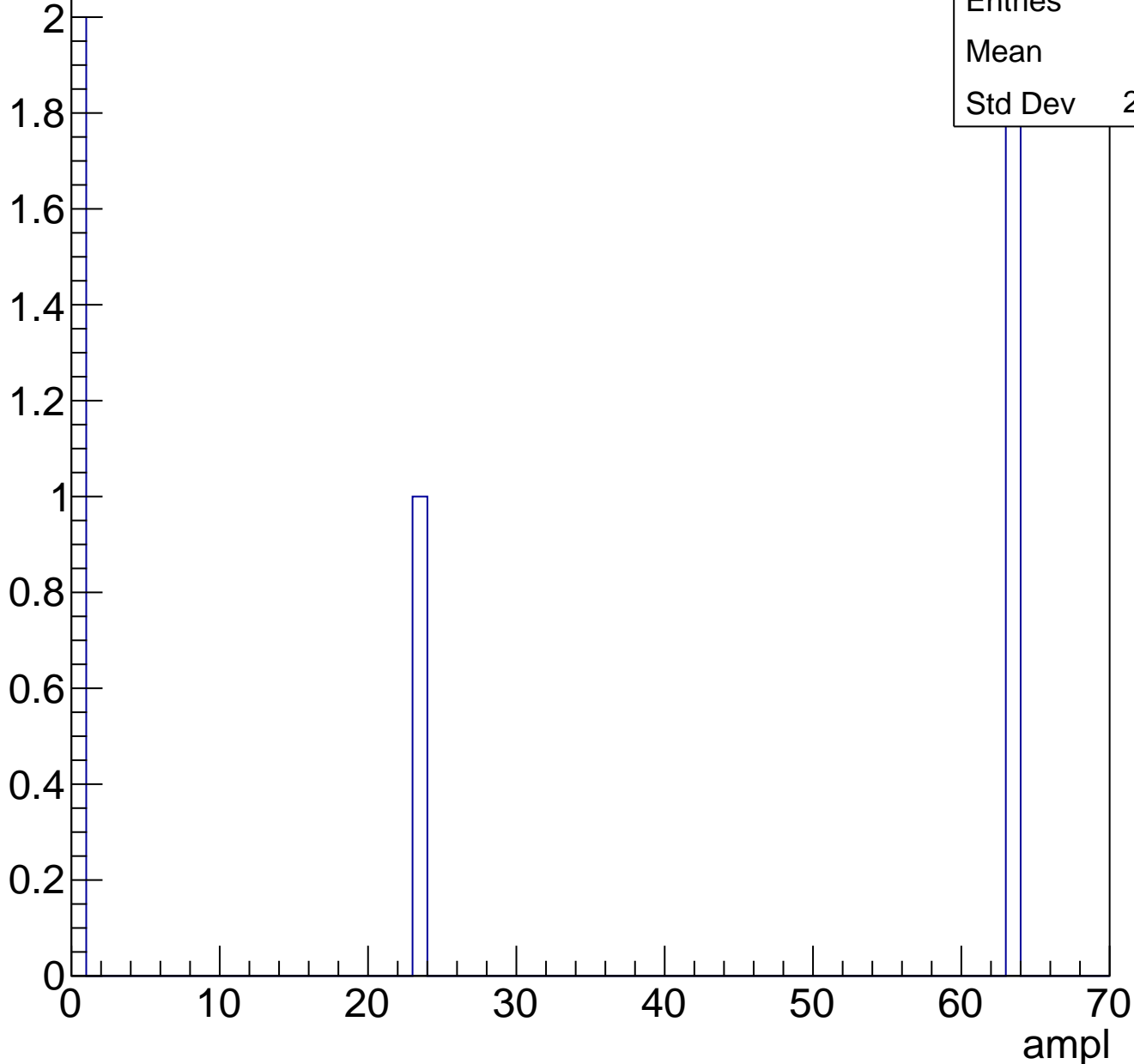
Entry



B1L102S, U6-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch41, adc0

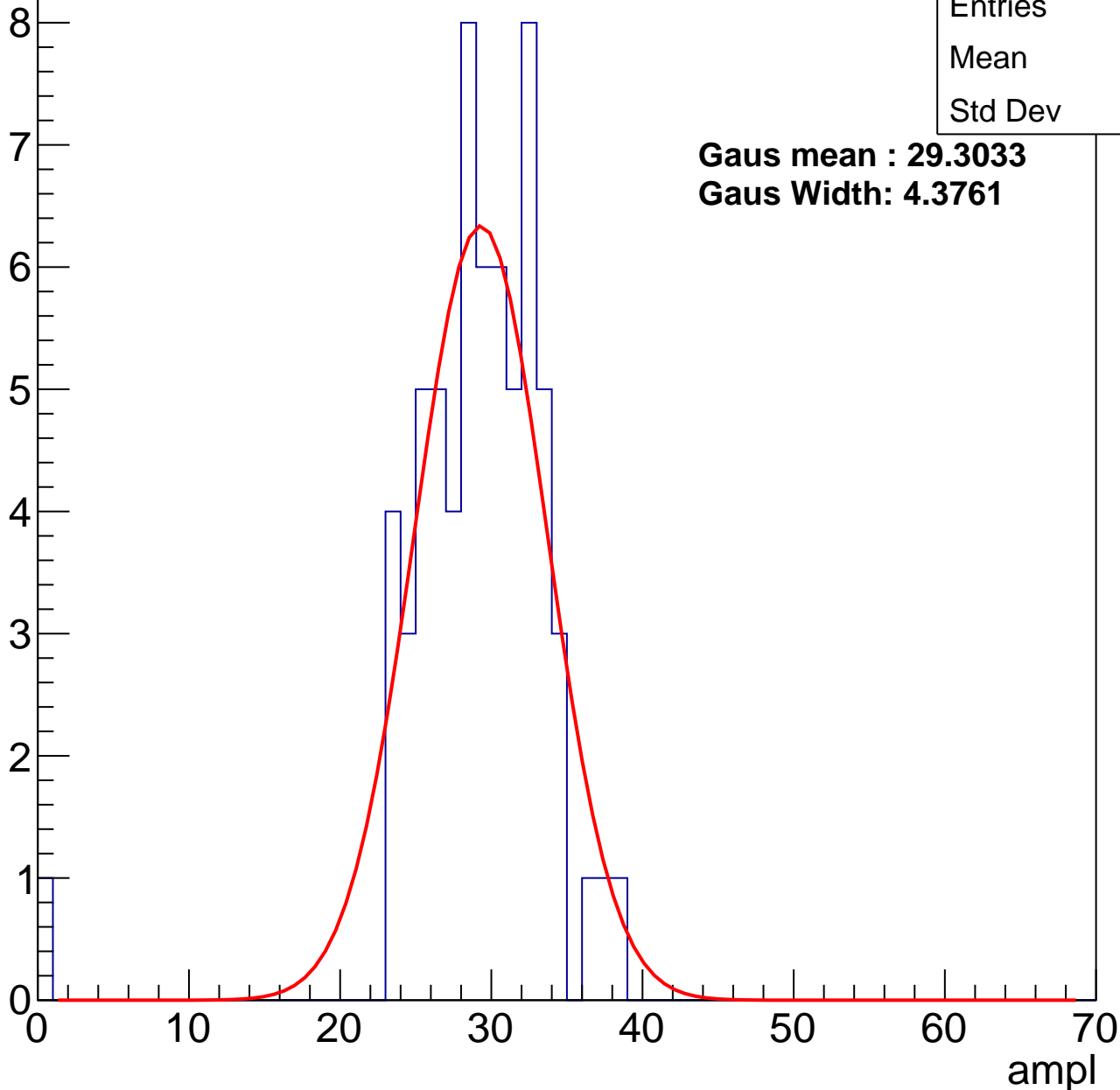
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.7
Std Dev	5

Gaus mean : 29.3033

Gaus Width: 4.3761



B1L102S, U6-ch41, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	36
Std Dev	3.356

Gaus mean : 36.8573

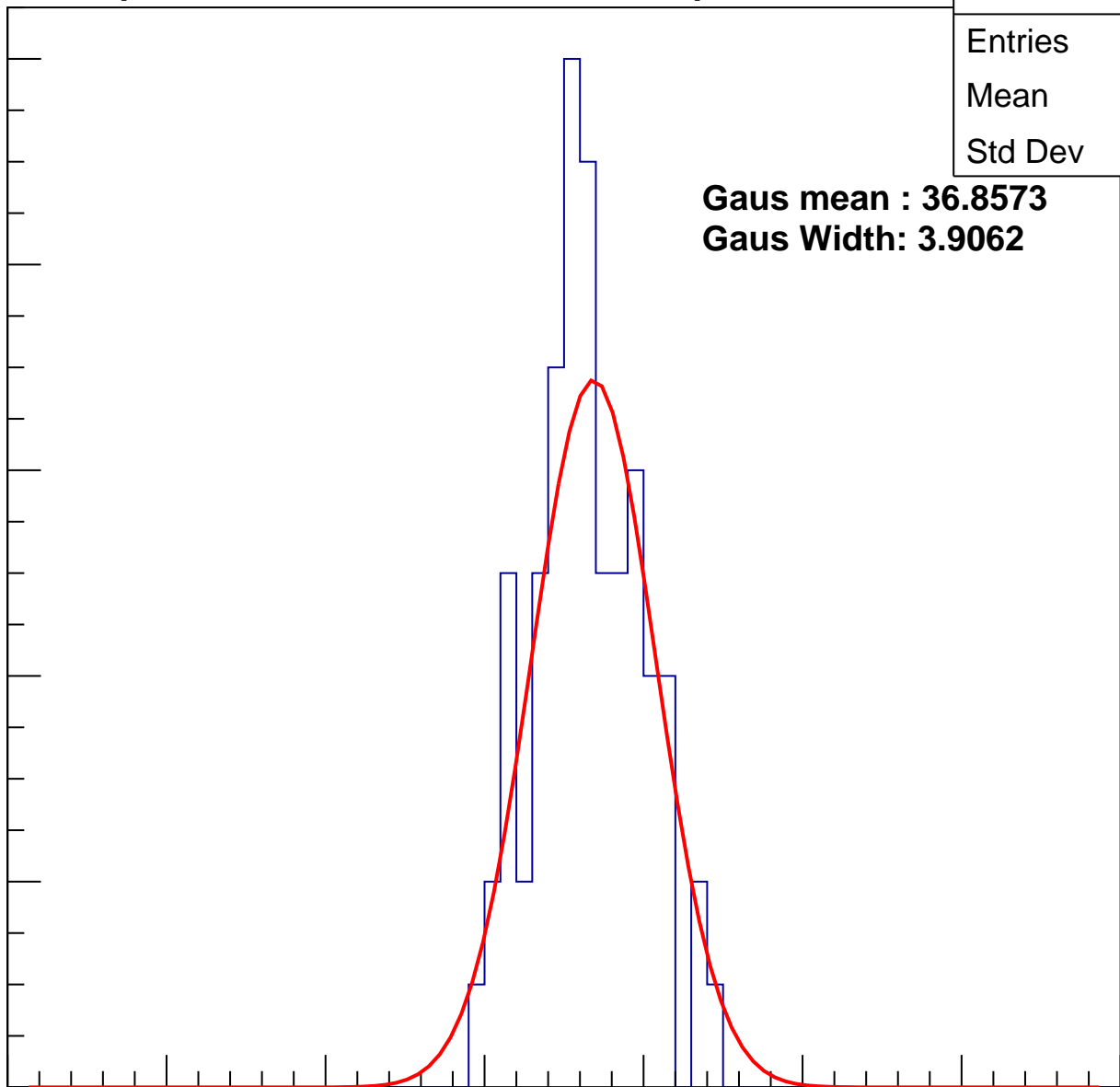
Gaus Width: 3.9062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch41, adc2

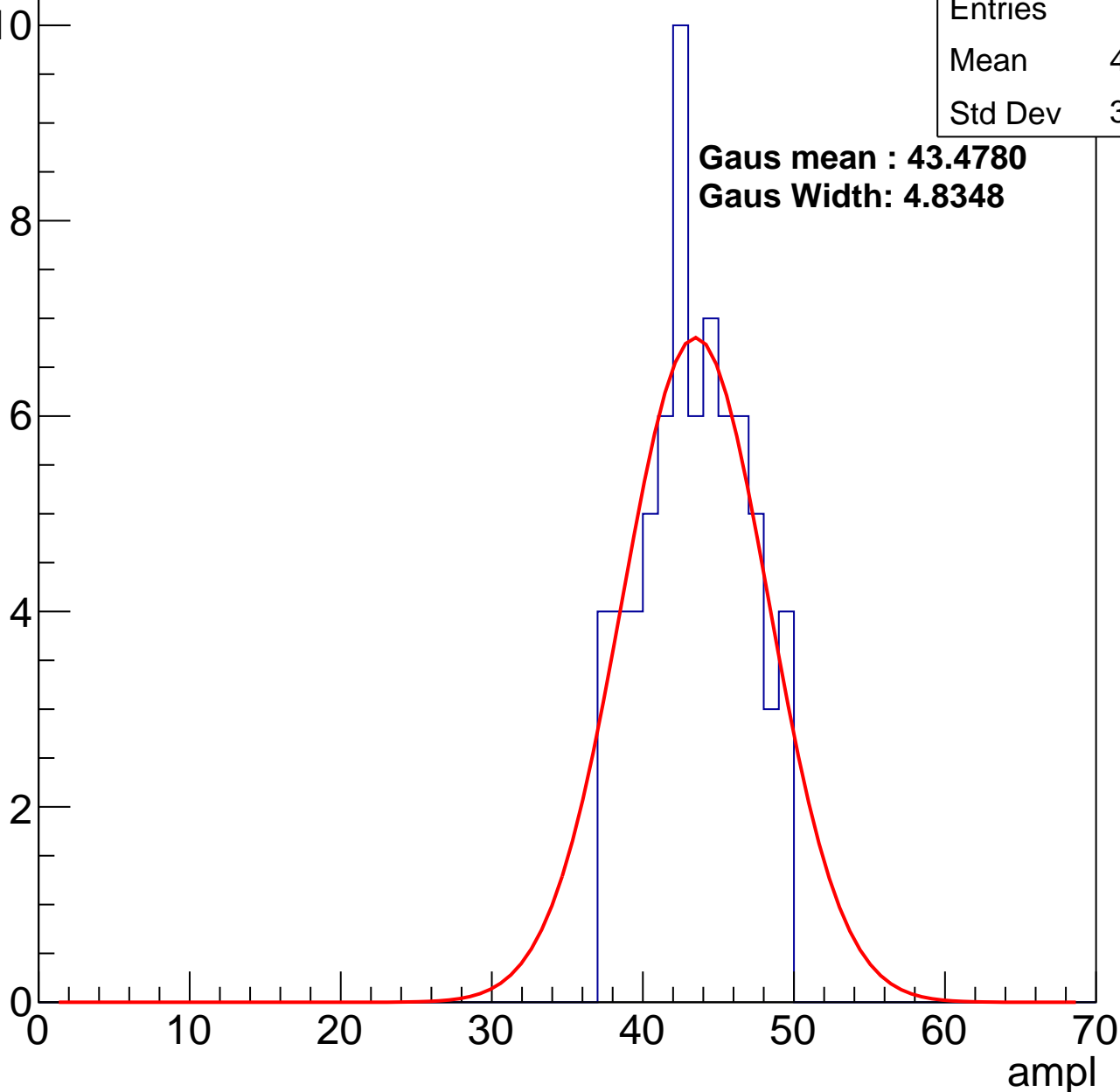
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.99
Std Dev	3.319

Gaus mean : 43.4780

Gaus Width: 4.8348

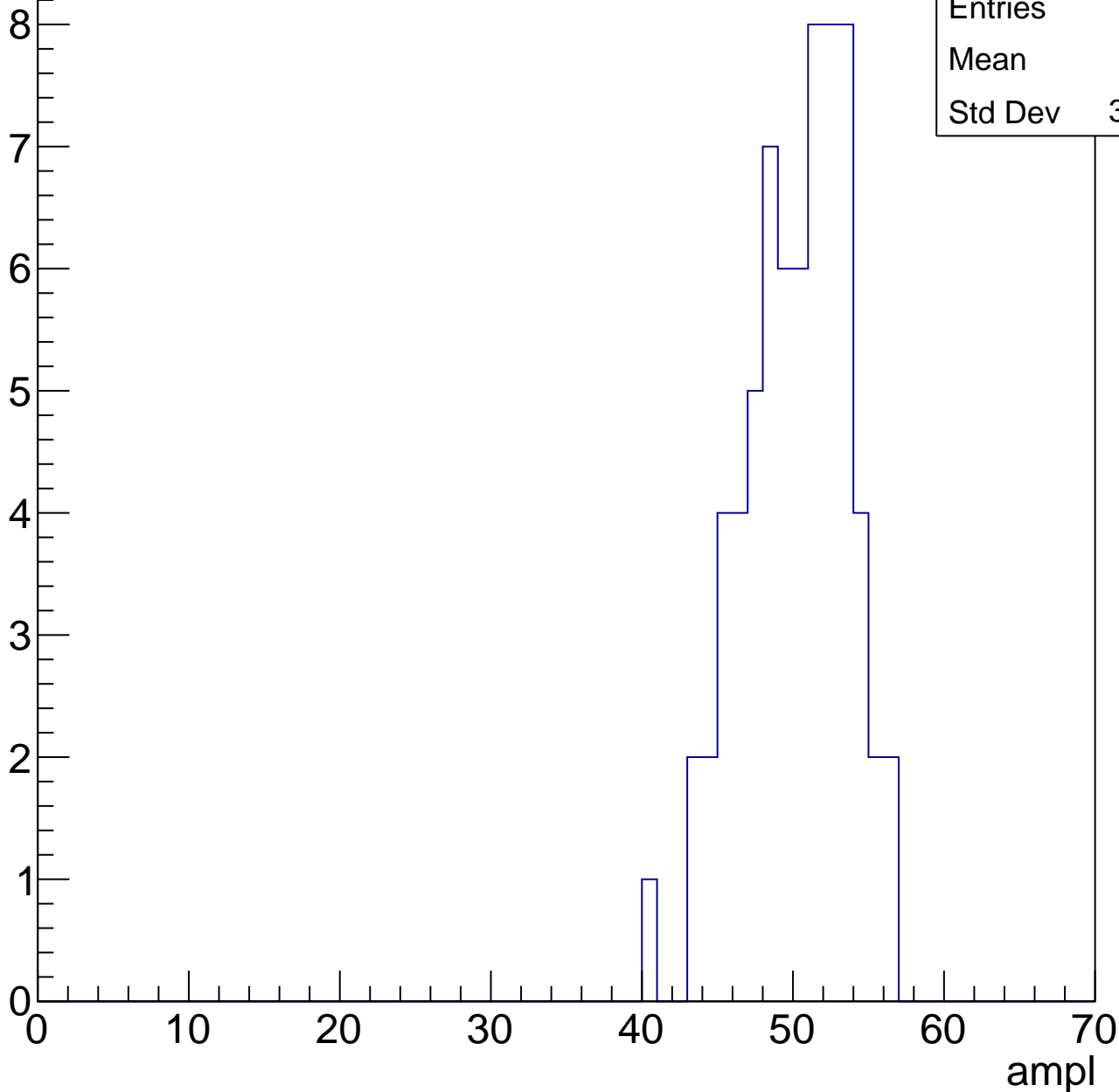


B1L102S, U6-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

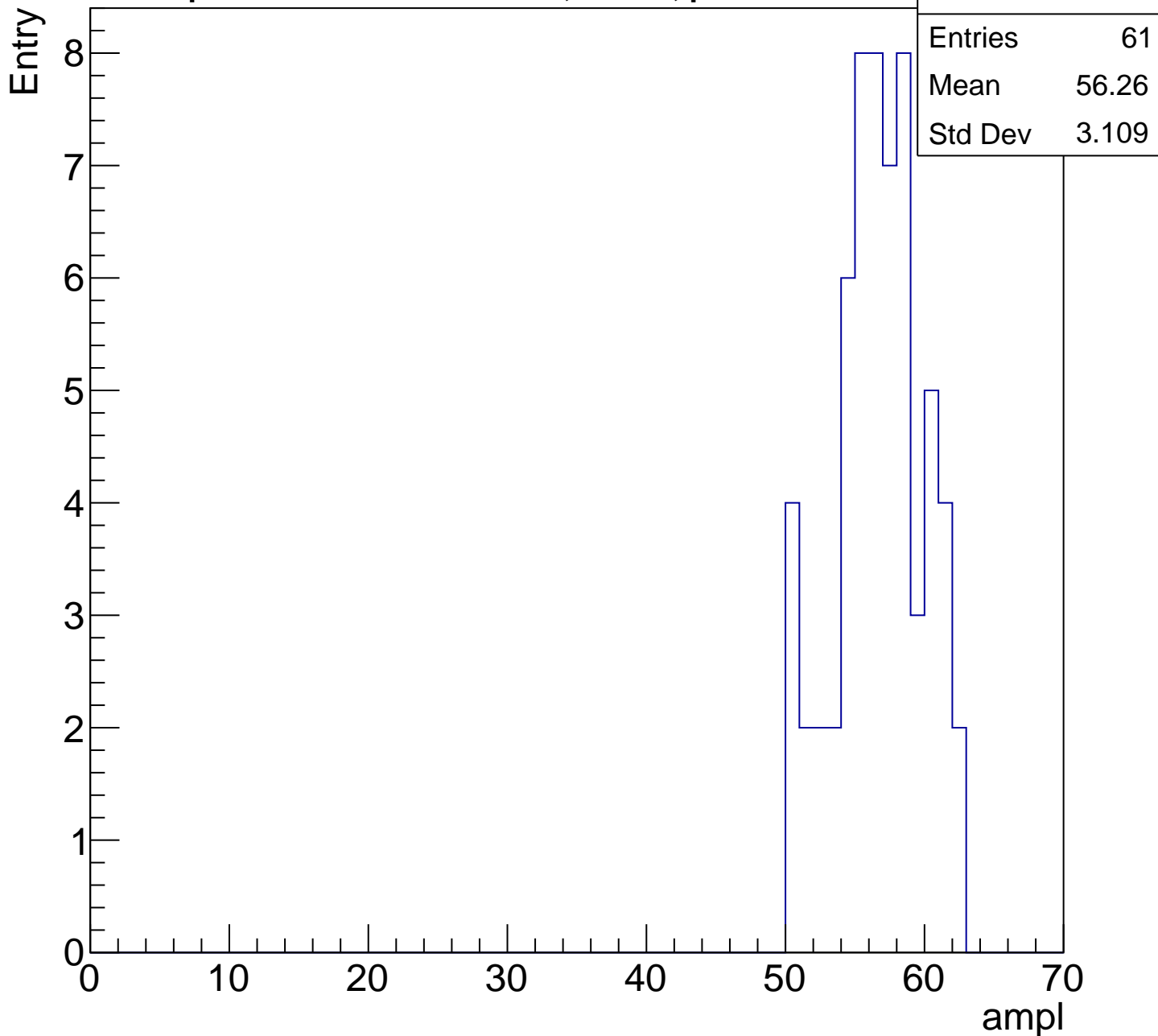
Entry

Entries	69
Mean	49.7
Std Dev	3.415



B1L102S, U6-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

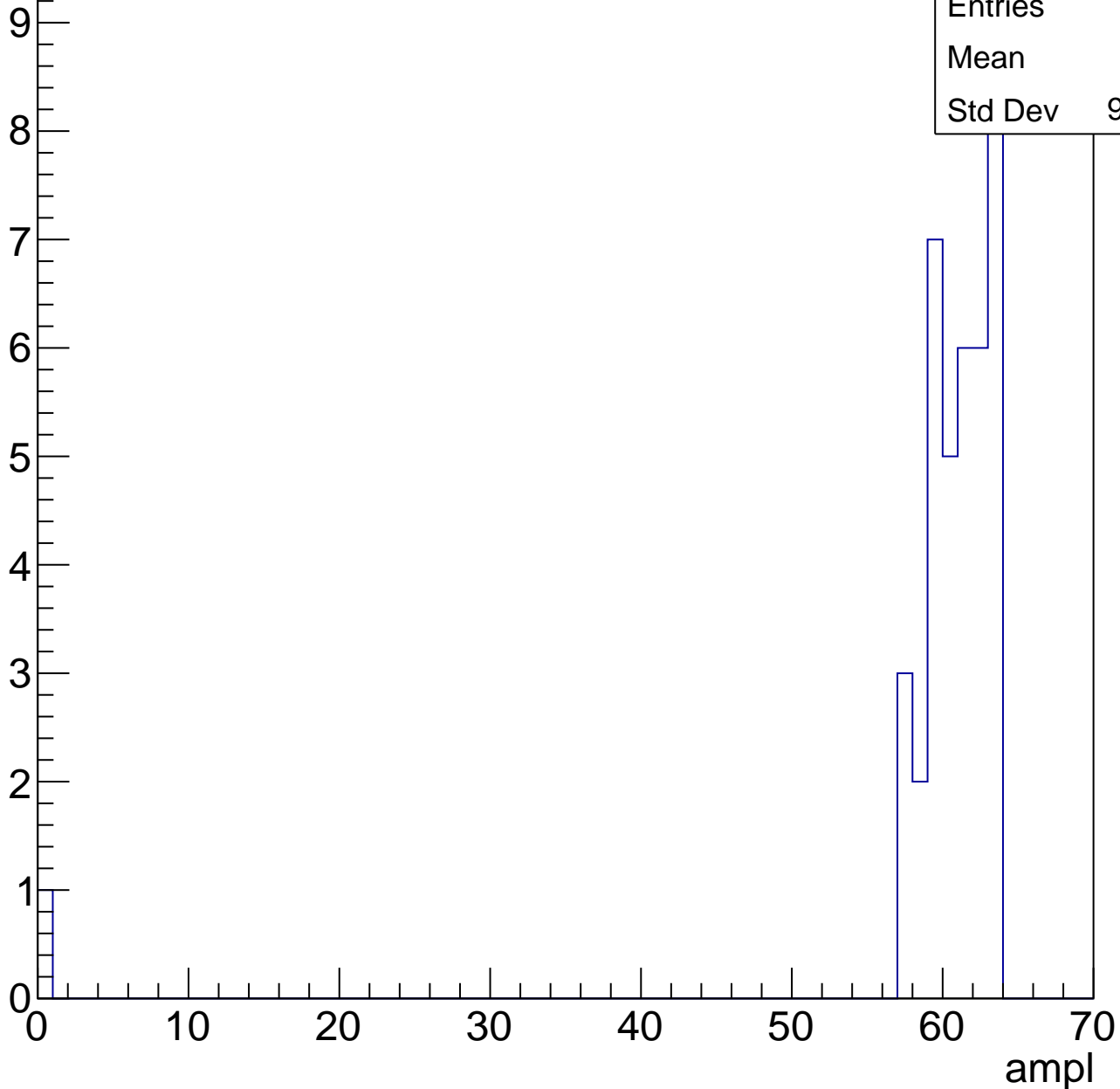


B1L102S, U6-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	39
Mean	59.1
Std Dev	9.769



B1L102S, U6-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L102S, U6-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch42, adc0

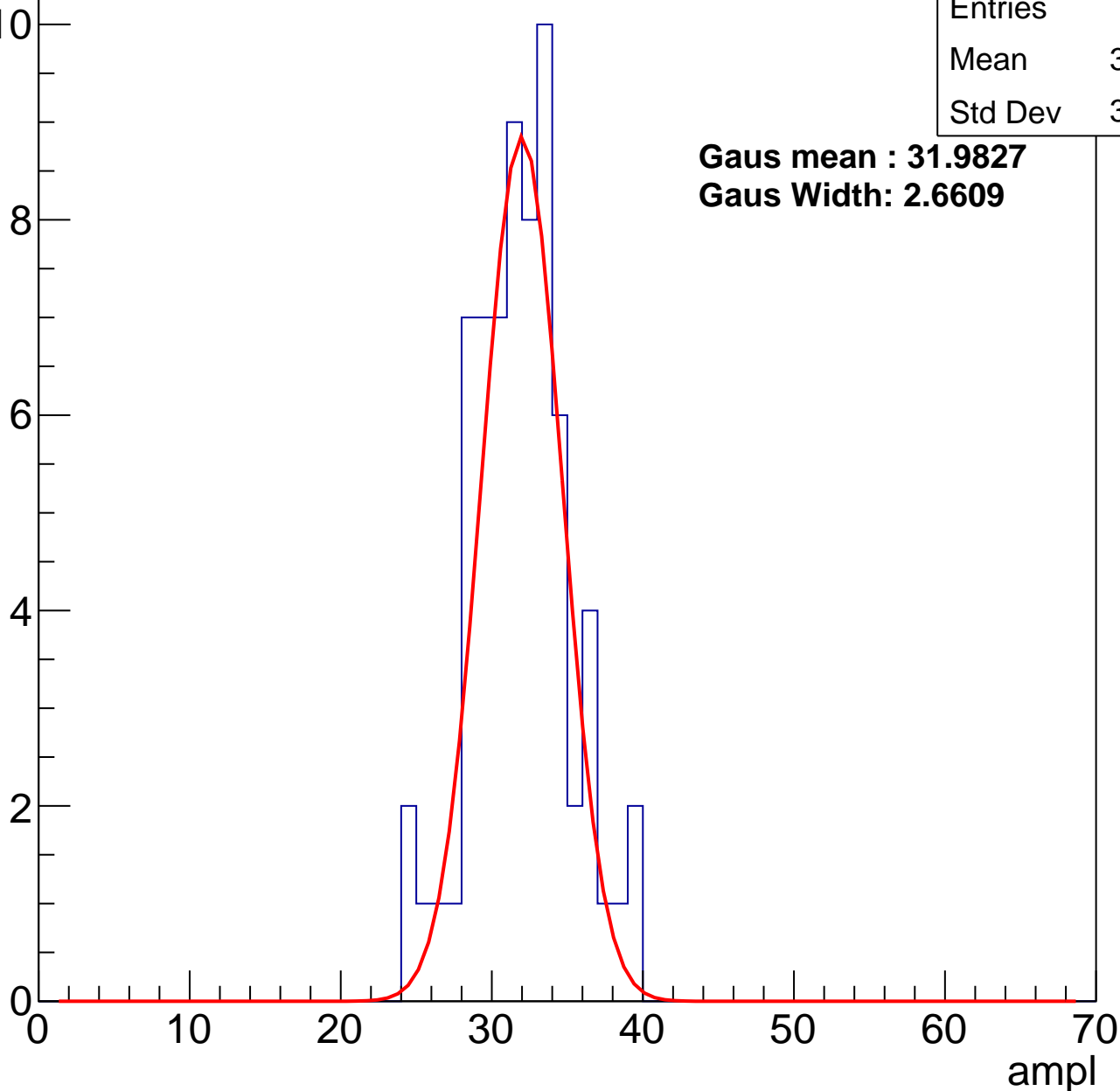
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	31.46
Std Dev	3.197

Gaus mean : 31.9827

Gaus Width: 2.6609



B1L102S, U6-ch42, adc1

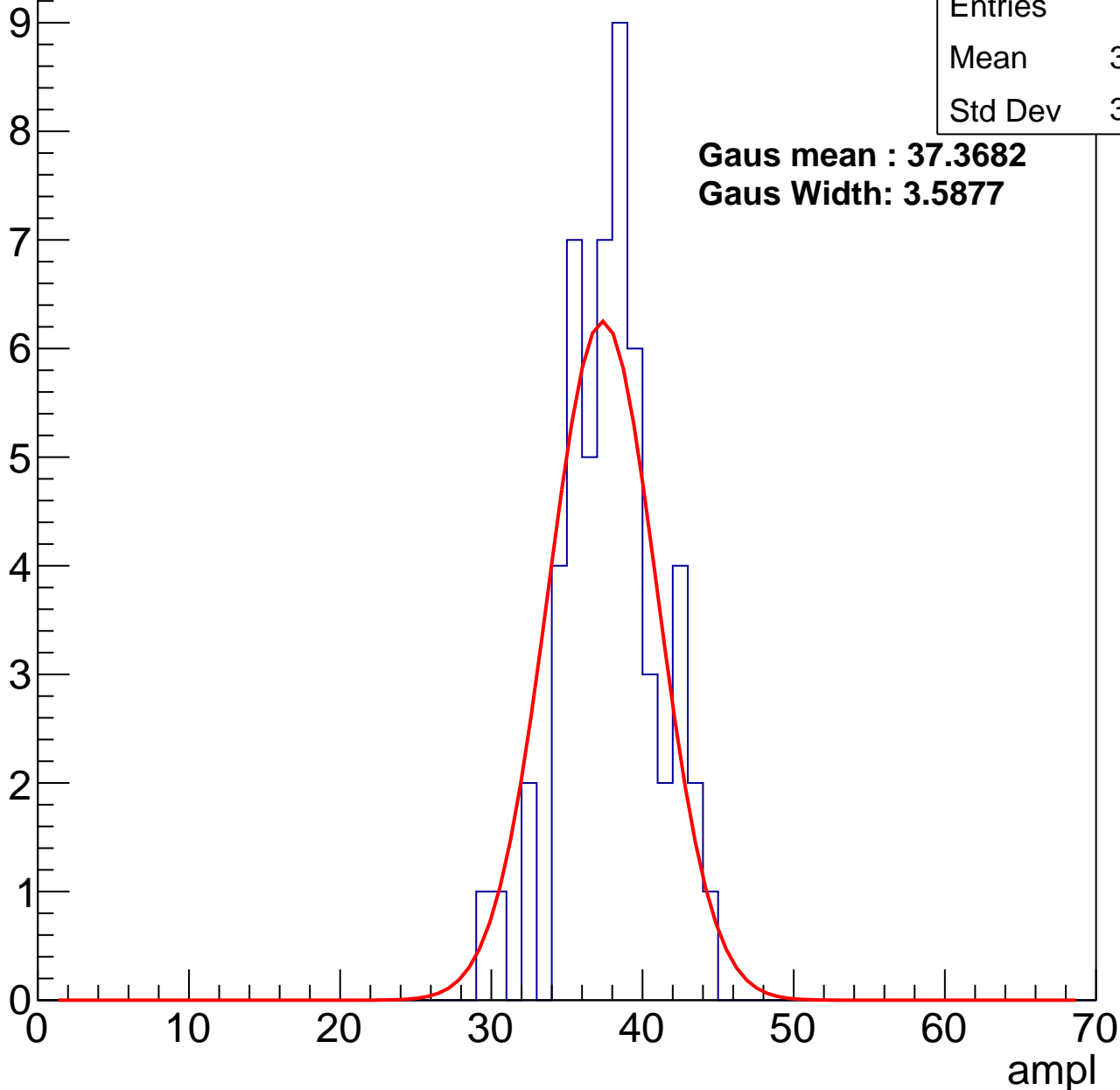
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.39
Std Dev	3.153

Gaus mean : 37.3682

Gaus Width: 3.5877



B1L102S, U6-ch42, adc2

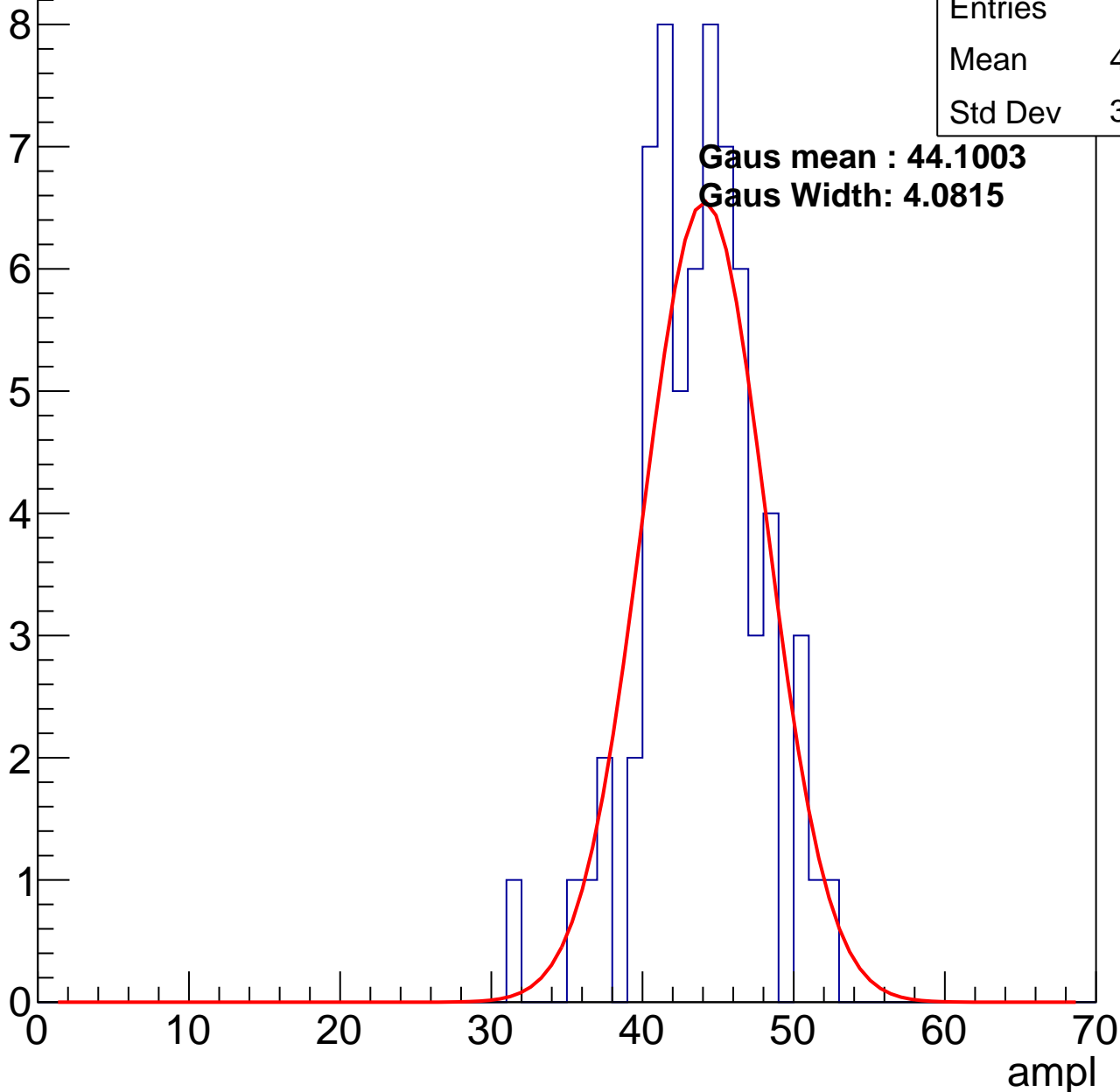
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.32
Std Dev	3.862

Gaus mean : 44.1003

Gaus Width: 4.0815

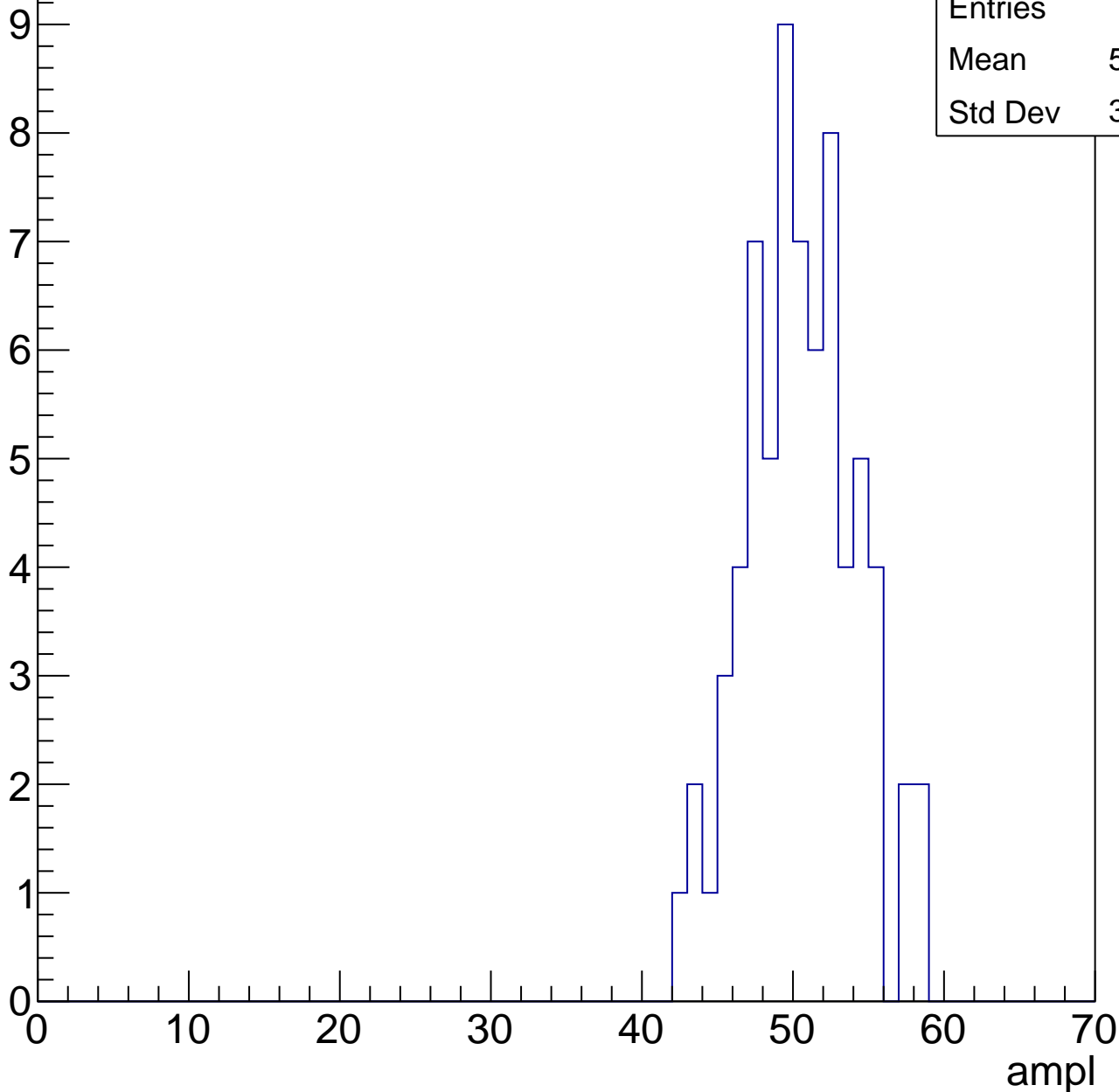


B1L102S, U6-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.07
Std Dev	3.603



B1L102S, U6-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	53
Mean	56
Std Dev	3.156

Entry

10

8

6

4

2

0

0

10

20

30

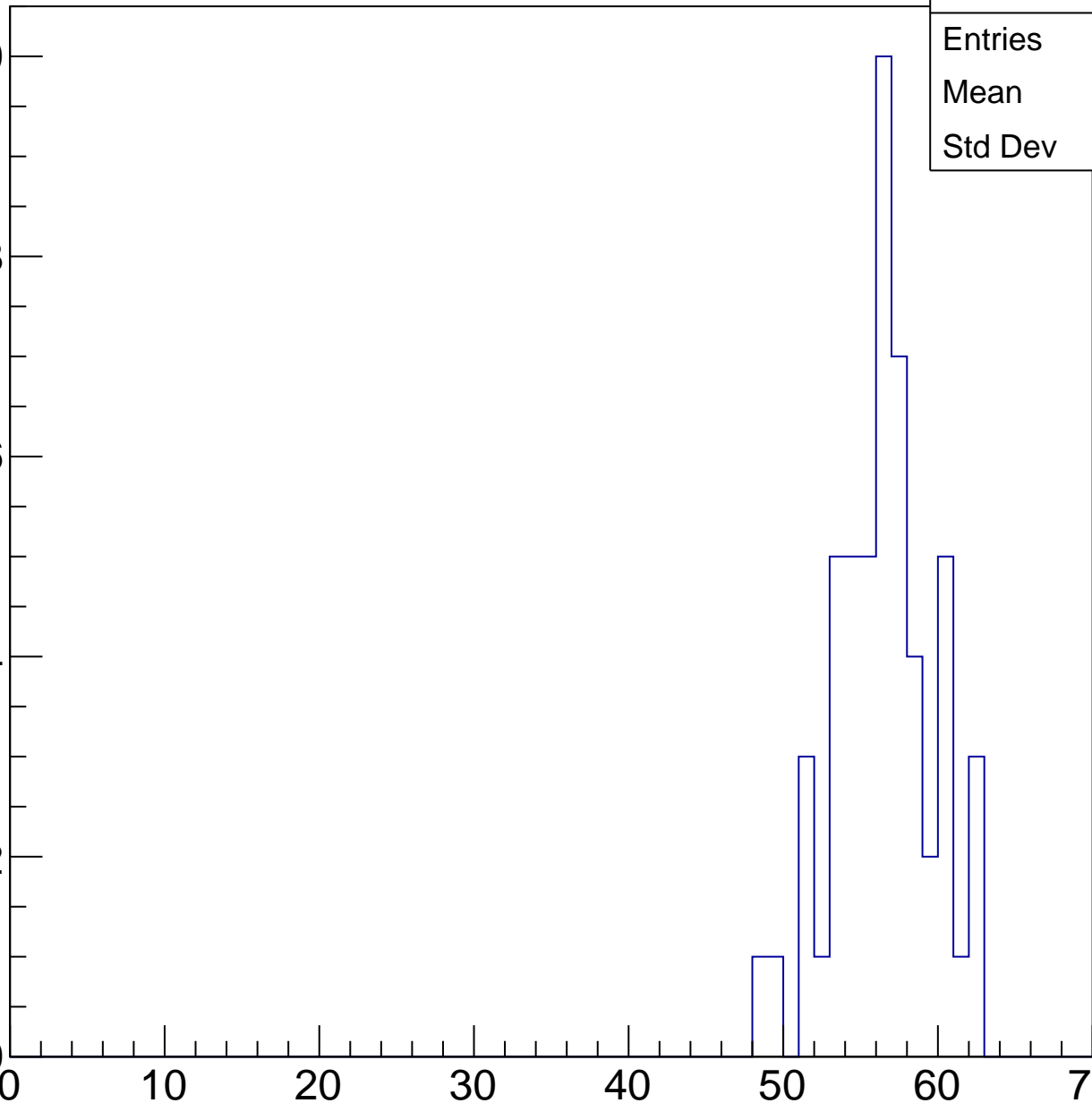
40

50

ampl

60

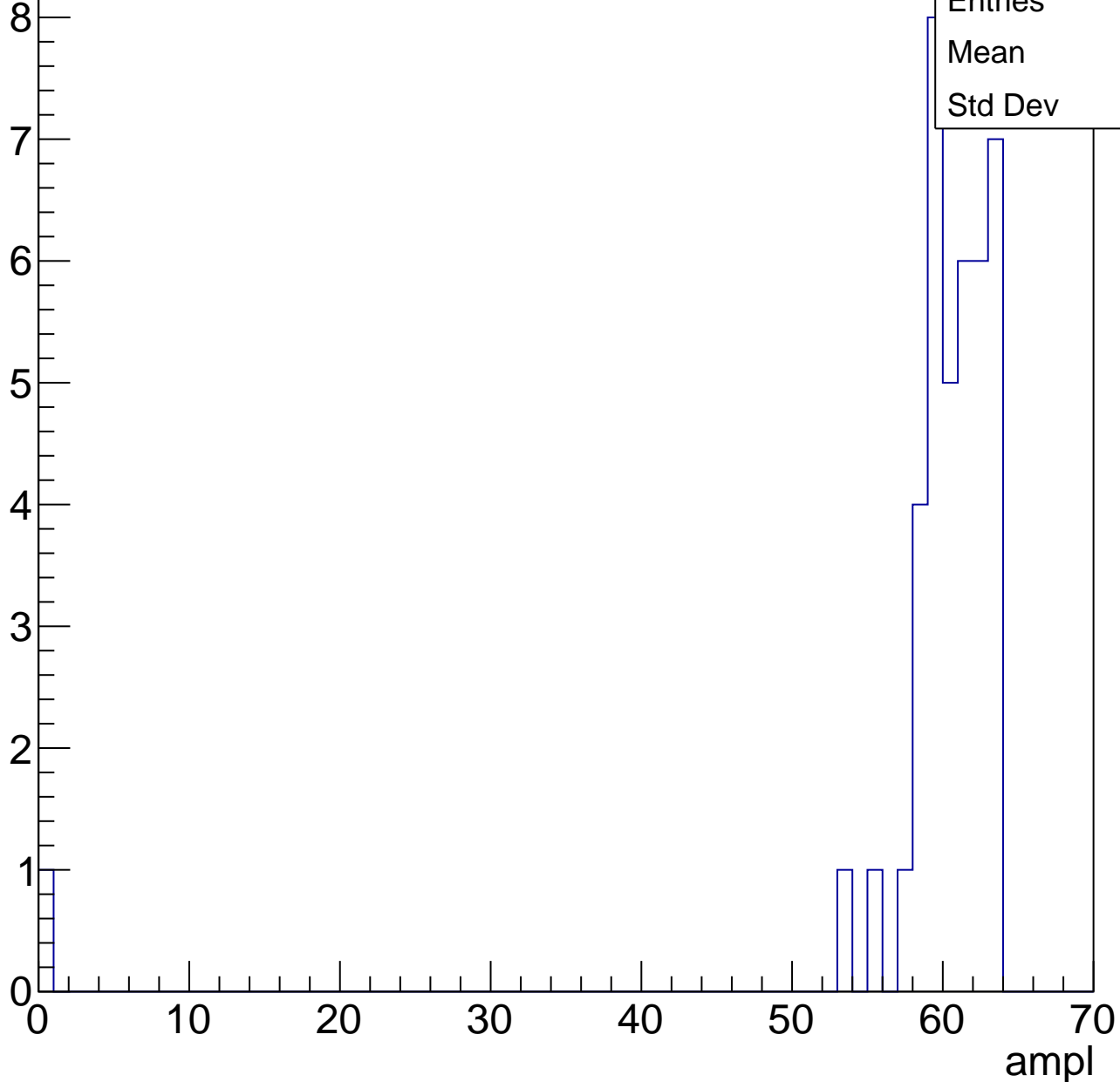
70



B1L102S, U6-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

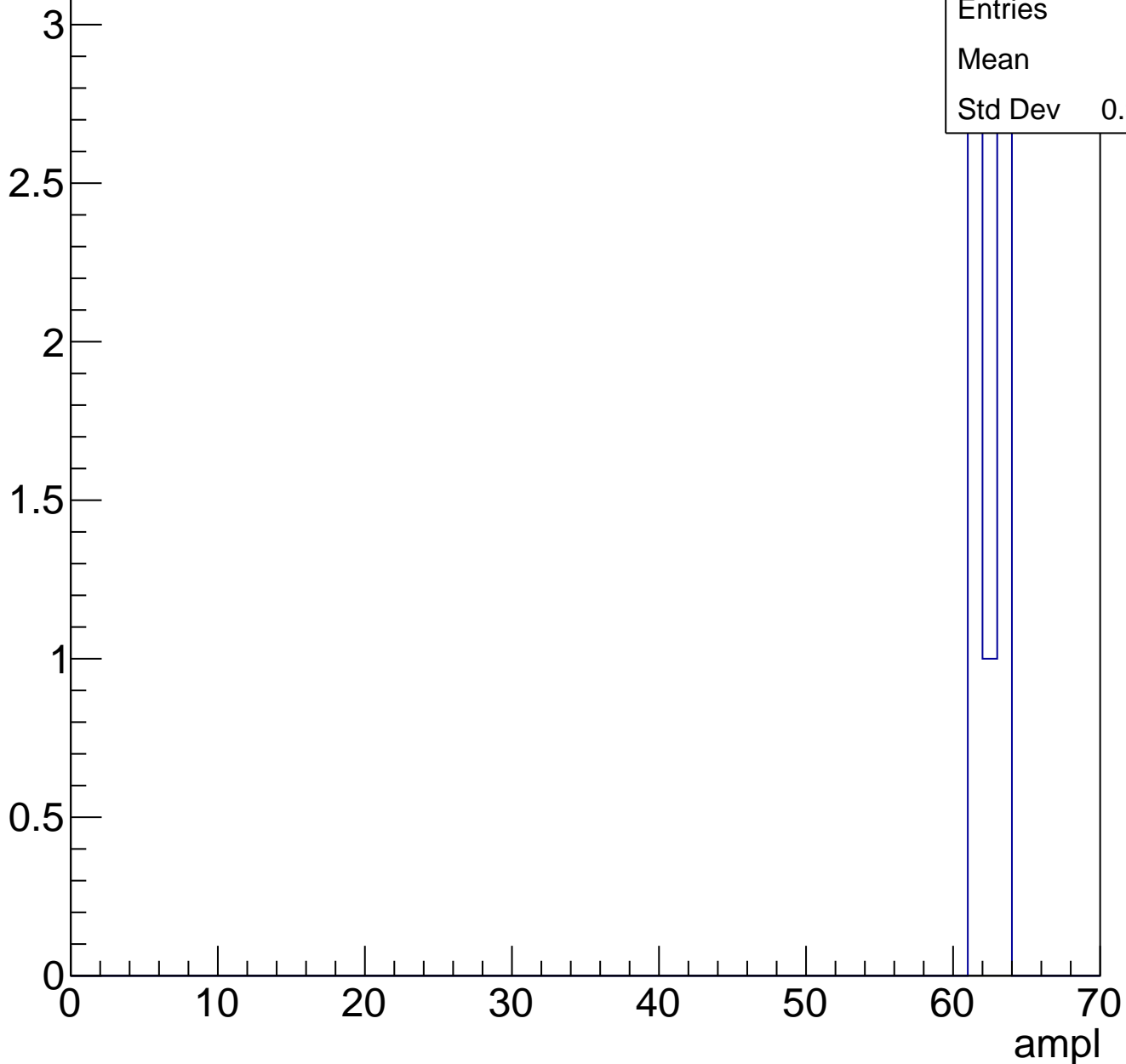
Entry



B1L102S, U6-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch43, adc0

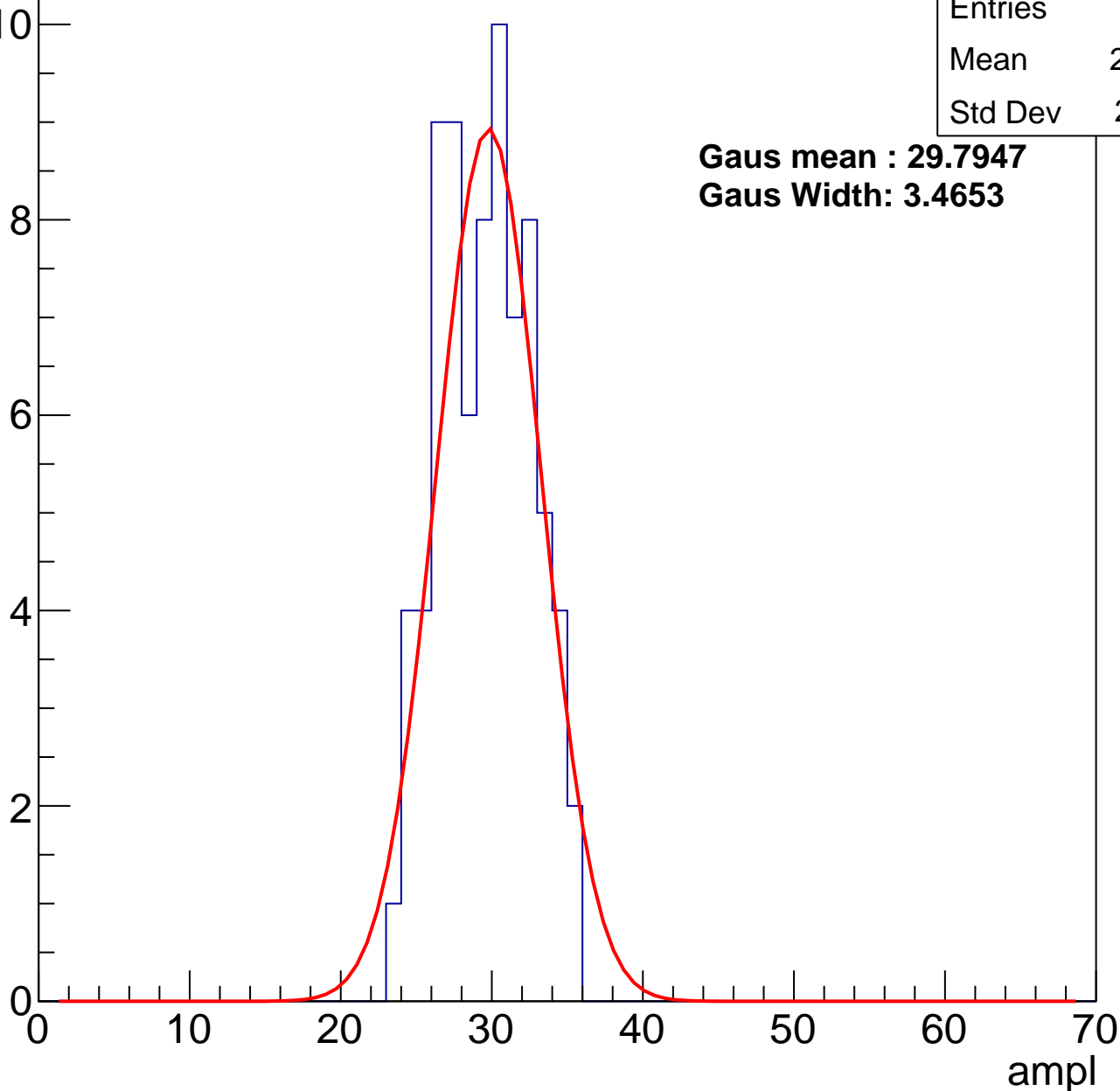
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	29.09
Std Dev	2.981

Gaus mean : 29.7947

Gaus Width: 3.4653



B1L102S, U6-ch43, adc1

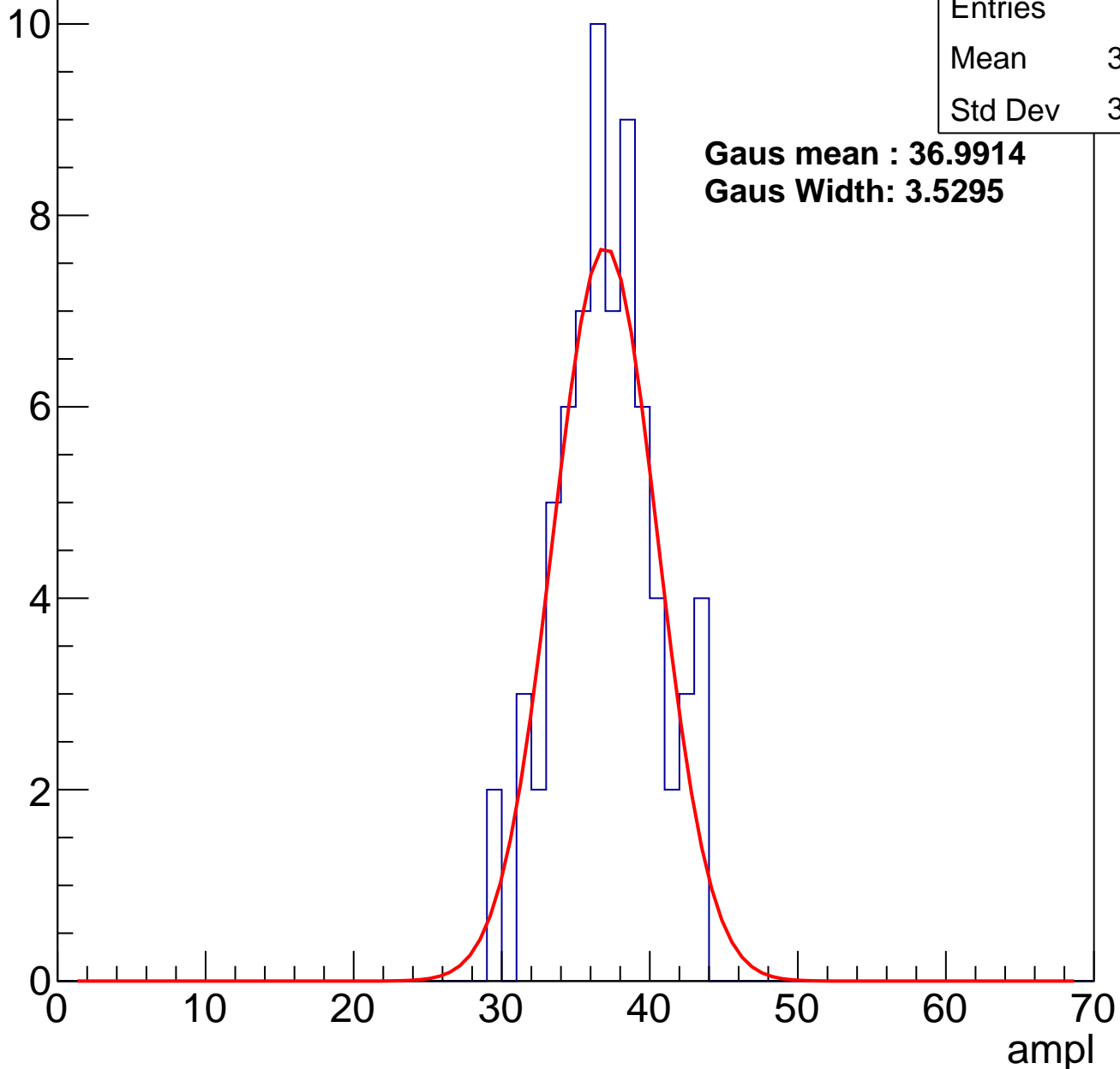
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	36.63
Std Dev	3.313

Gaus mean : 36.9914

Gaus Width: 3.5295

Entry



B1L102S, U6-ch43, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.19
Std Dev	3.292

Gaus mean : 43.9483

Gaus Width: 3.5482

10

8

6

4

2

0

0

10

20

30

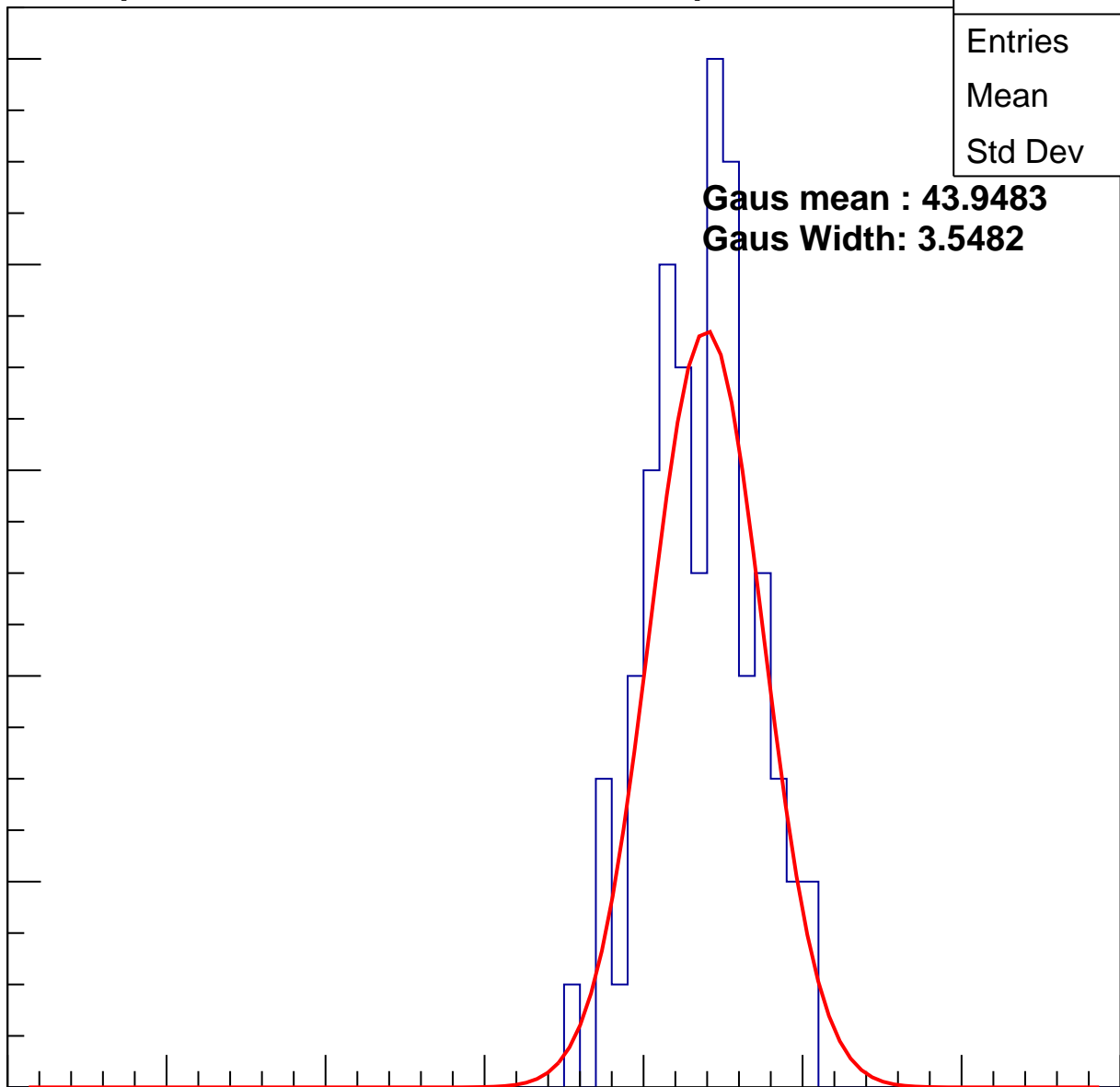
40

50

60

70

ampl

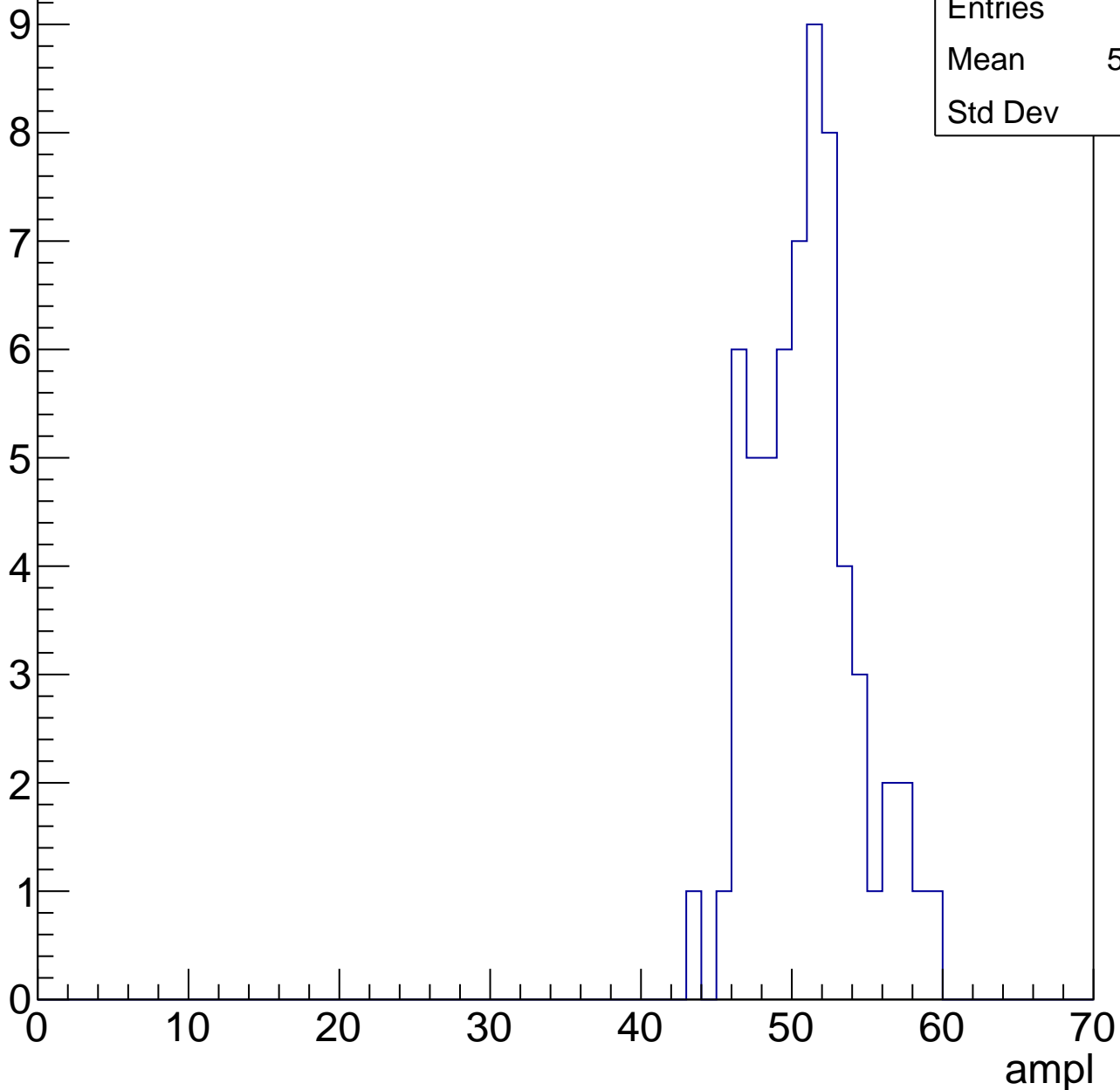


B1L102S, U6-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	50.48
Std Dev	3.34

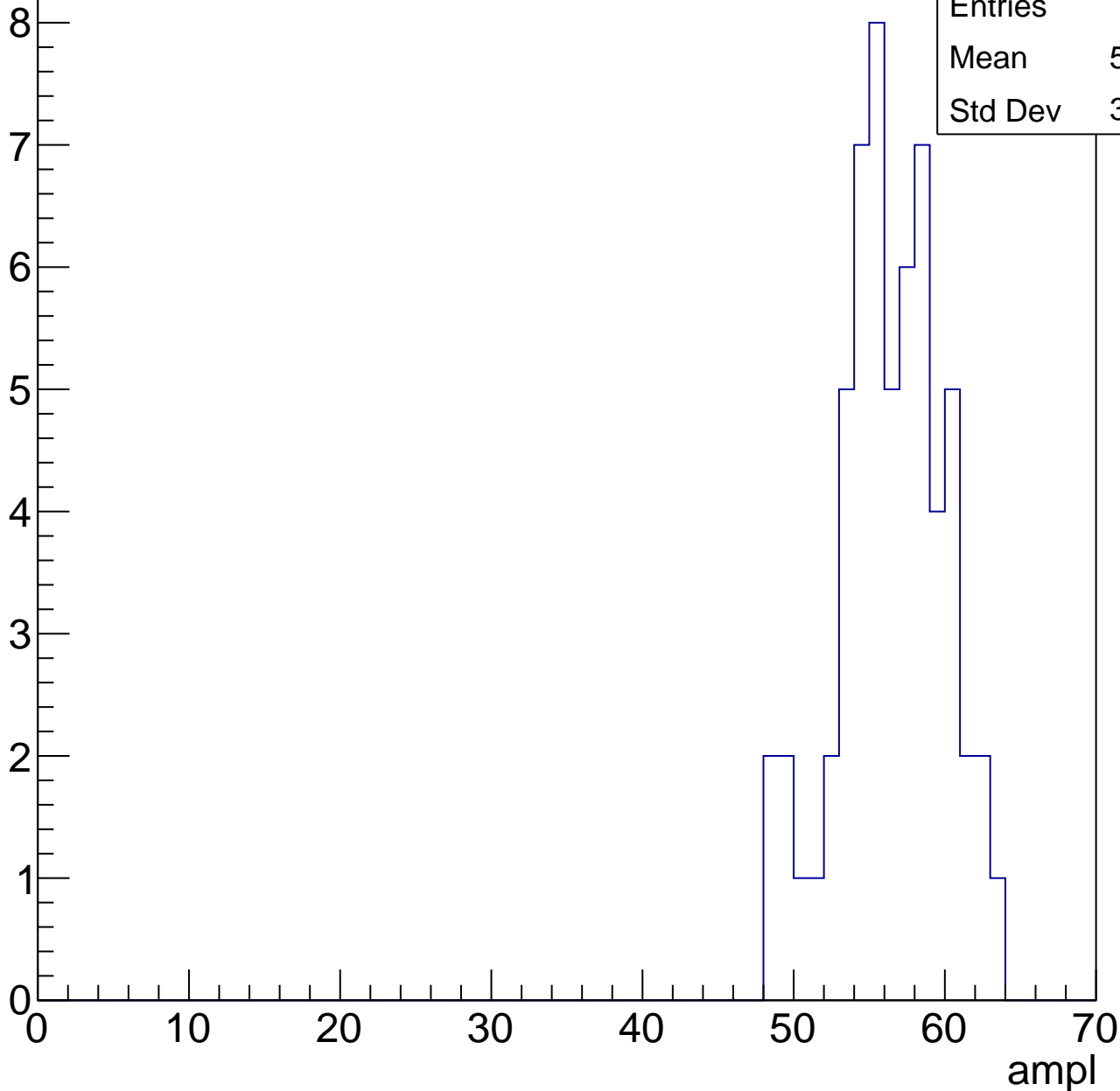


B1L102S, U6-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.92
Std Dev	3.466

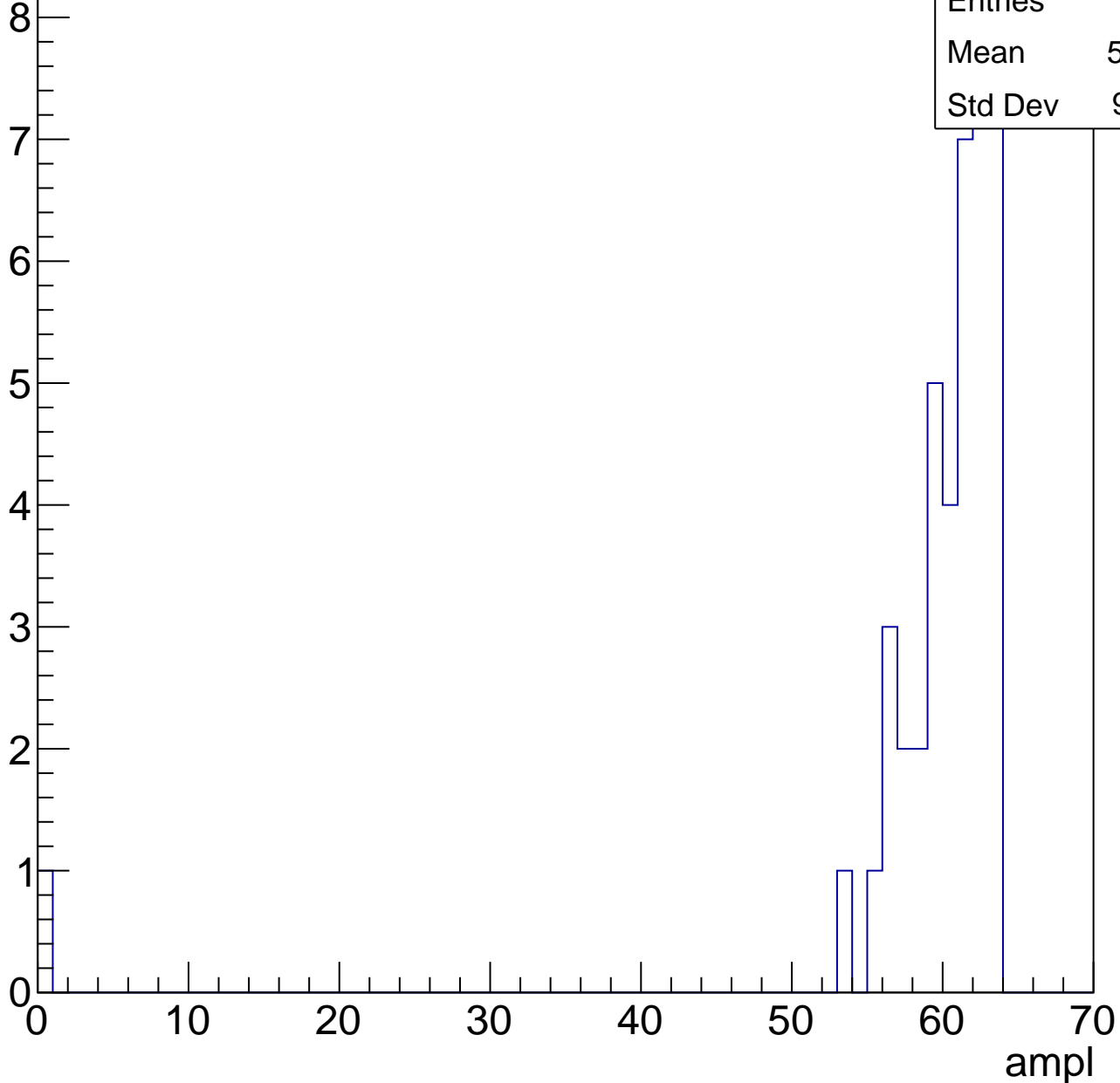


B1L102S, U6-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

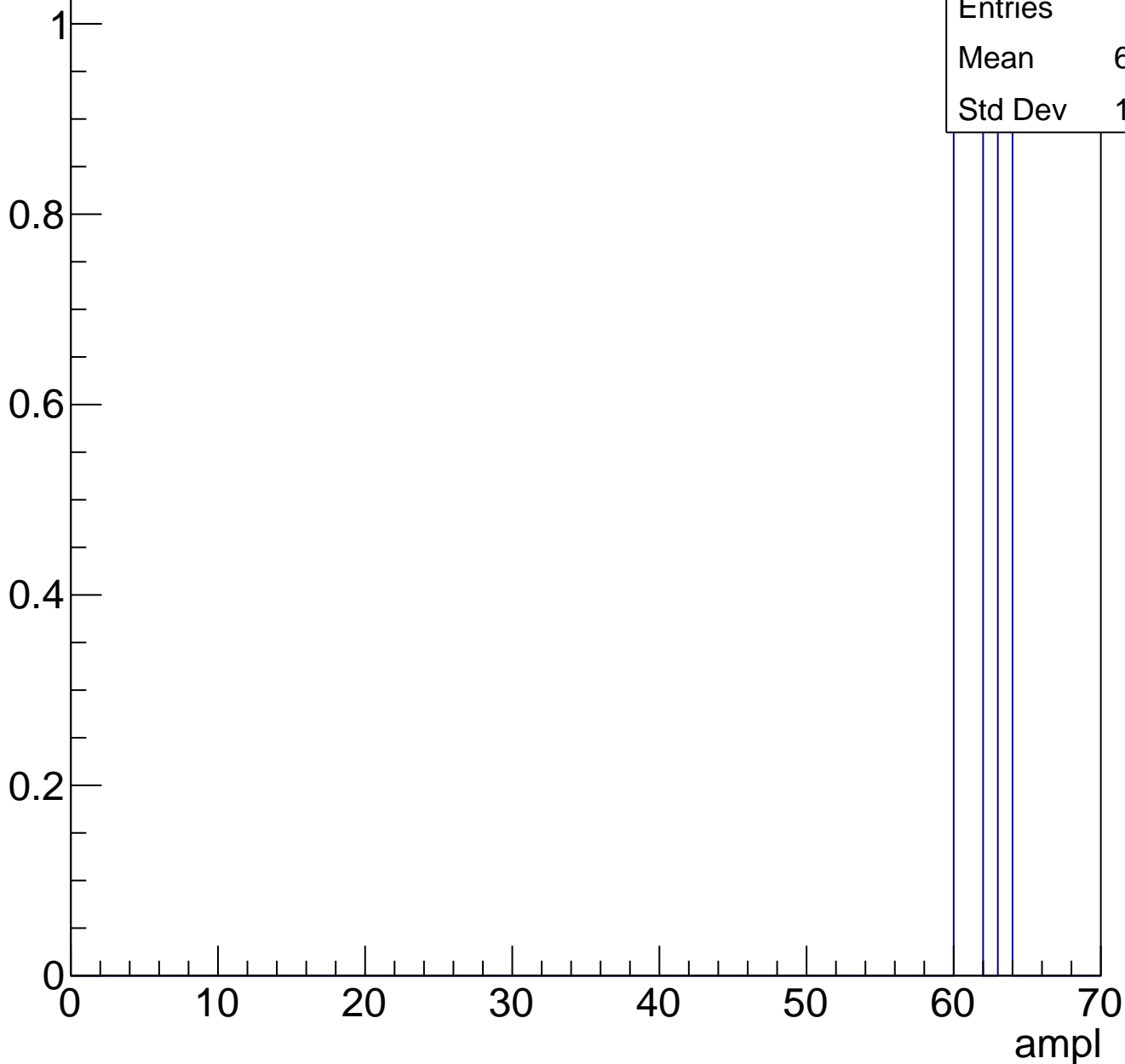
Entries	42
Mean	58.76
Std Dev	9.511



B1L102S, U6-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch44, adc0

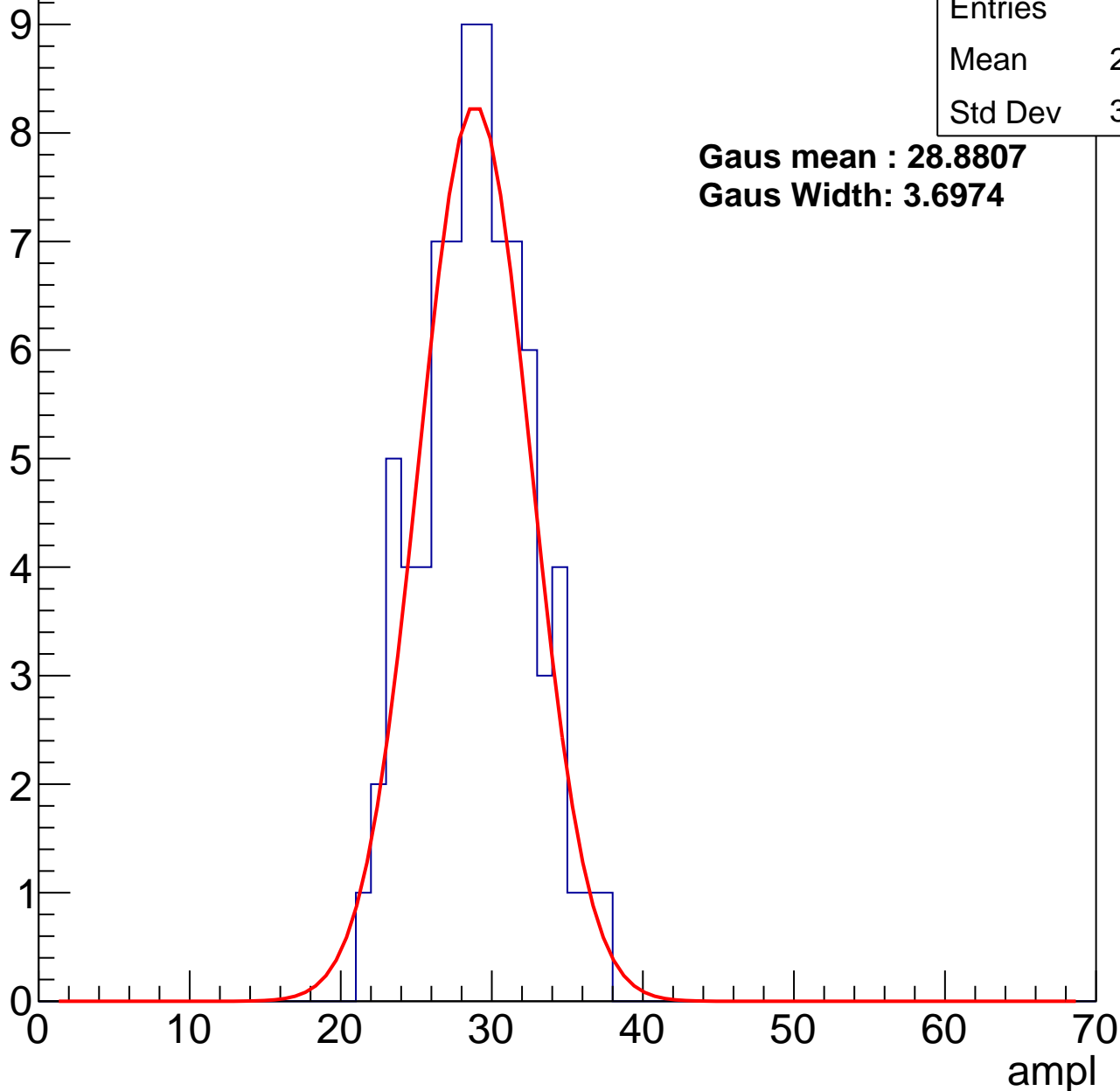
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.49
Std Dev	3.529

Gaus mean : 28.8807

Gaus Width: 3.6974



B1L102S, U6-ch44, adc1

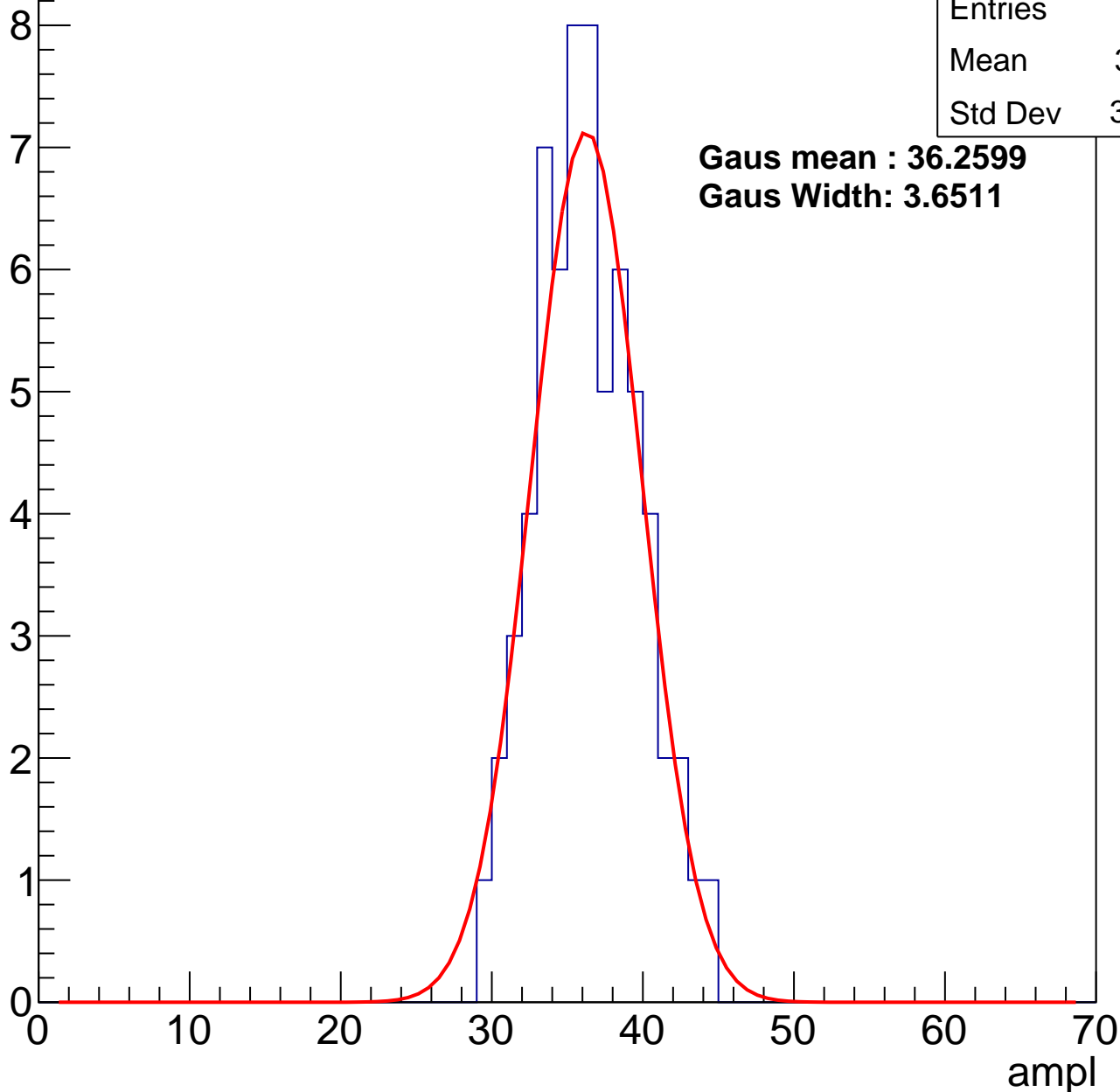
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.91
Std Dev	3.345

Gaus mean : 36.2599

Gaus Width: 3.6511



B1L102S, U6-ch44, adc2

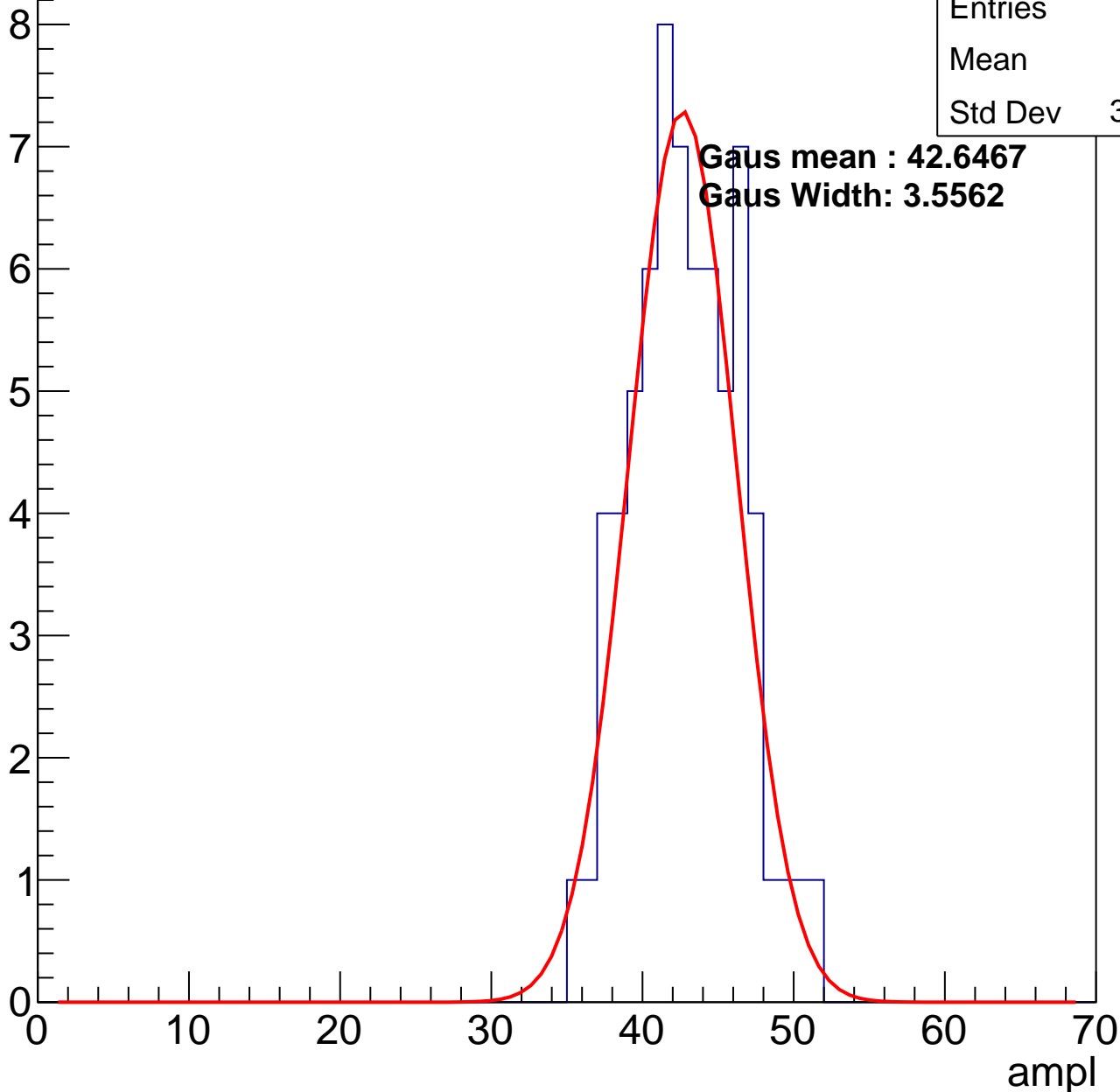
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.4
Std Dev	3.498

Gaus mean : 42.6467

Gaus Width: 3.5562

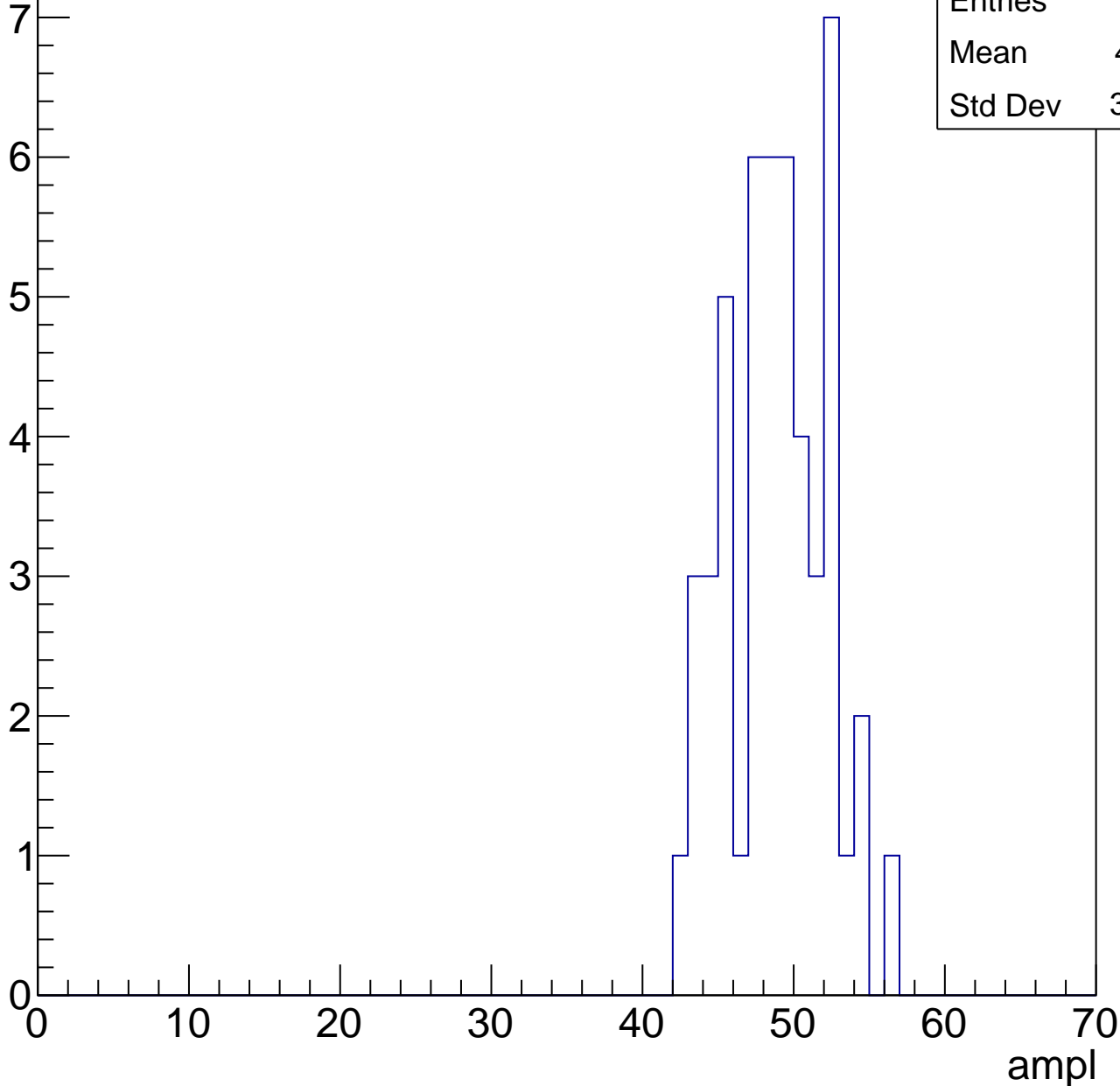


B1L102S, U6-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

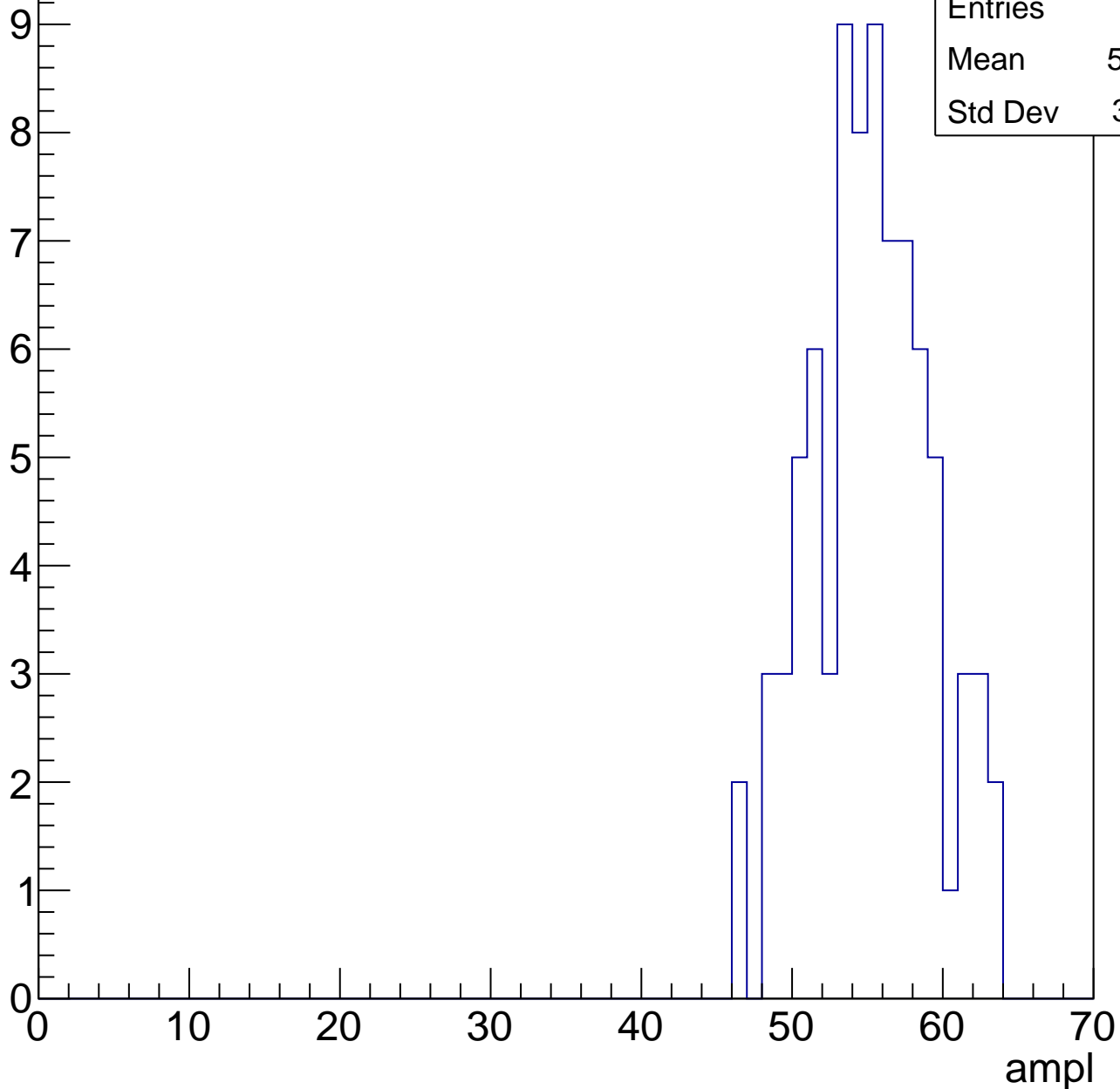
Entries	49
Mean	48.41
Std Dev	3.282



B1L102S, U6-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.02
Std Dev	2.196

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

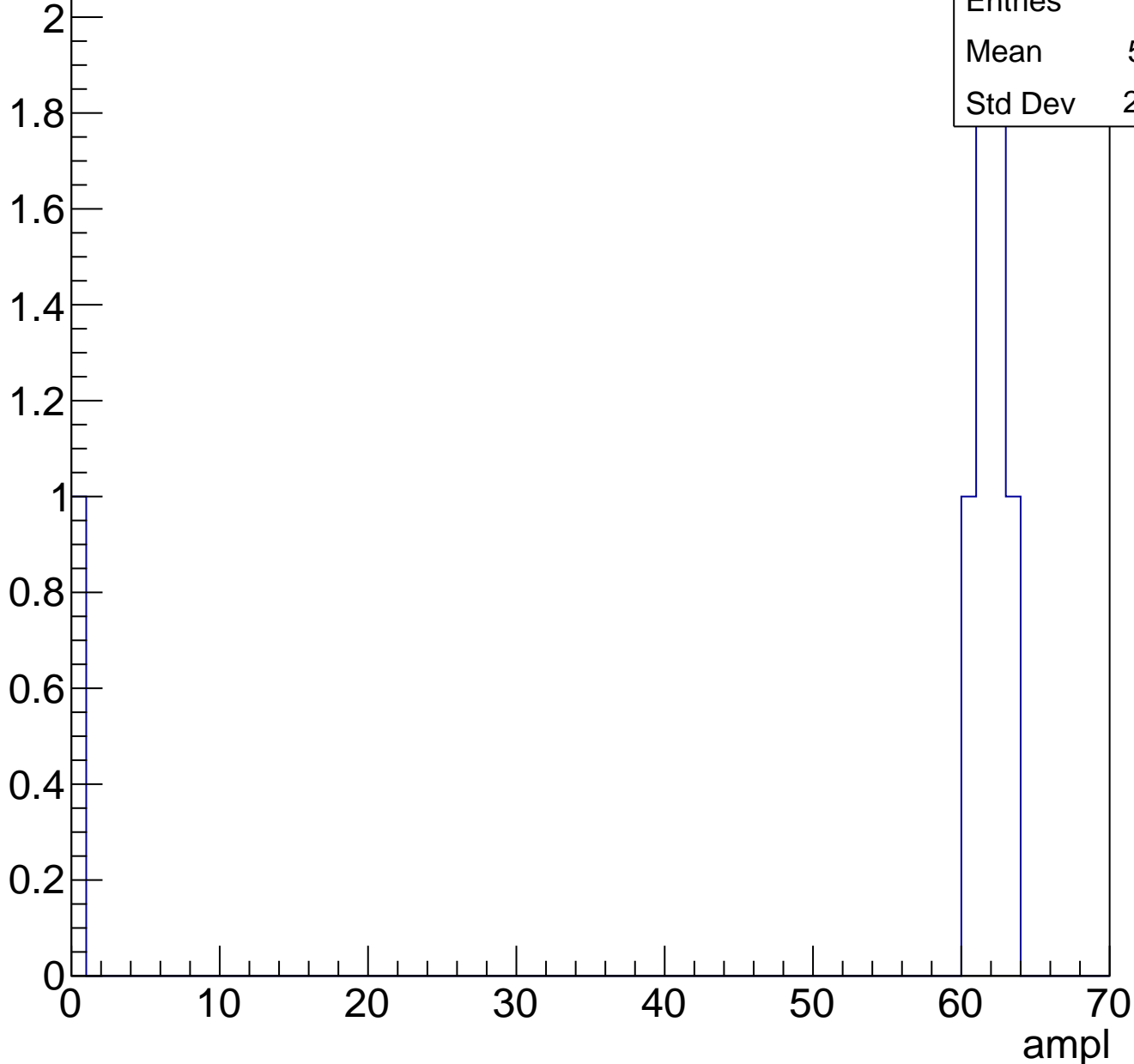
7

8

B1L102S, U6-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

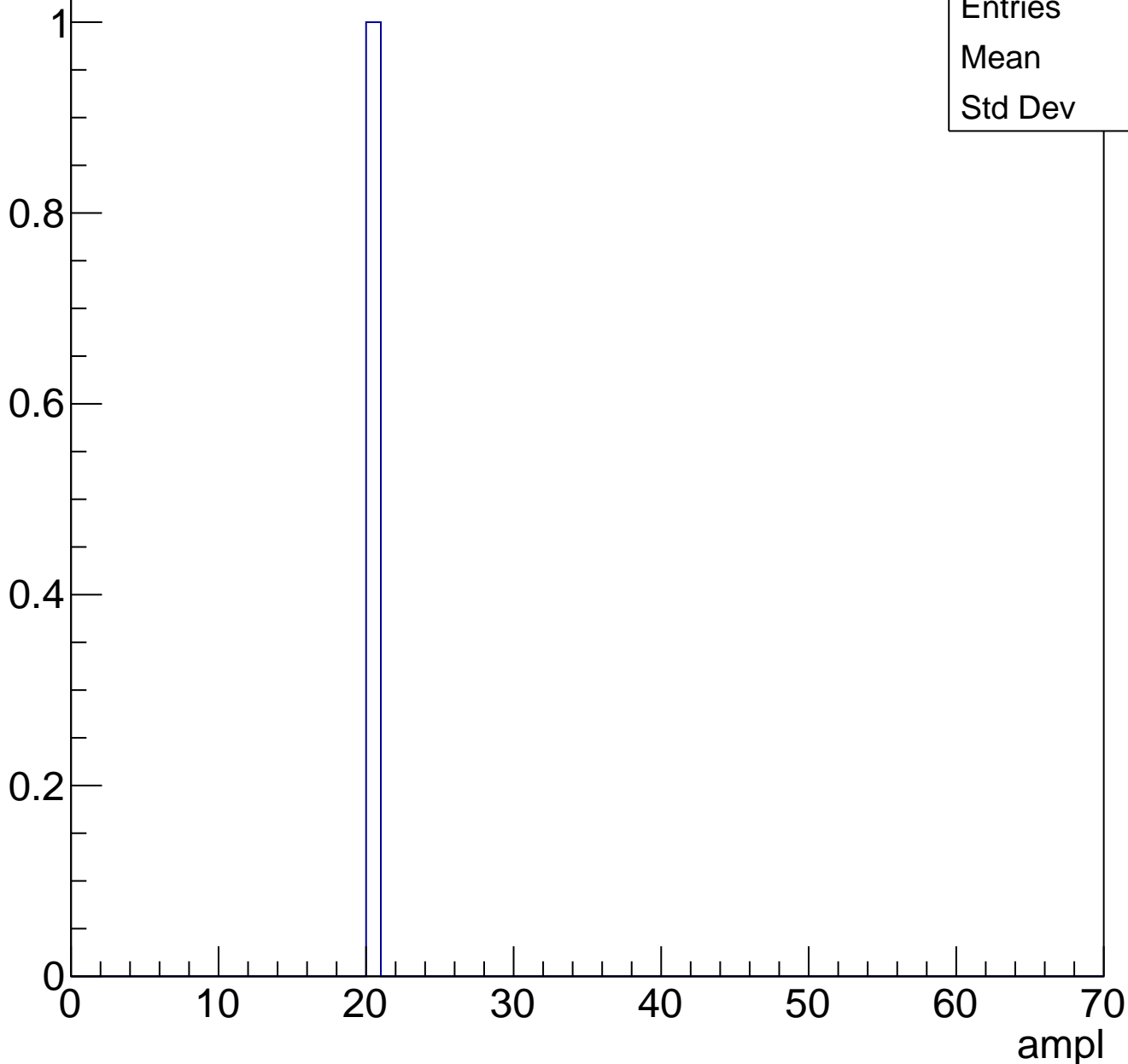
Entry



B1L102S, U6-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch45, adc0

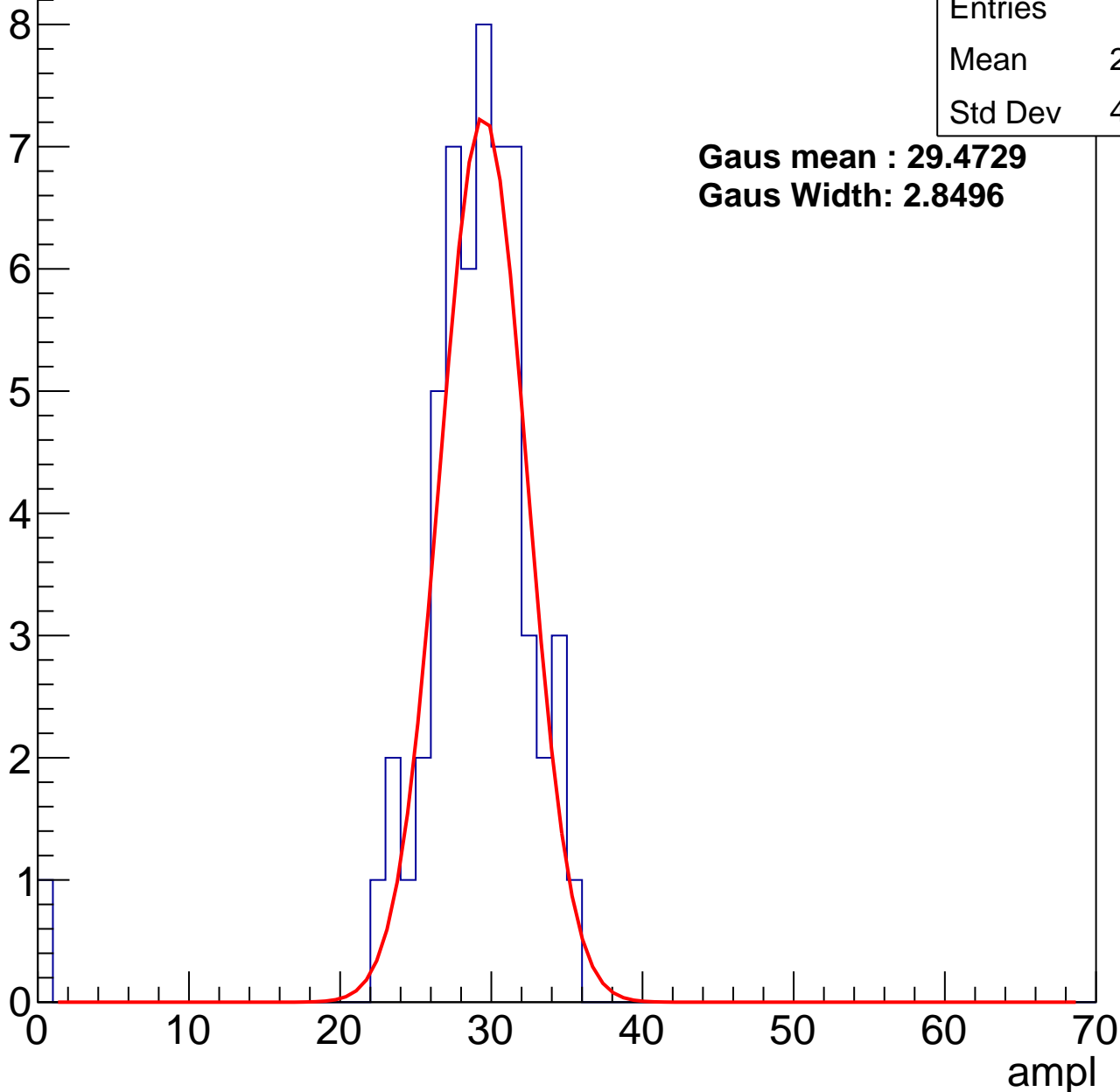
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.34
Std Dev	4.778

Gaus mean : 29.4729

Gaus Width: 2.8496



B1L102S, U6-ch45, adc1

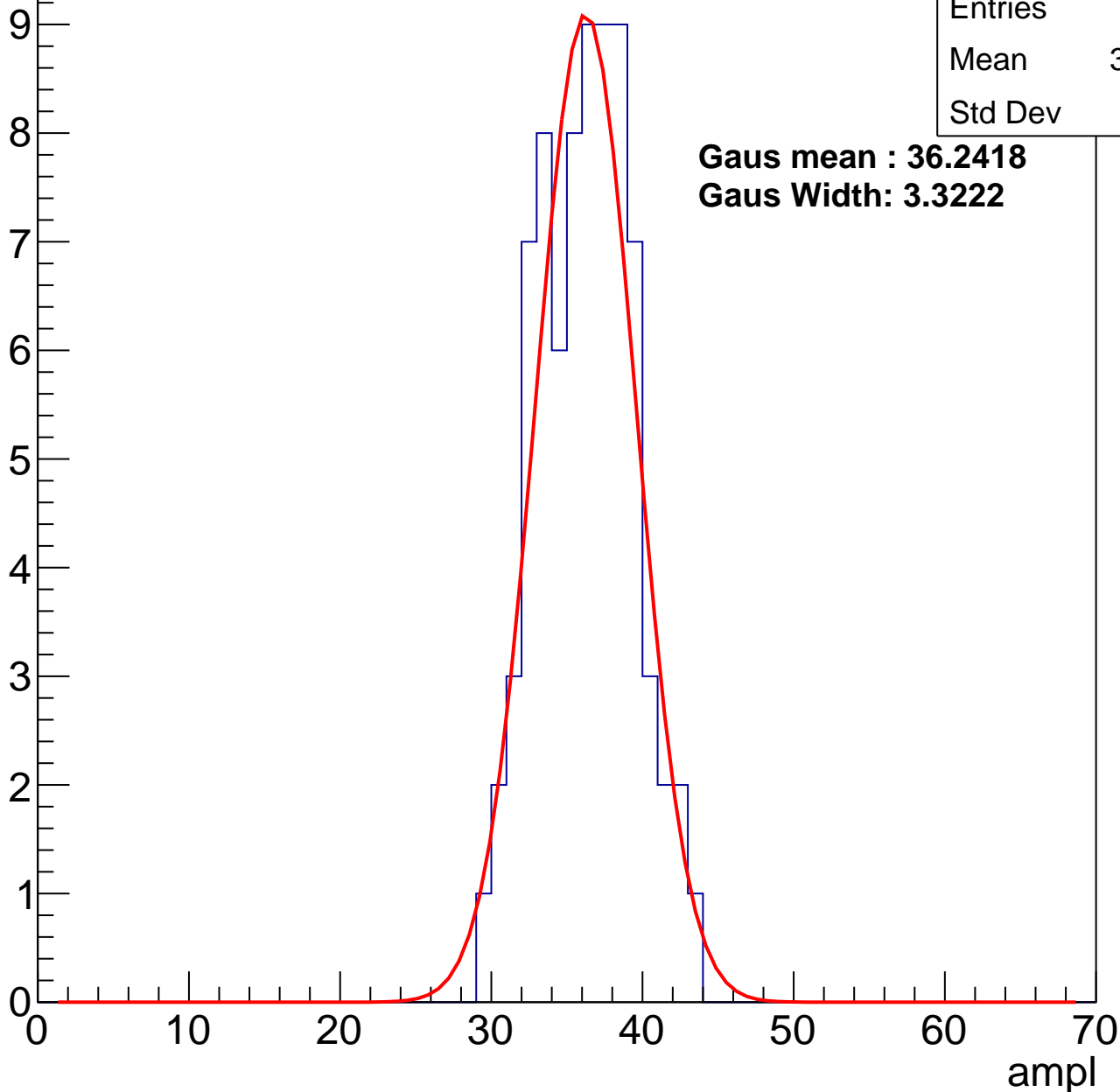
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	35.78
Std Dev	3.09

Gaus mean : 36.2418

Gaus Width: 3.3222



B1L102S, U6-ch45, adc2

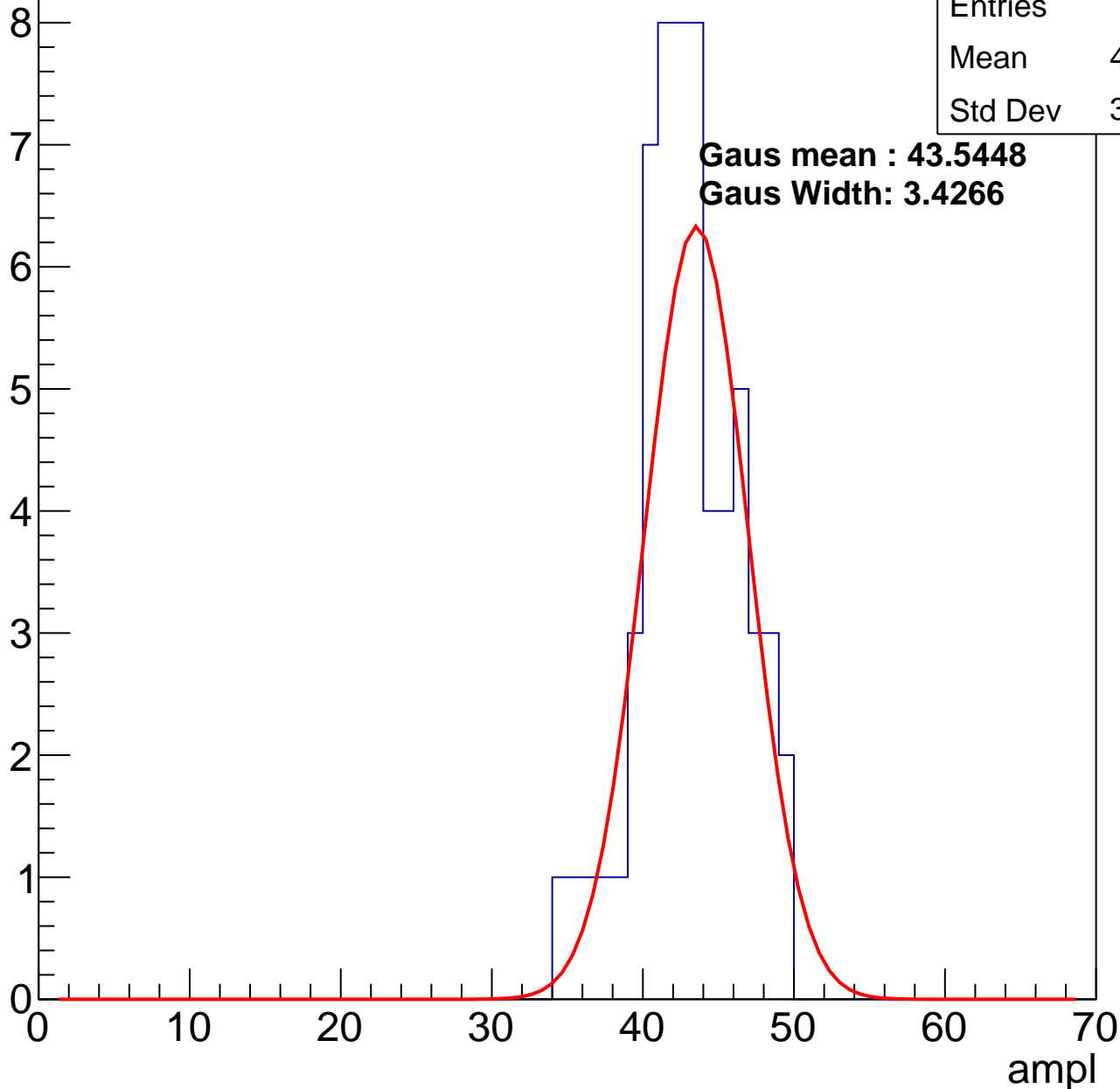
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.57
Std Dev	3.303

Gaus mean : 43.5448

Gaus Width: 3.4266

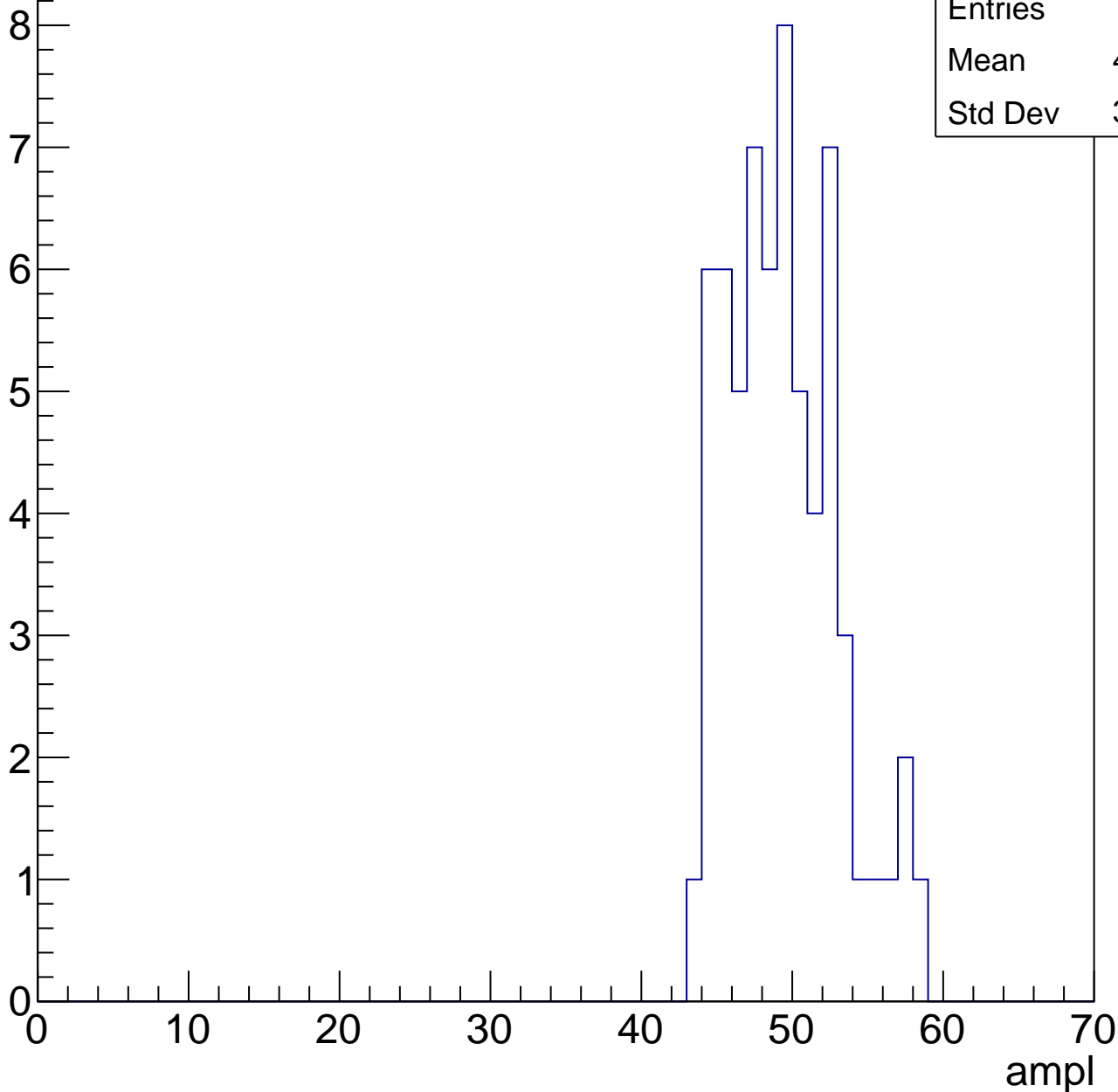


B1L102S, U6-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

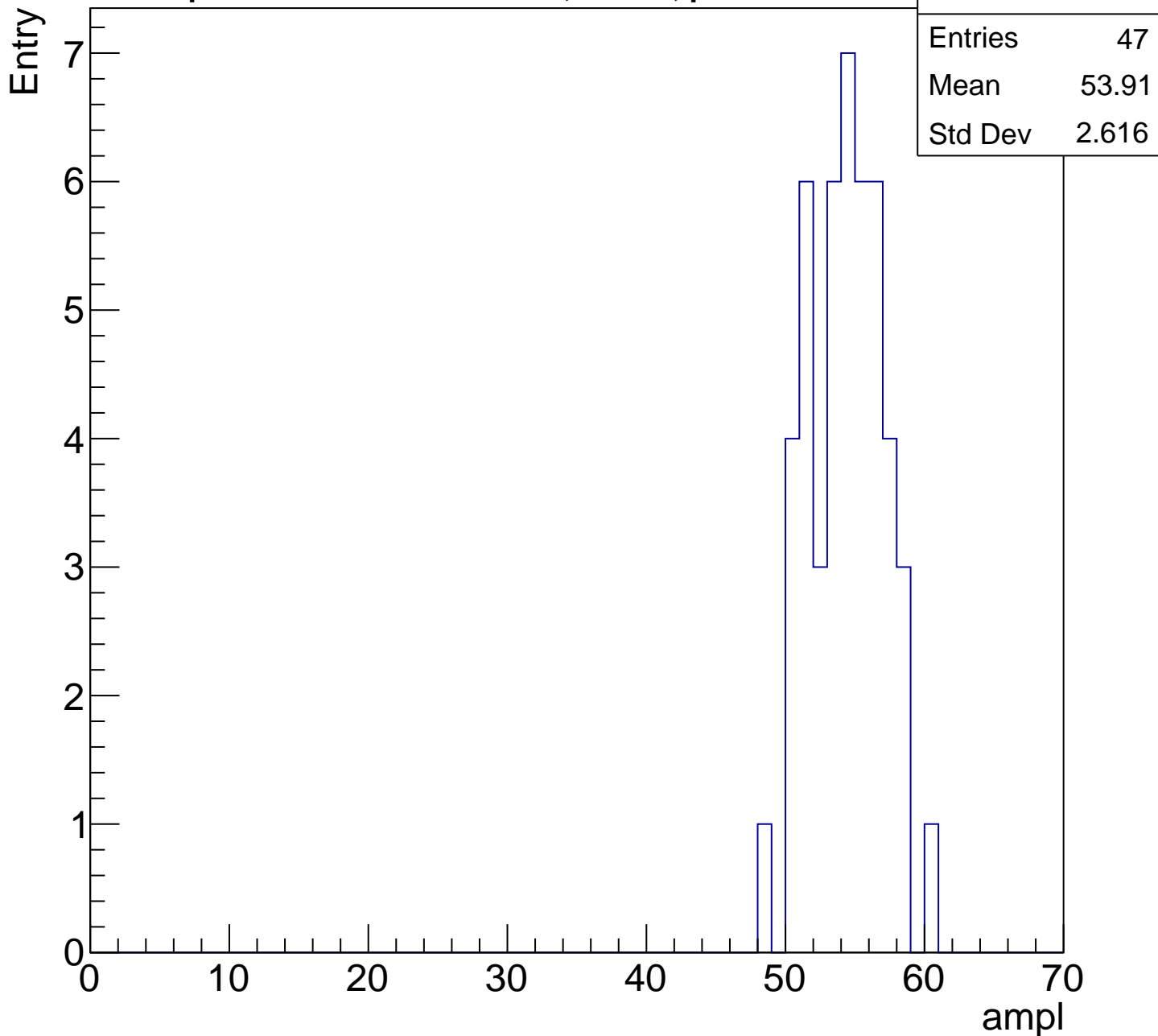
Entry

Entries	64
Mean	48.91
Std Dev	3.561



B1L102S, U6-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

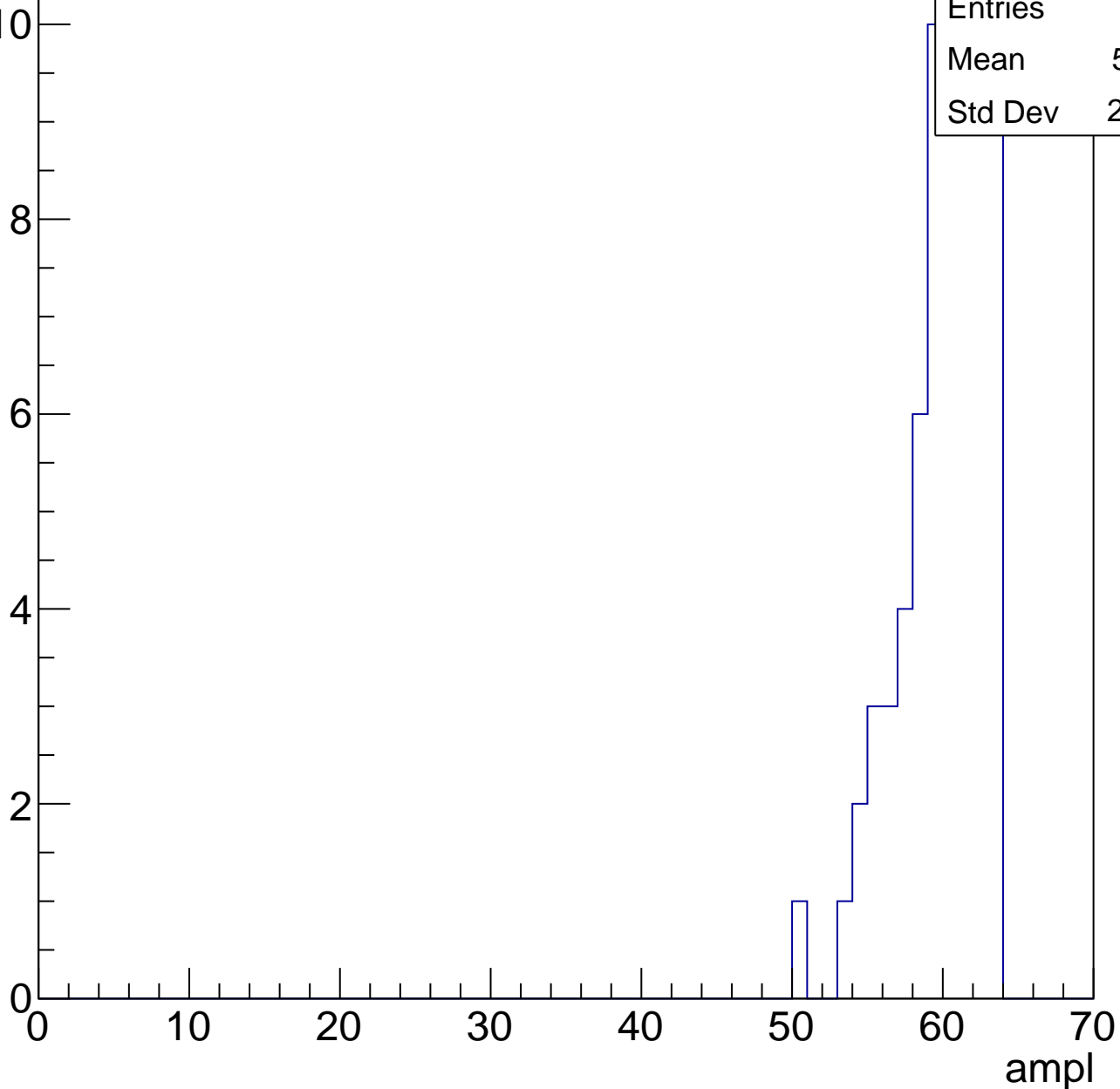


B1L102S, U6-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	59.51
Std Dev	2.789



B1L102S, U6-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch46, adc0

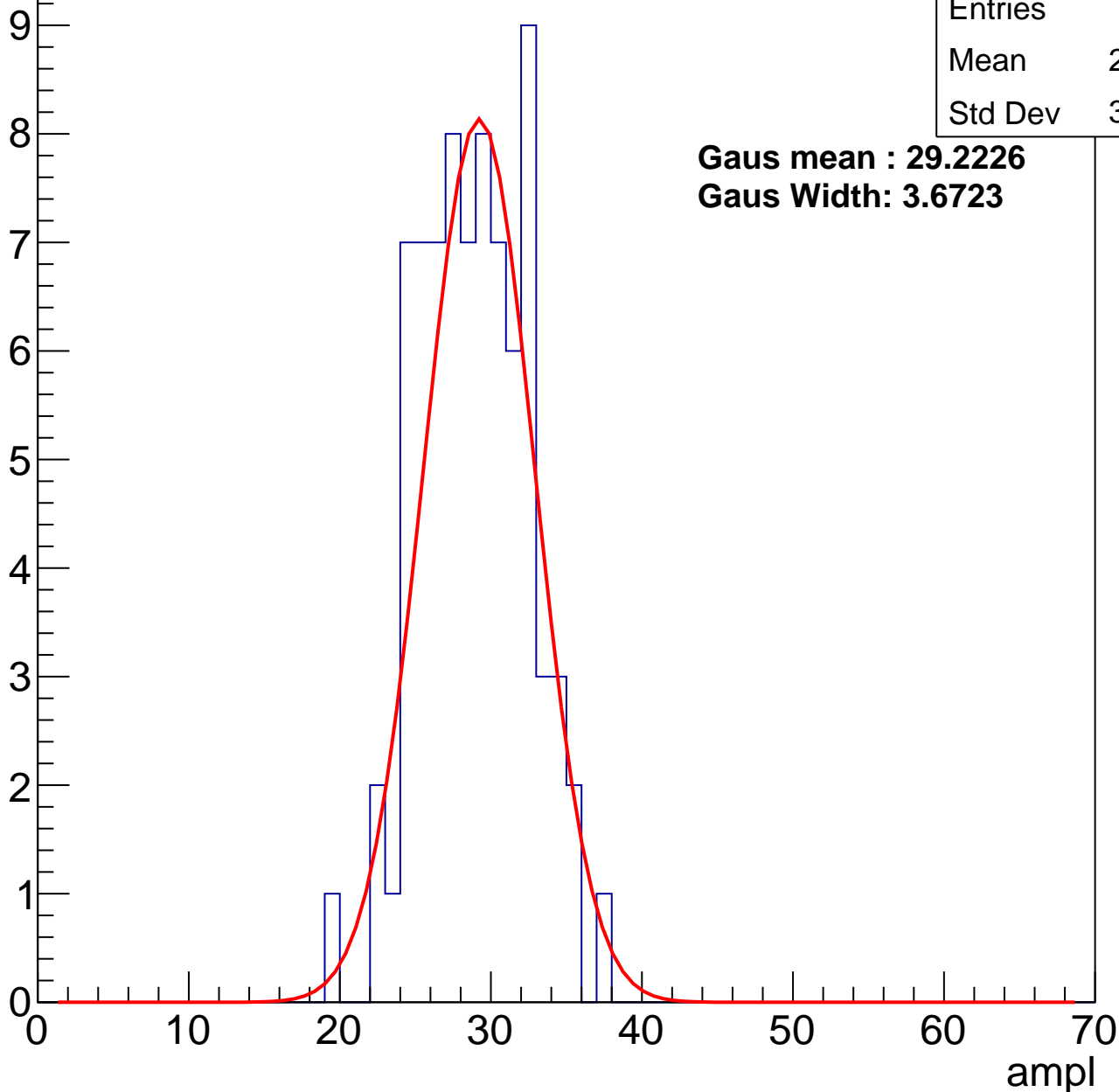
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.44
Std Dev	3.503

Gaus mean : 29.2226

Gaus Width: 3.6723



B1L102S, U6-ch46, adc1

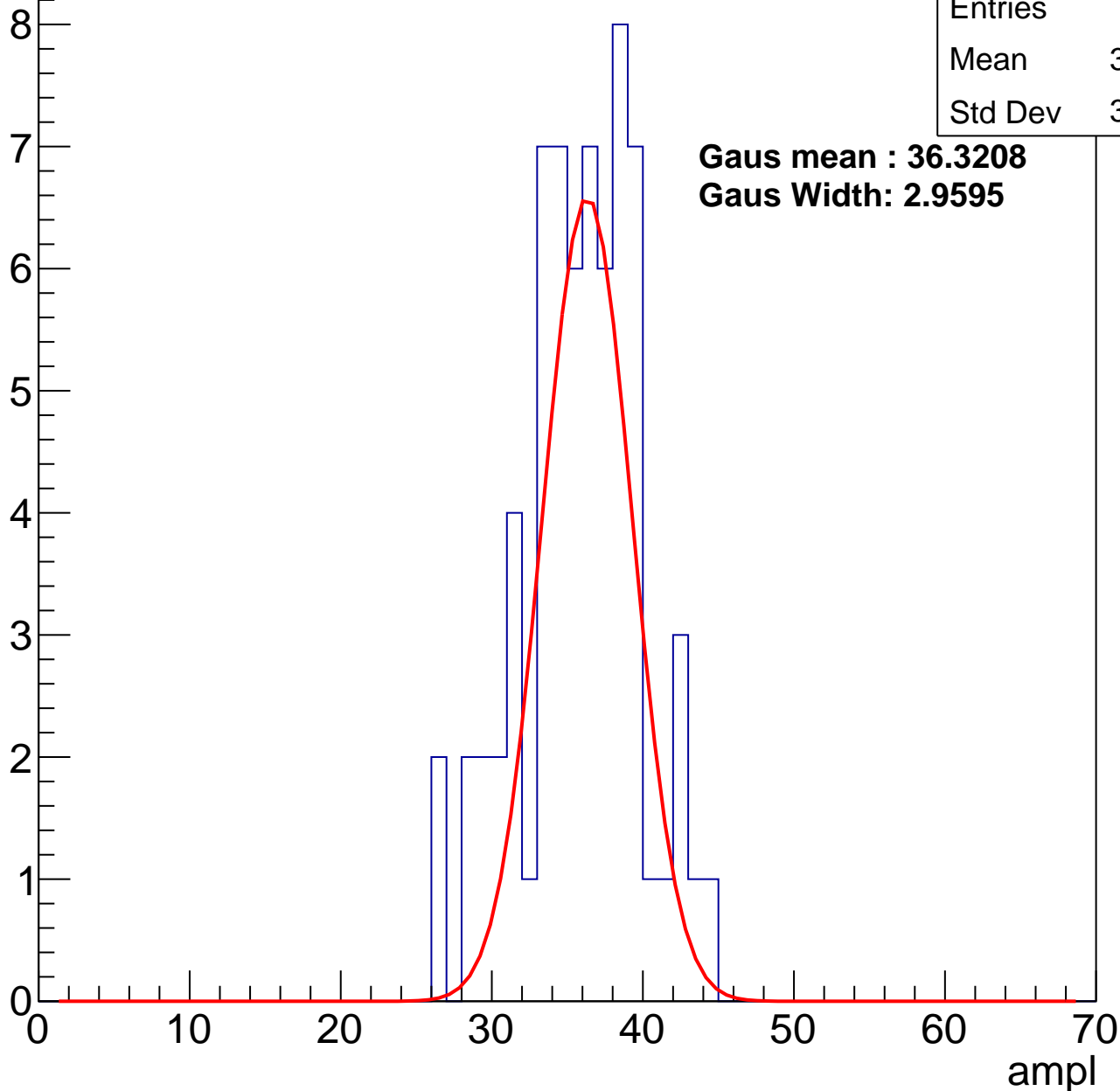
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.38
Std Dev	3.926

Gaus mean : 36.3208

Gaus Width: 2.9595



B1L102S, U6-ch46, adc2

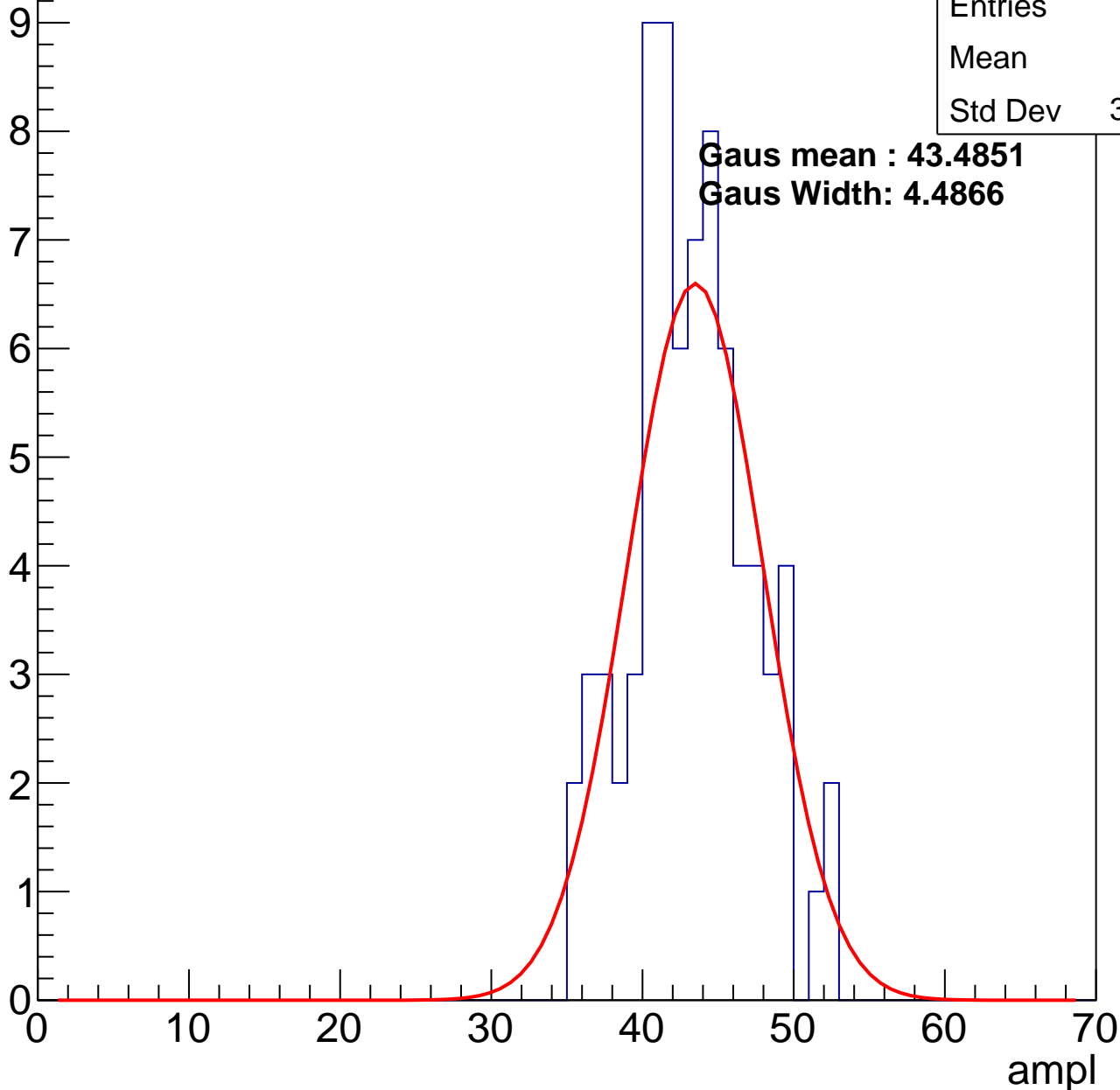
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.8
Std Dev	3.937

Gaus mean : 43.4851

Gaus Width: 4.4866

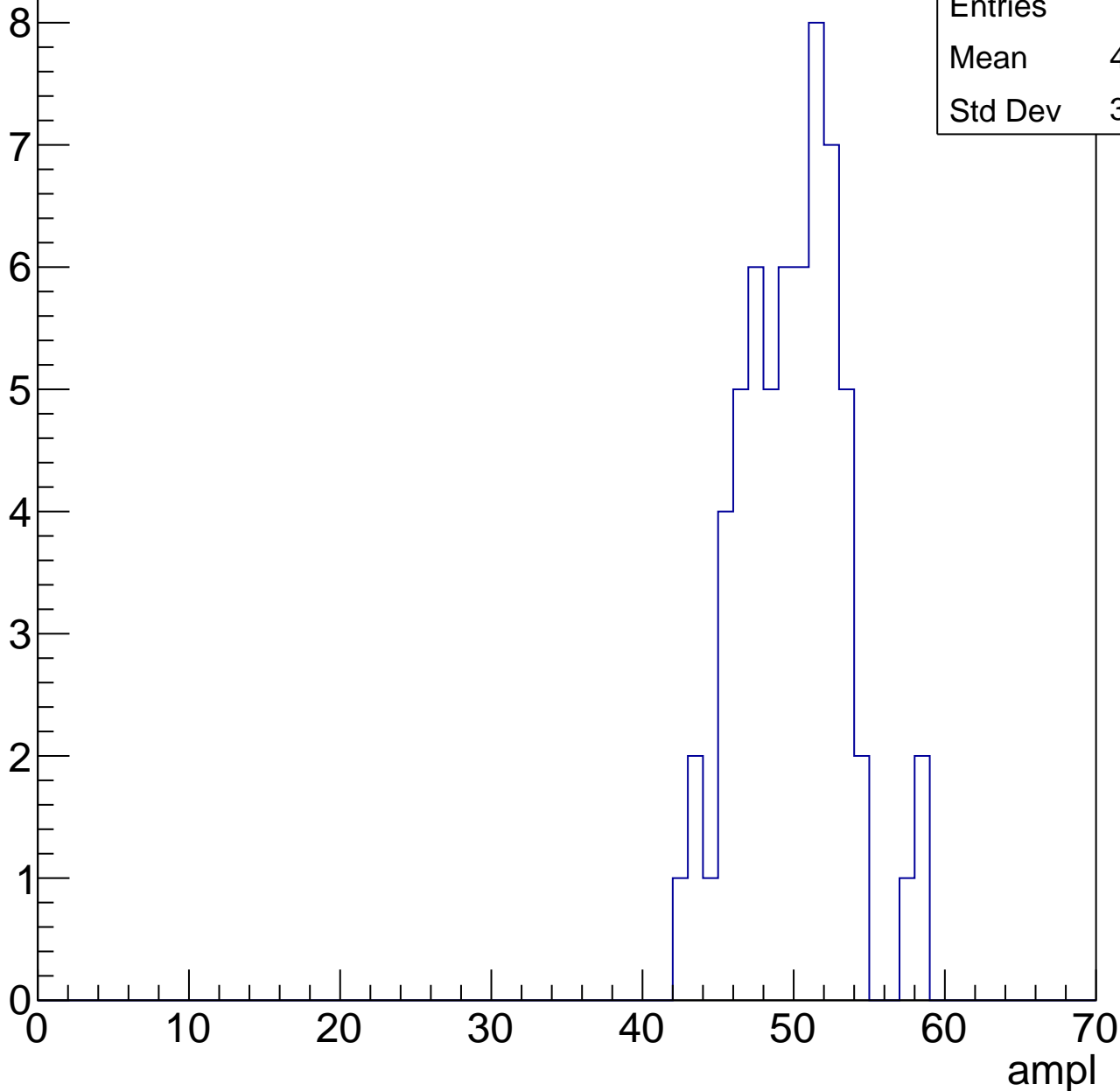


B1L102S, U6-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.44
Std Dev	3.457

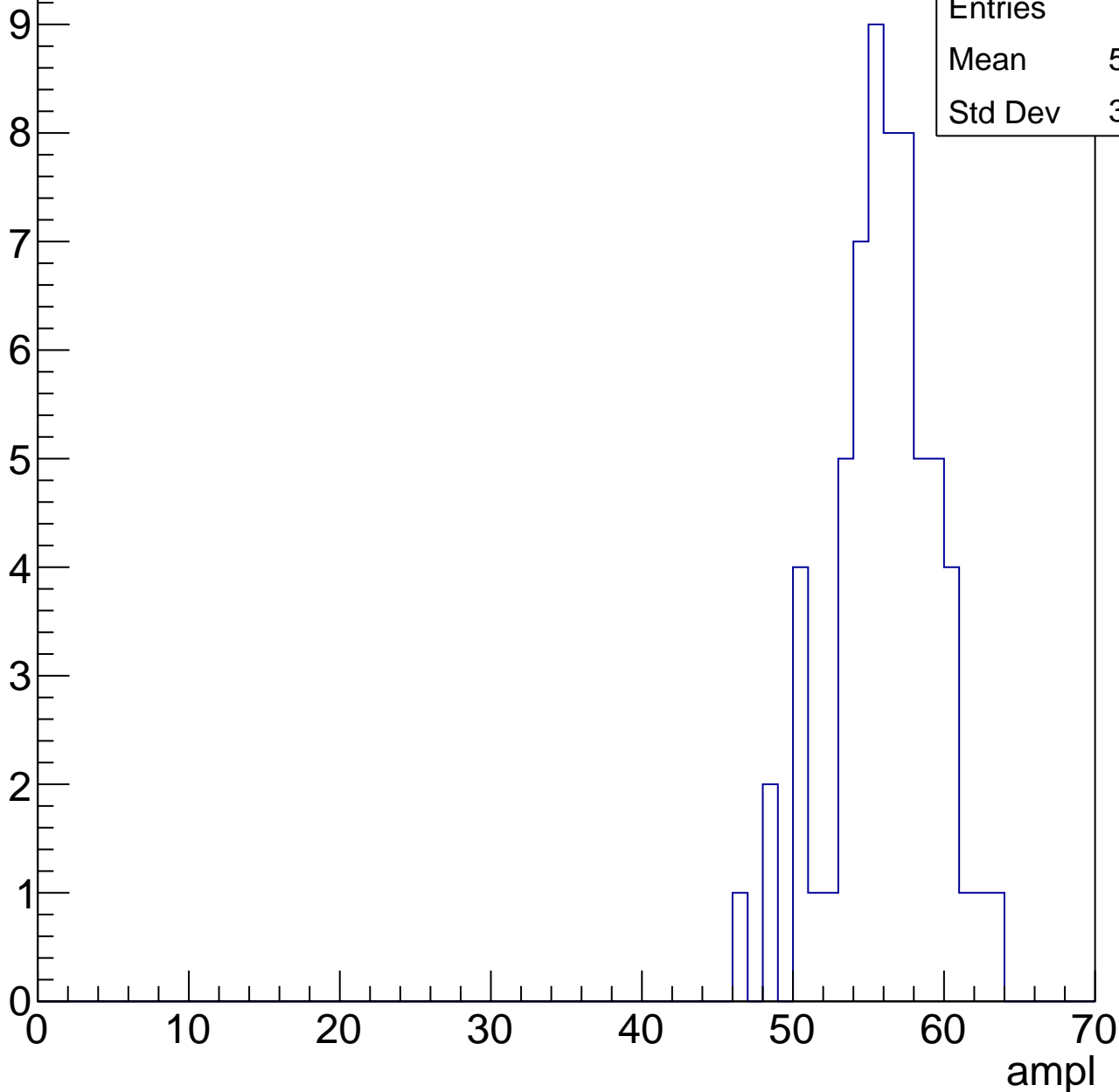


B1L102S, U6-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.52
Std Dev	3.399

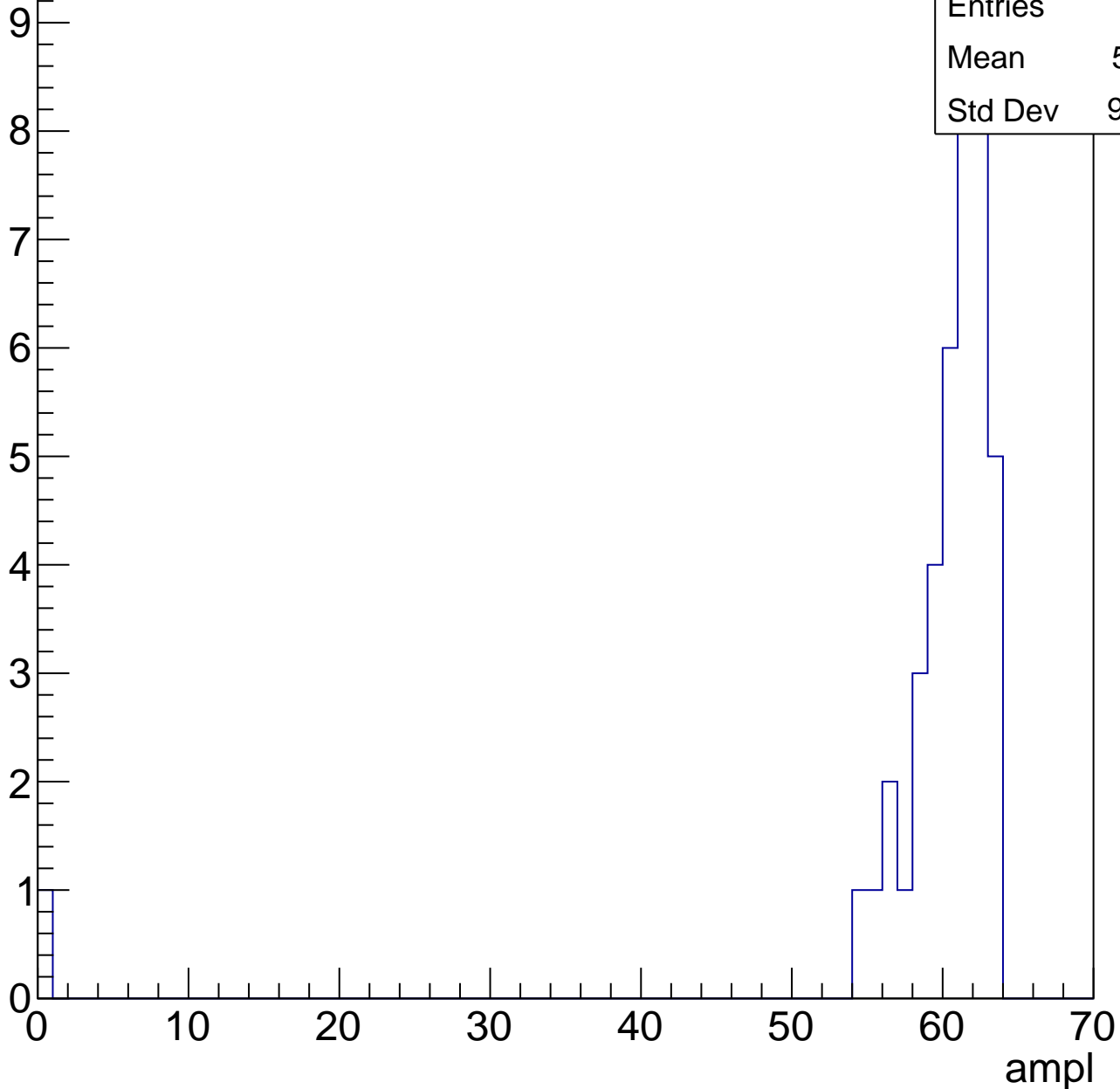


B1L102S, U6-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

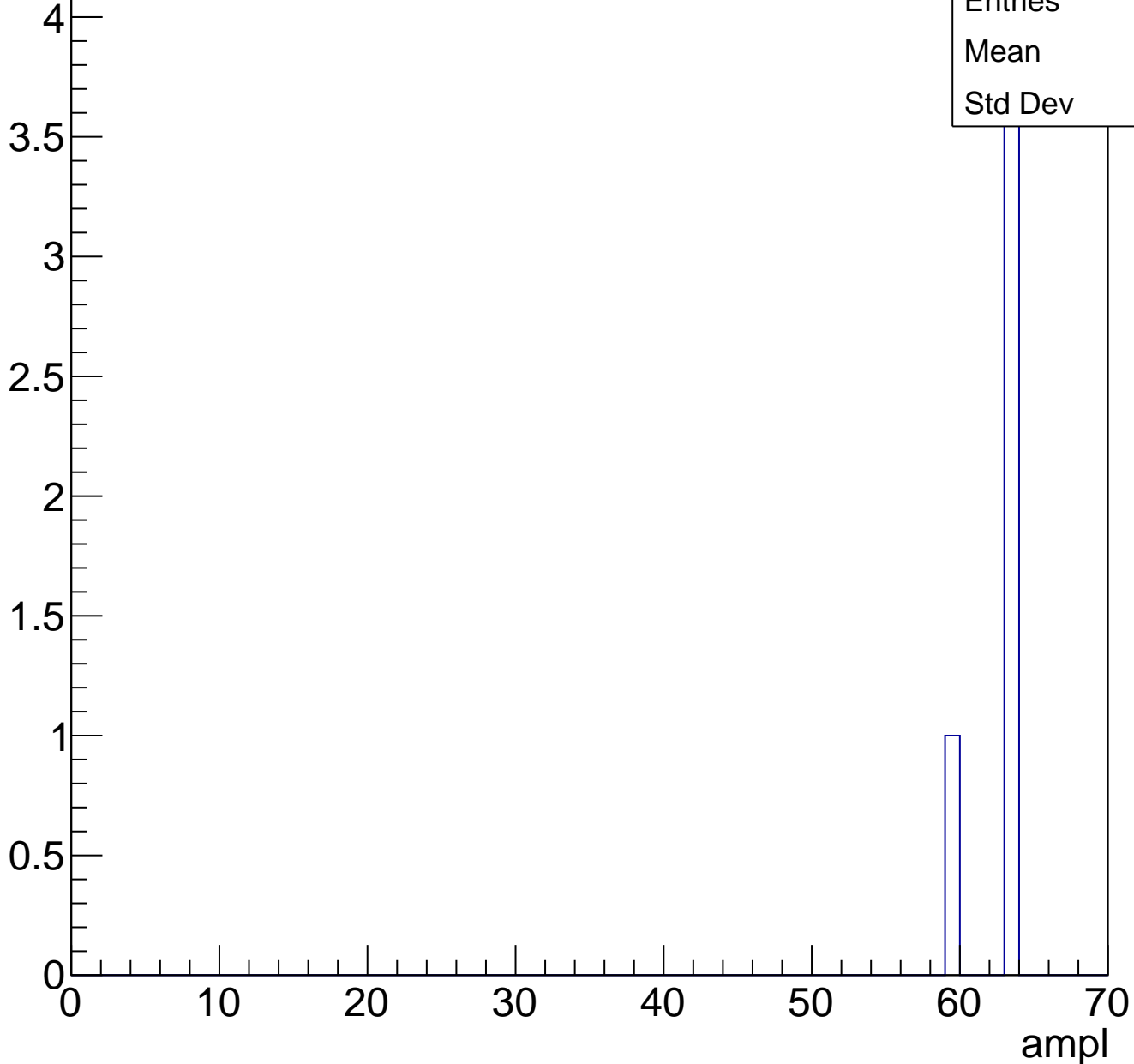
Entries	42
Mean	58.81
Std Dev	9.447



B1L102S, U6-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch47, adc0

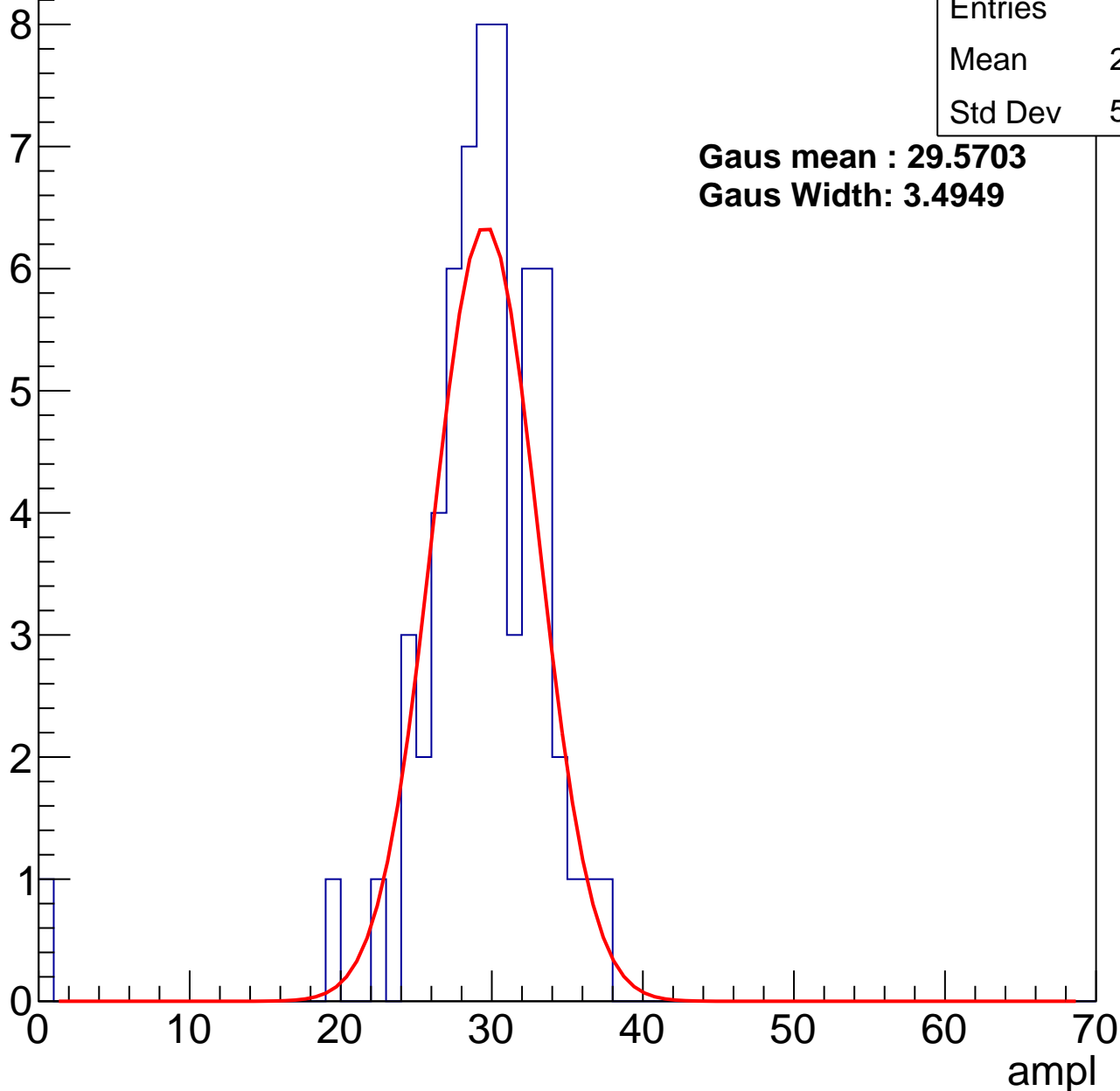
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.79
Std Dev	5.009

Gaus mean : 29.5703

Gaus Width: 3.4949



B1L102S, U6-ch47, adc1

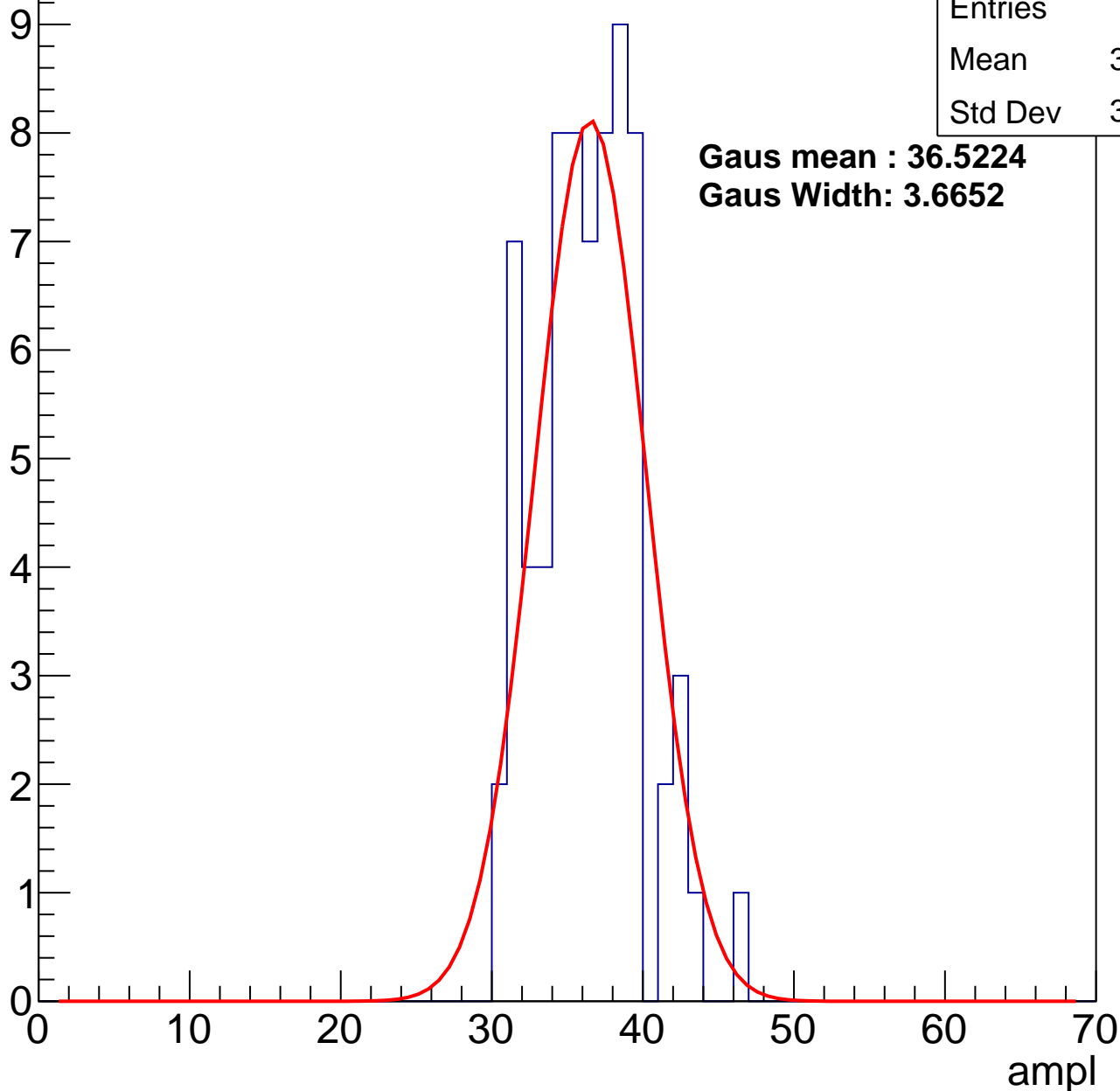
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.94
Std Dev	3.354

Gaus mean : 36.5224

Gaus Width: 3.6652



B1L102S, U6-ch47, adc2

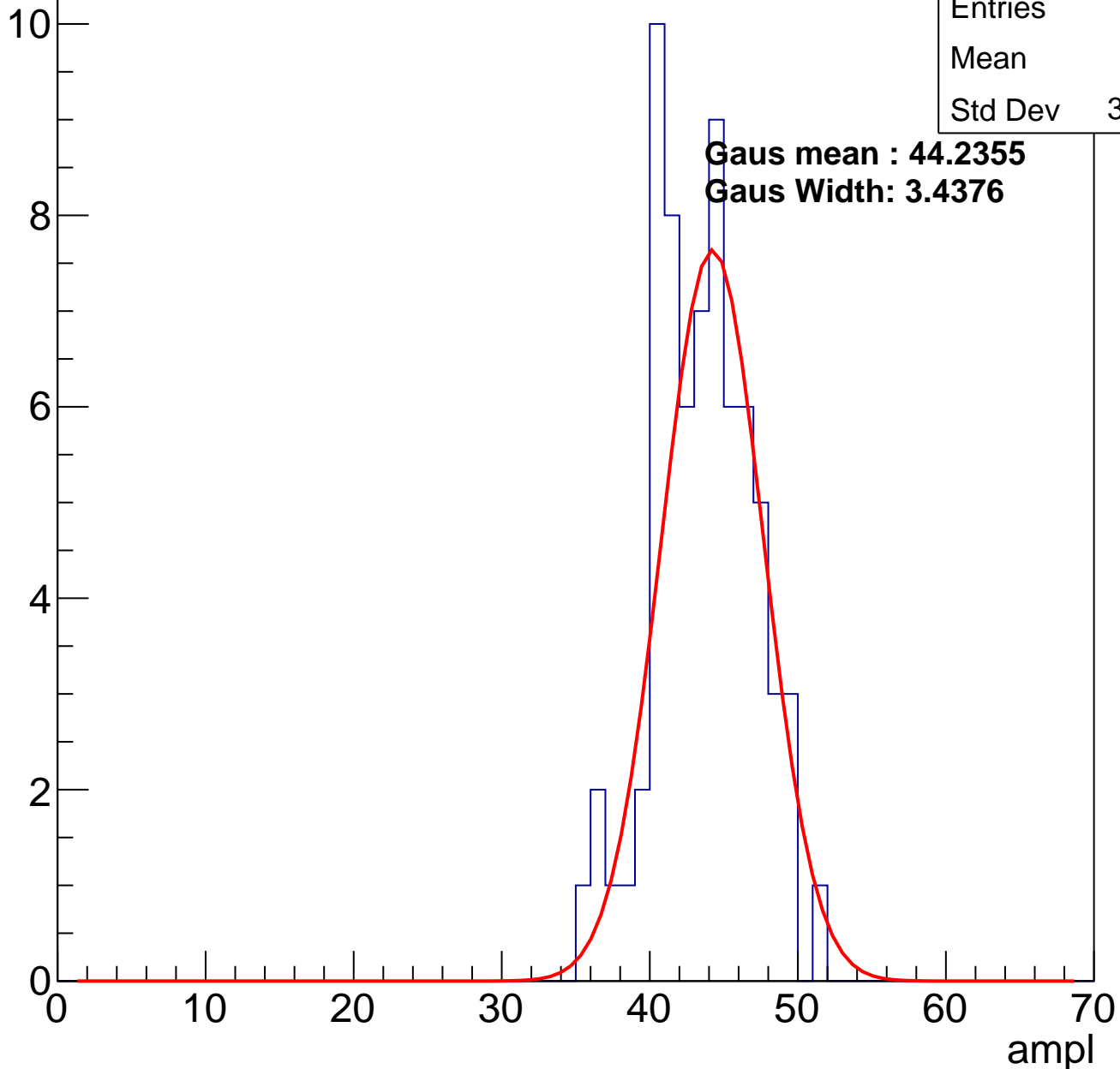
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	43.1
Std Dev	3.349

Gaus mean : 44.2355

Gaus Width: 3.4376

Entry

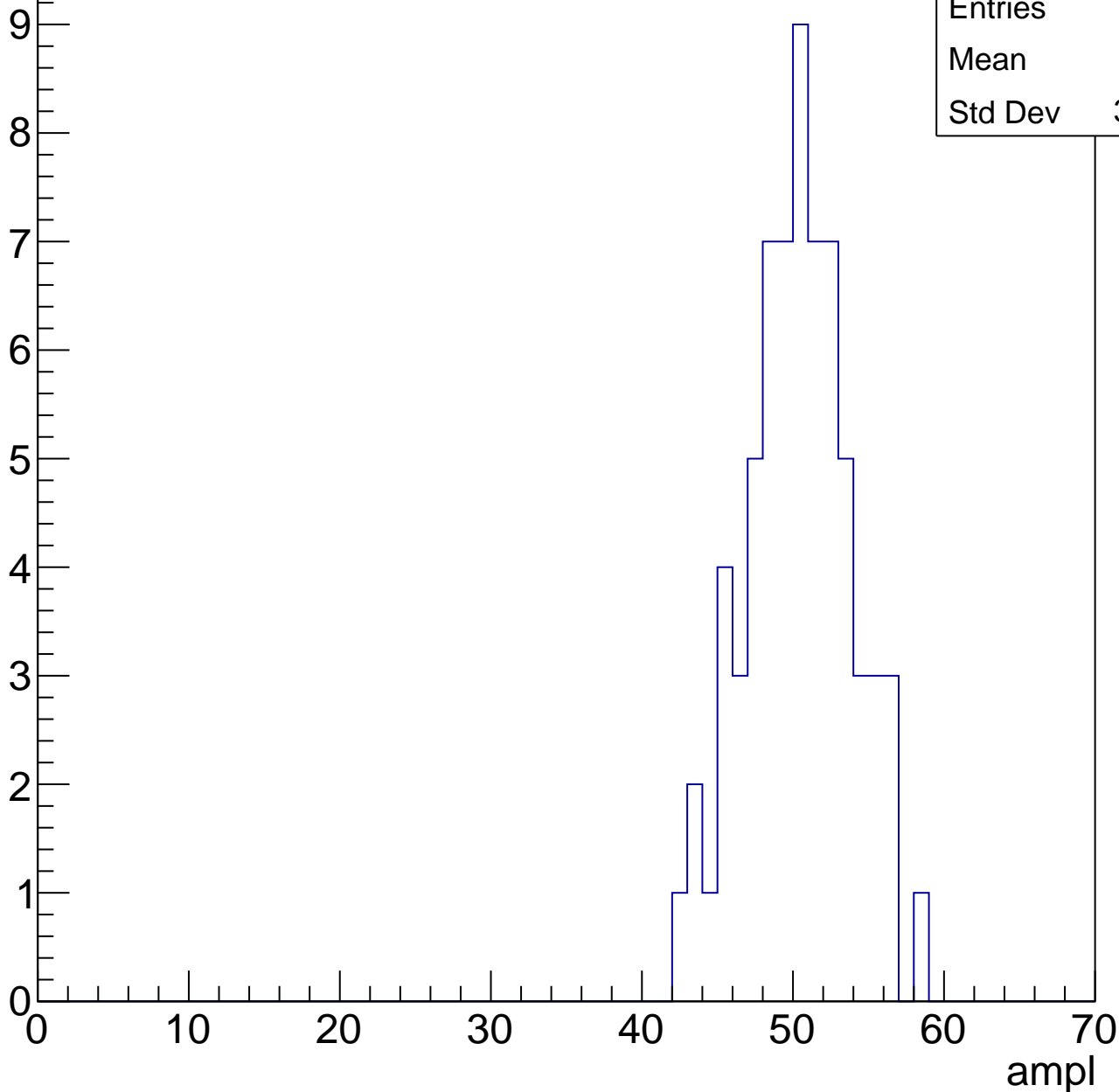


B1L102S, U6-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	49.9
Std Dev	3.431

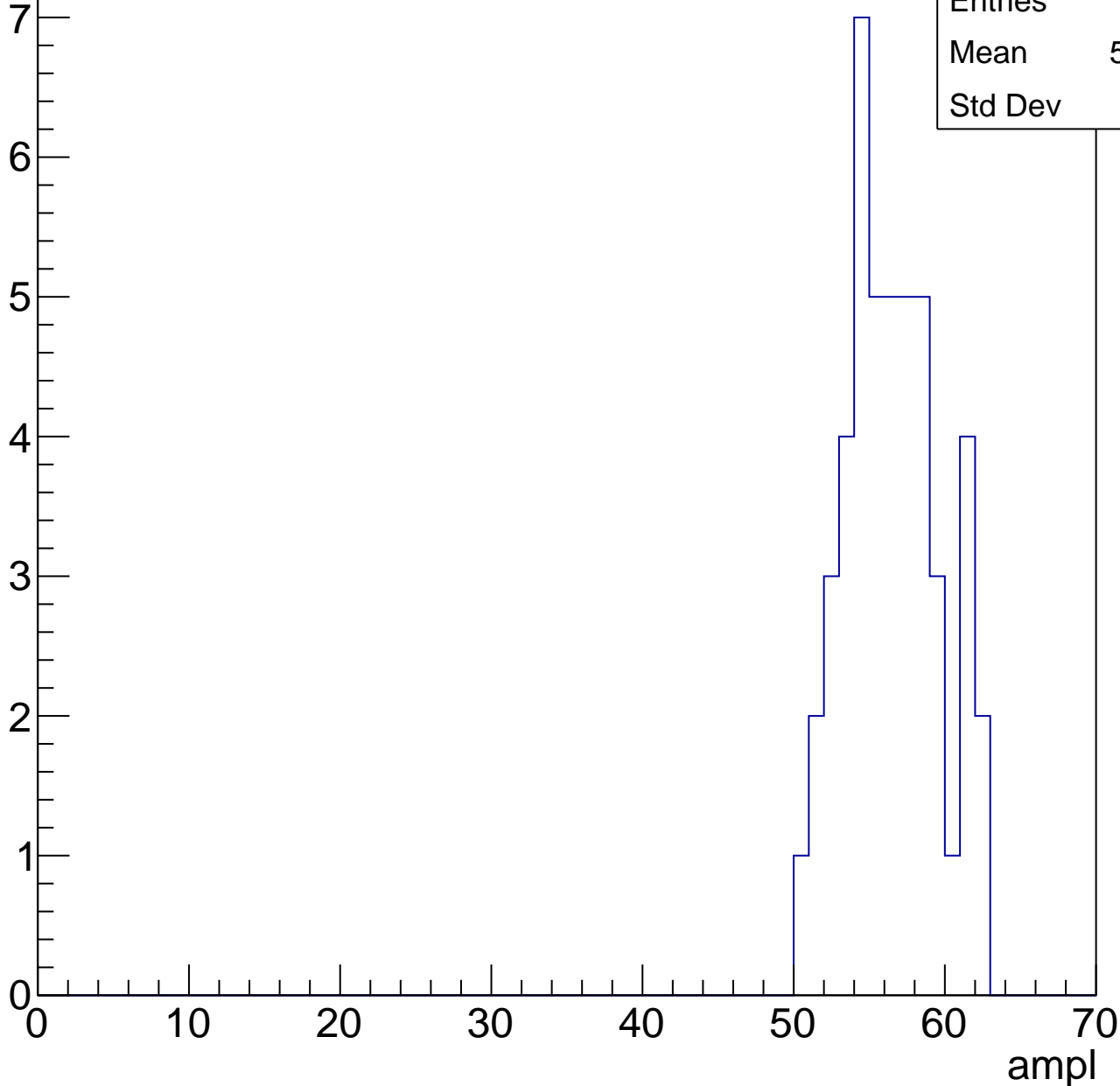


B1L102S, U6-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	56.02
Std Dev	3.07

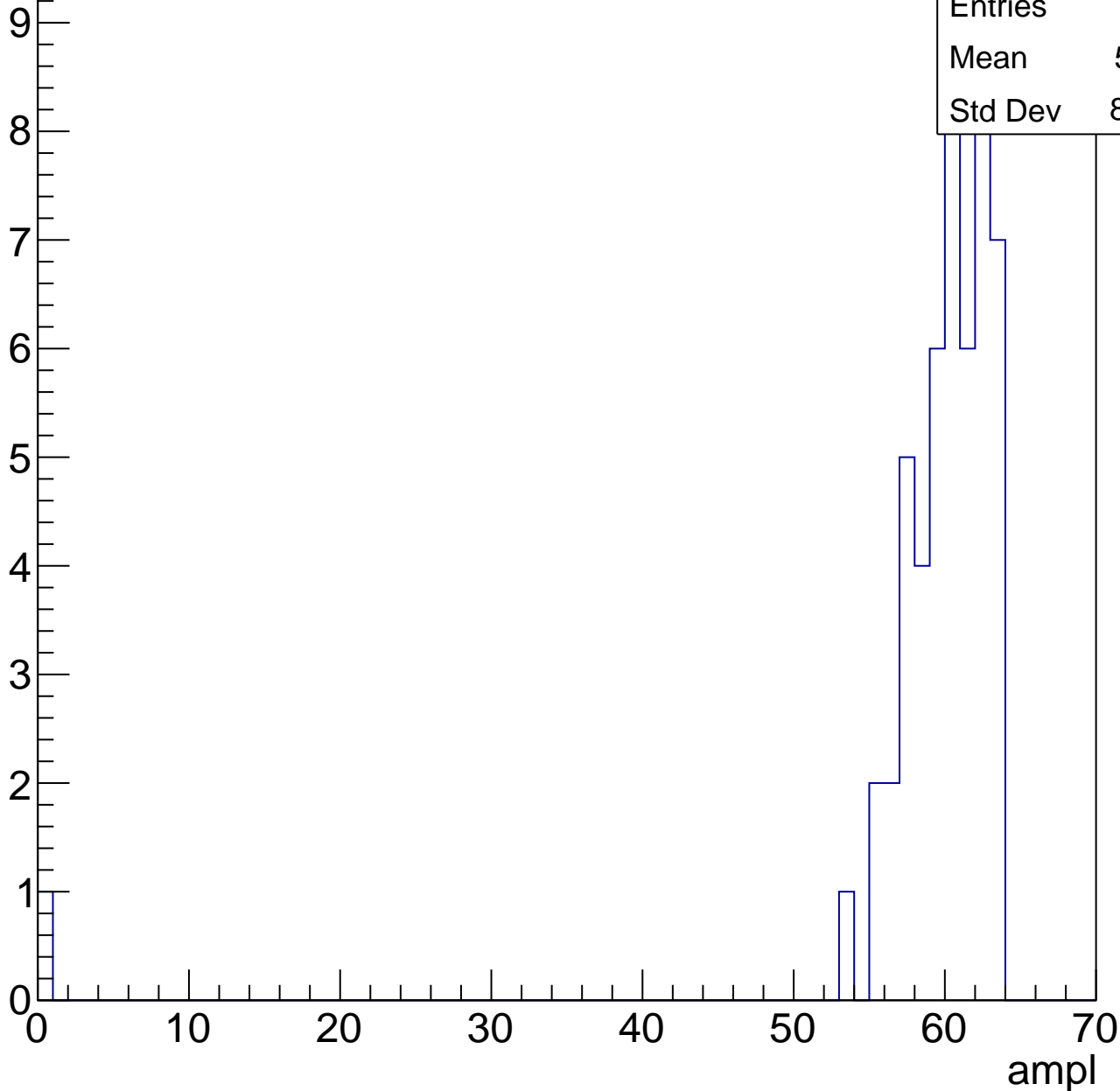


B1L102S, U6-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

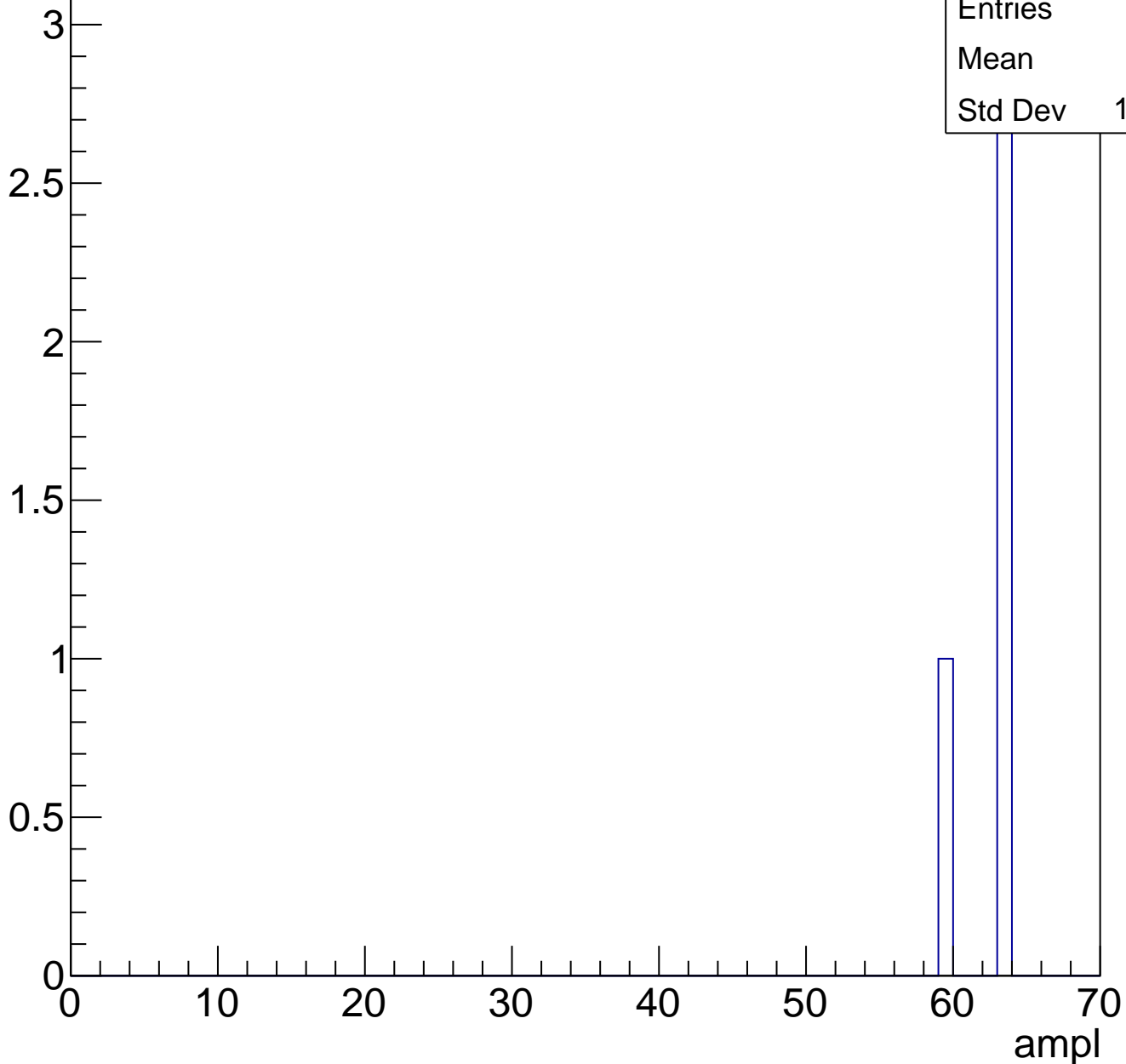
Entries	51
Mean	58.61
Std Dev	8.632



B1L102S, U6-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch48, adc0

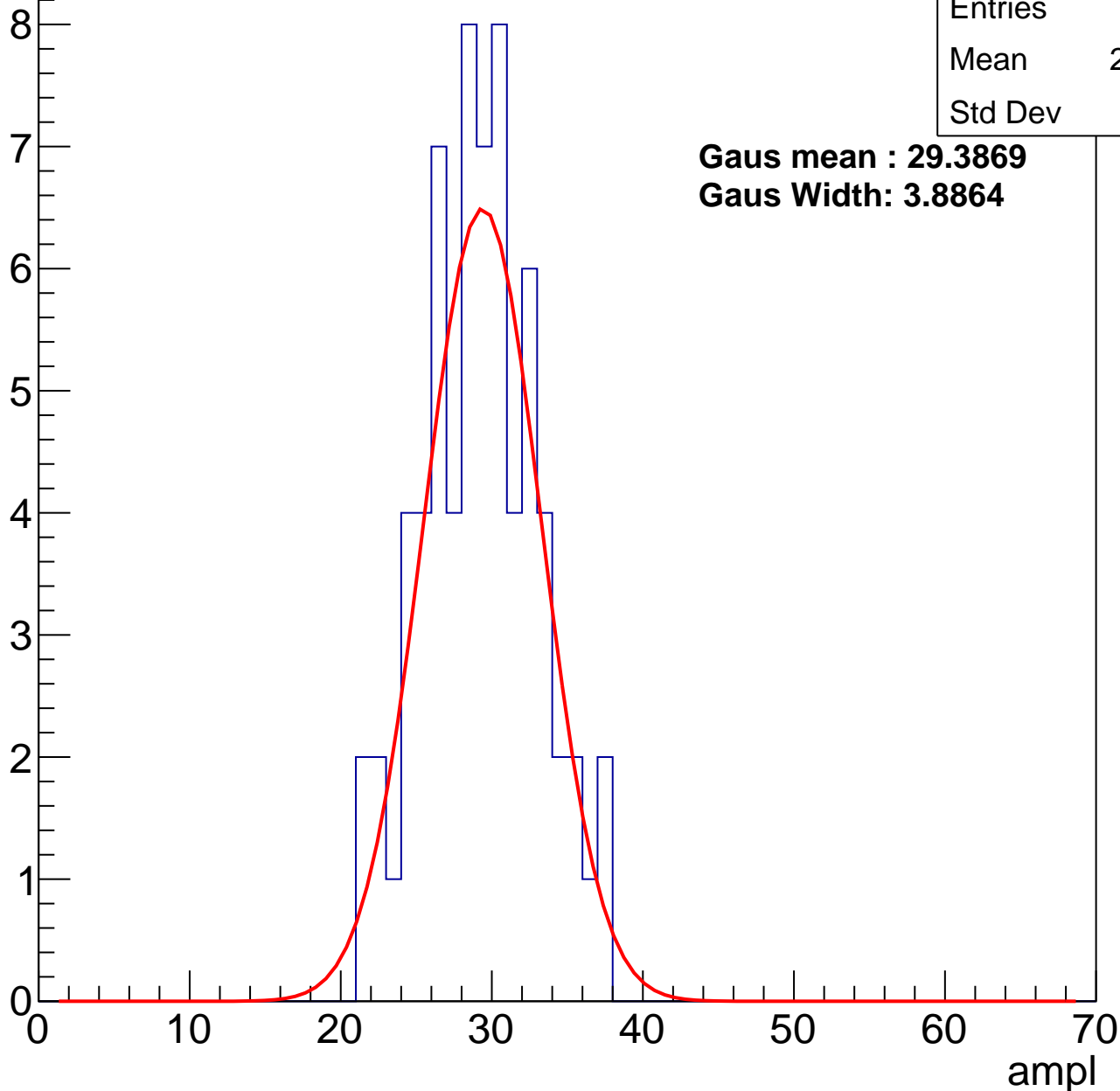
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.79
Std Dev	3.74

Gaus mean : 29.3869

Gaus Width: 3.8864



B1L102S, U6-ch48, adc1

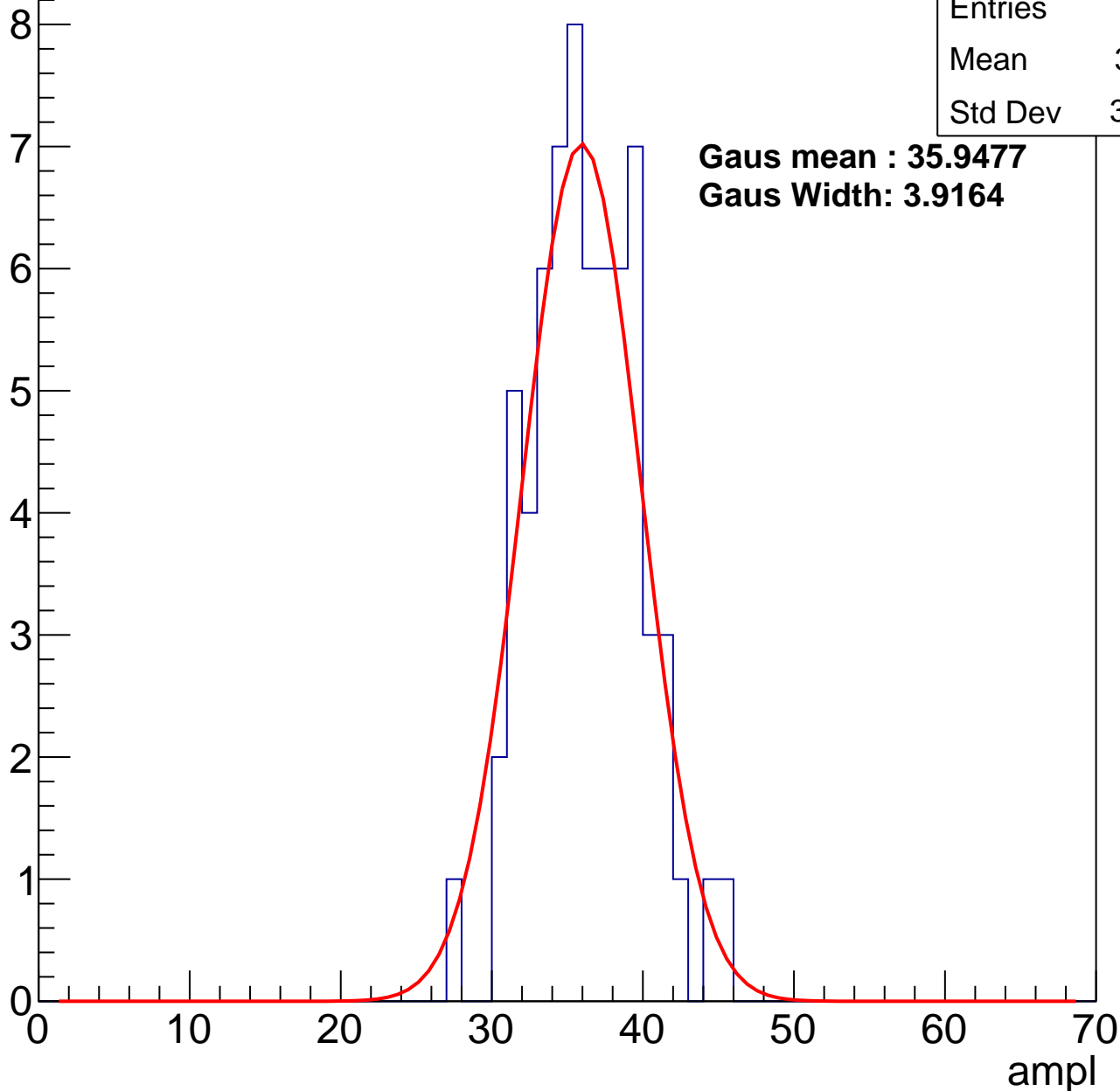
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.81
Std Dev	3.512

Gaus mean : 35.9477

Gaus Width: 3.9164



B1L102S, U6-ch48, adc2

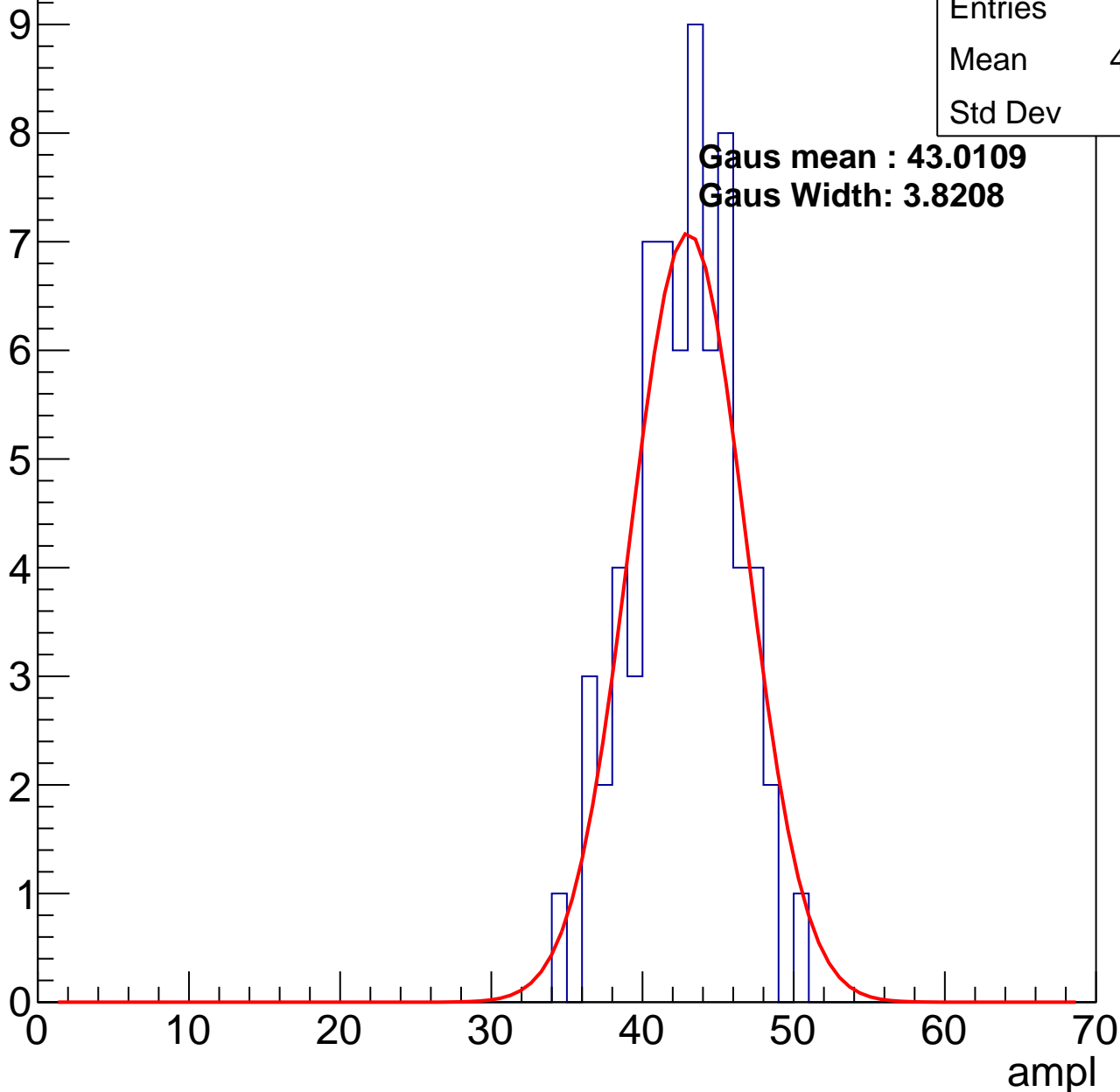
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.28
Std Dev	3.34

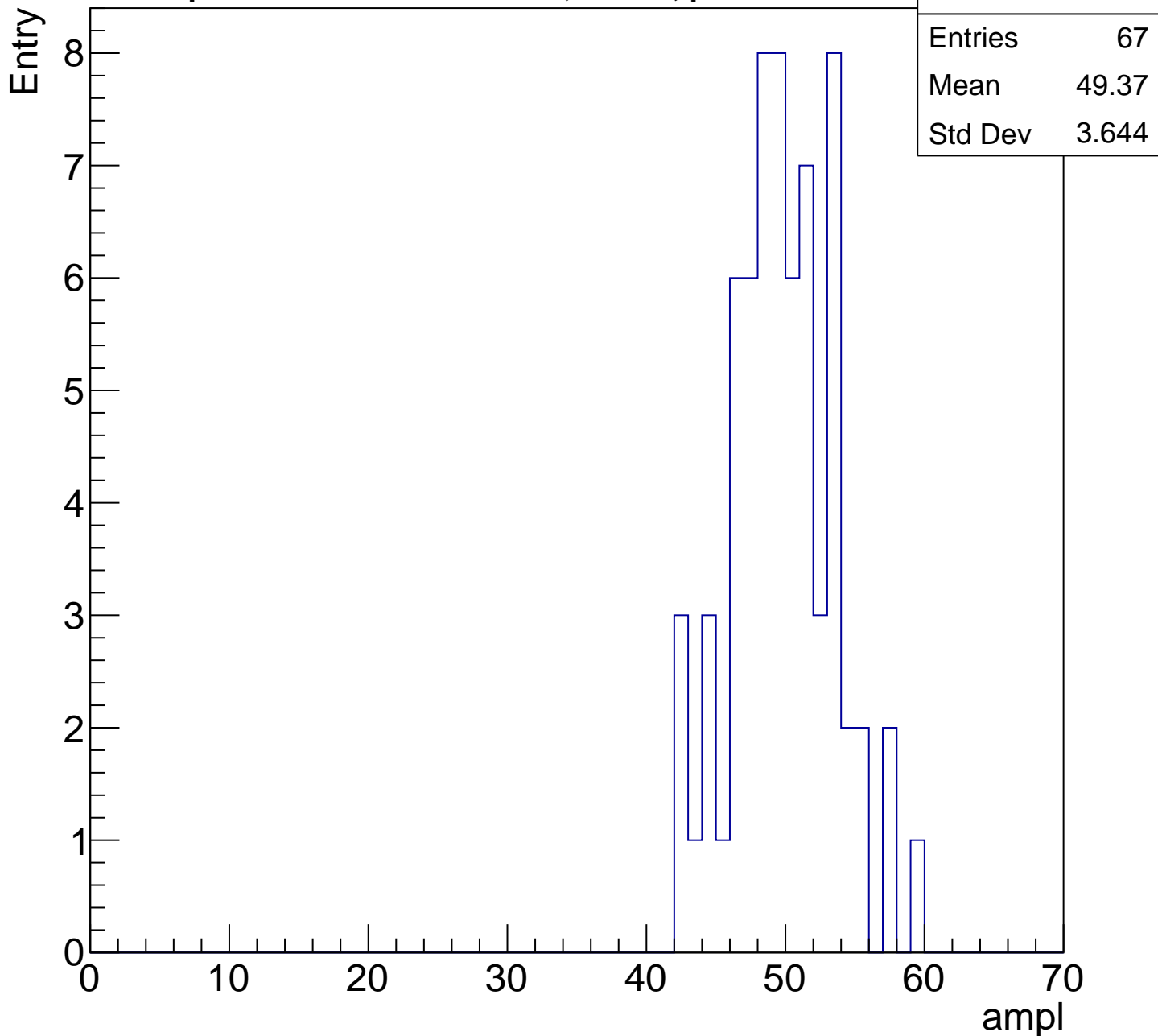
Gaus mean : 43.0109

Gaus Width: 3.8208



B1L102S, U6-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

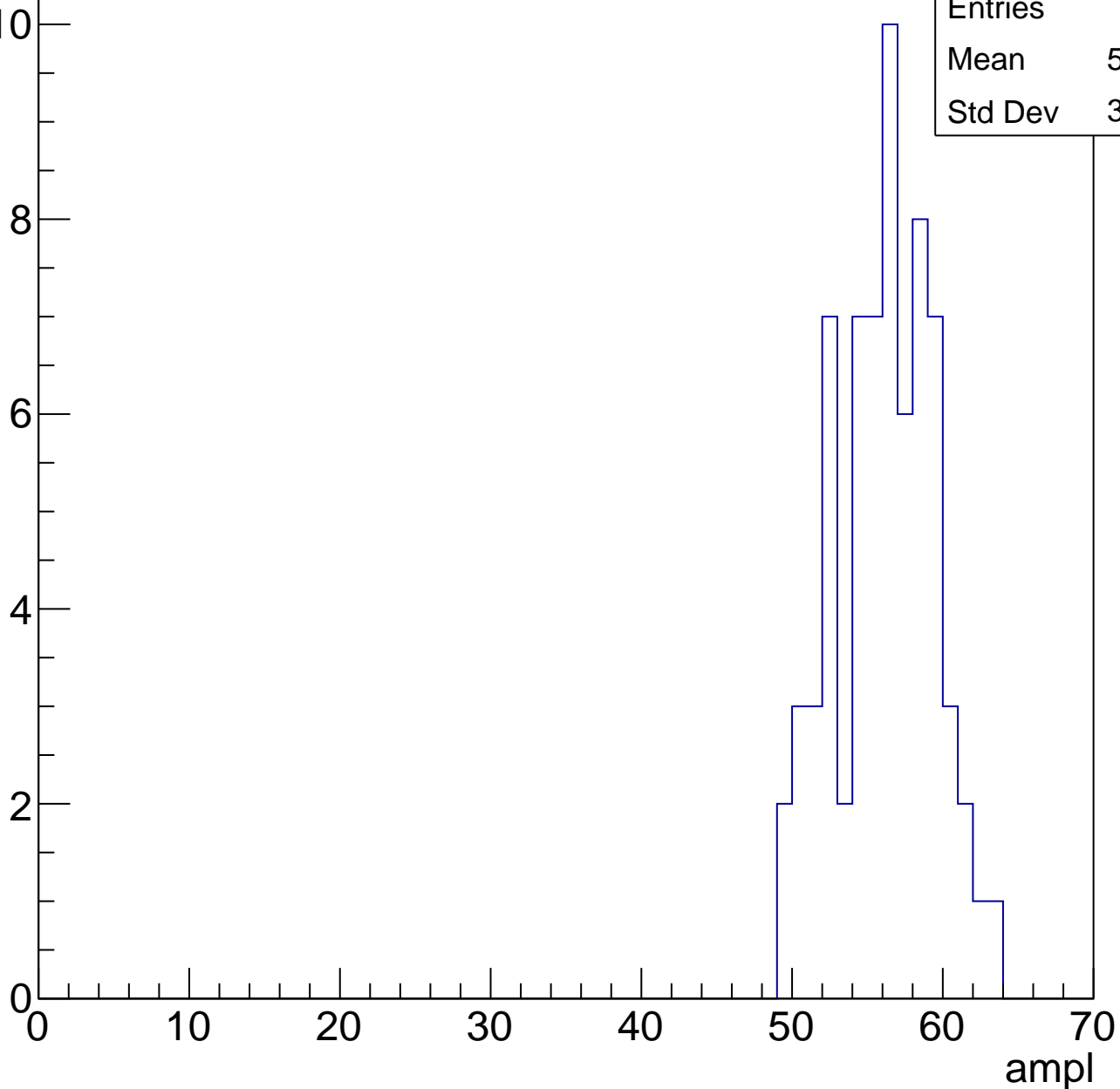


B1L102S, U6-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	55.65
Std Dev	3.234

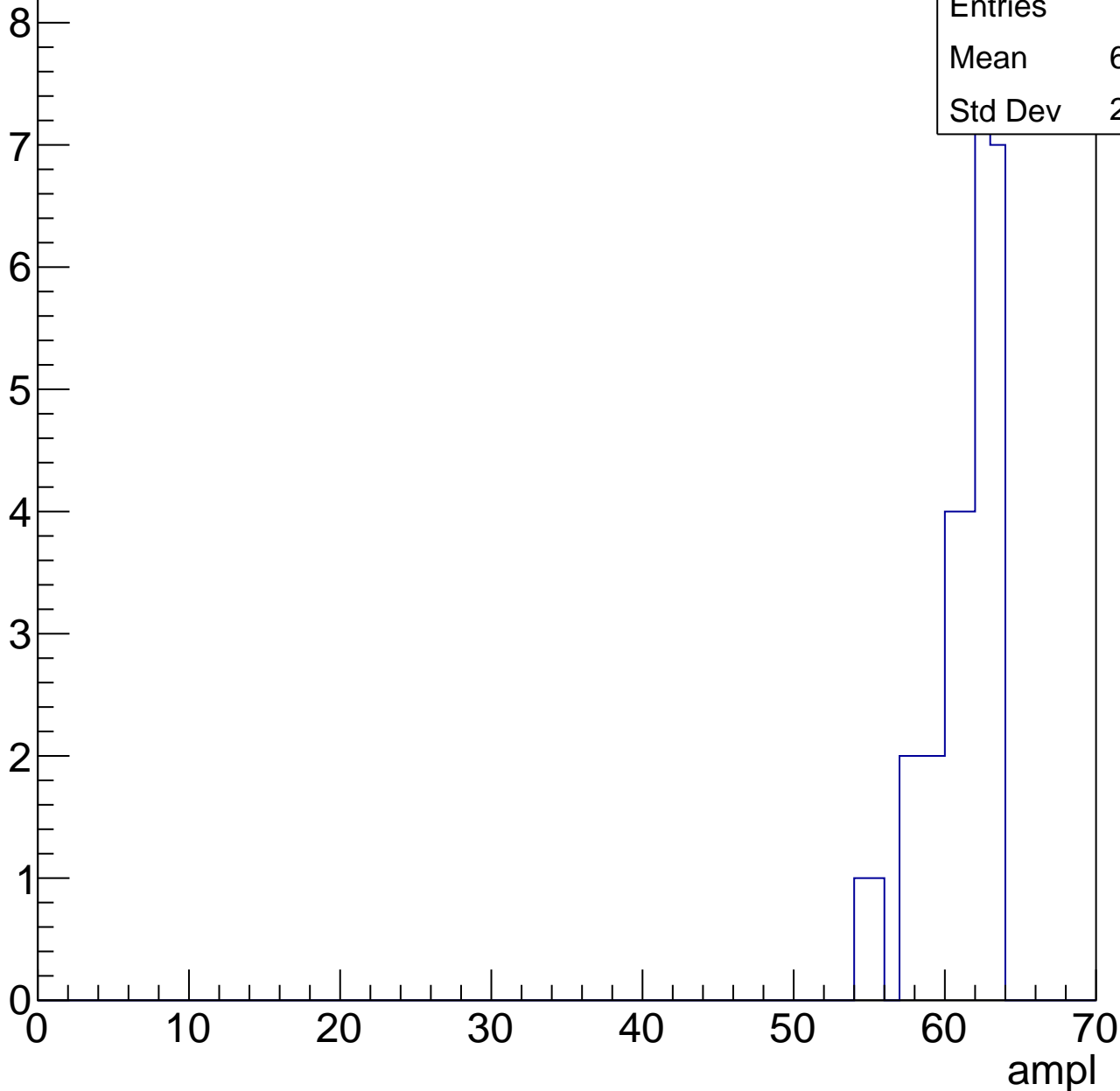


B1L102S, U6-ch48, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

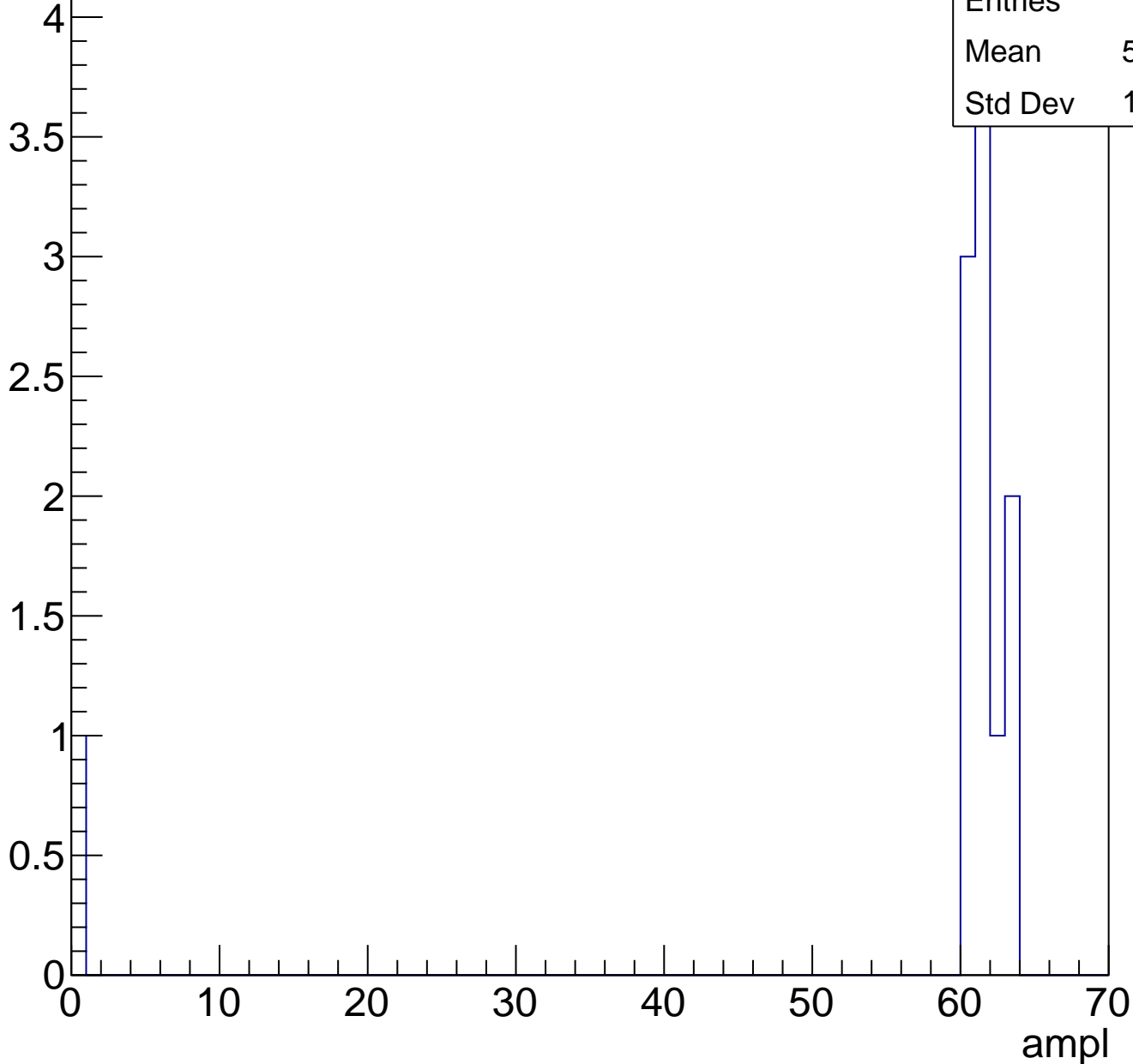
Entries	31
Mean	60.58
Std Dev	2.393



B1L102S, U6-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch49, adc0

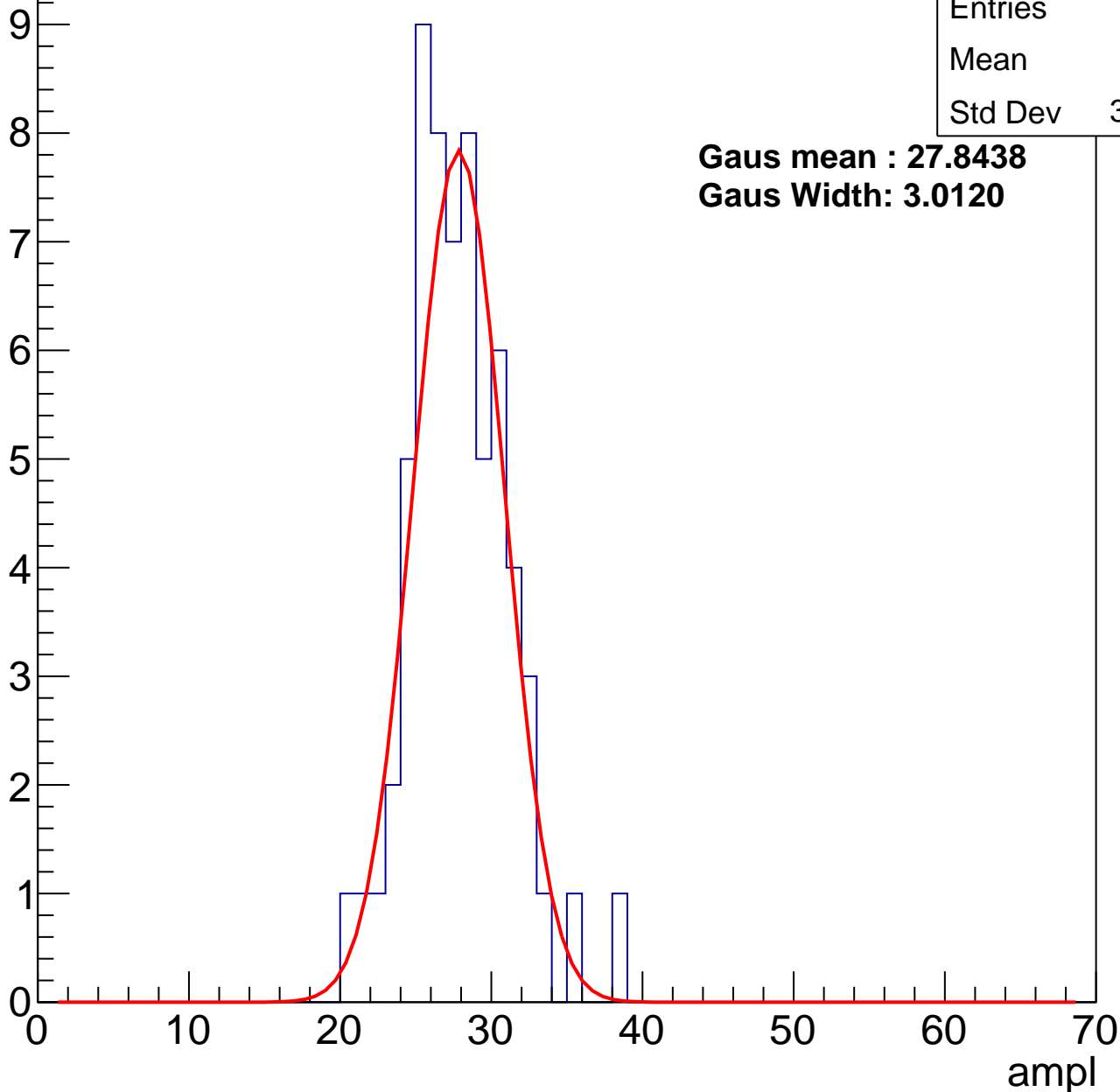
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.4
Std Dev	3.244

Gaus mean : 27.8438

Gaus Width: 3.0120



B1L102S, U6-ch49, adc1

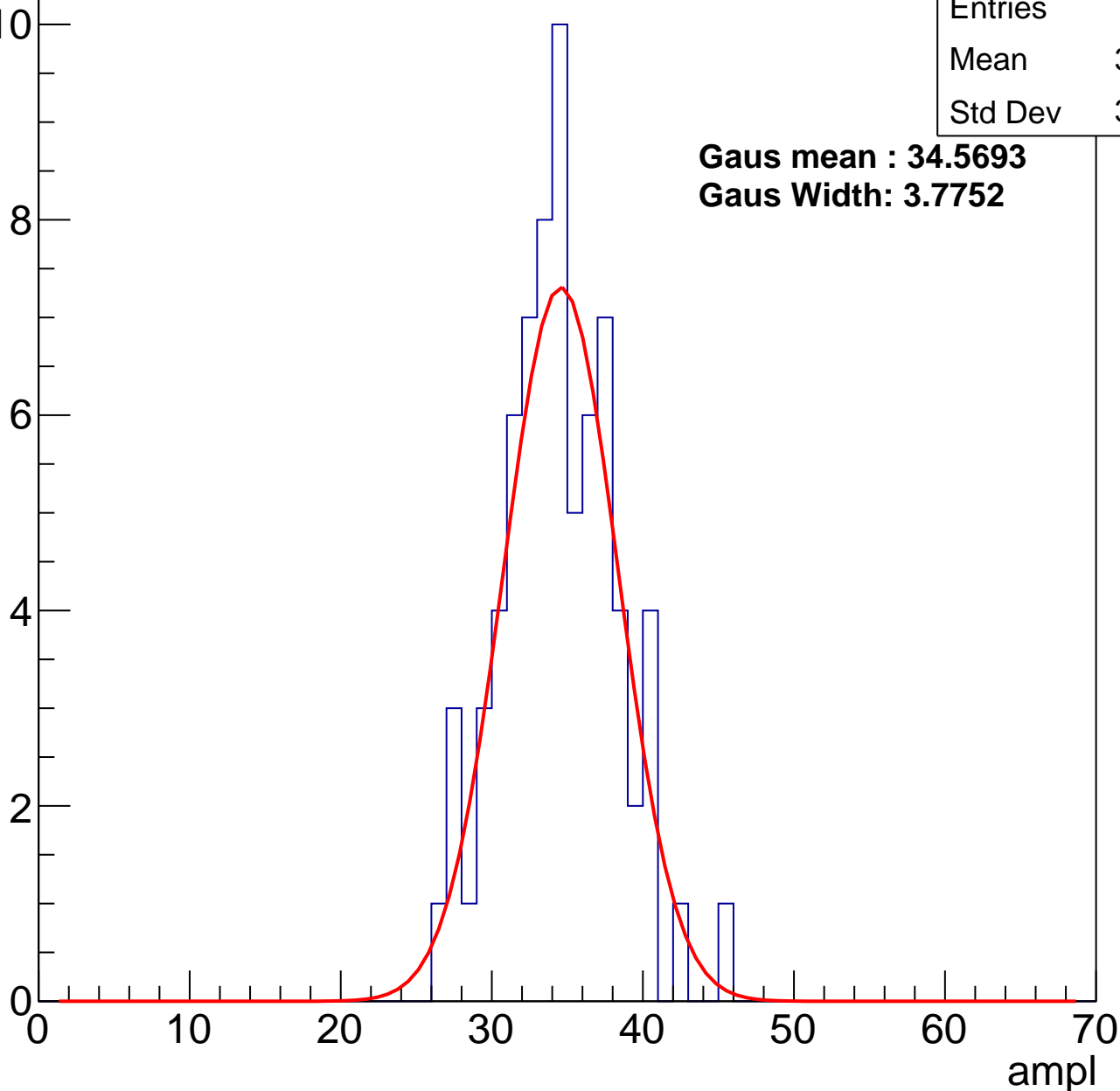
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.01
Std Dev	3.721

Gaus mean : 34.5693

Gaus Width: 3.7752



B1L102S, U6-ch49, adc2

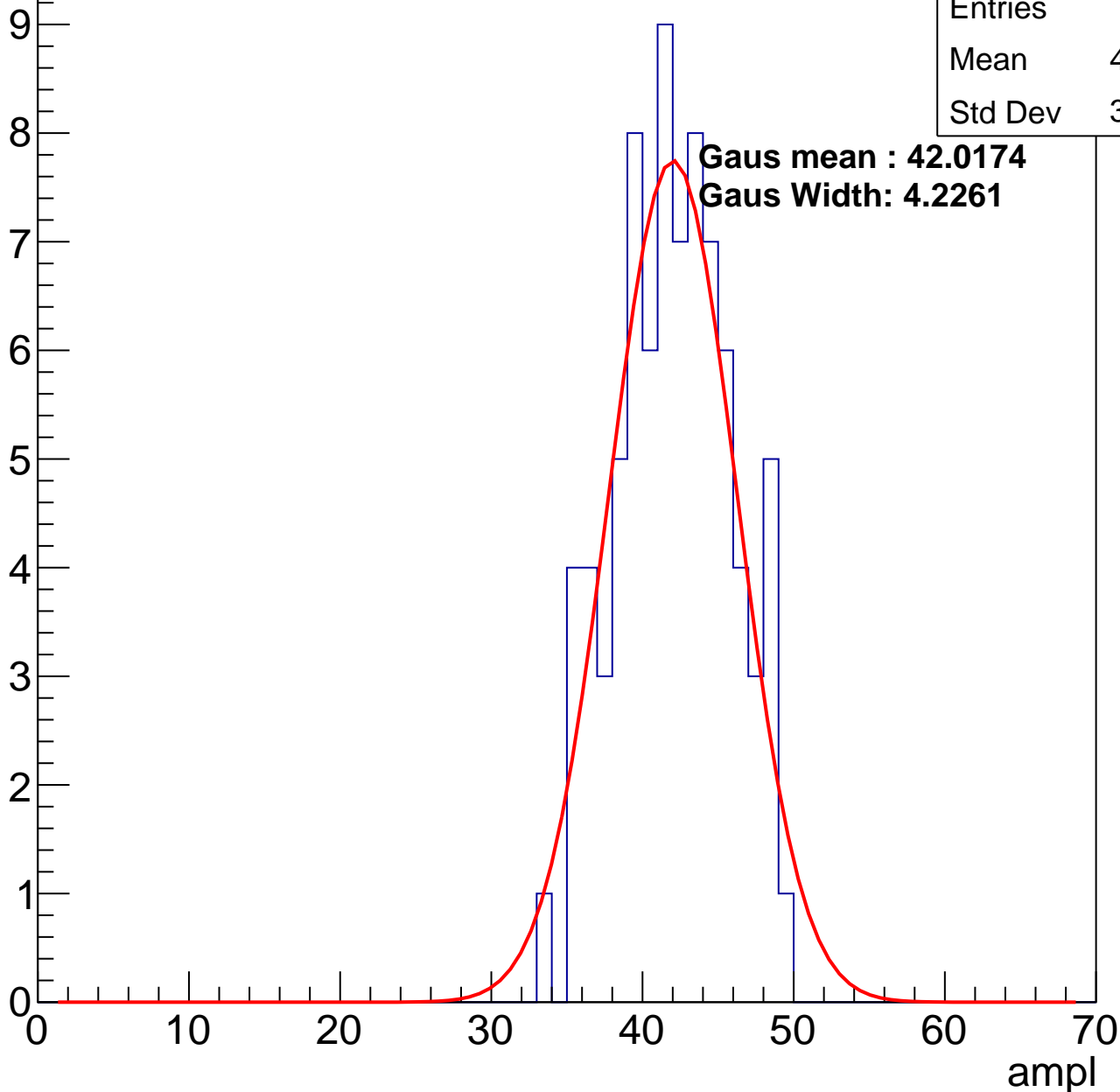
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	41.59
Std Dev	3.728

Gaus mean : 42.0174

Gaus Width: 4.2261

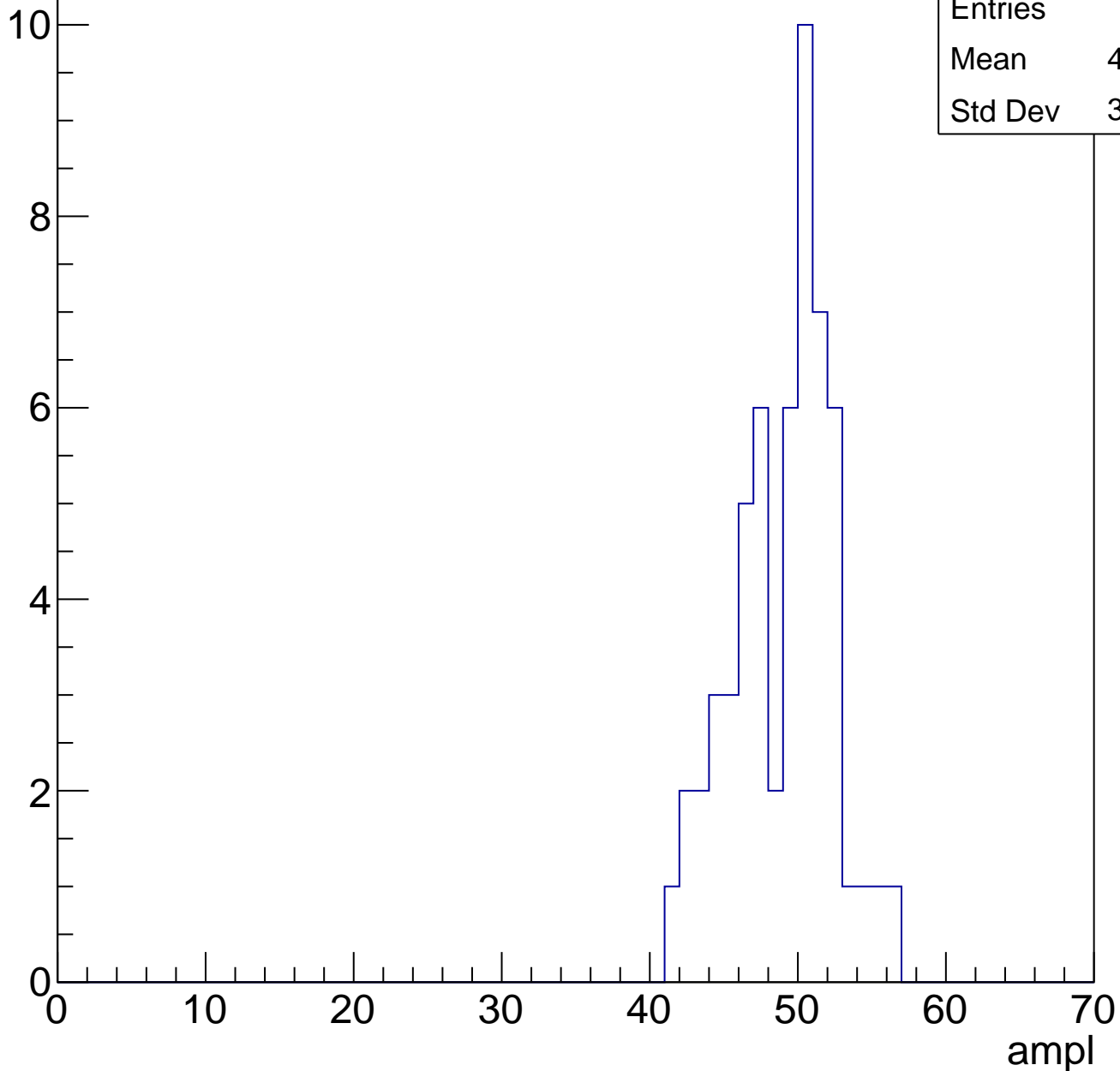


B1L102S, U6-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

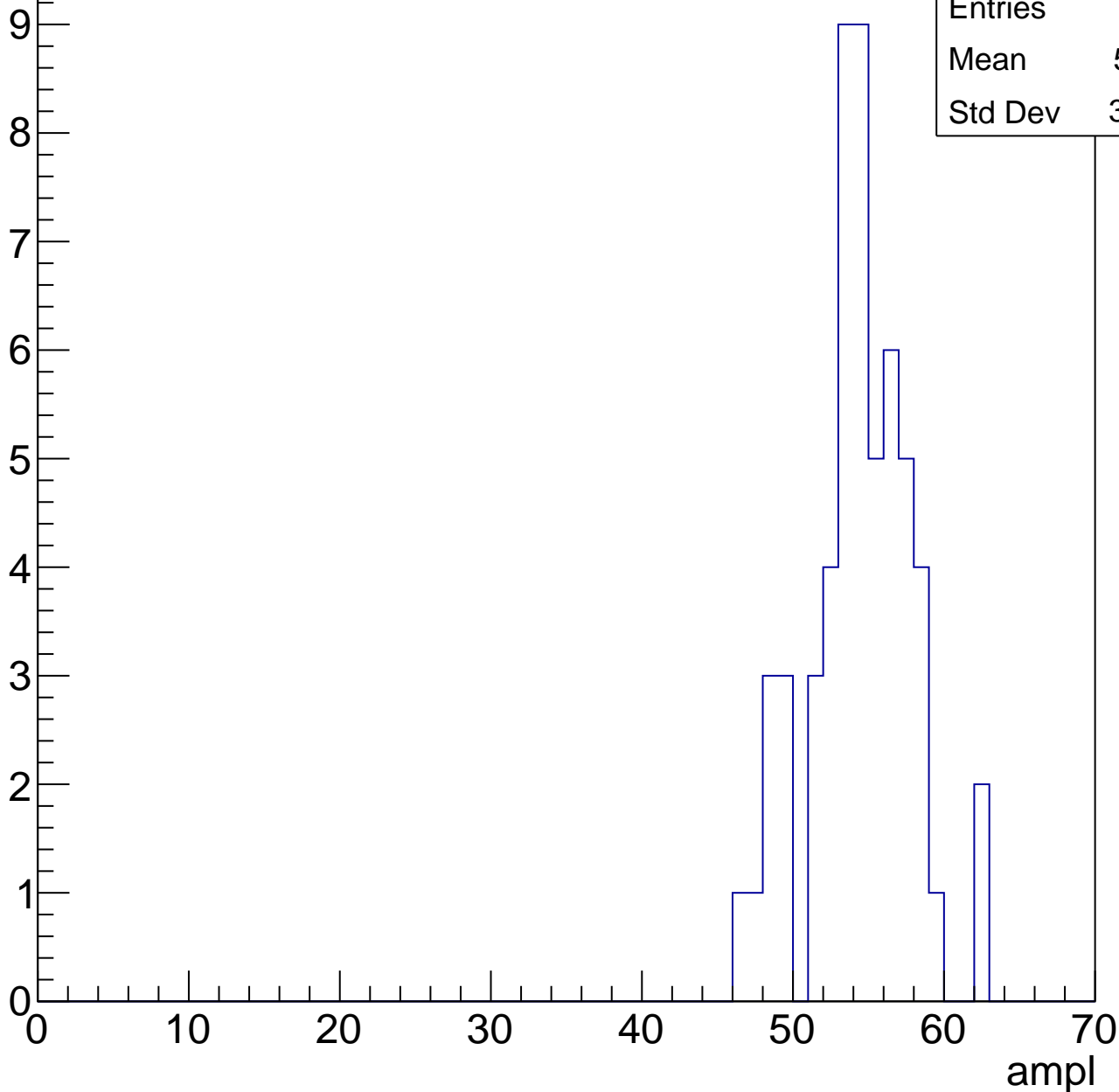
Entries	57
Mean	48.54
Std Dev	3.335



B1L102S, U6-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

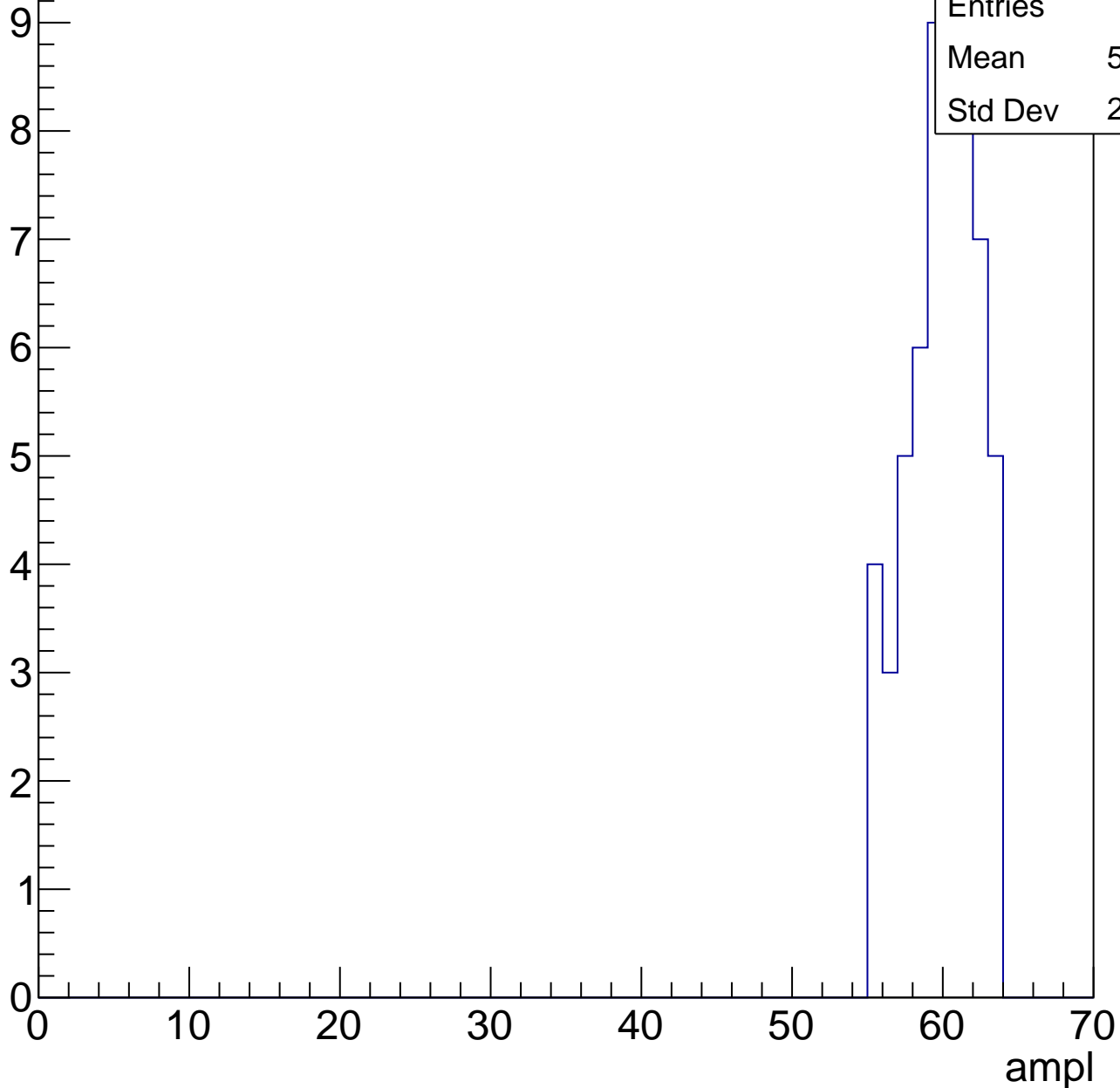


Entries	56
Mean	53.91
Std Dev	3.377

B1L102S, U6-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

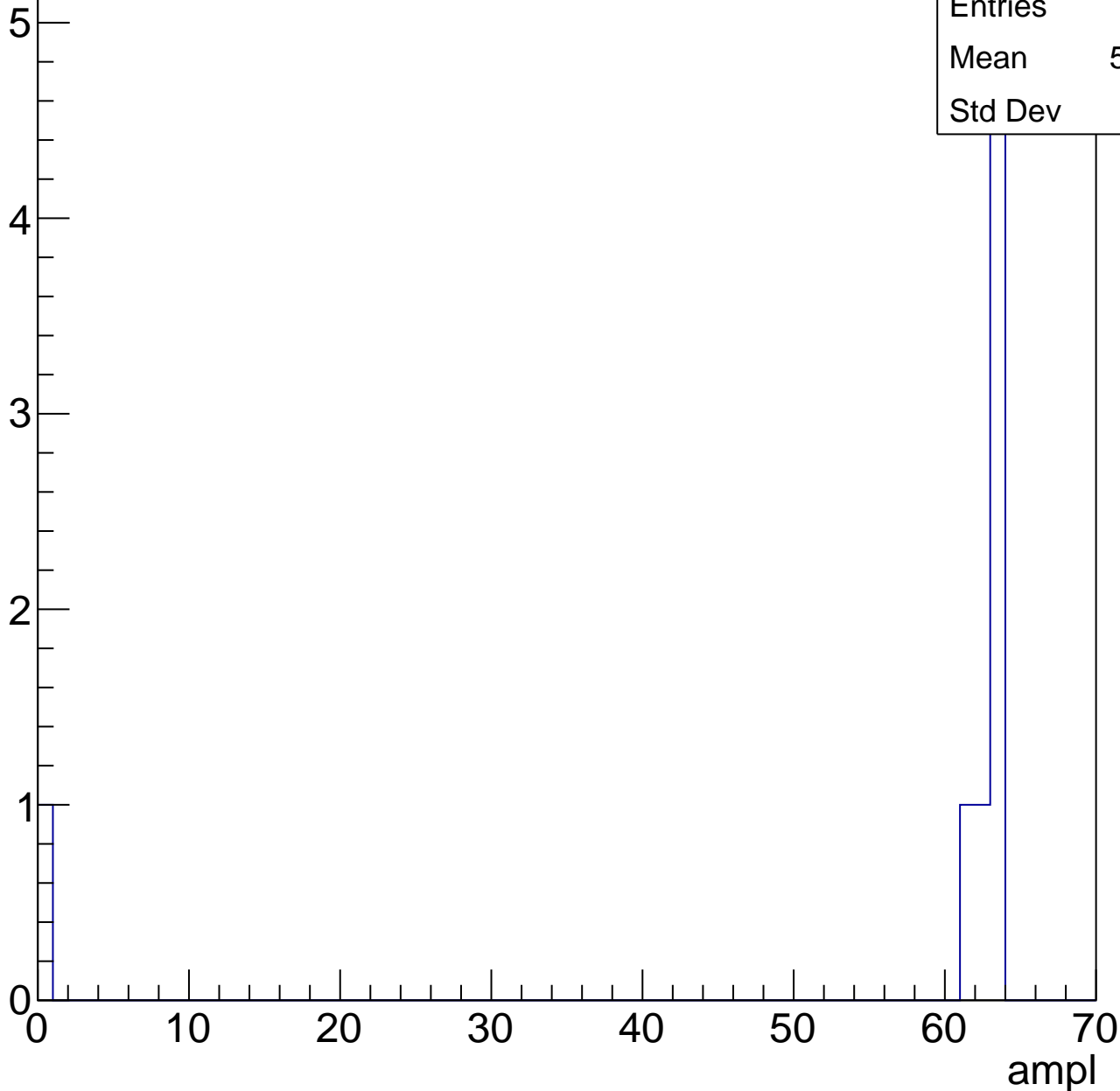


B1L102S, U6-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

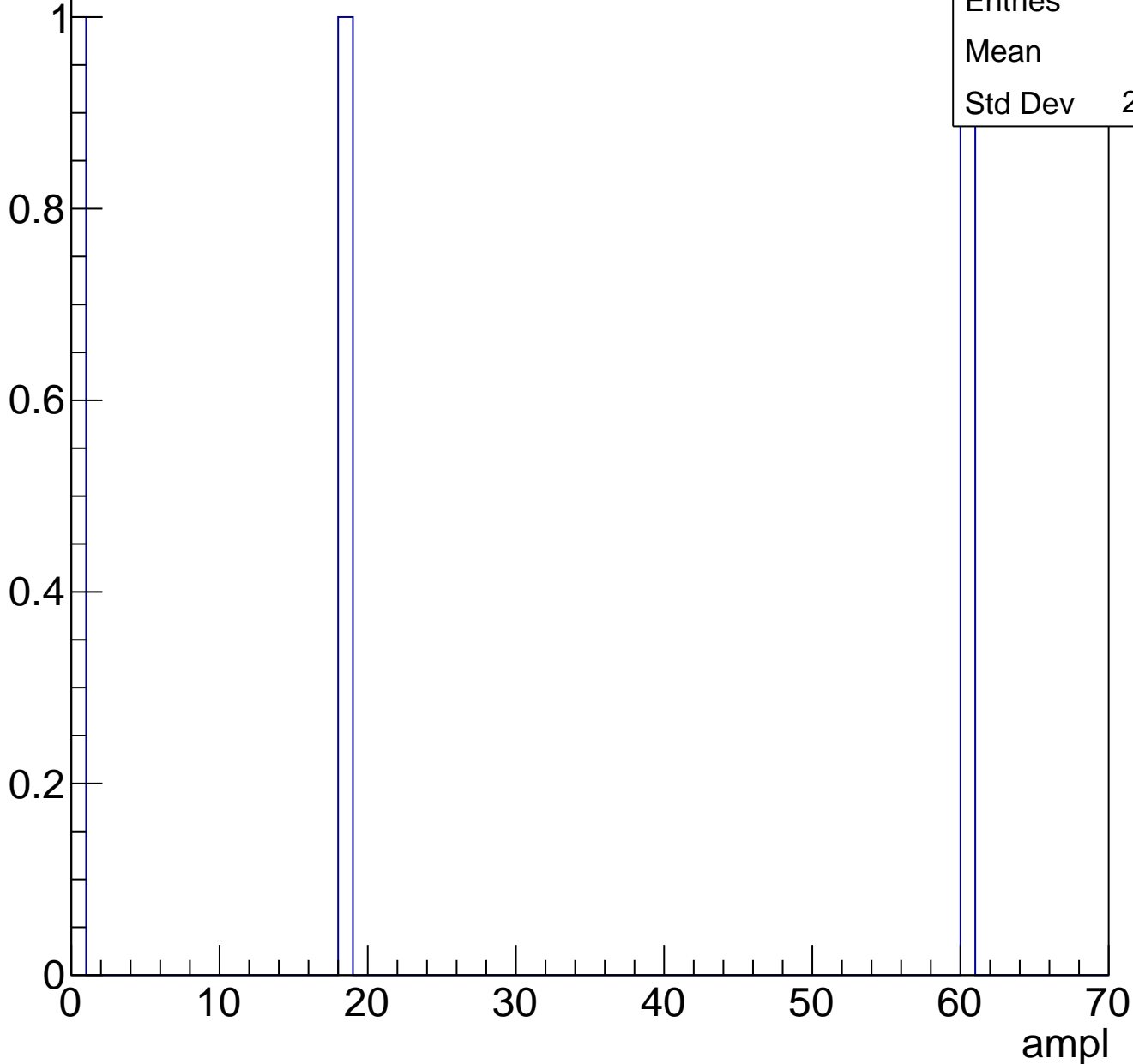
Entries	8
Mean	54.75
Std Dev	20.7



B1L102S, U6-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	26
Std Dev	25.14

B1L102S, U6-ch50, adc0

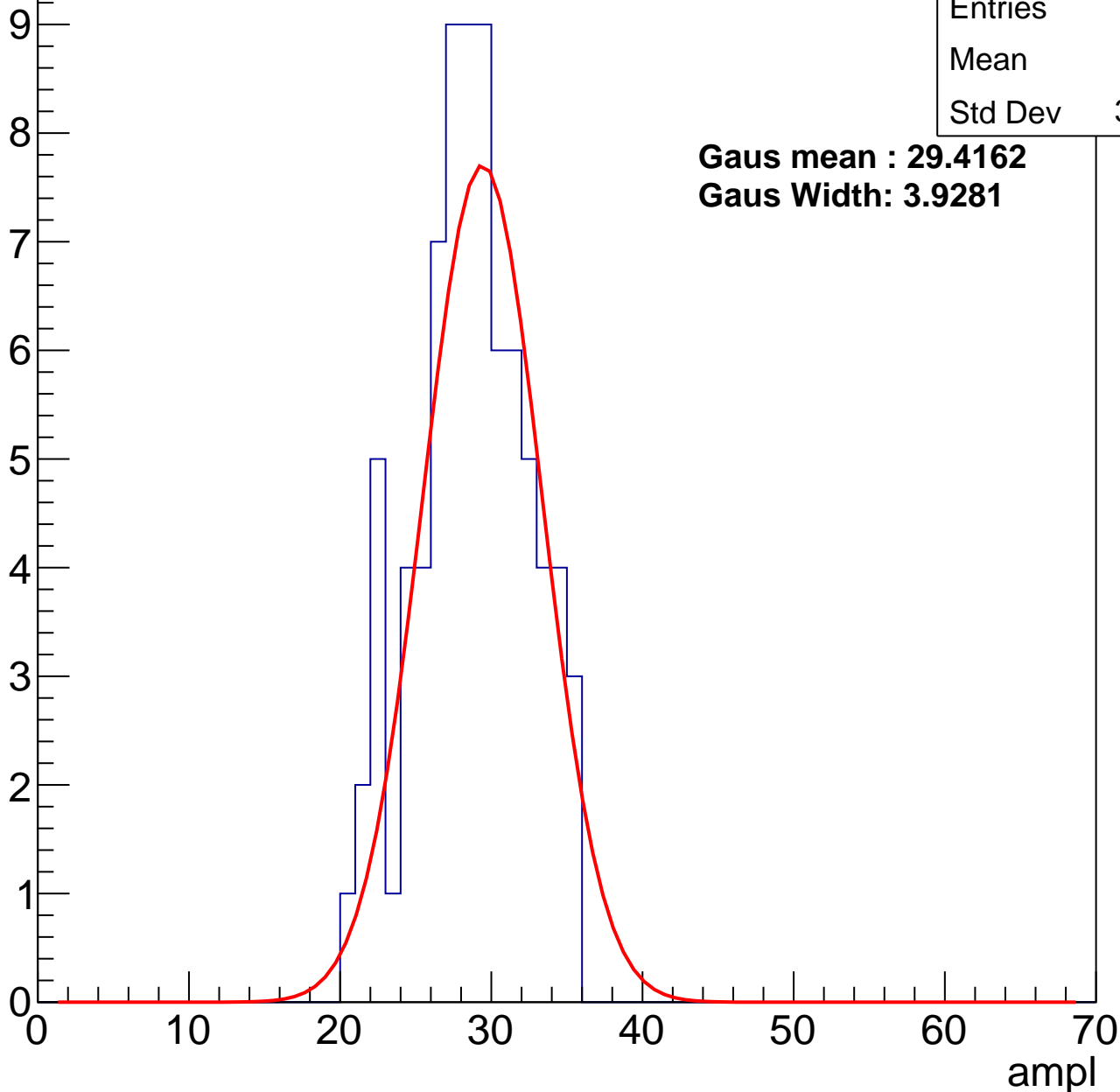
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.2
Std Dev	3.661

Gaus mean : 29.4162

Gaus Width: 3.9281



B1L102S, U6-ch50, adc1

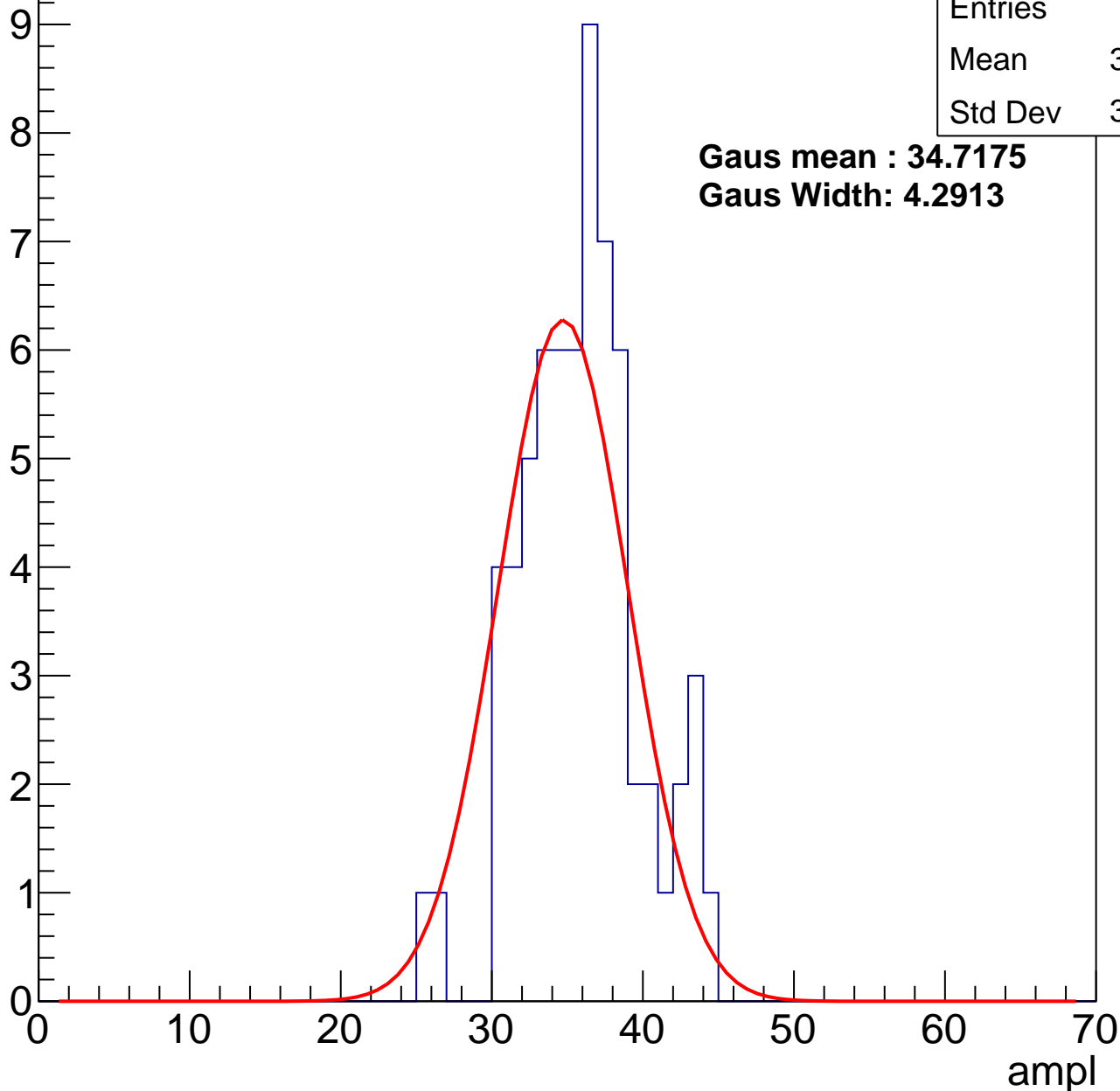
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.36
Std Dev	3.875

Gaus mean : 34.7175

Gaus Width: 4.2913



B1L102S, U6-ch50, adc2

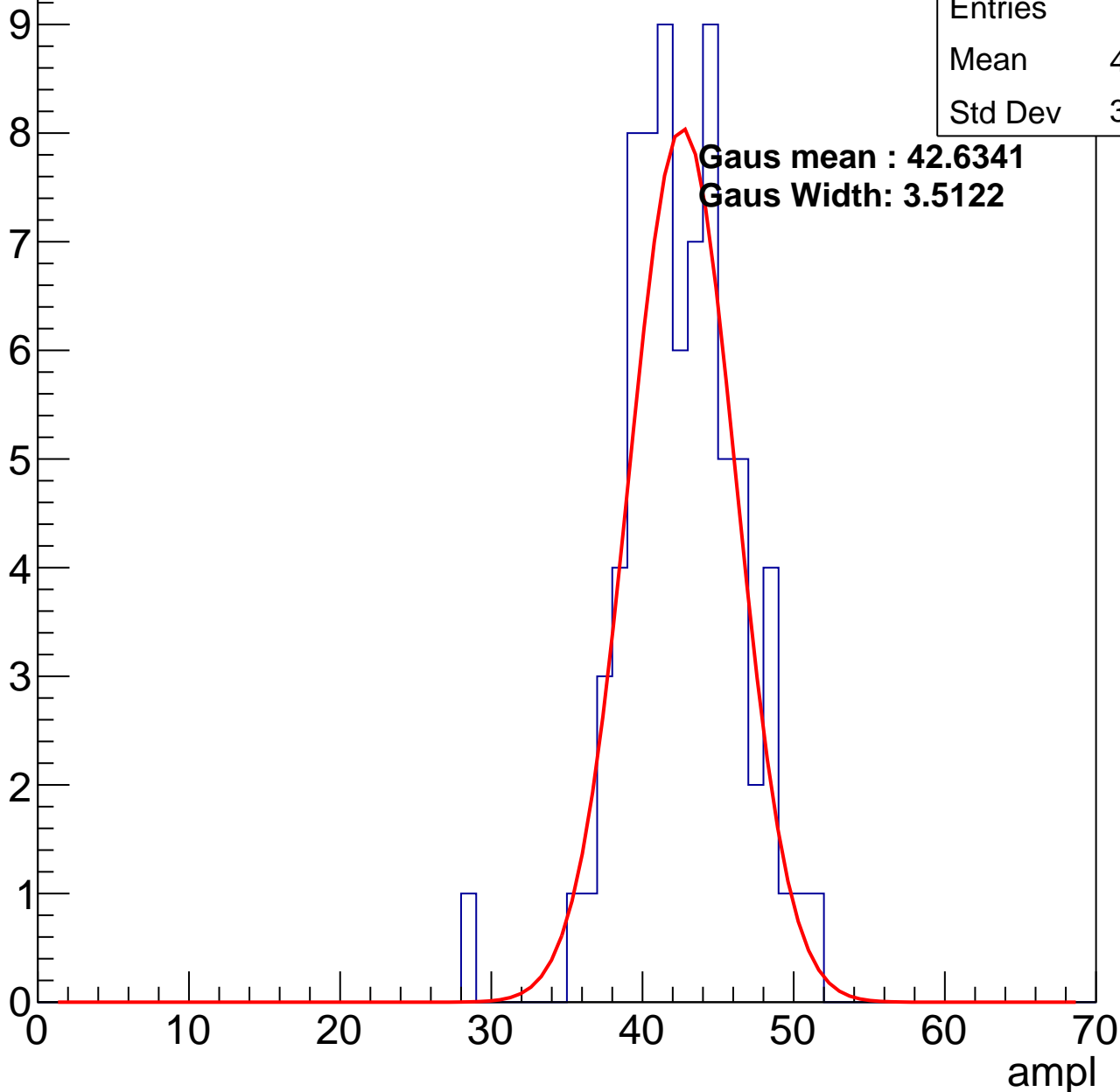
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.14
Std Dev	3.783

Gaus mean : 42.6341

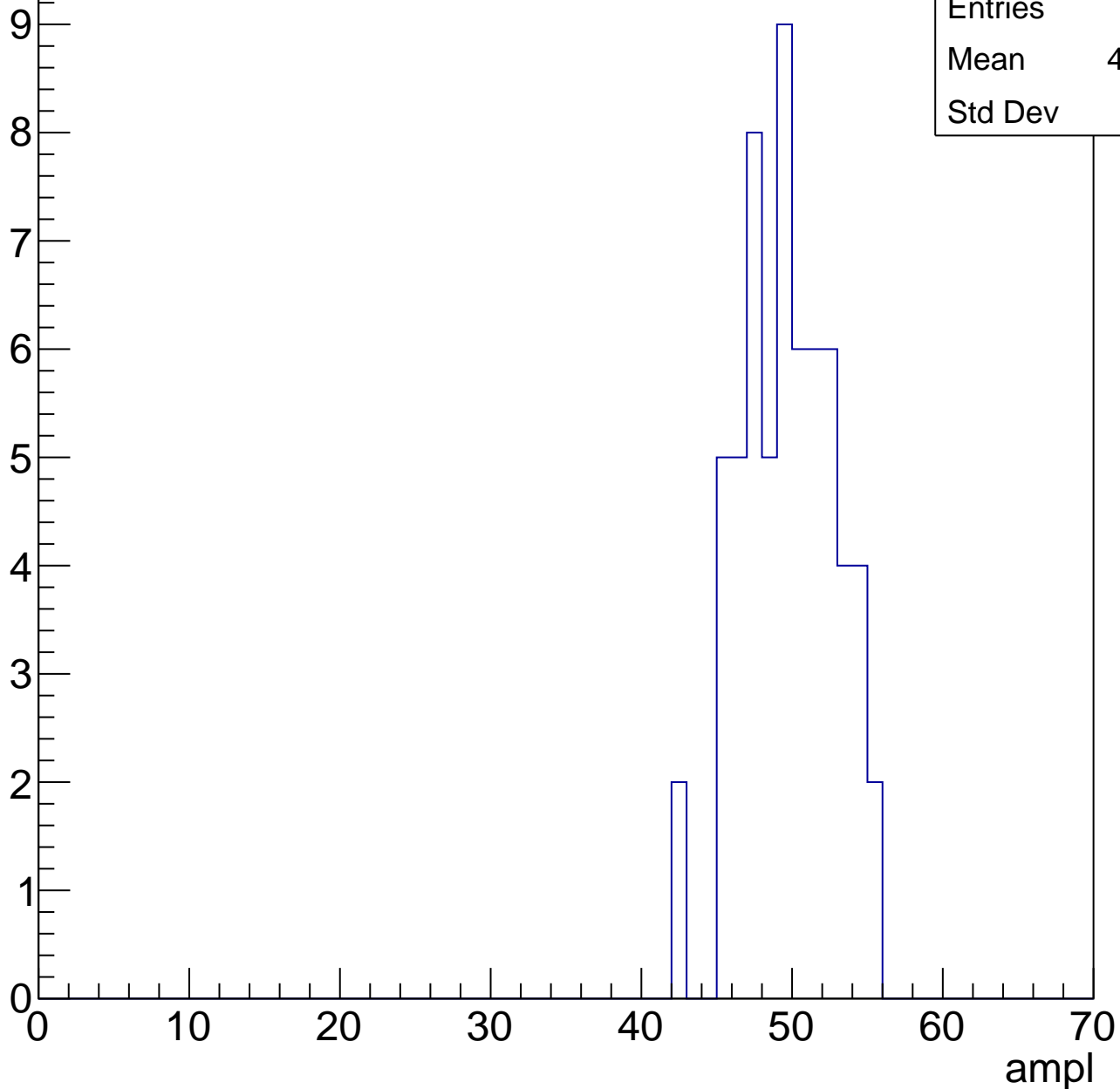
Gaus Width: 3.5122



B1L102S, U6-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



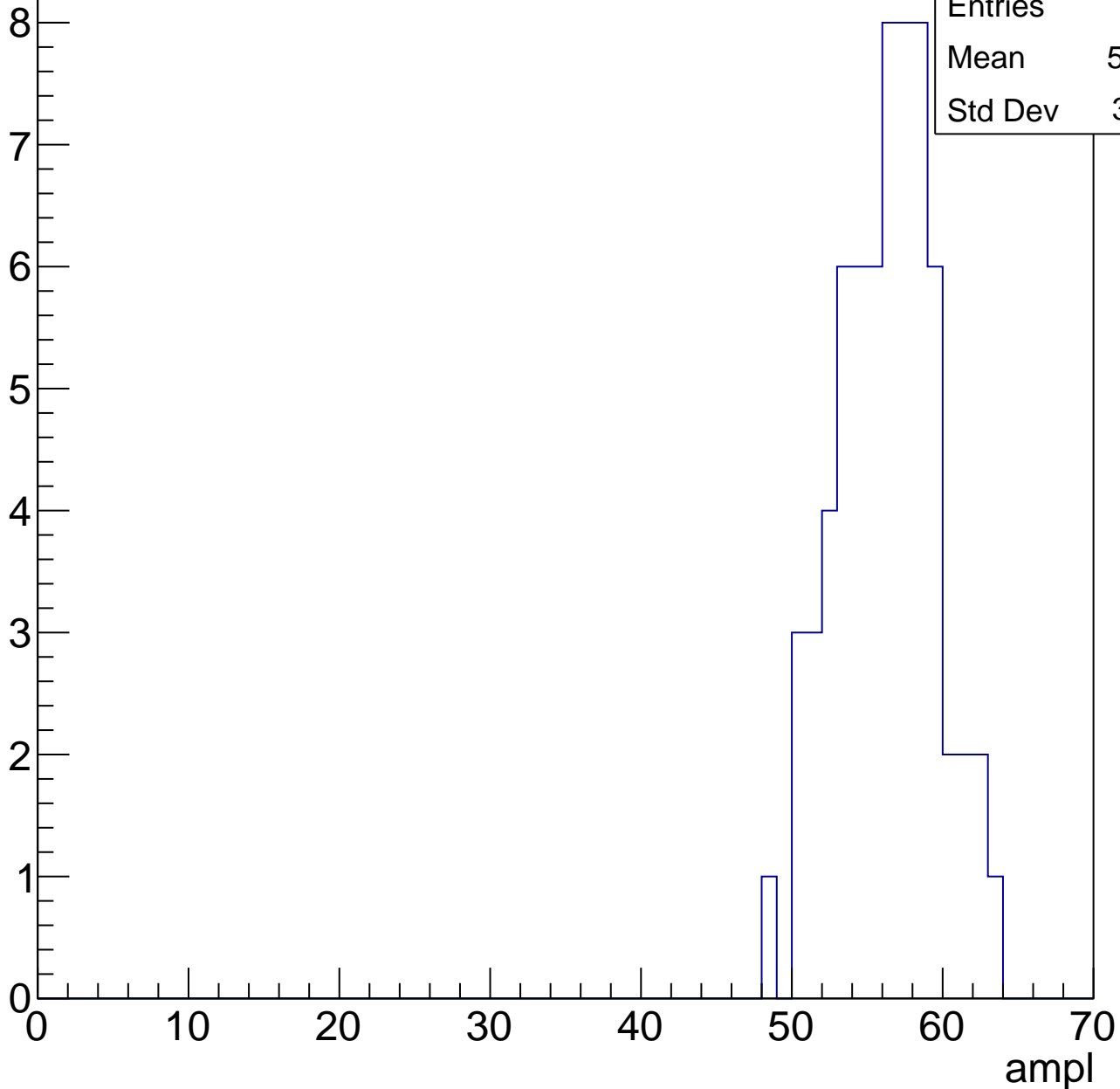
Entries	62
Mean	49.23
Std Dev	3.05

B1L102S, U6-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	55.79
Std Dev	3.231



B1L102S, U6-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

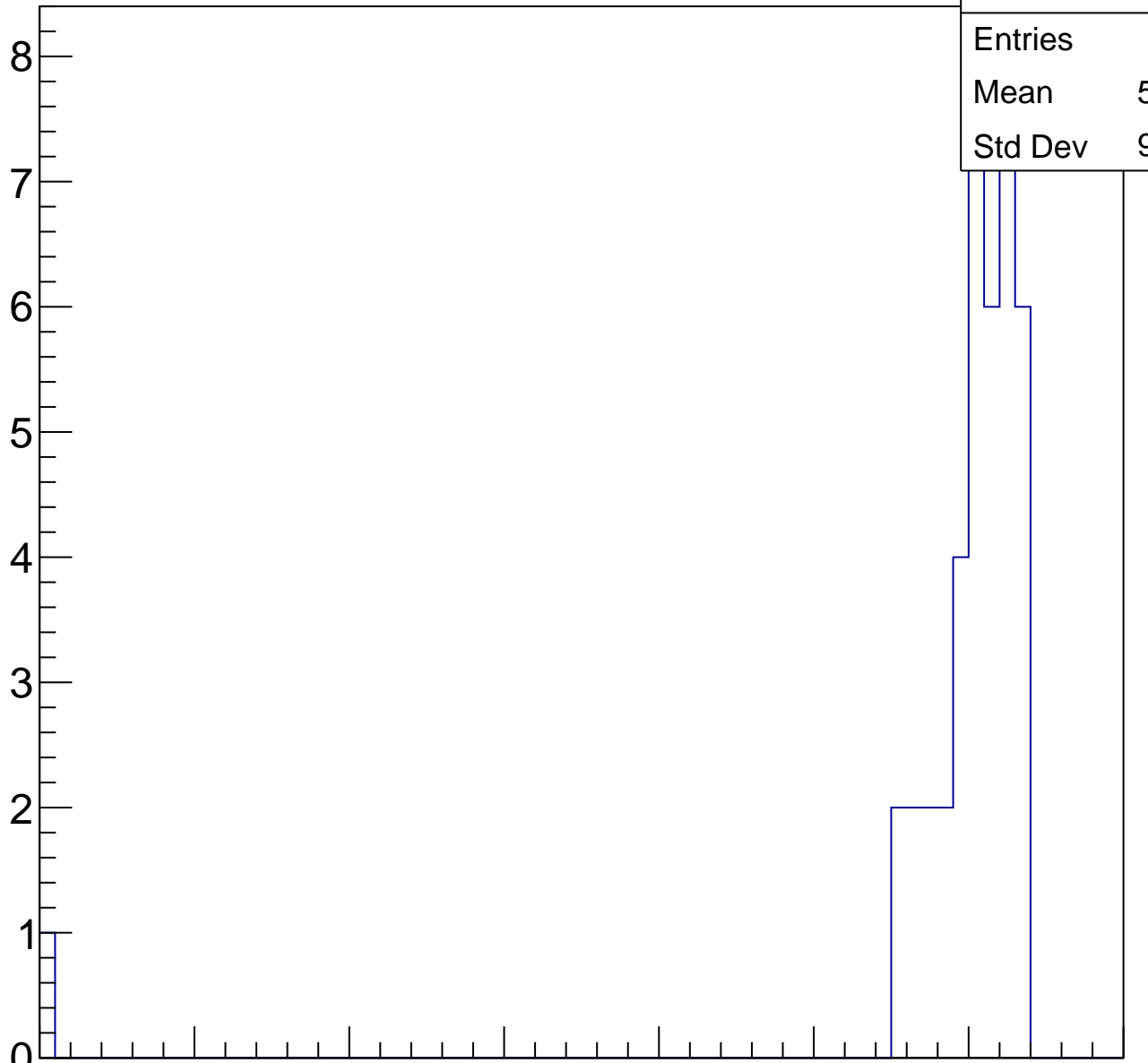
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.73
Std Dev	9.548

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.2

Std Dev

0.9798

B1L102S, U6-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch51, adc0

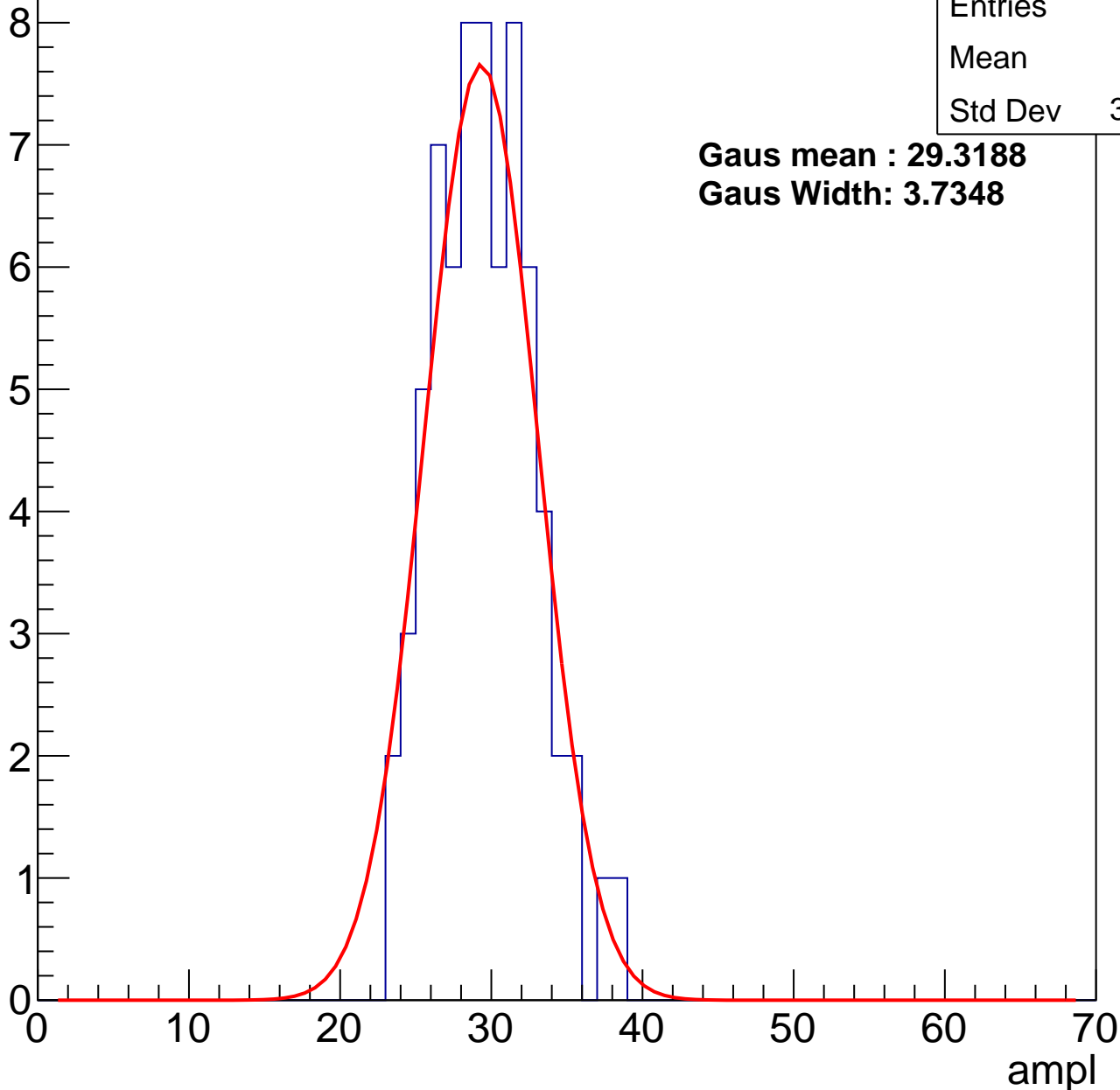
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	29.1
Std Dev	3.284

Gaus mean : 29.3188

Gaus Width: 3.7348



B1L102S, U6-ch51, adc1

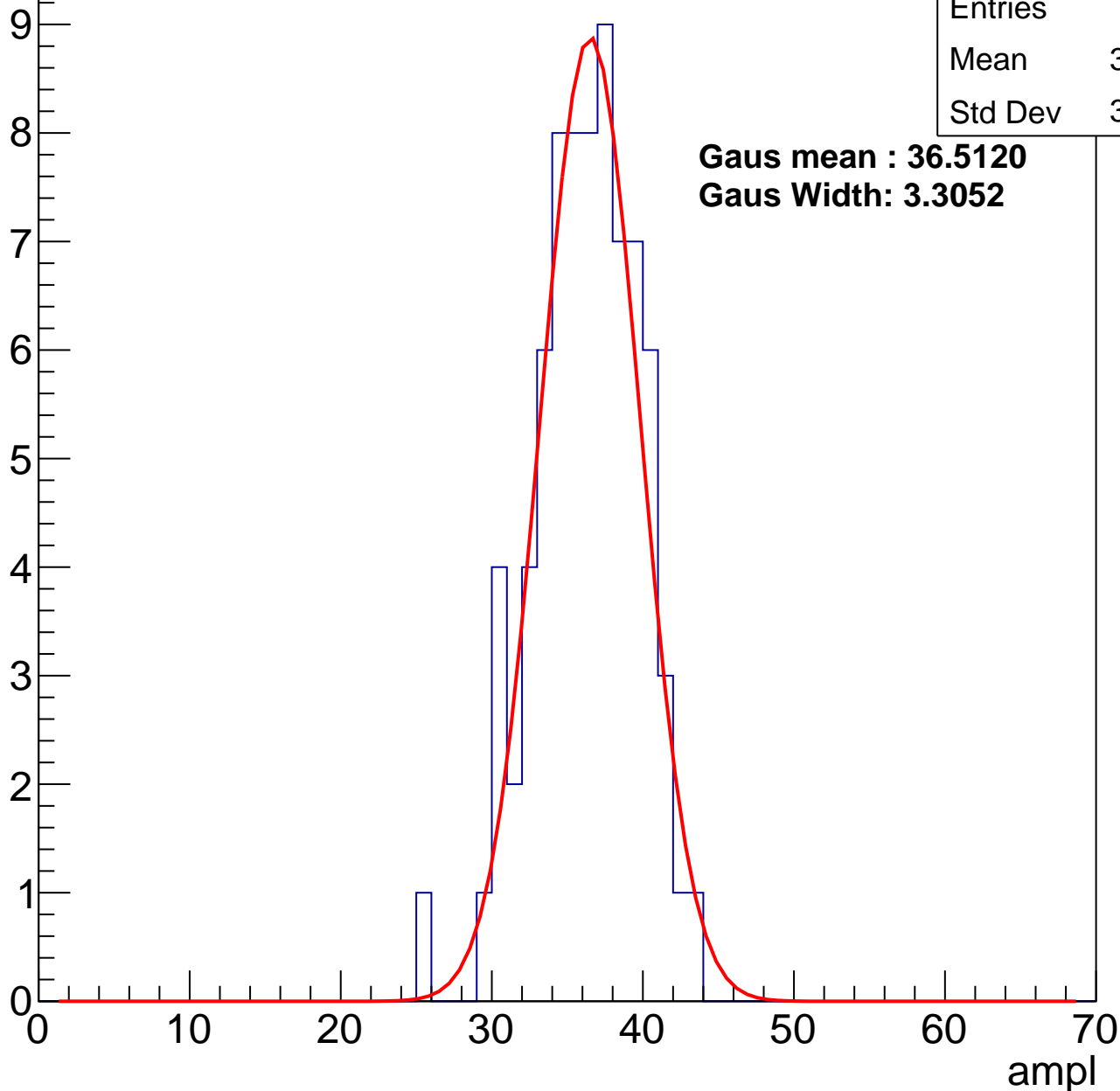
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.82
Std Dev	3.394

Gaus mean : 36.5120

Gaus Width: 3.3052



B1L102S, U6-ch51, adc2

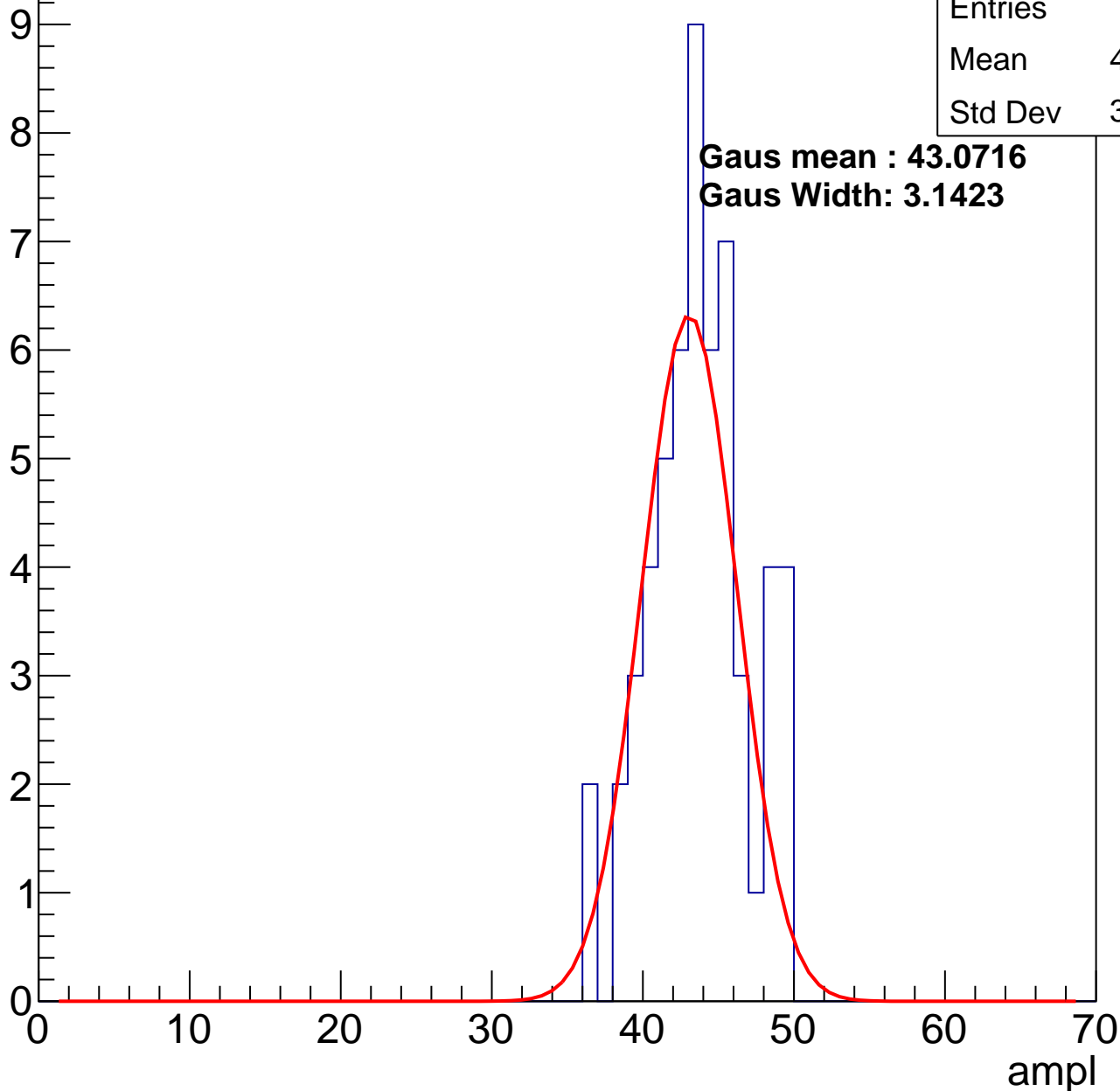
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	43.23
Std Dev	3.207

Gaus mean : 43.0716

Gaus Width: 3.1423



B1L102S, U6-ch51, adc3

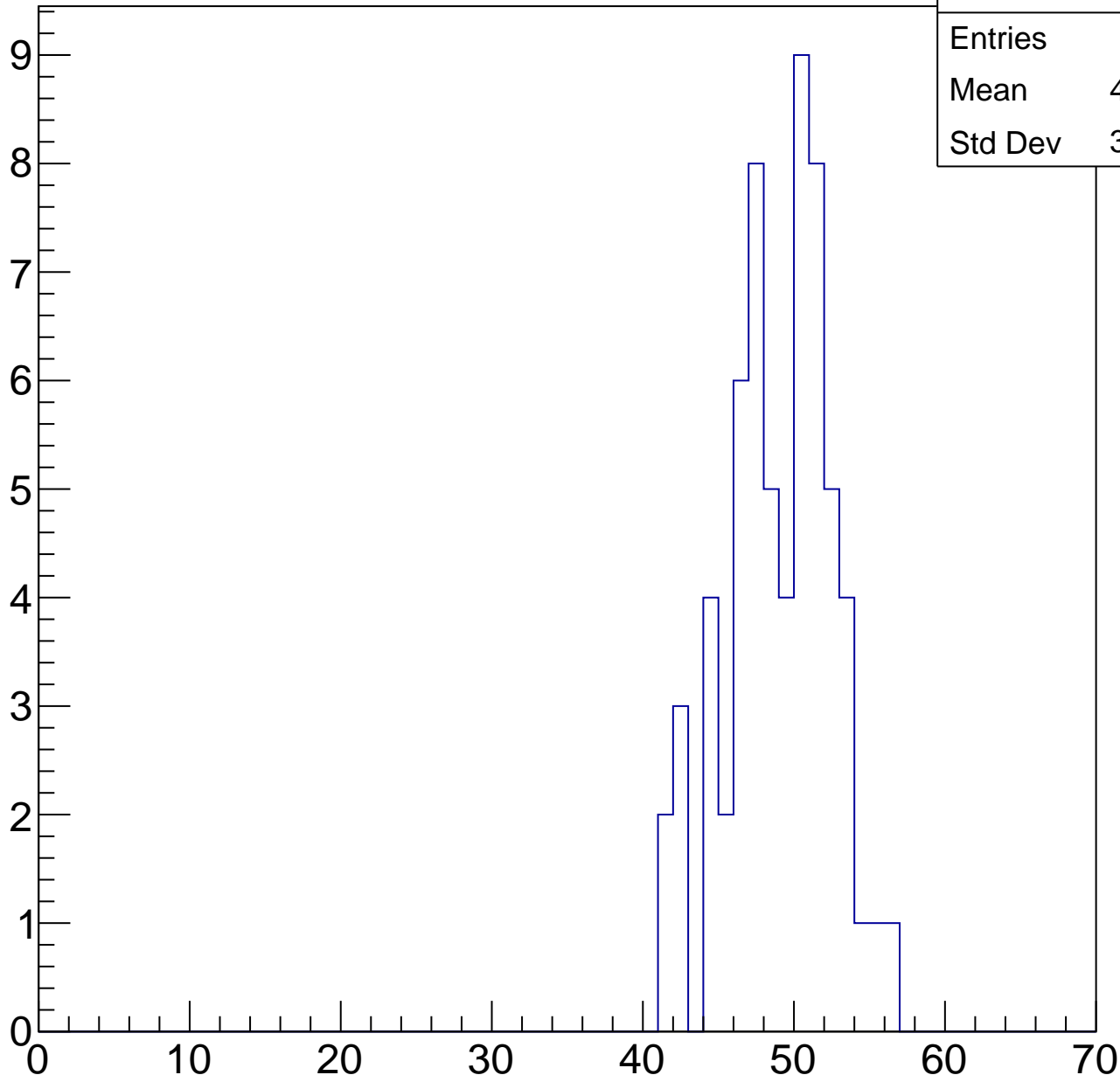
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	48.52
Std Dev	3.436

ampl

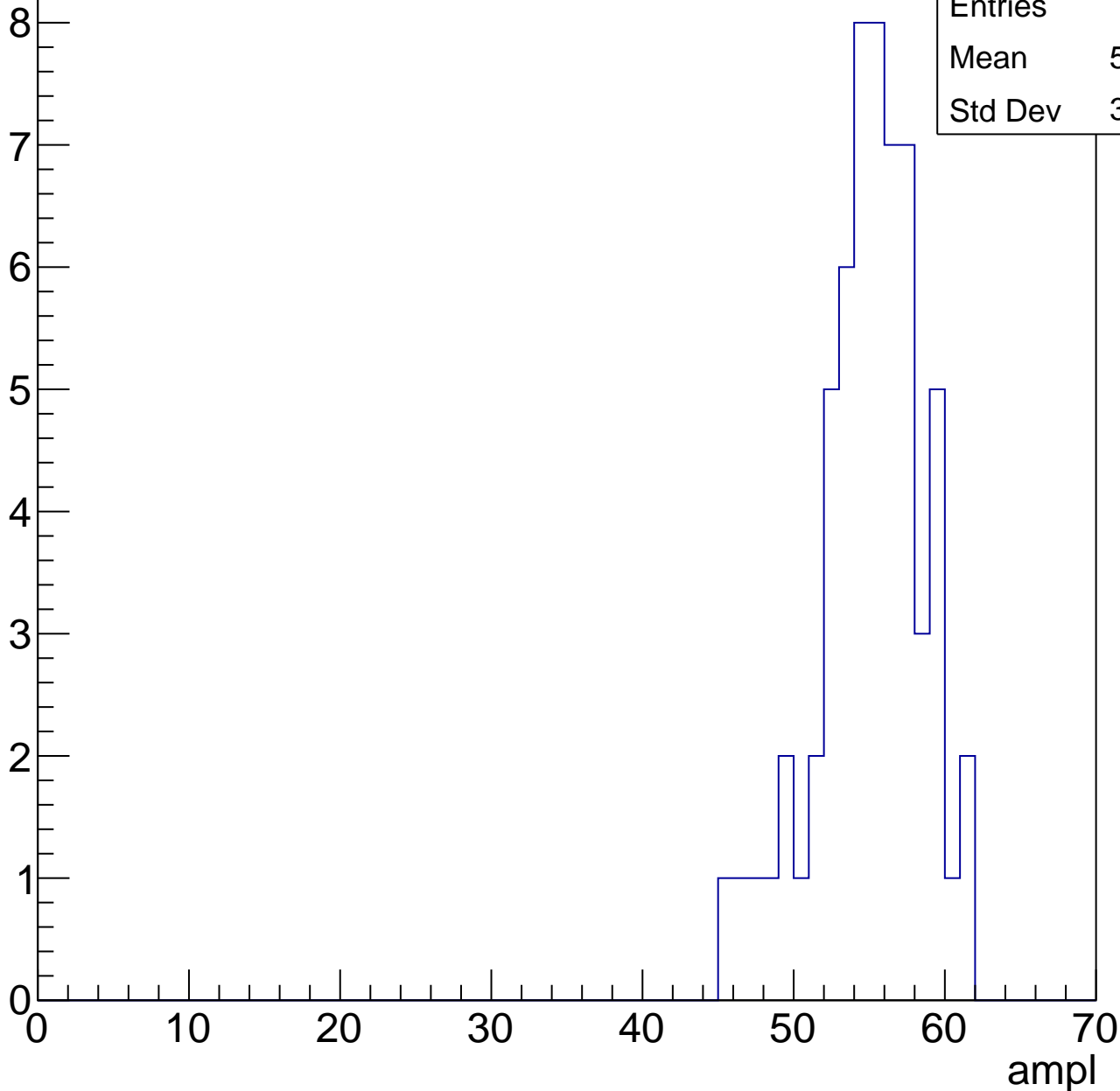


B1L102S, U6-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.56
Std Dev	3.452



B1L102S, U6-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.94
Std Dev	2.273

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

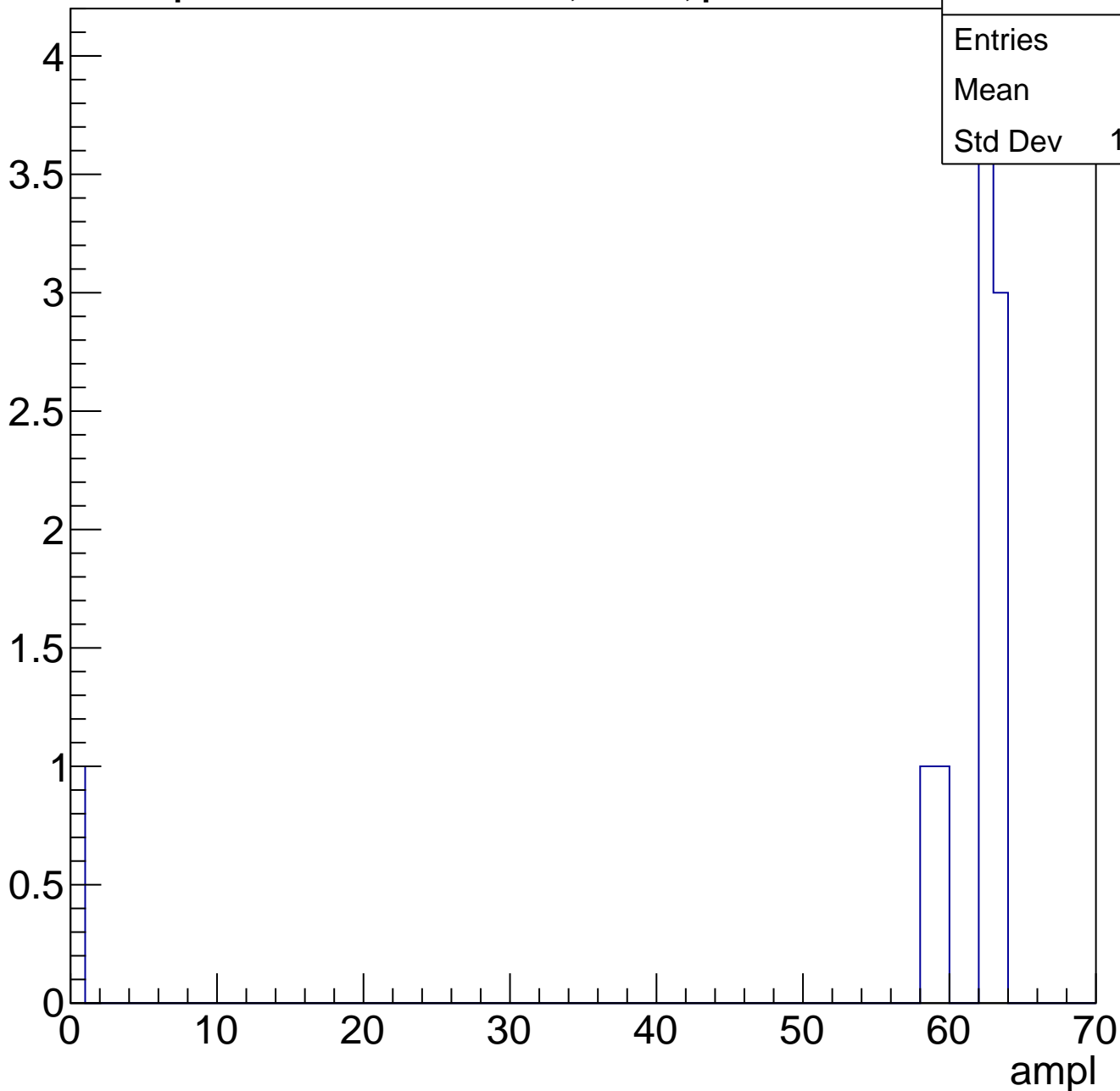
8

9

B1L102S, U6-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	55.4
Std Dev	18.54

B1L102S, U6-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U6-ch52, adc0

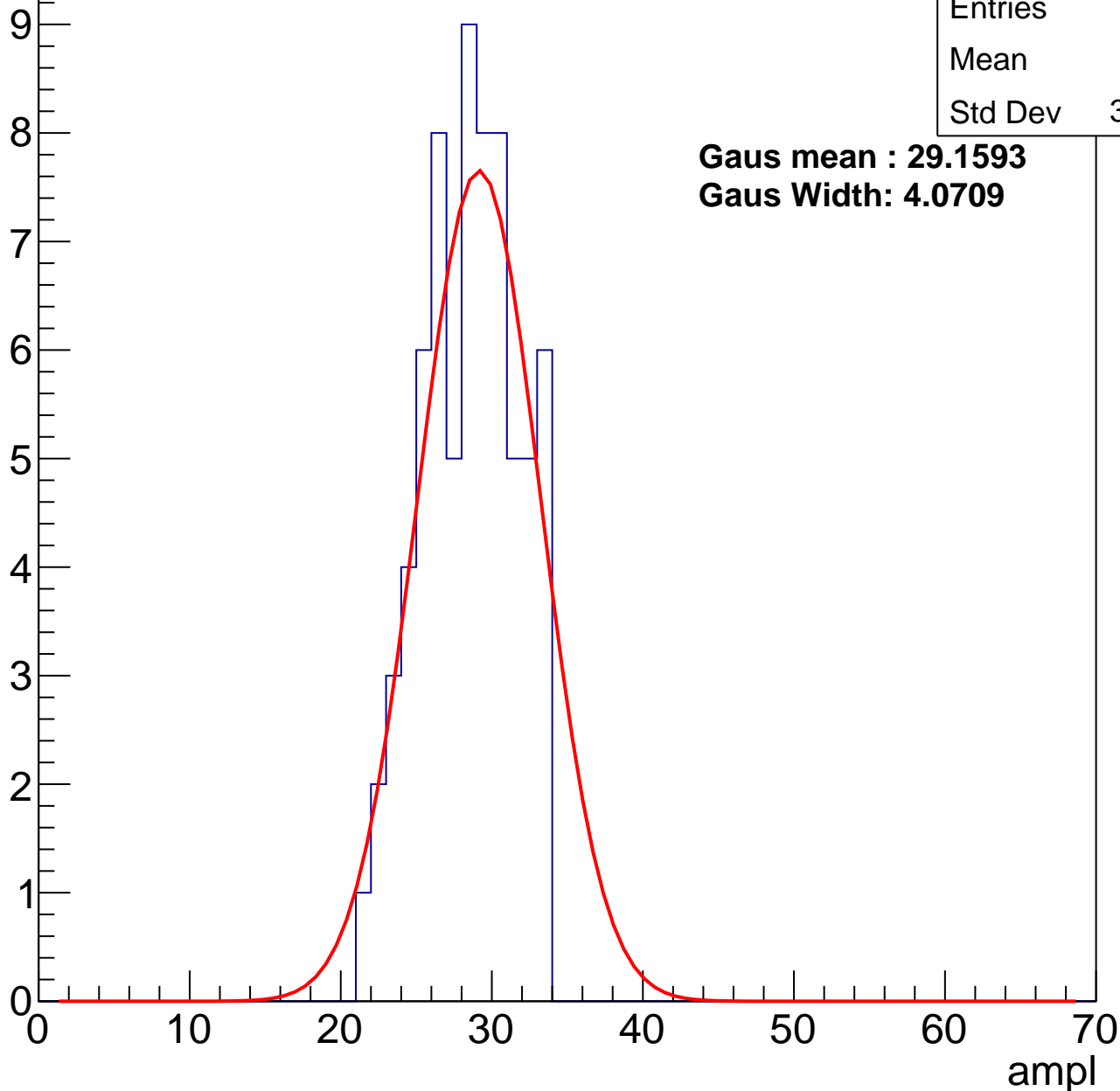
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28
Std Dev	3.085

Gaus mean : 29.1593

Gaus Width: 4.0709



B1L102S, U6-ch52, adc1

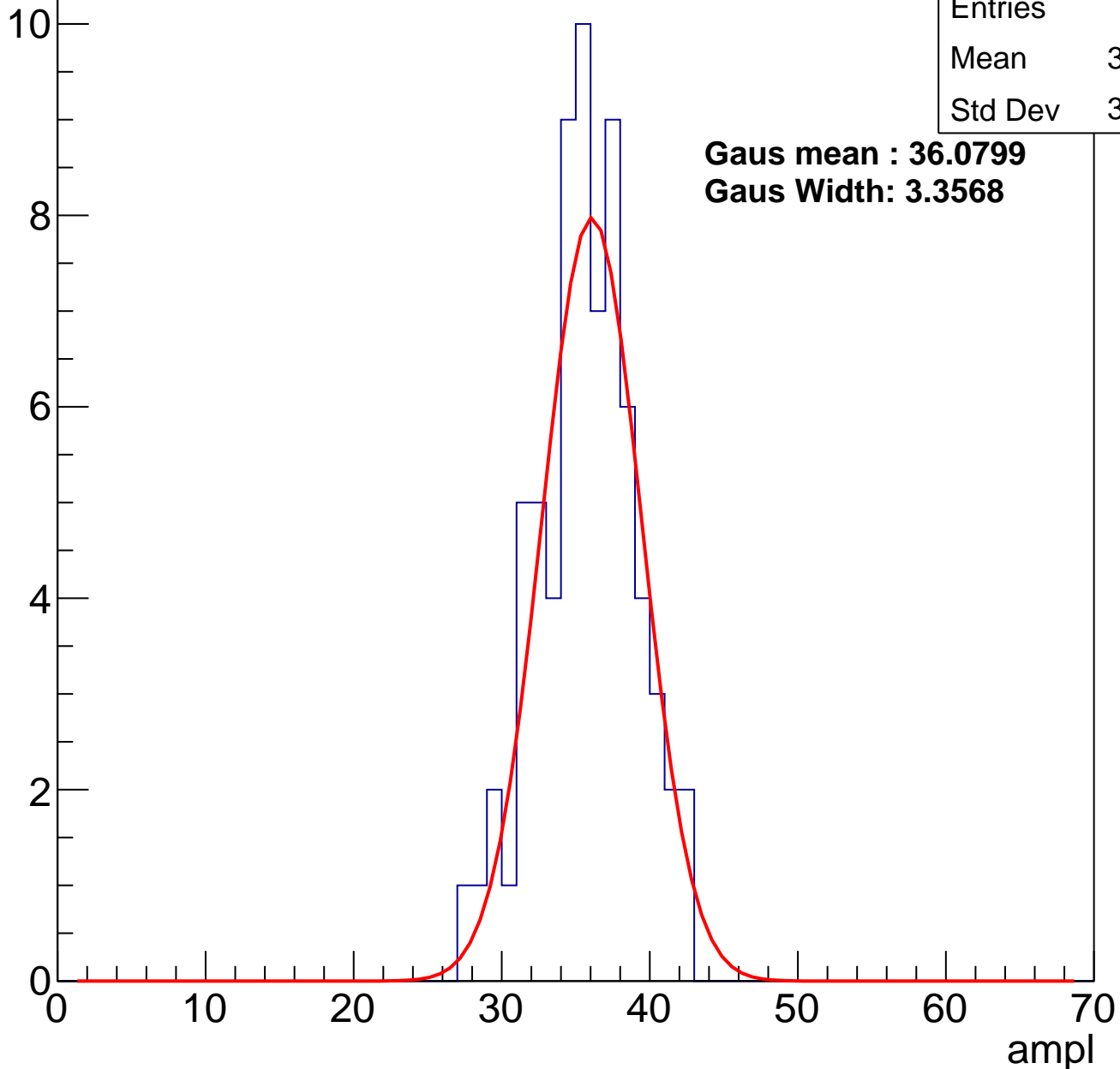
calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	35.23
Std Dev	3.277

Gaus mean : 36.0799

Gaus Width: 3.3568

Entry



B1L102S, U6-ch52, adc2

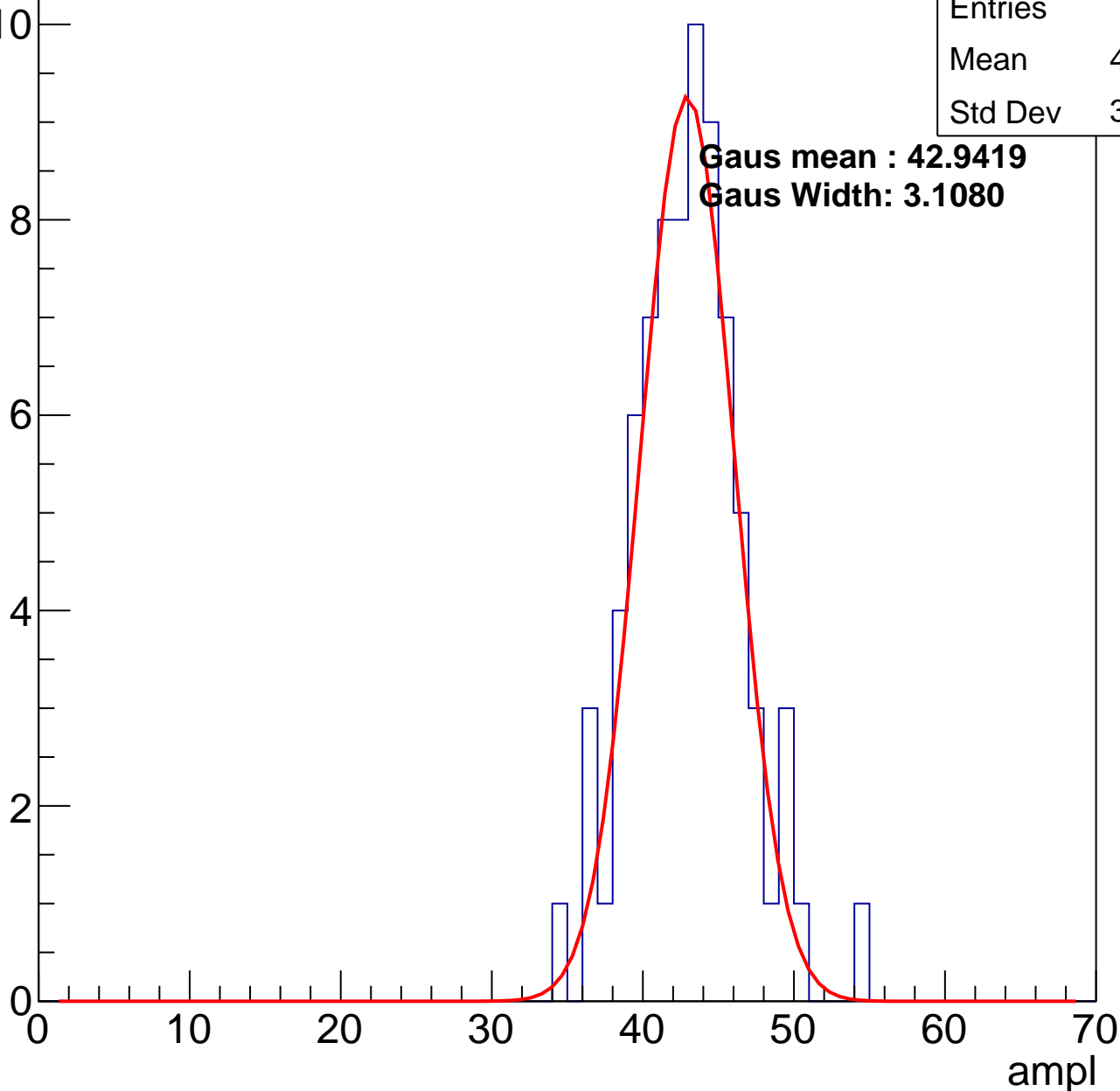
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.56
Std Dev	3.547

Gaus mean : 42.9419

Gaus Width: 3.1080

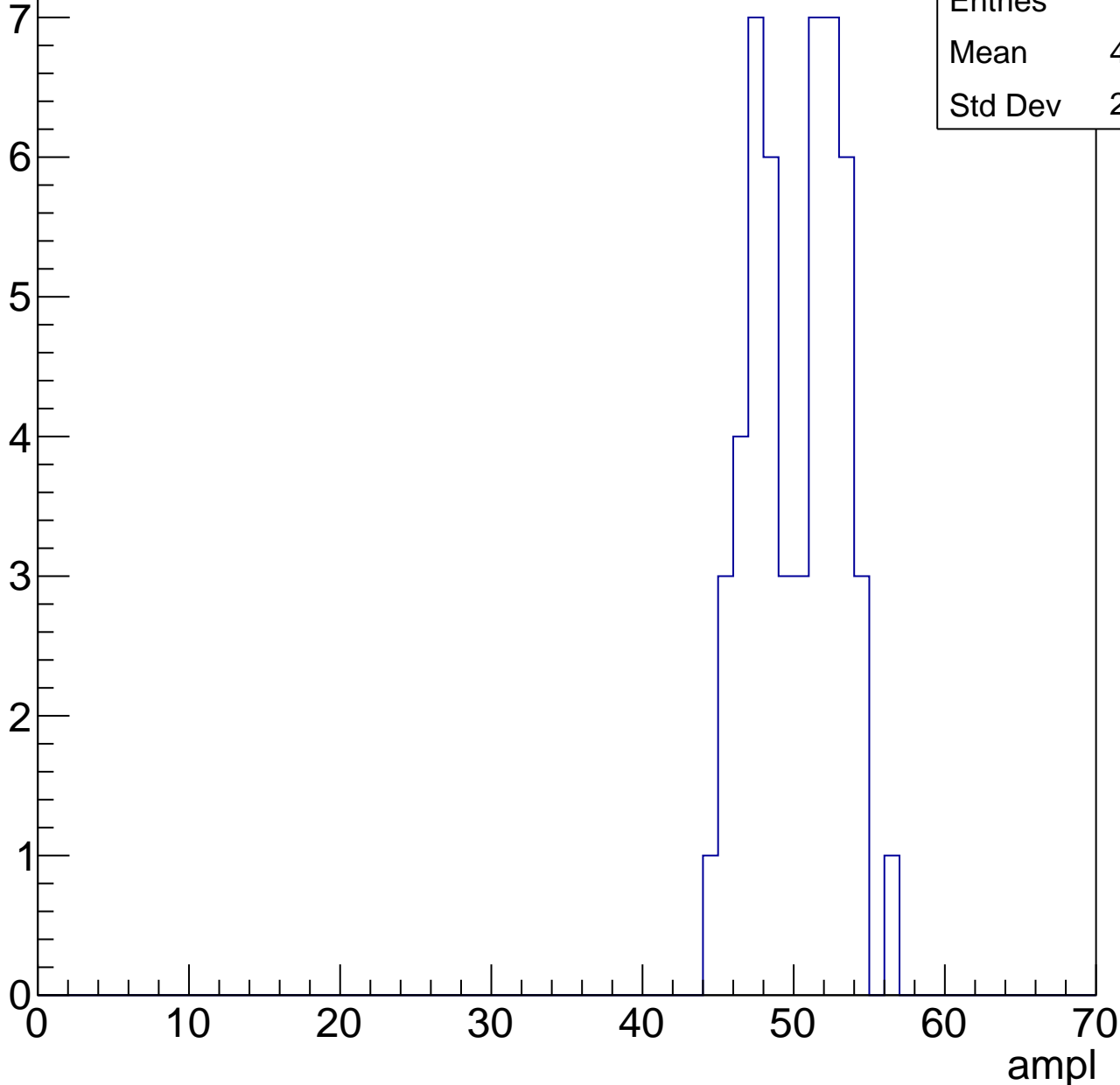


B1L102S, U6-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.69
Std Dev	2.914

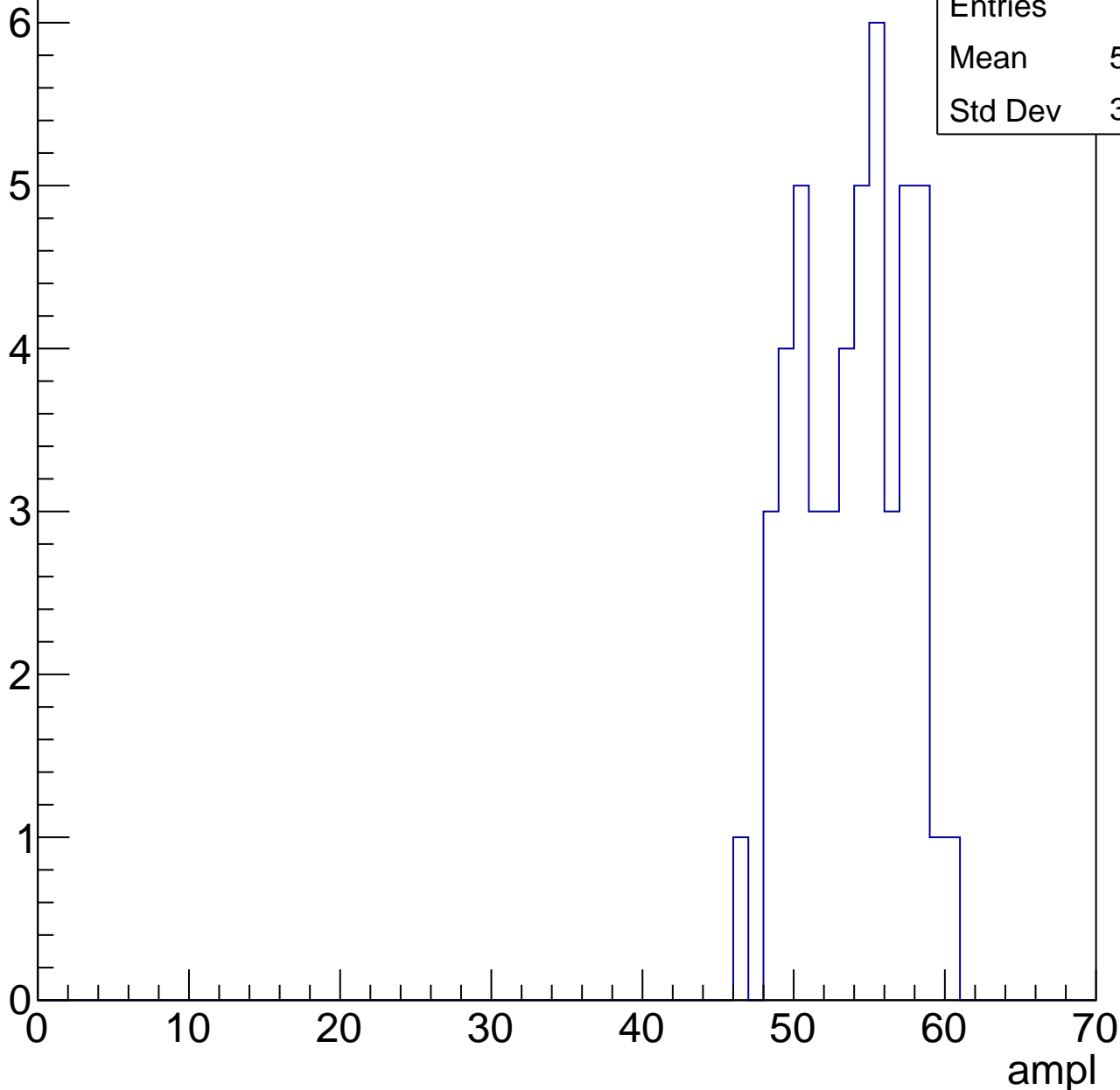


B1L102S, U6-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.45
Std Dev	3.453

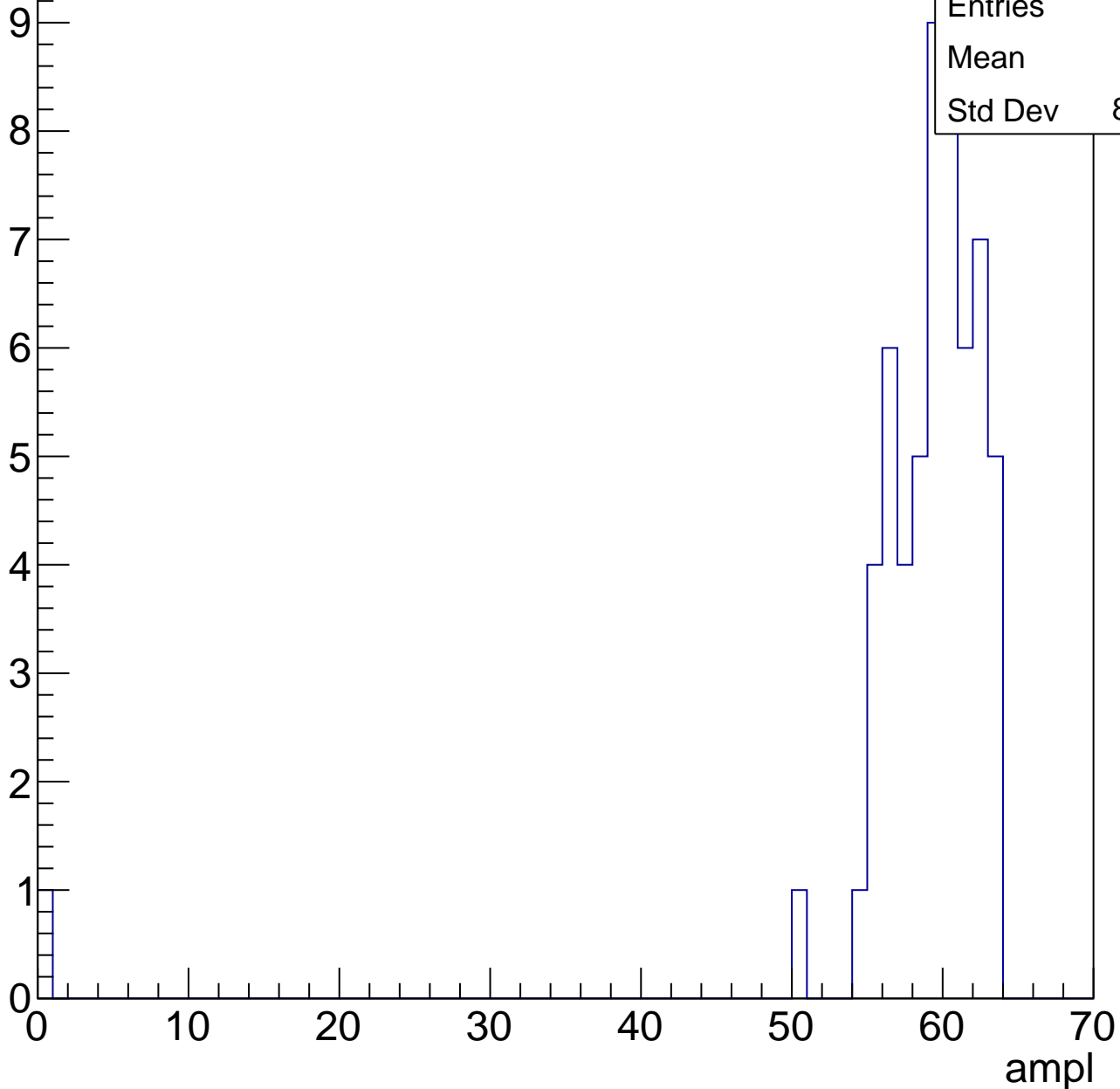


B1L102S, U6-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

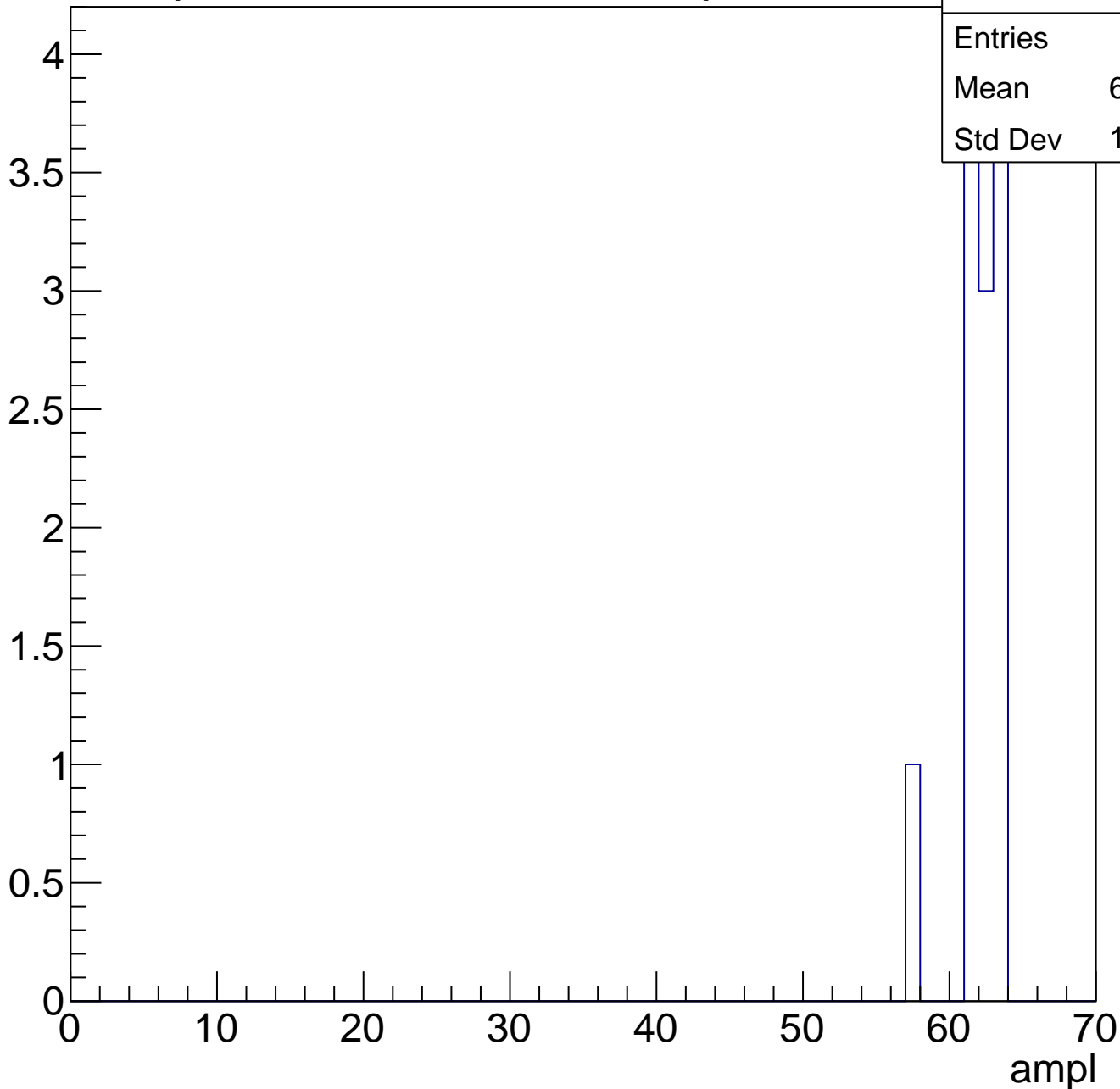
Entries	58
Mean	58
Std Dev	8.141



B1L102S, U6-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch53, adc0

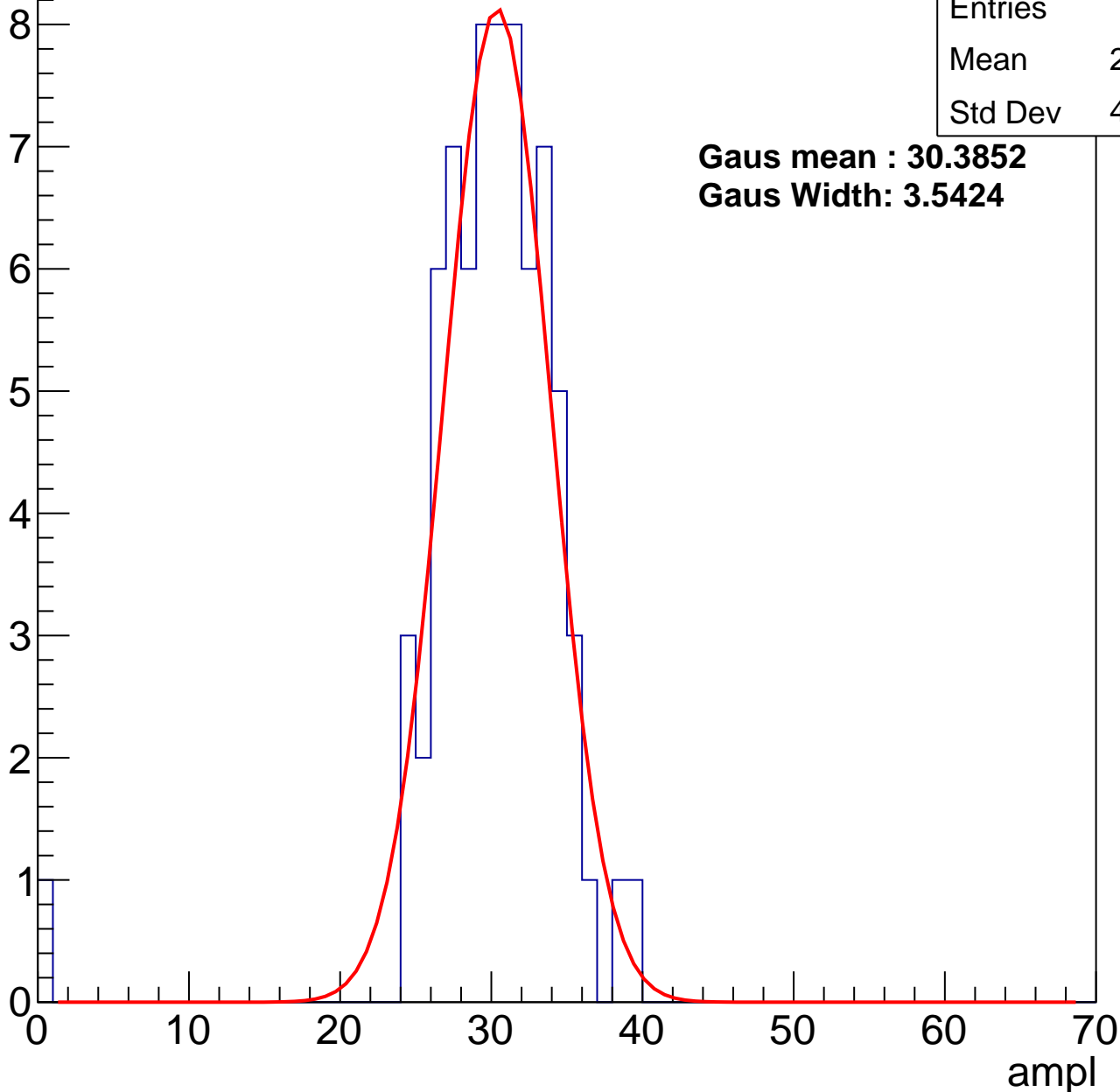
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	29.67
Std Dev	4.786

Gaus mean : 30.3852

Gaus Width: 3.5424



B1L102S, U6-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	37.09
Std Dev	3.414

Gaus mean : 37.8398

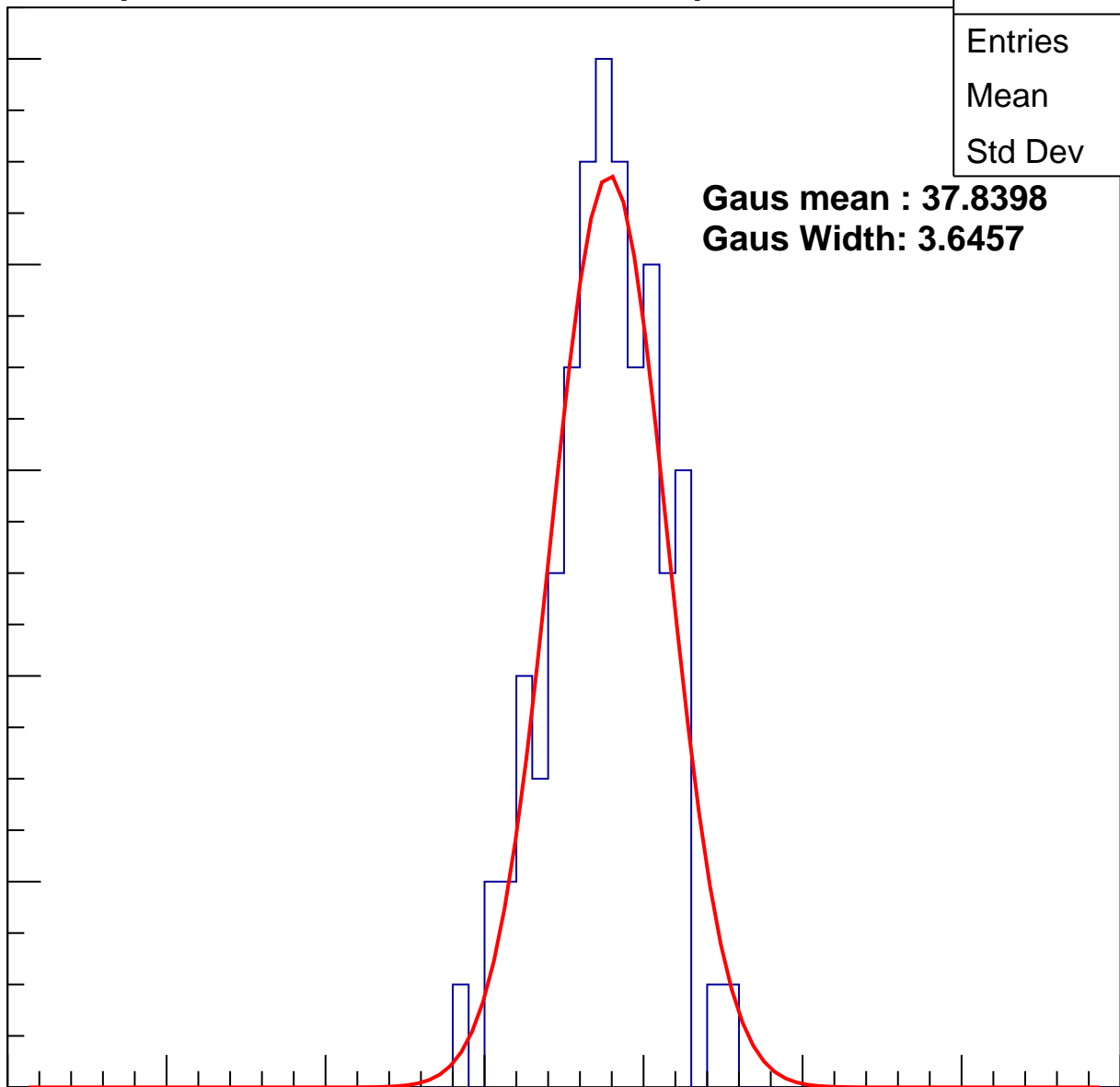
Gaus Width: 3.6457

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch53, adc2

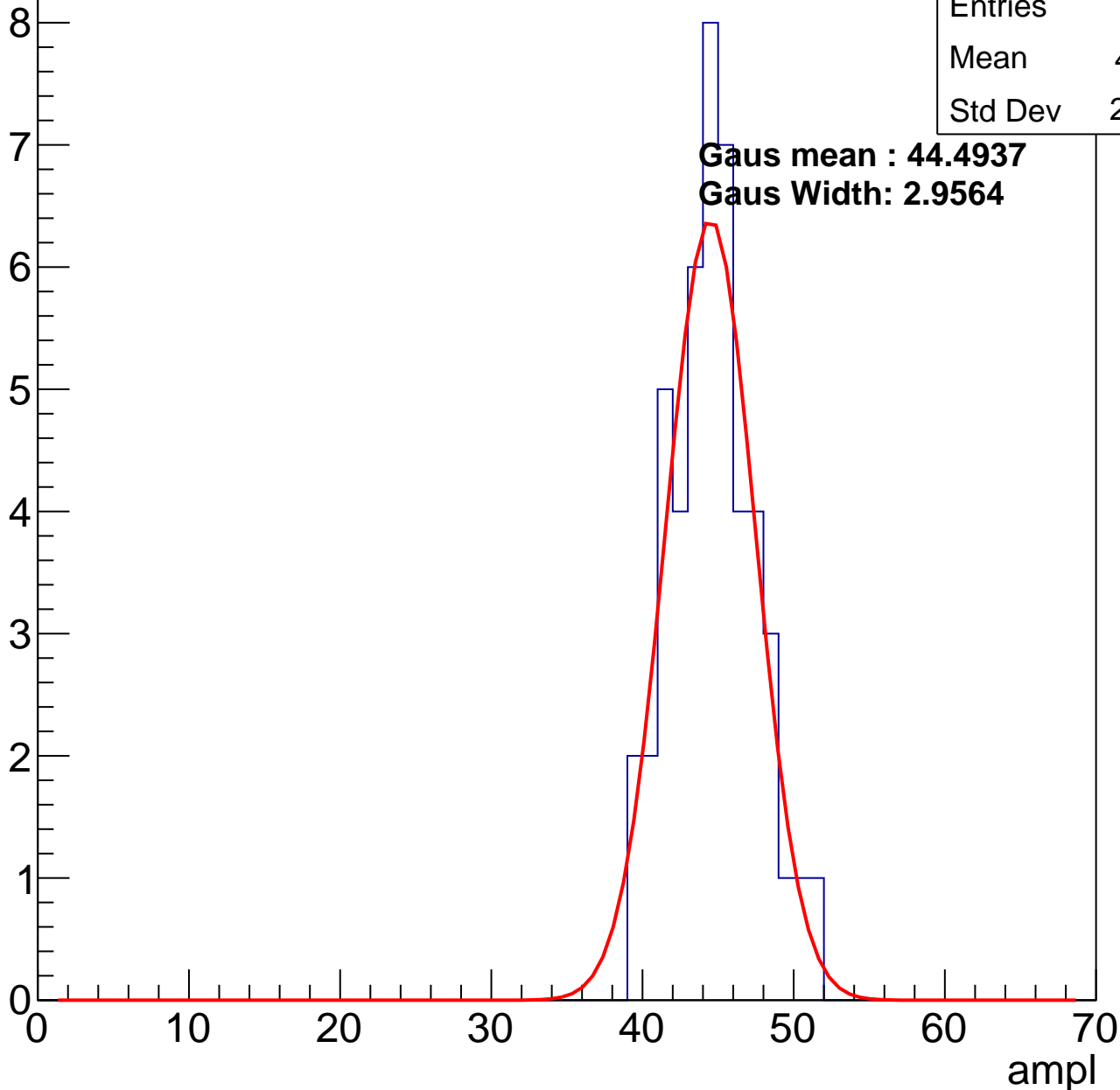
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	44.21
Std Dev	2.753

Gaus mean : 44.4937

Gaus Width: 2.9564

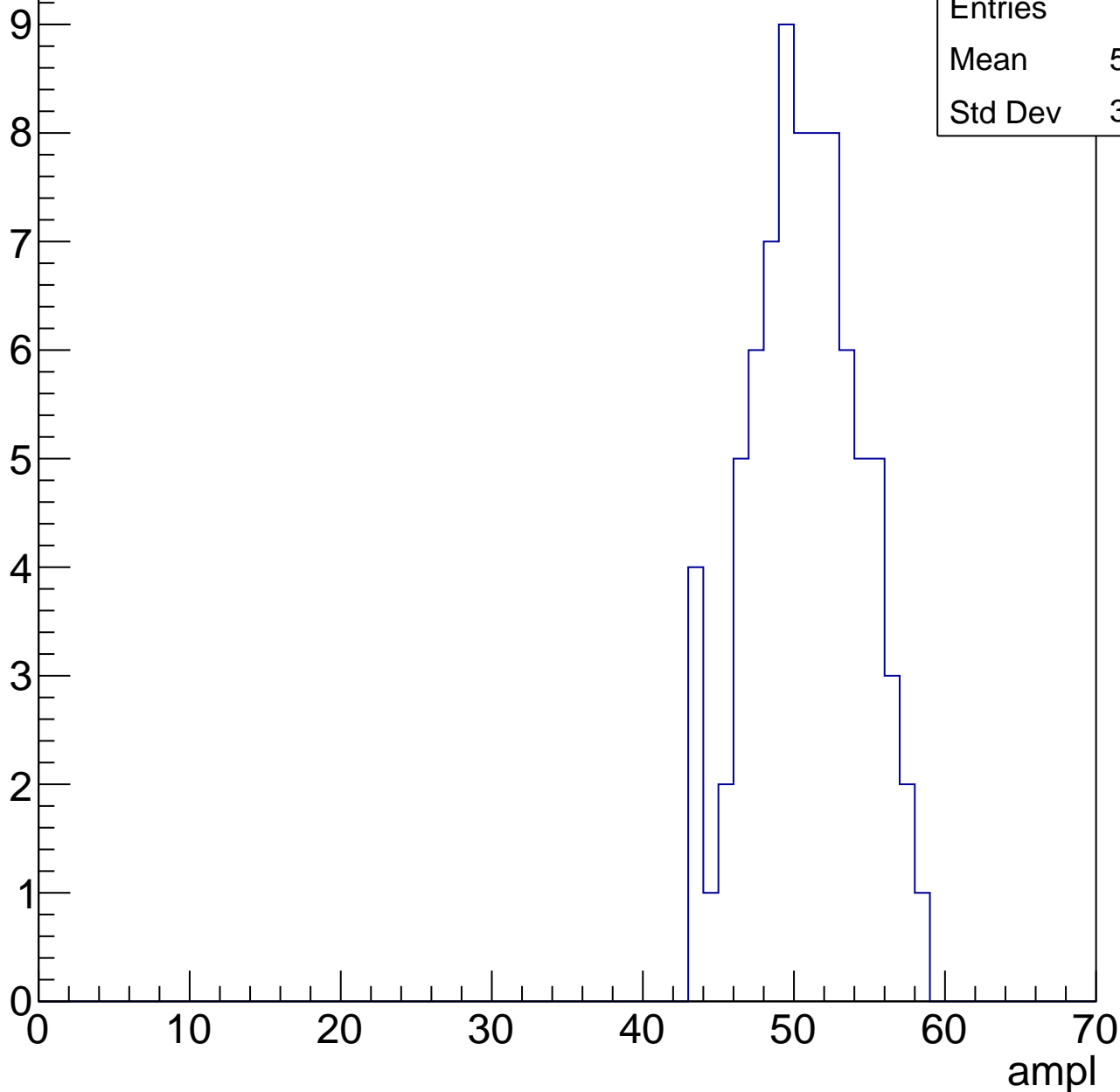


B1L102S, U6-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	50.27
Std Dev	3.564

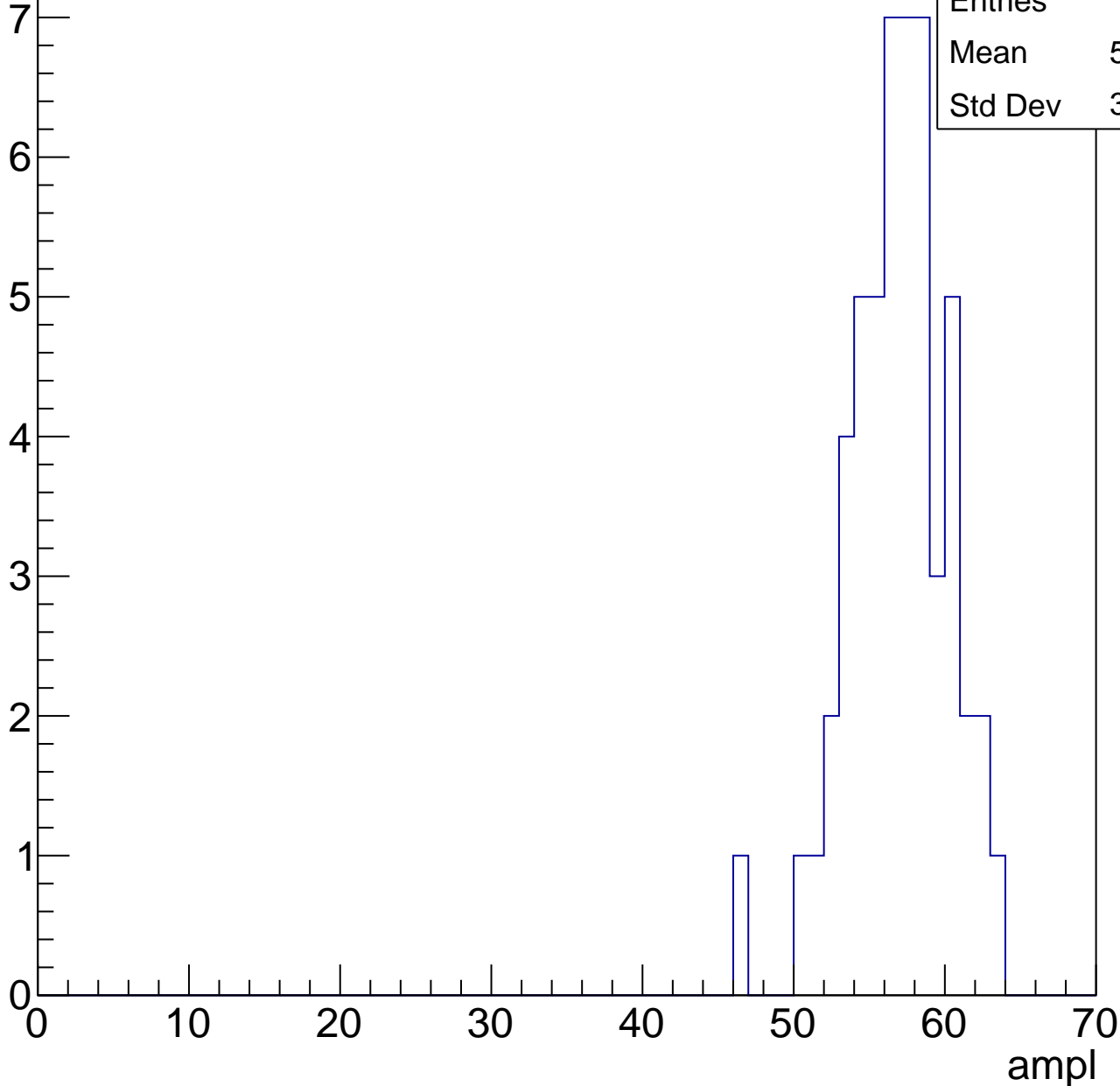


B1L102S, U6-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	56.43
Std Dev	3.242

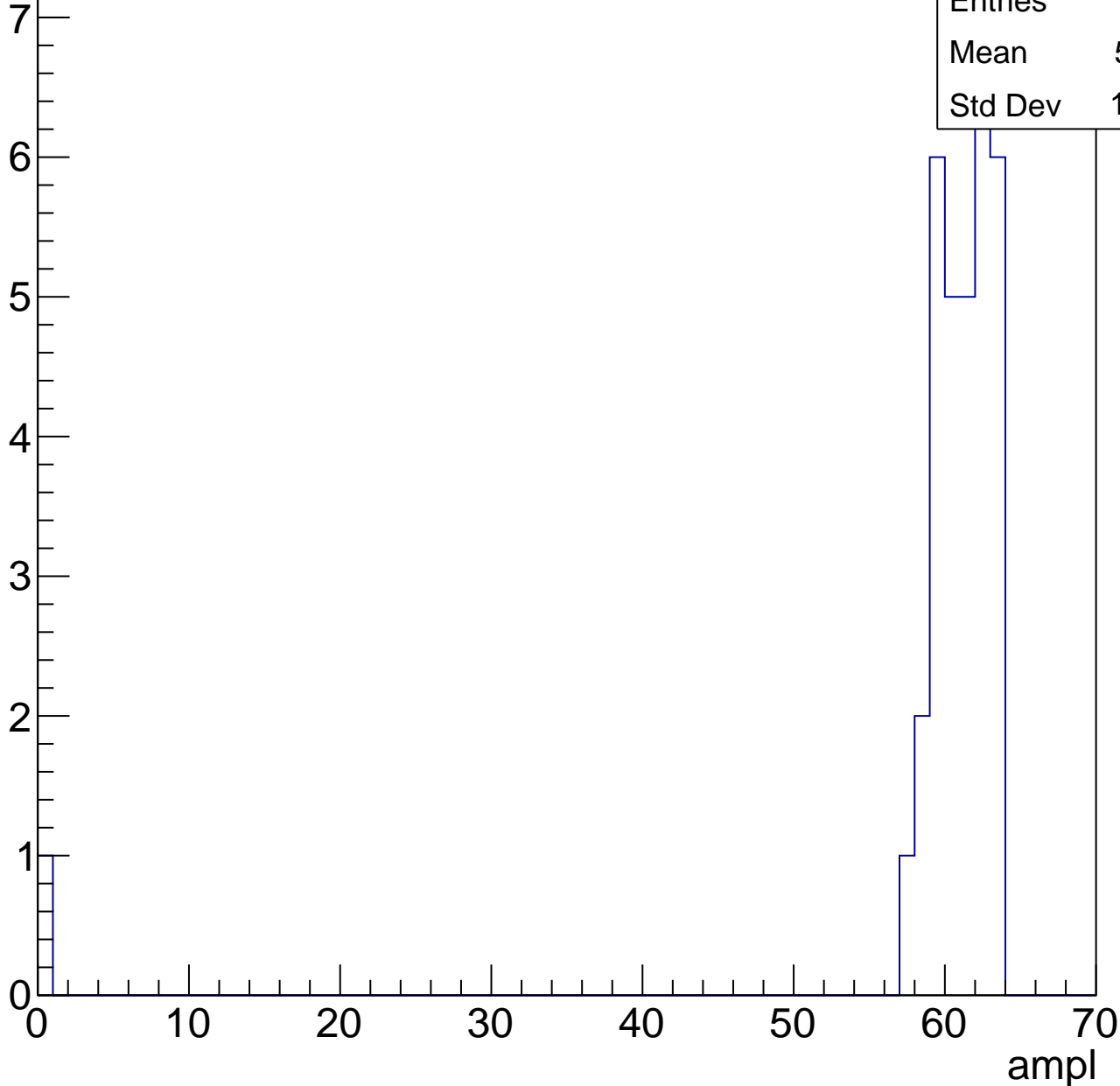


B1L102S, U6-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

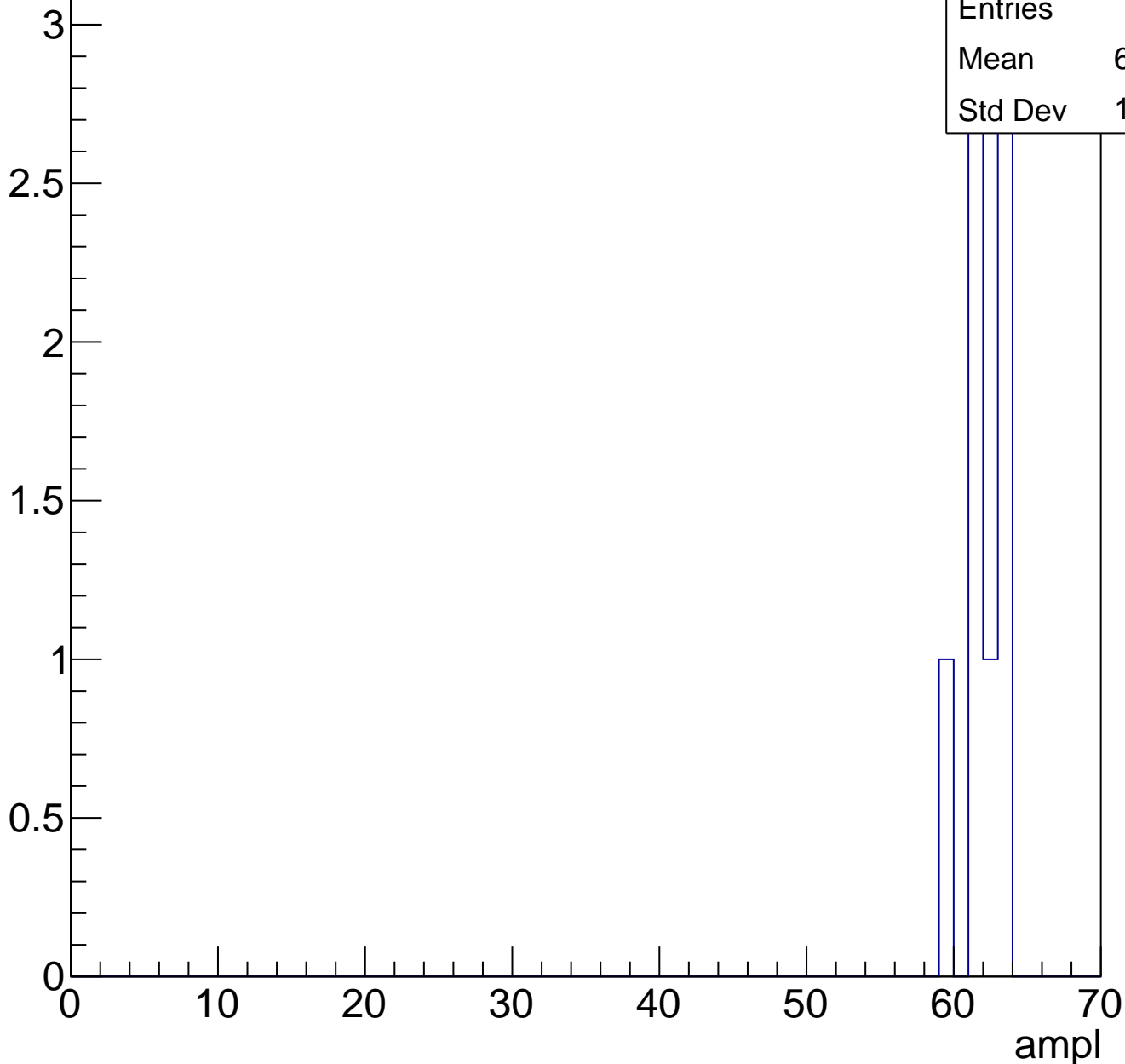
Entries	33
Mean	58.91
Std Dev	10.55



B1L102S, U6-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch54, adc0

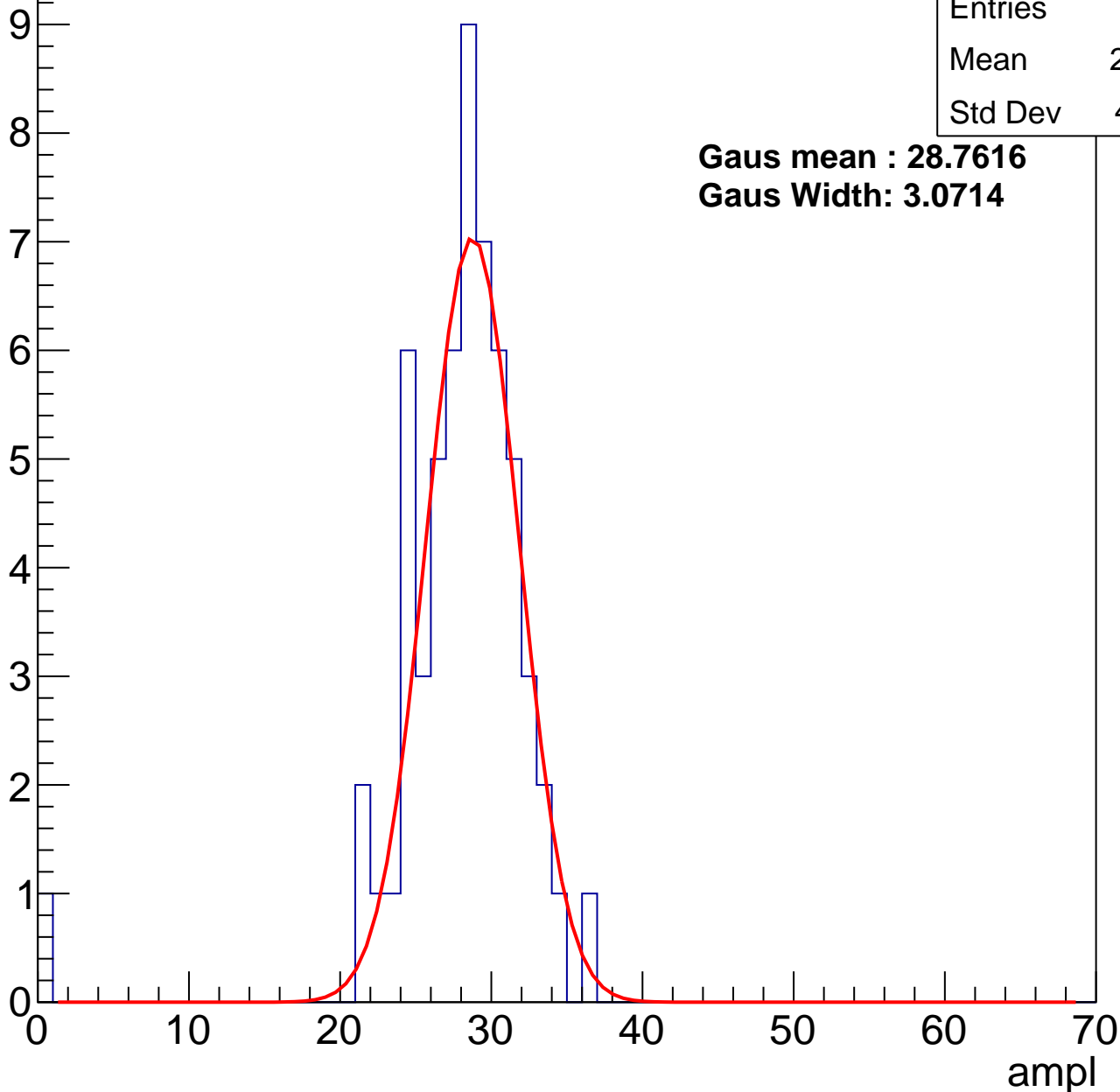
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	27.46
Std Dev	4.781

Gaus mean : 28.7616

Gaus Width: 3.0714



B1L102S, U6-ch54, adc1

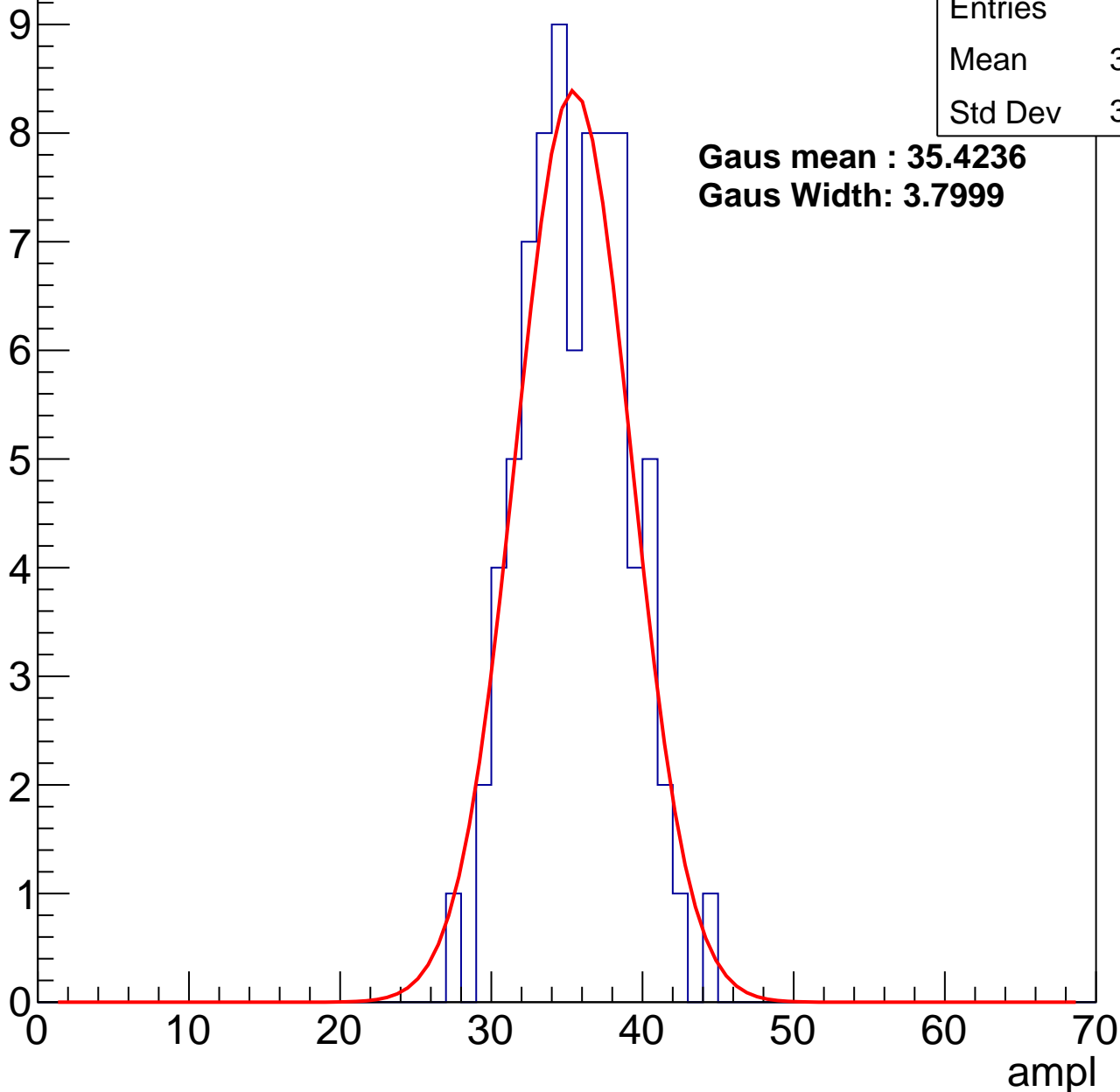
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.14
Std Dev	3.415

Gaus mean : 35.4236

Gaus Width: 3.7999



B1L102S, U6-ch54, adc2

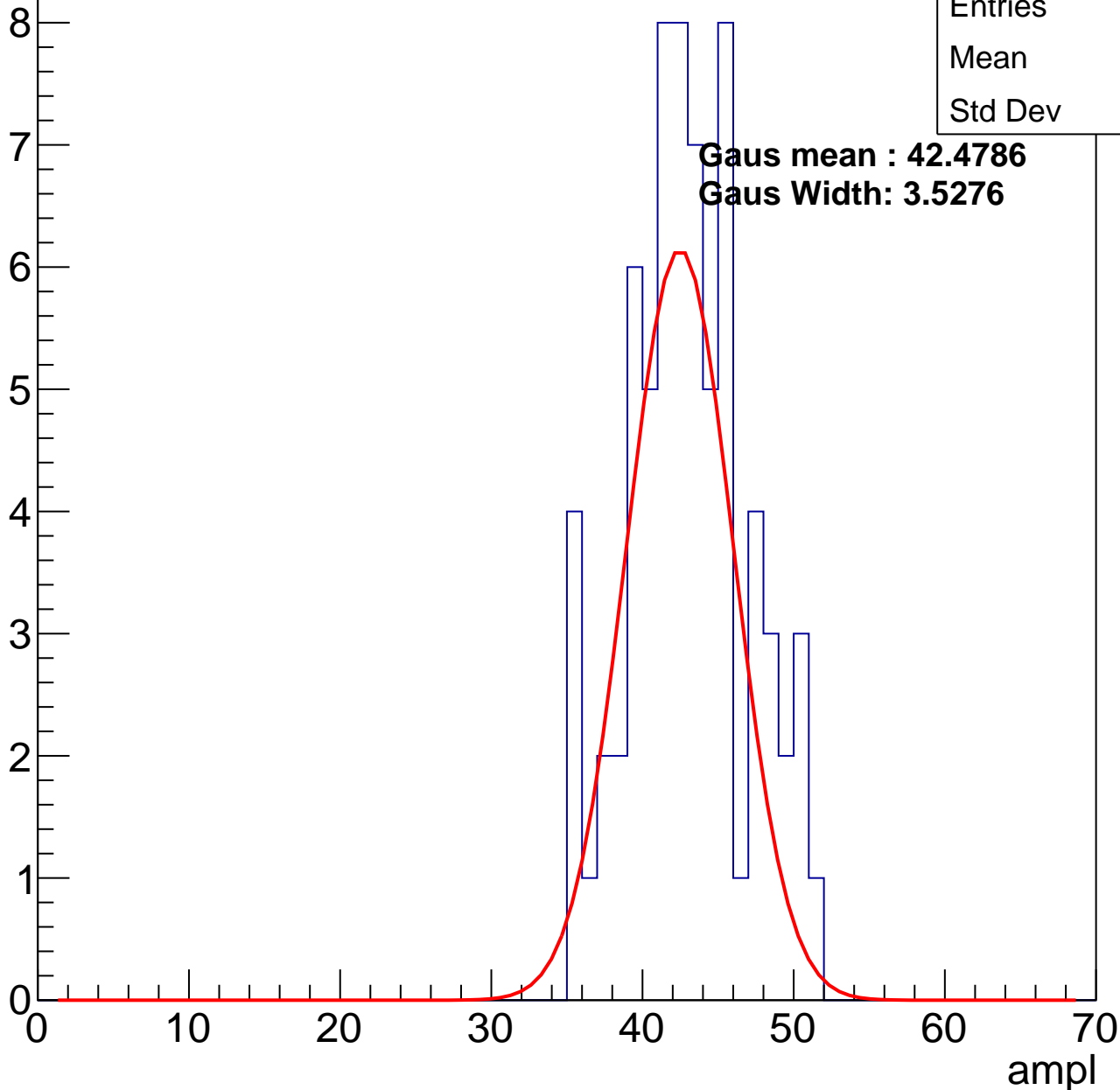
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.6
Std Dev	3.9

Gaus mean : 42.4786

Gaus Width: 3.5276

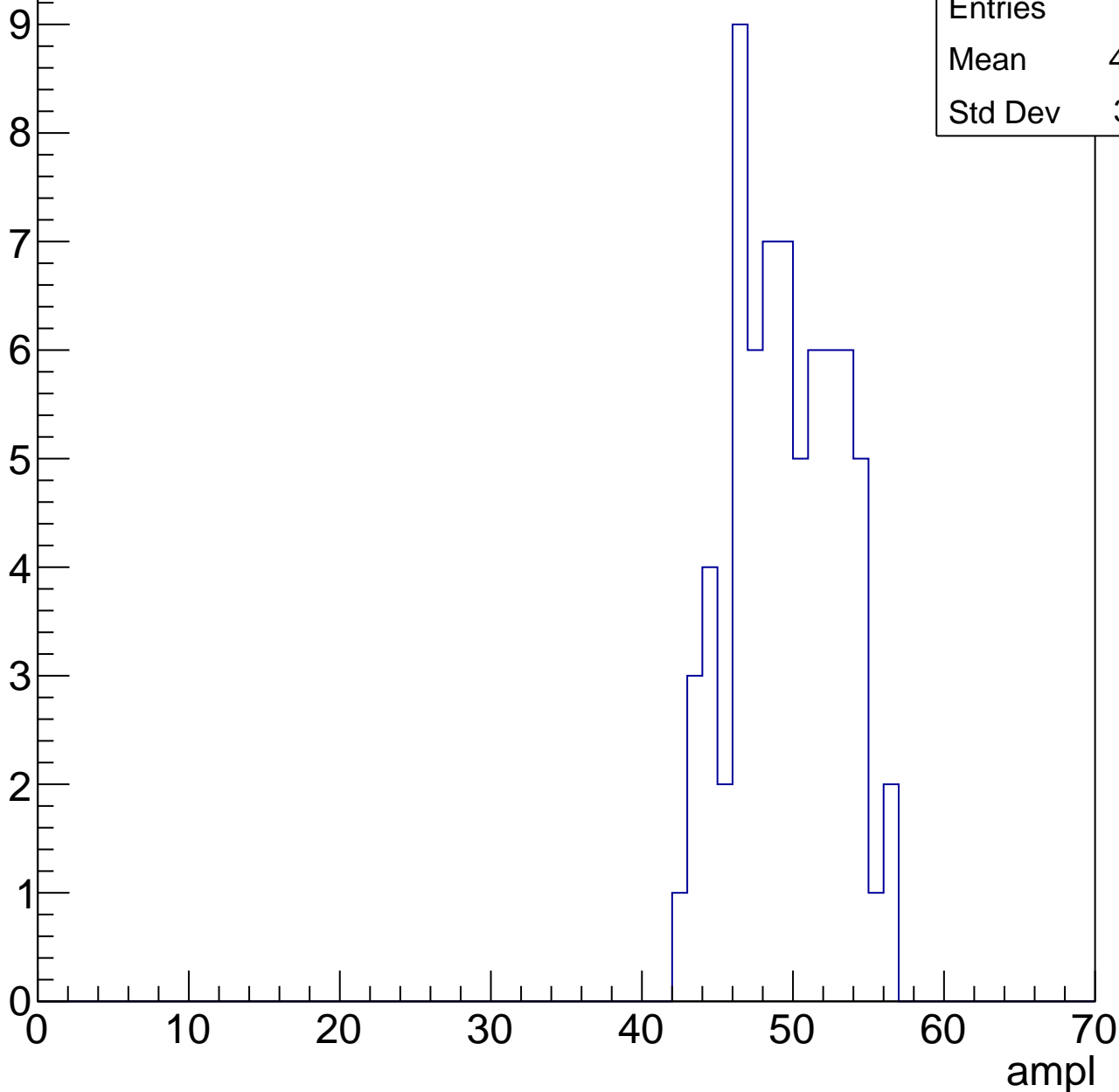


B1L102S, U6-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

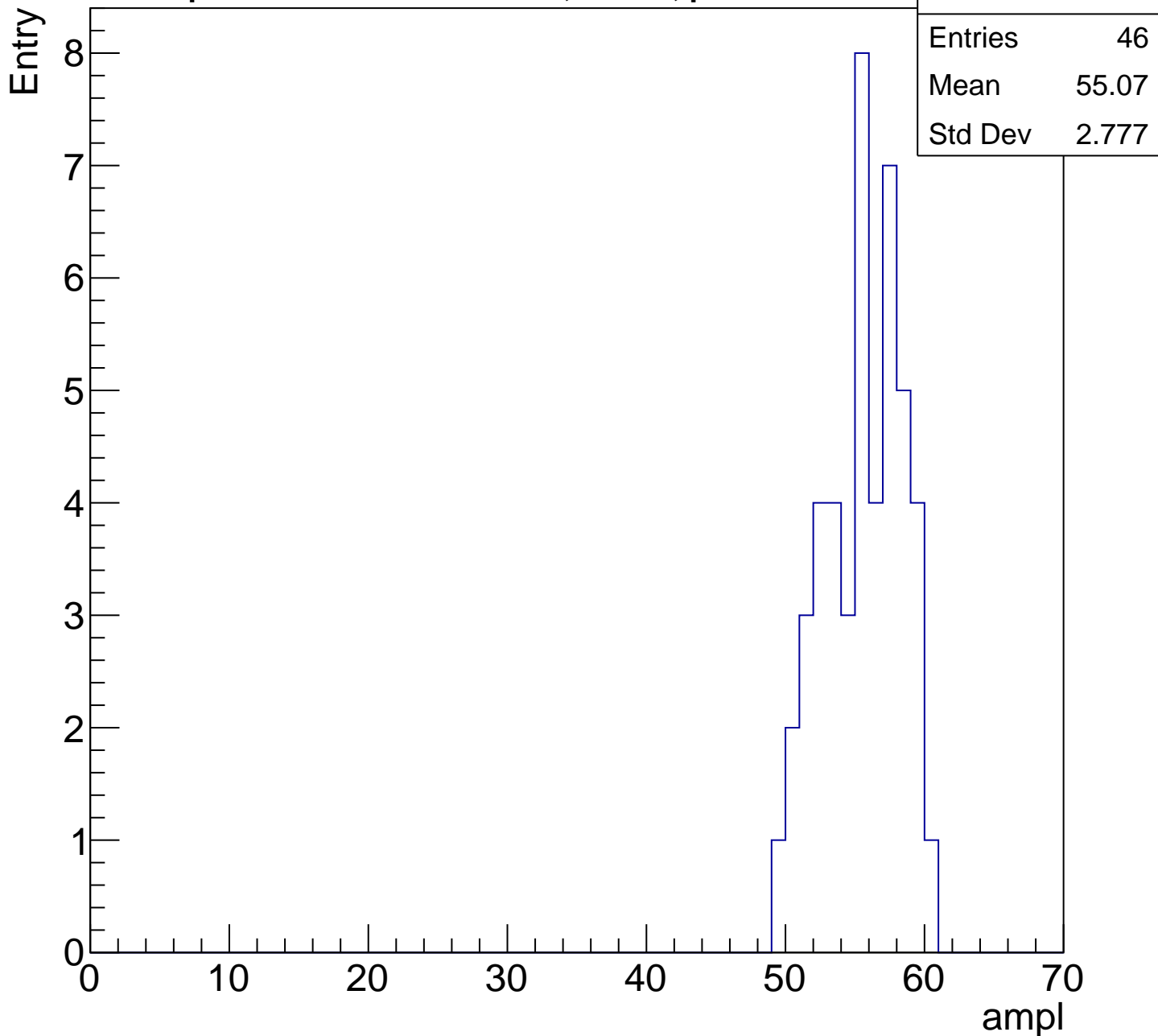
Entry

Entries	70
Mean	49.07
Std Dev	3.461



B1L102S, U6-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

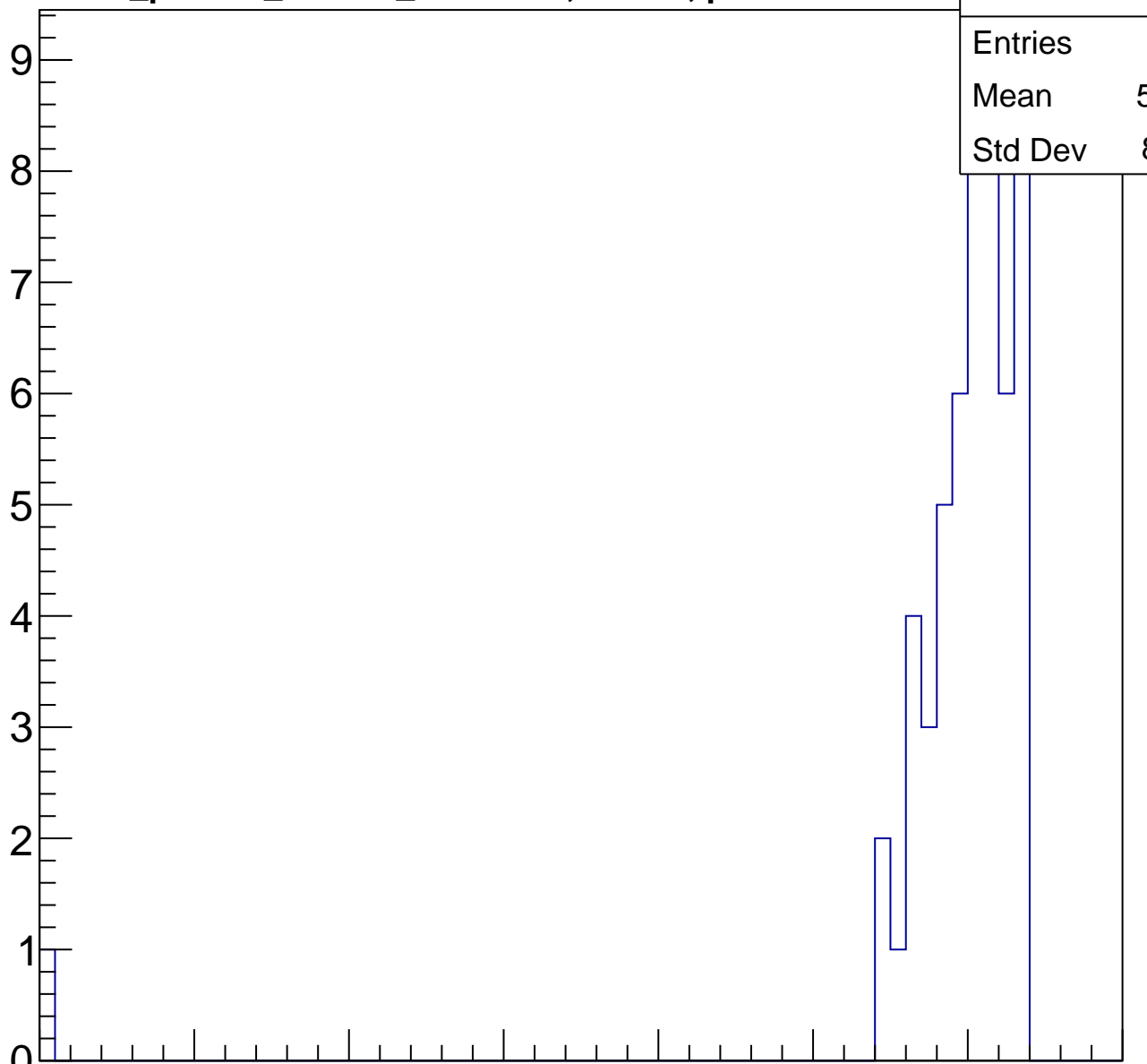
Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.65
Std Dev	8.411

ampl

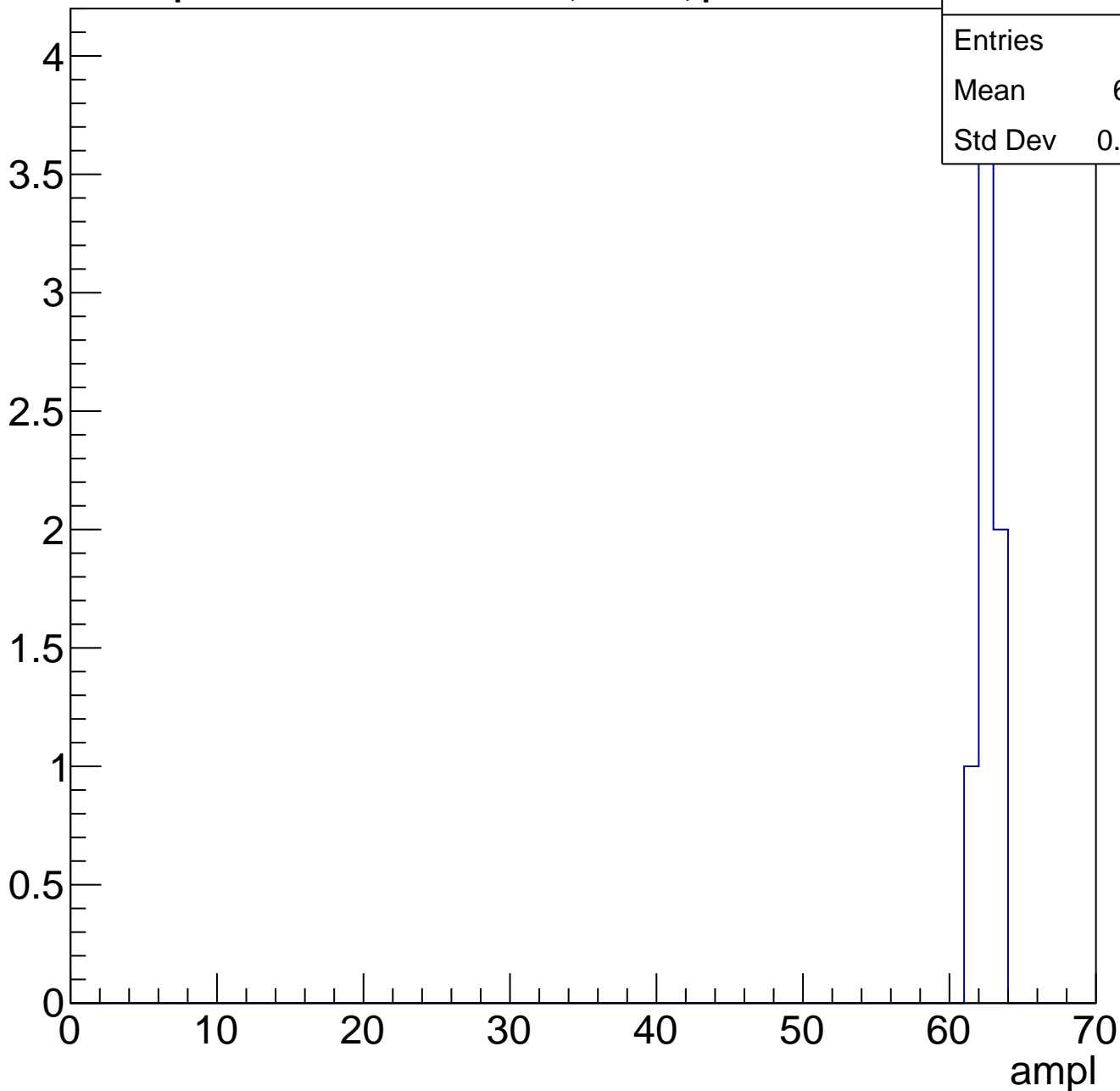
0 10 20 30 40 50 60 70



B1L102S, U6-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch55, adc0

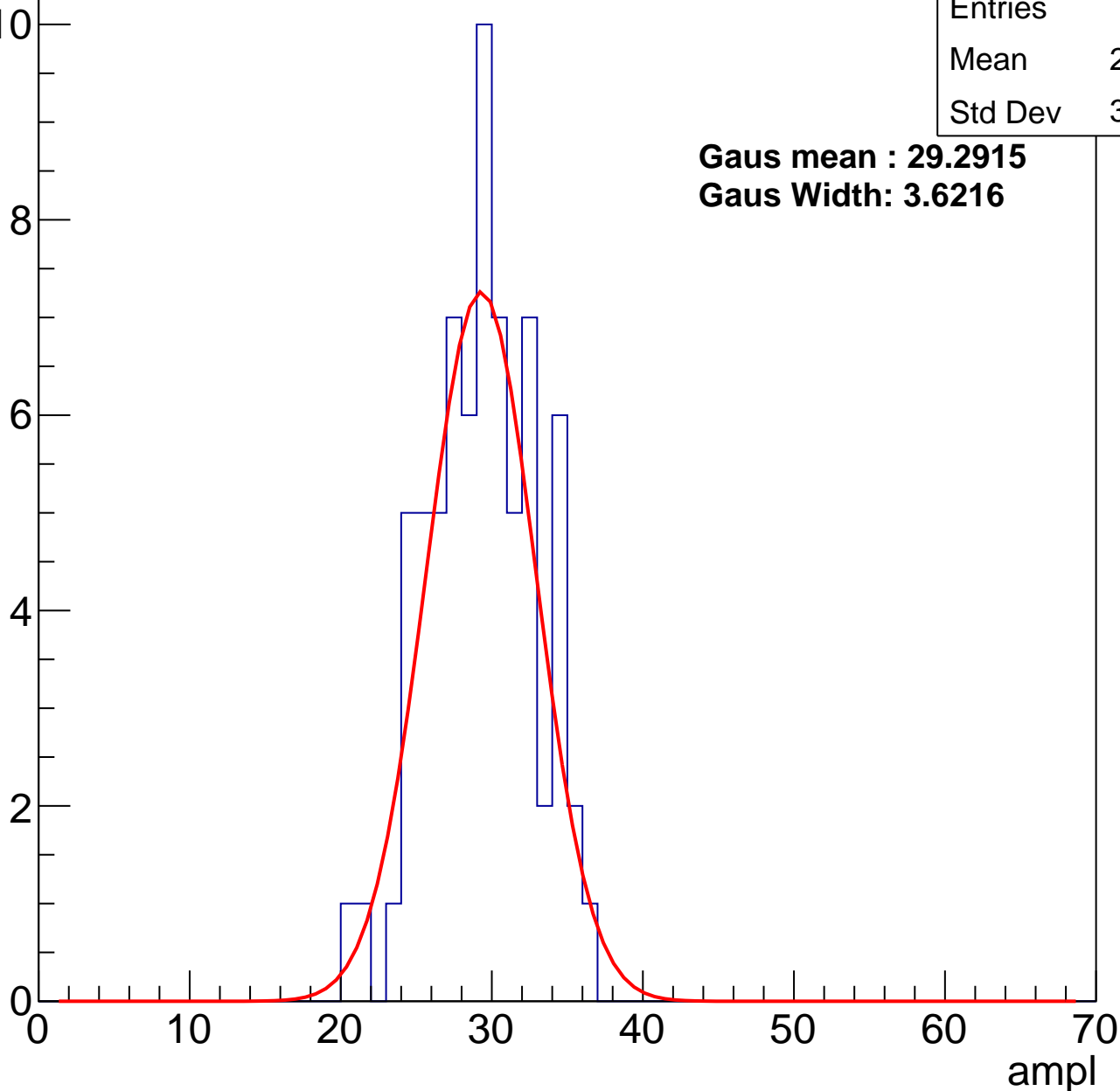
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.89
Std Dev	3.474

Gaus mean : 29.2915

Gaus Width: 3.6216



B1L102S, U6-ch55, adc1

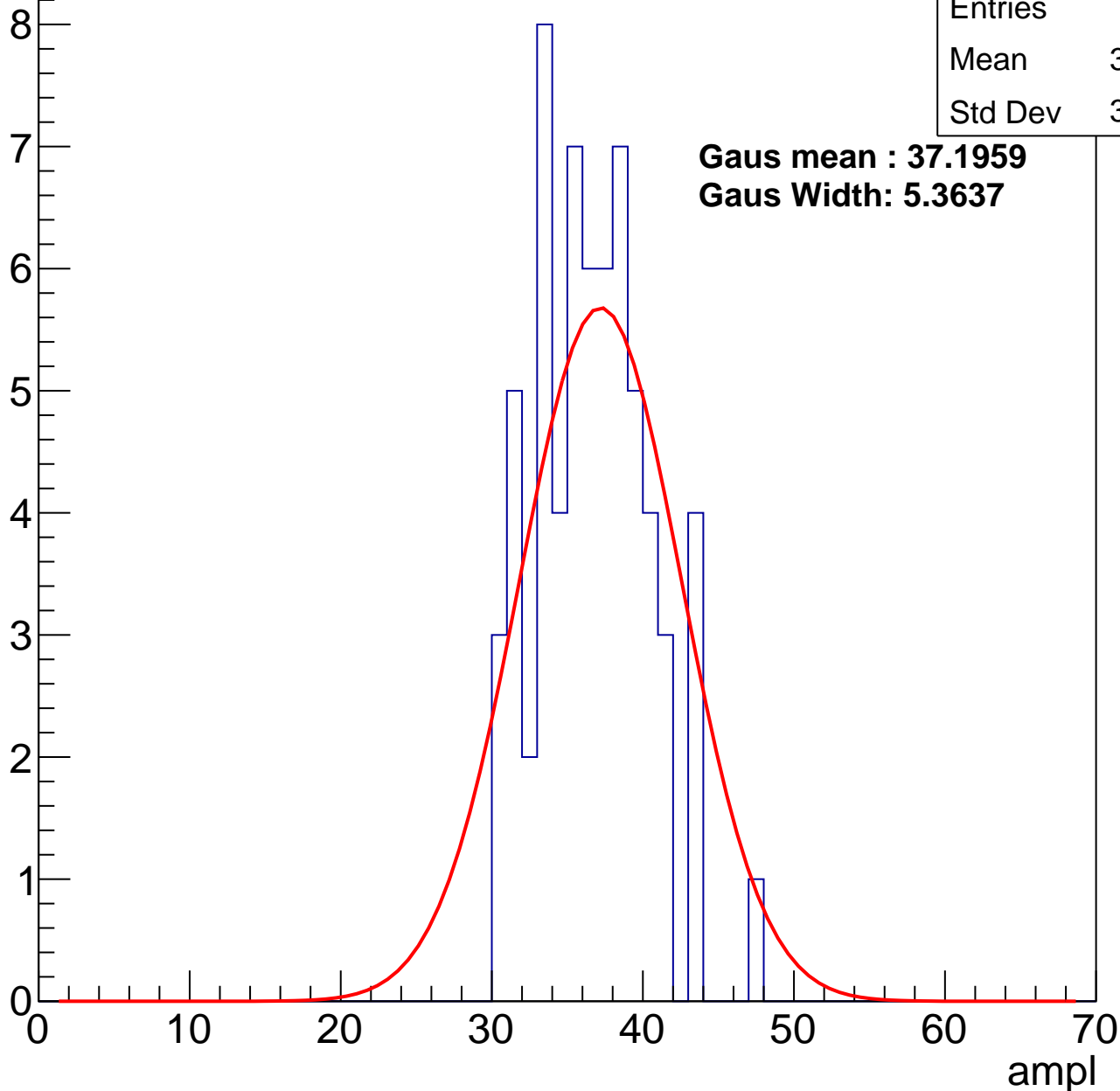
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.23
Std Dev	3.704

Gaus mean : 37.1959

Gaus Width: 5.3637



B1L102S, U6-ch55, adc2

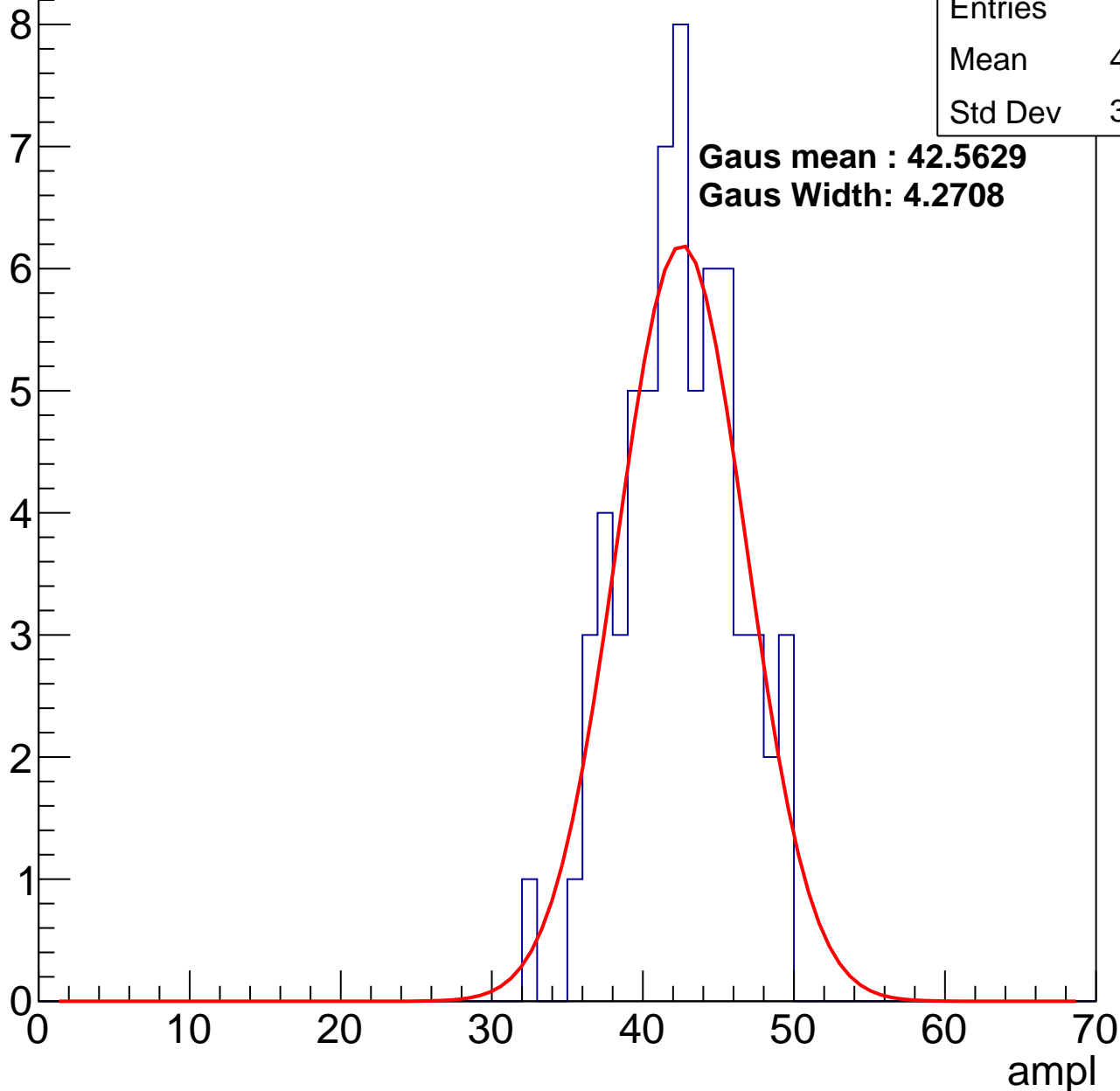
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.94
Std Dev	3.725

Gaus mean : 42.5629

Gaus Width: 4.2708

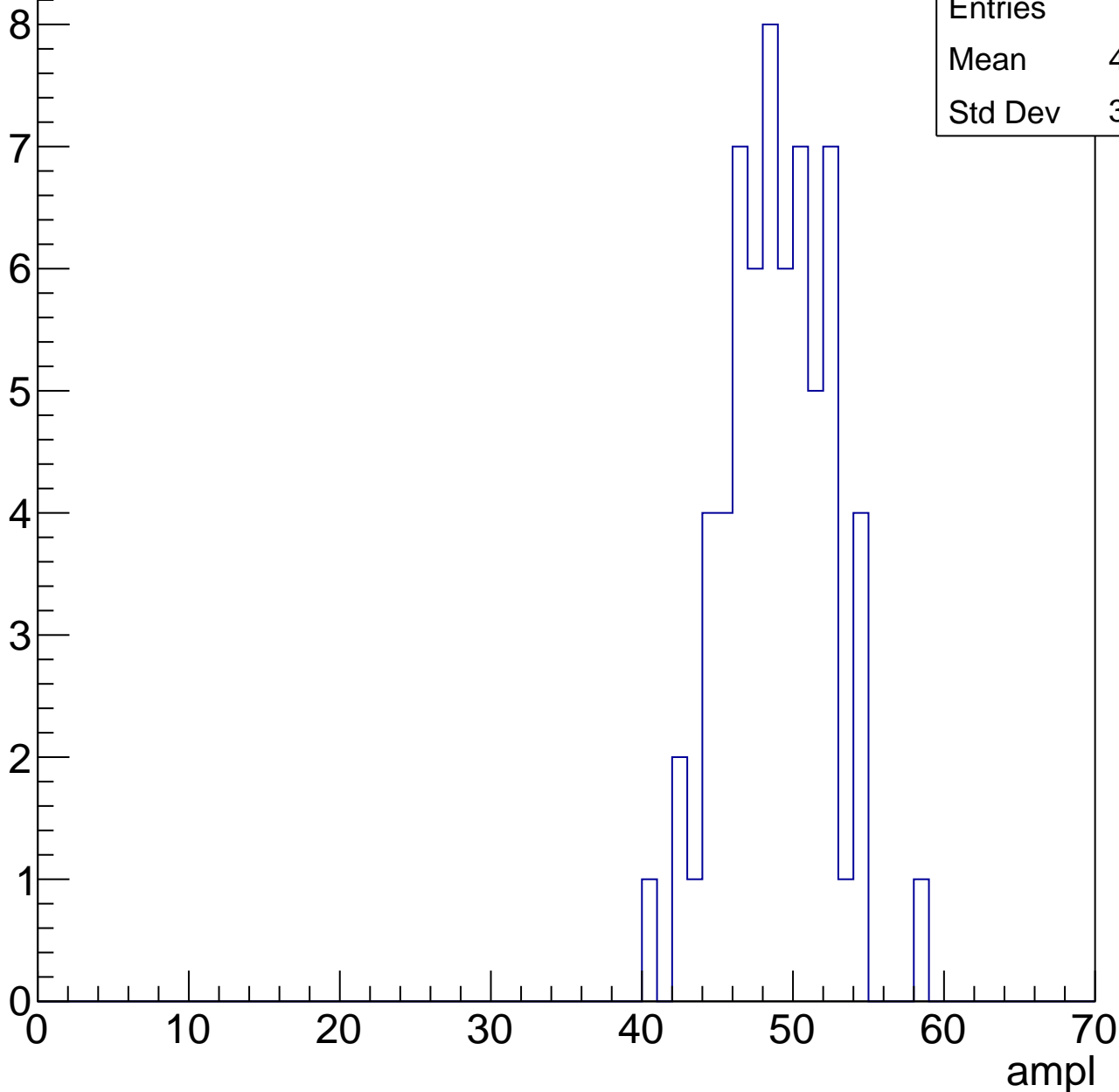


B1L102S, U6-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.45
Std Dev	3.405

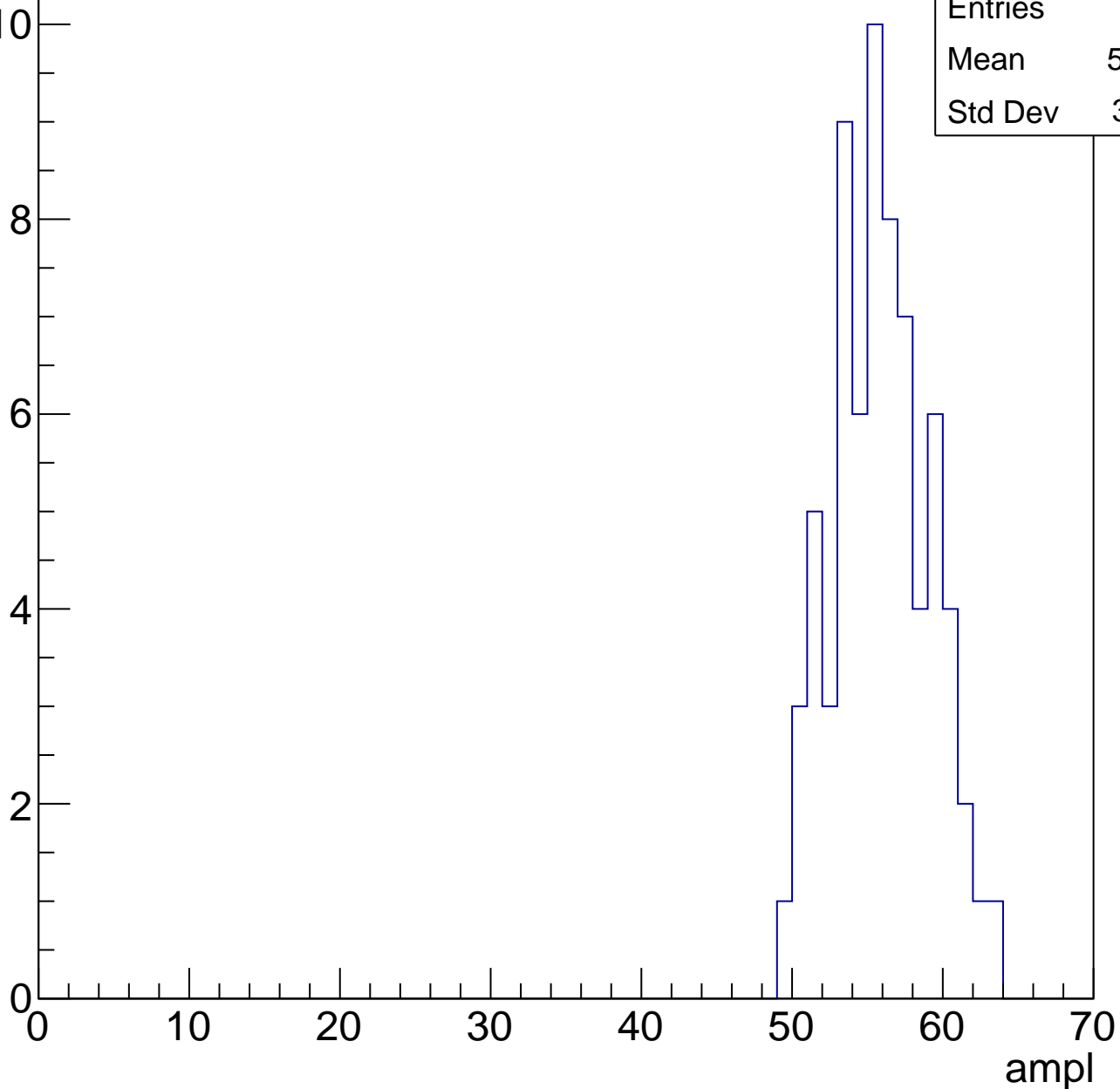


B1L102S, U6-ch55, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	55.44
Std Dev	3.161

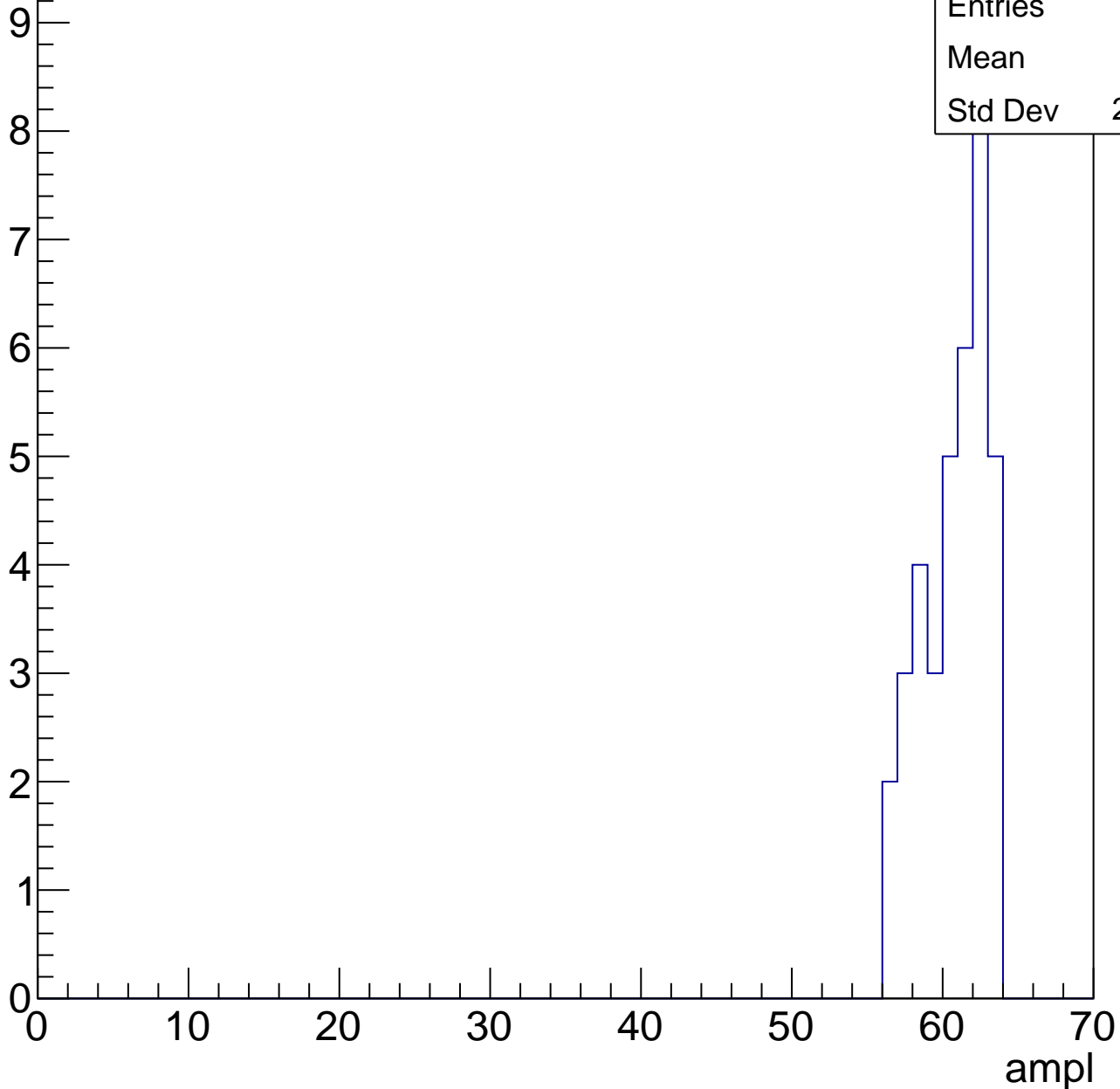


B1L102S, U6-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

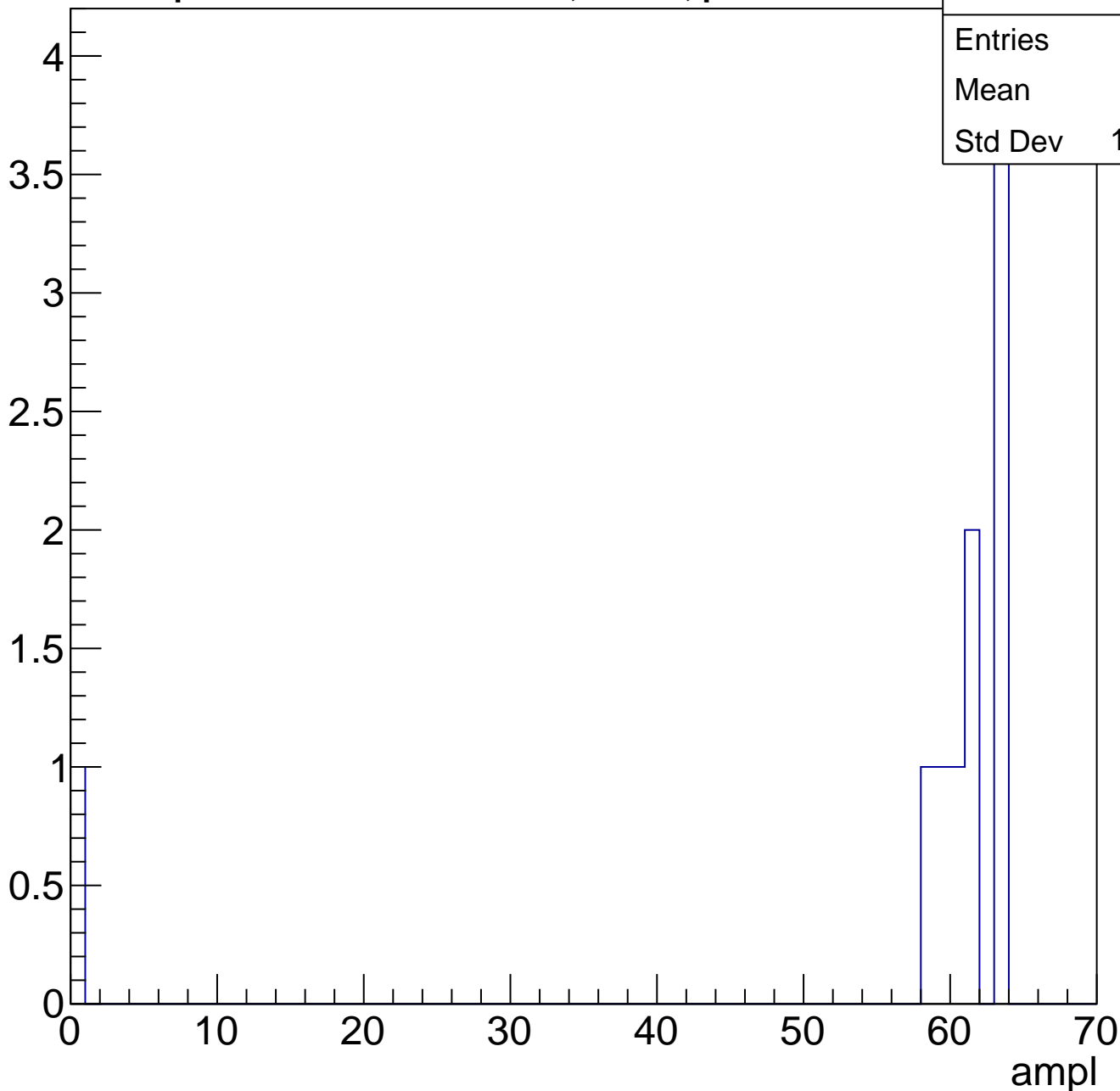
Entries	37
Mean	60.3
Std Dev	2.091



B1L102S, U6-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch56, adc0

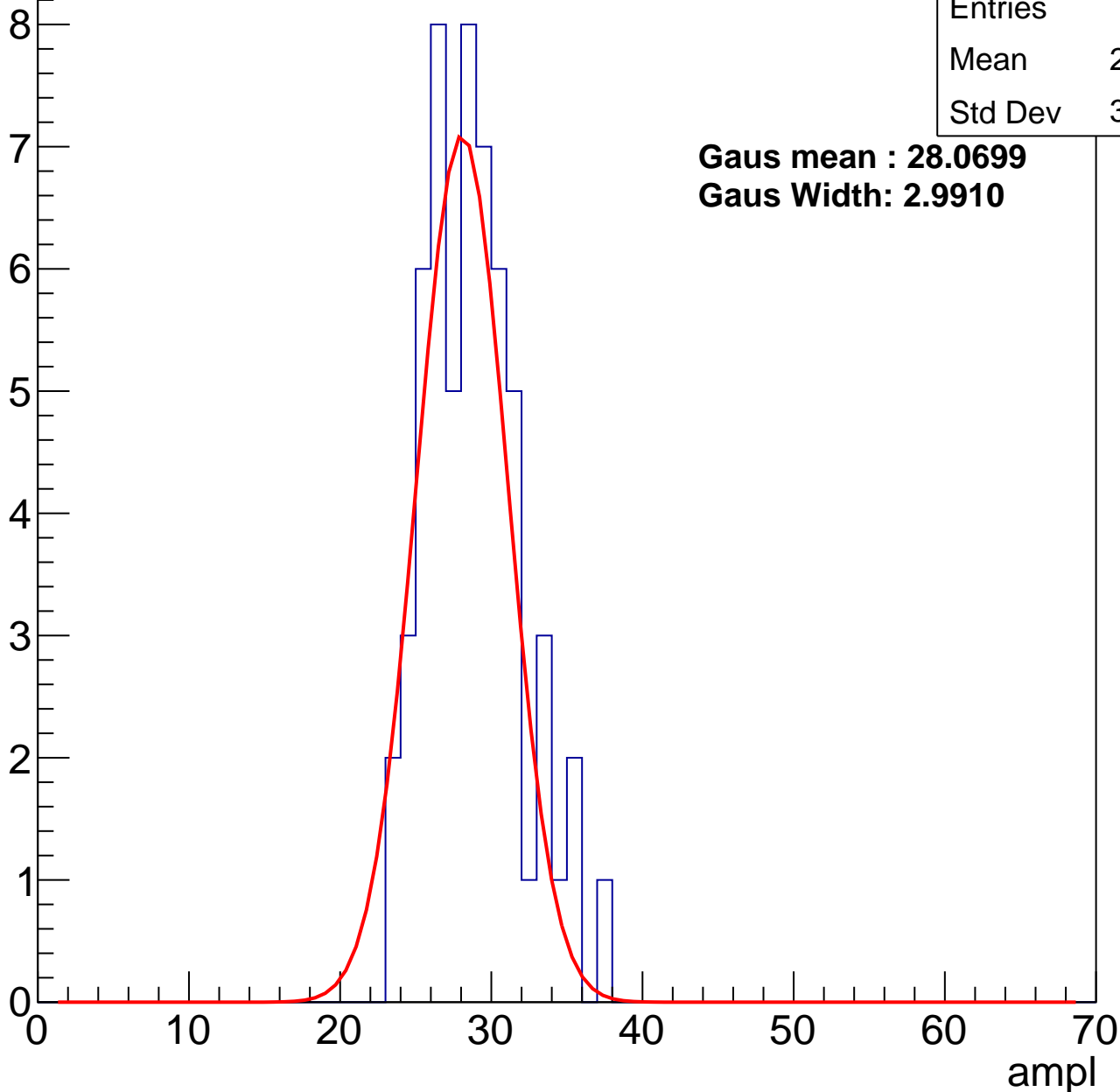
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.36
Std Dev	3.117

Gaus mean : 28.0699

Gaus Width: 2.9910



B1L102S, U6-ch56, adc1

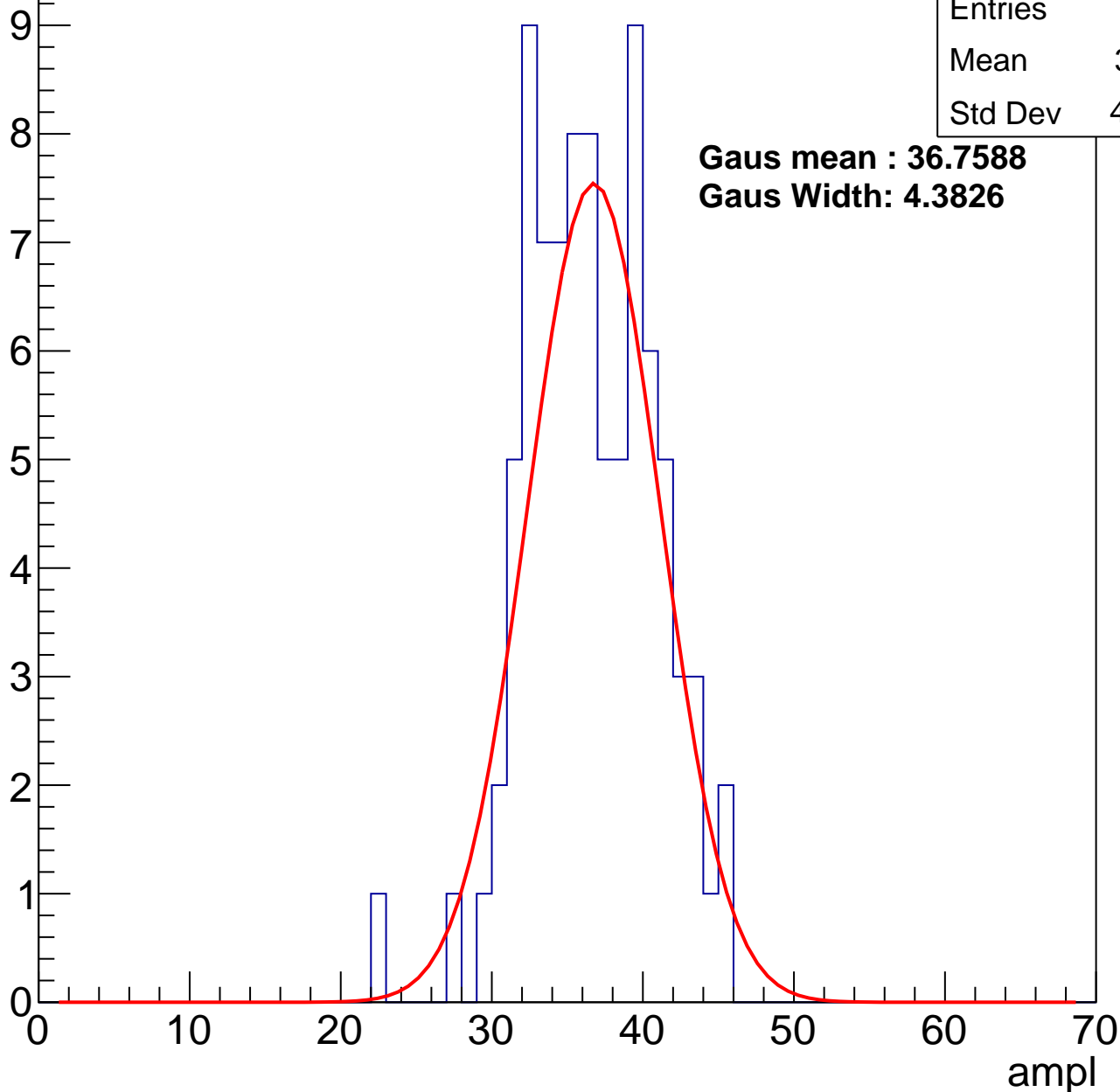
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	36.11
Std Dev	4.228

Gaus mean : 36.7588

Gaus Width: 4.3826



B1L102S, U6-ch56, adc2

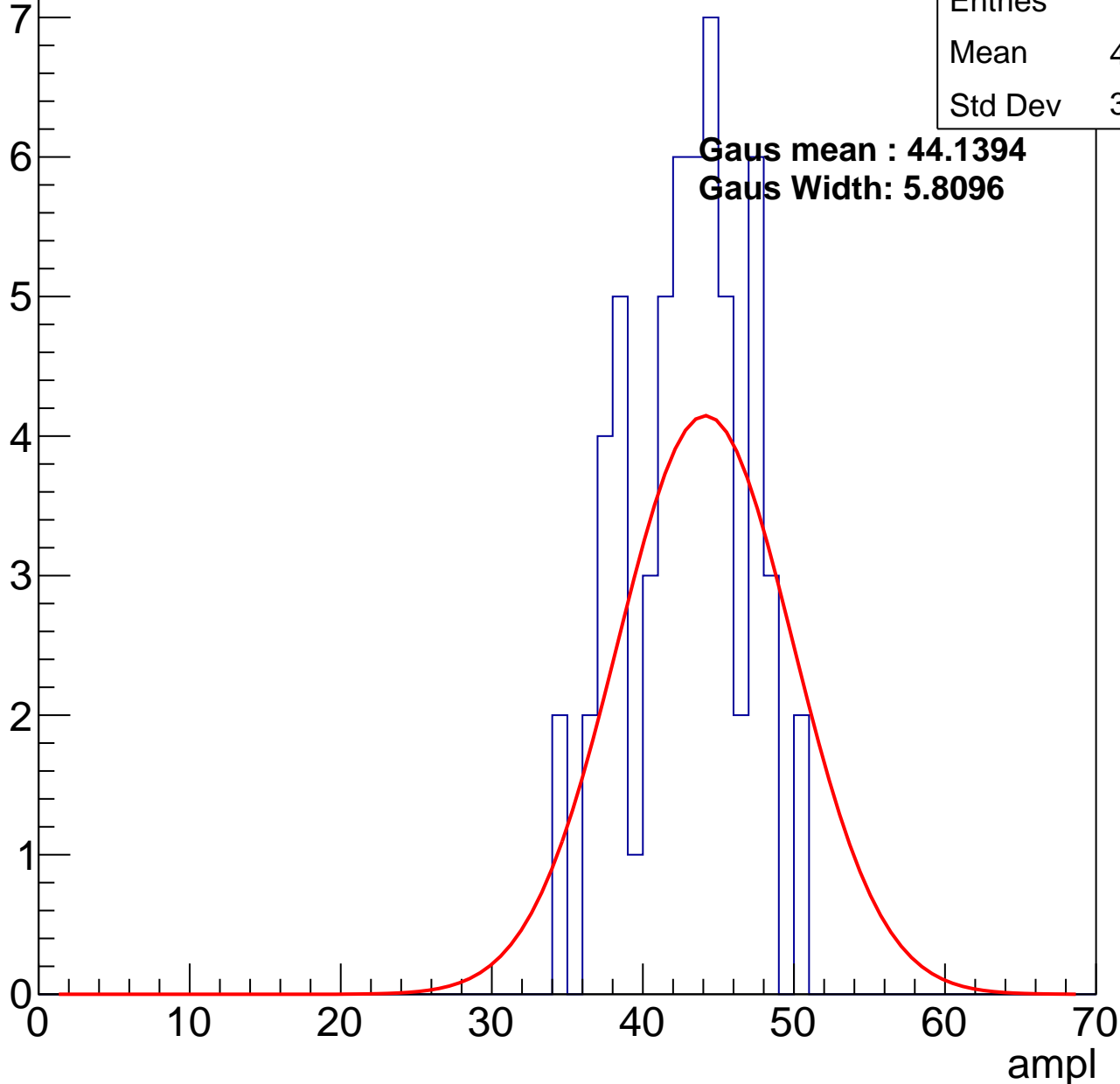
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.42
Std Dev	3.889

Gaus mean : 44.1394

Gaus Width: 5.8096



B1L102S, U6-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

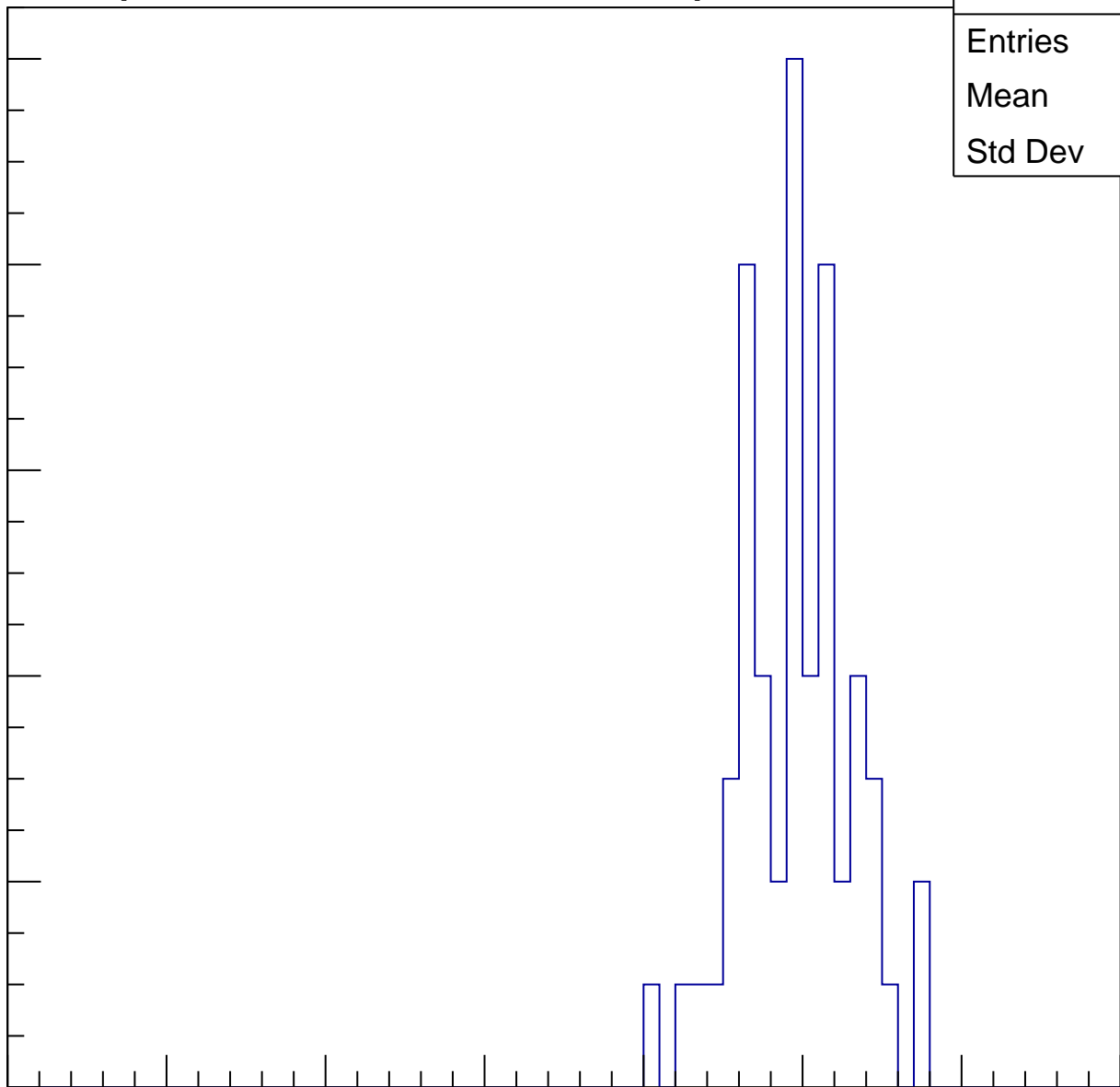
Entries	55
Mean	49.11
Std Dev	3.53

Entry

10
8
6
4
2
0

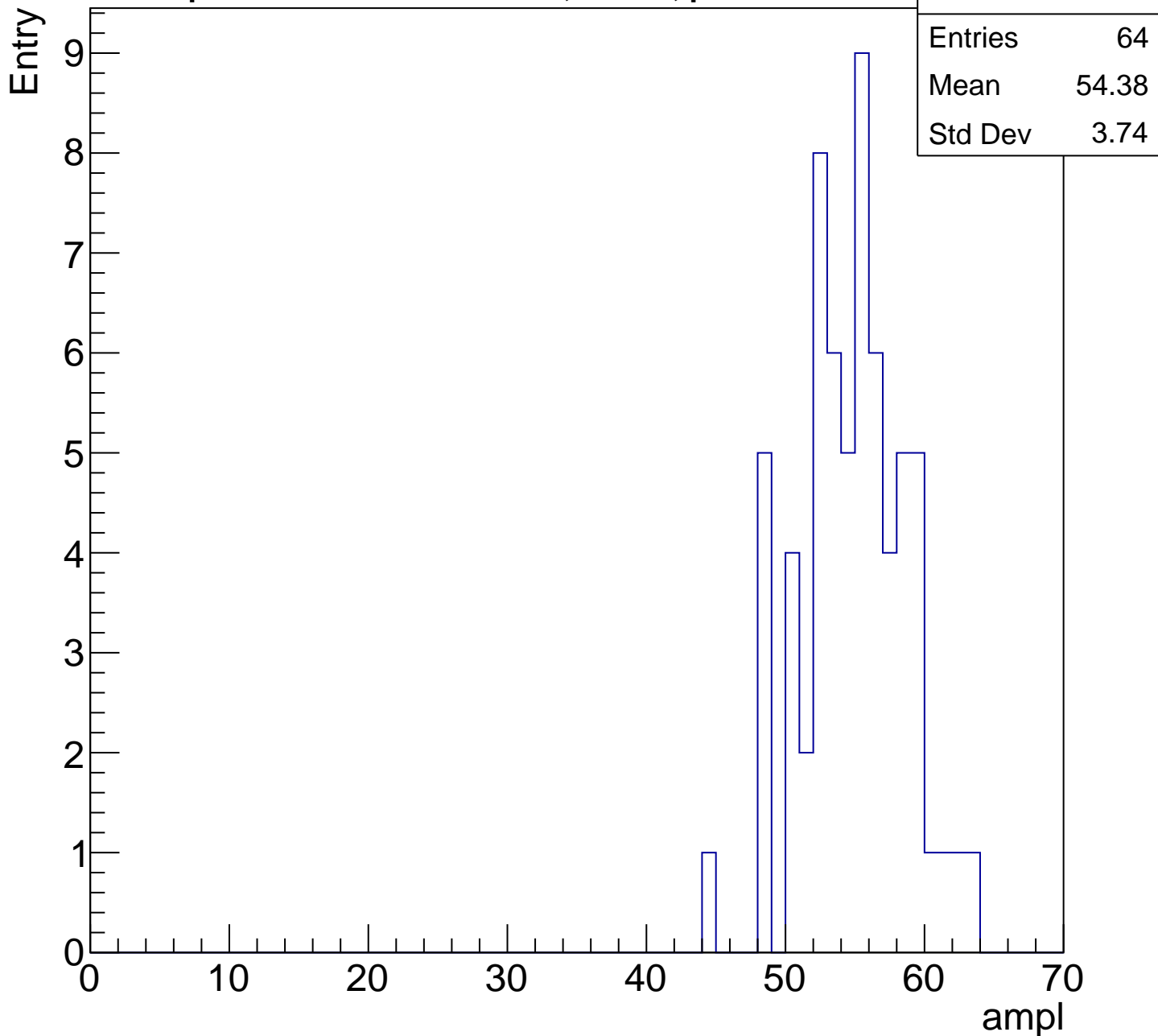
0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	59.64
Std Dev	2.487

ampl

0

10

20

30

40

50

60

70

B1L102S, U6-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

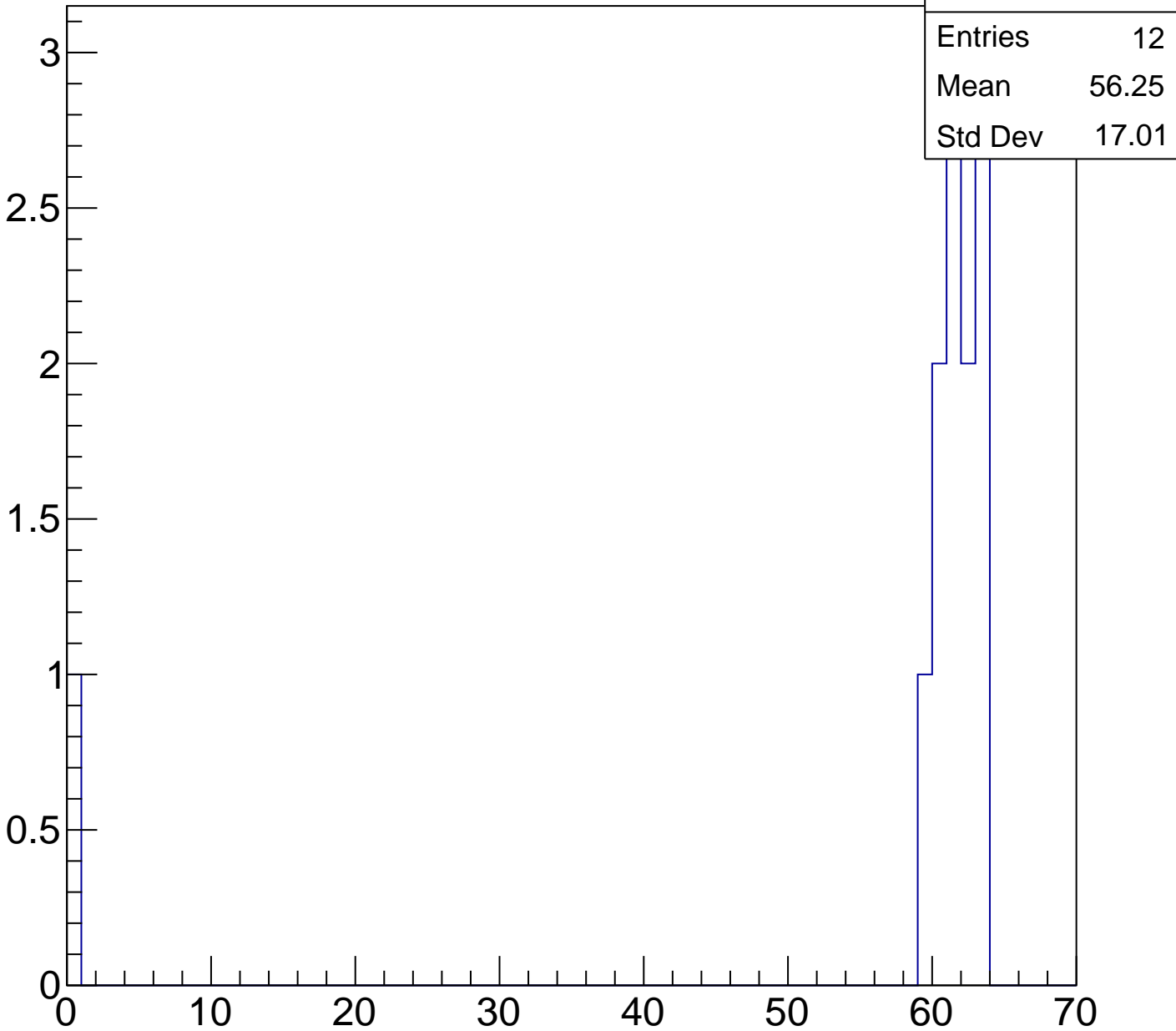
1

0.5

0

ampl

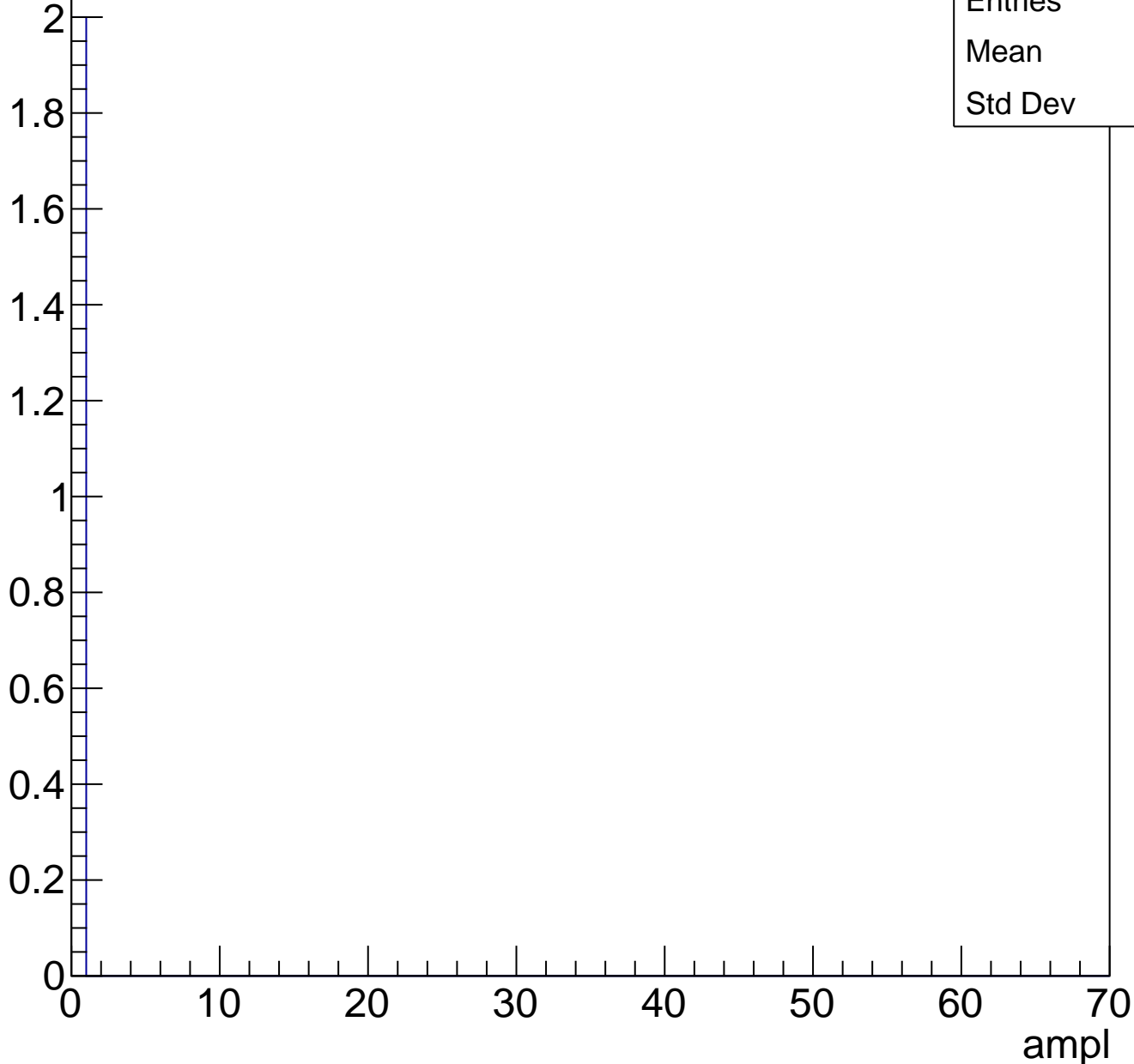
Entries	12
Mean	56.25
Std Dev	17.01



B1L102S, U6-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch57, adc0

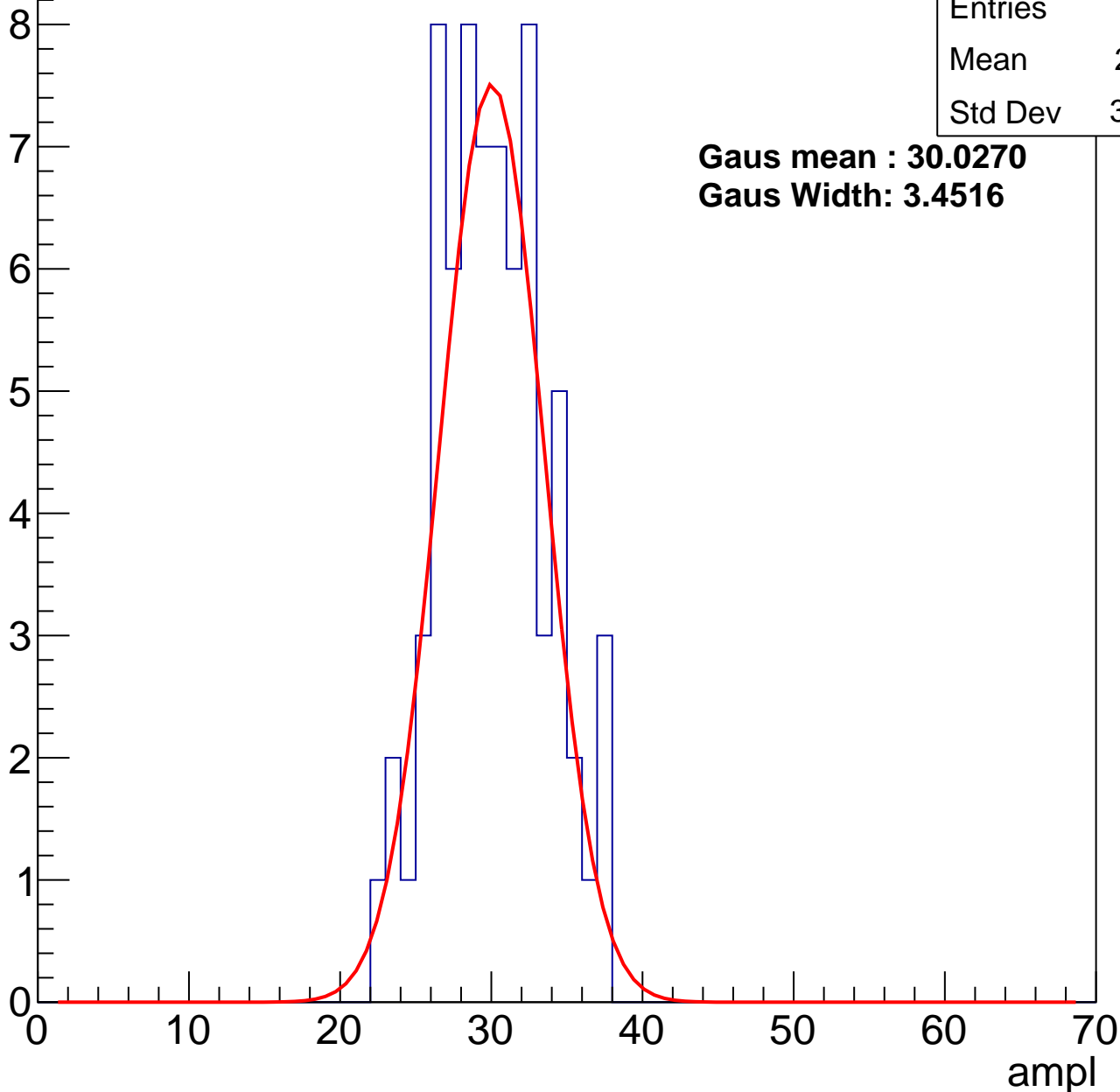
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	29.61
Std Dev	3.482

Gaus mean : 30.0270

Gaus Width: 3.4516



B1L102S, U6-ch57, adc1

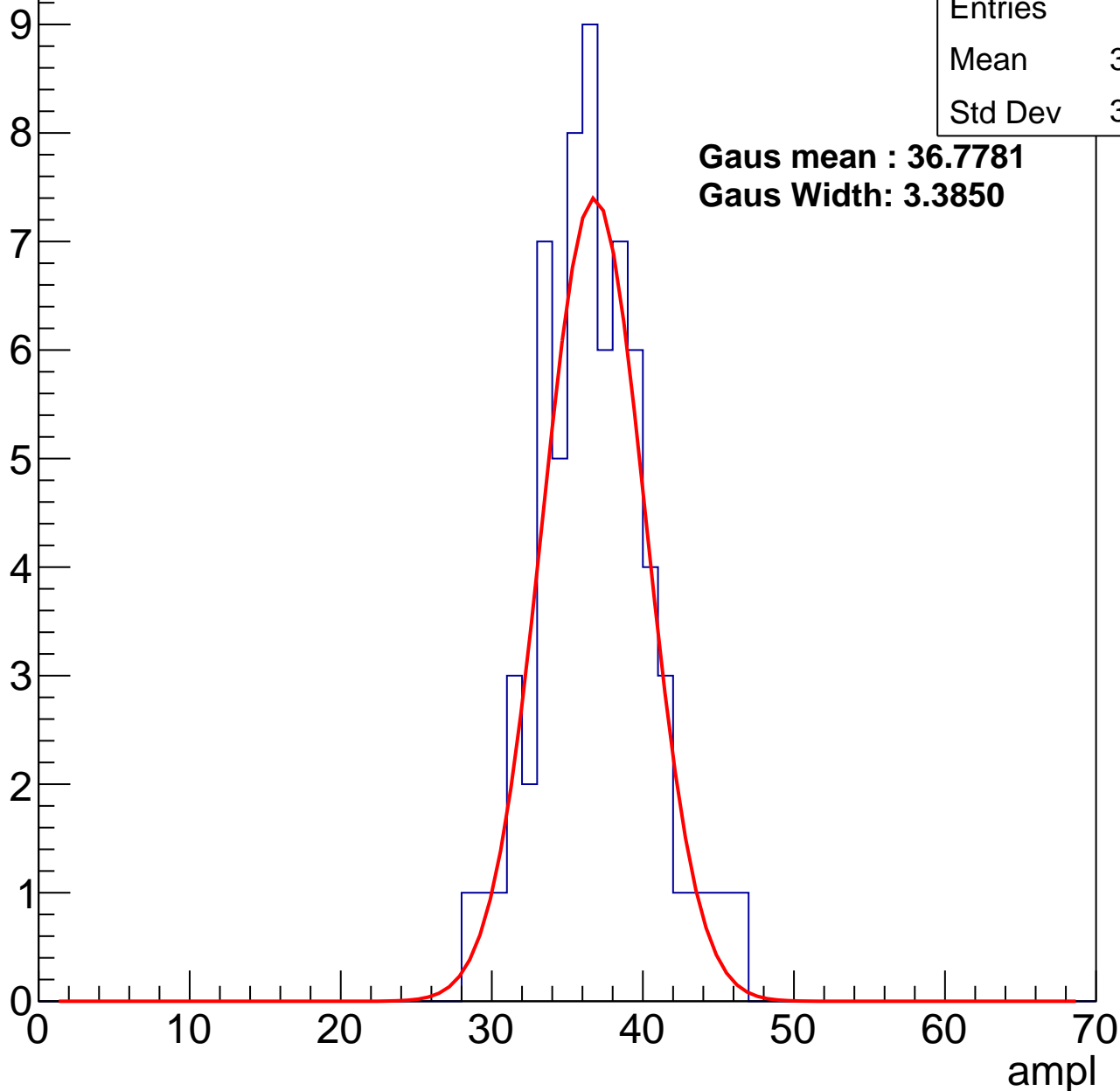
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	36.38
Std Dev	3.626

Gaus mean : 36.7781

Gaus Width: 3.3850



B1L102S, U6-ch57, adc2

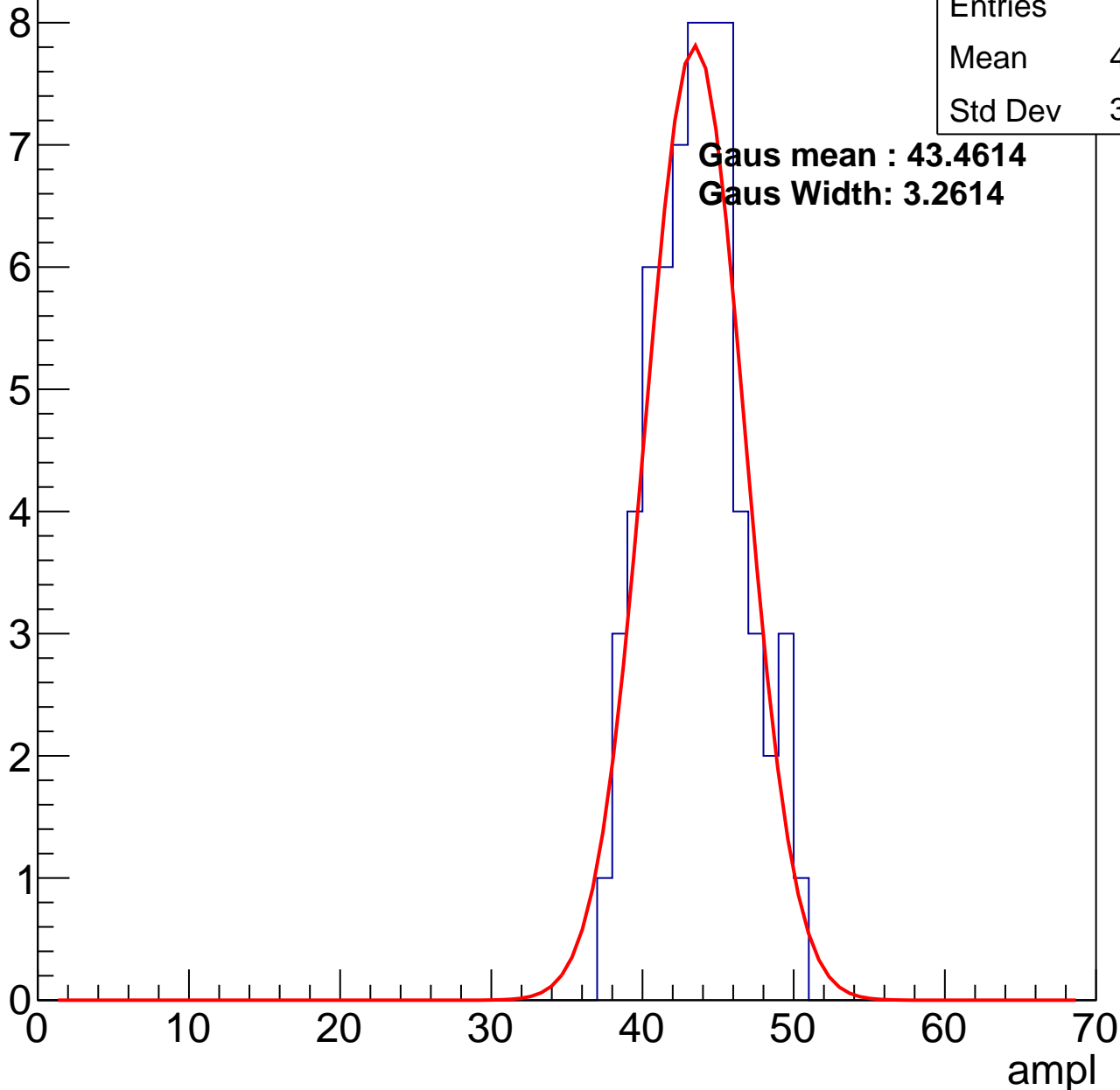
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.14
Std Dev	3.036

Gaus mean : 43.4614

Gaus Width: 3.2614

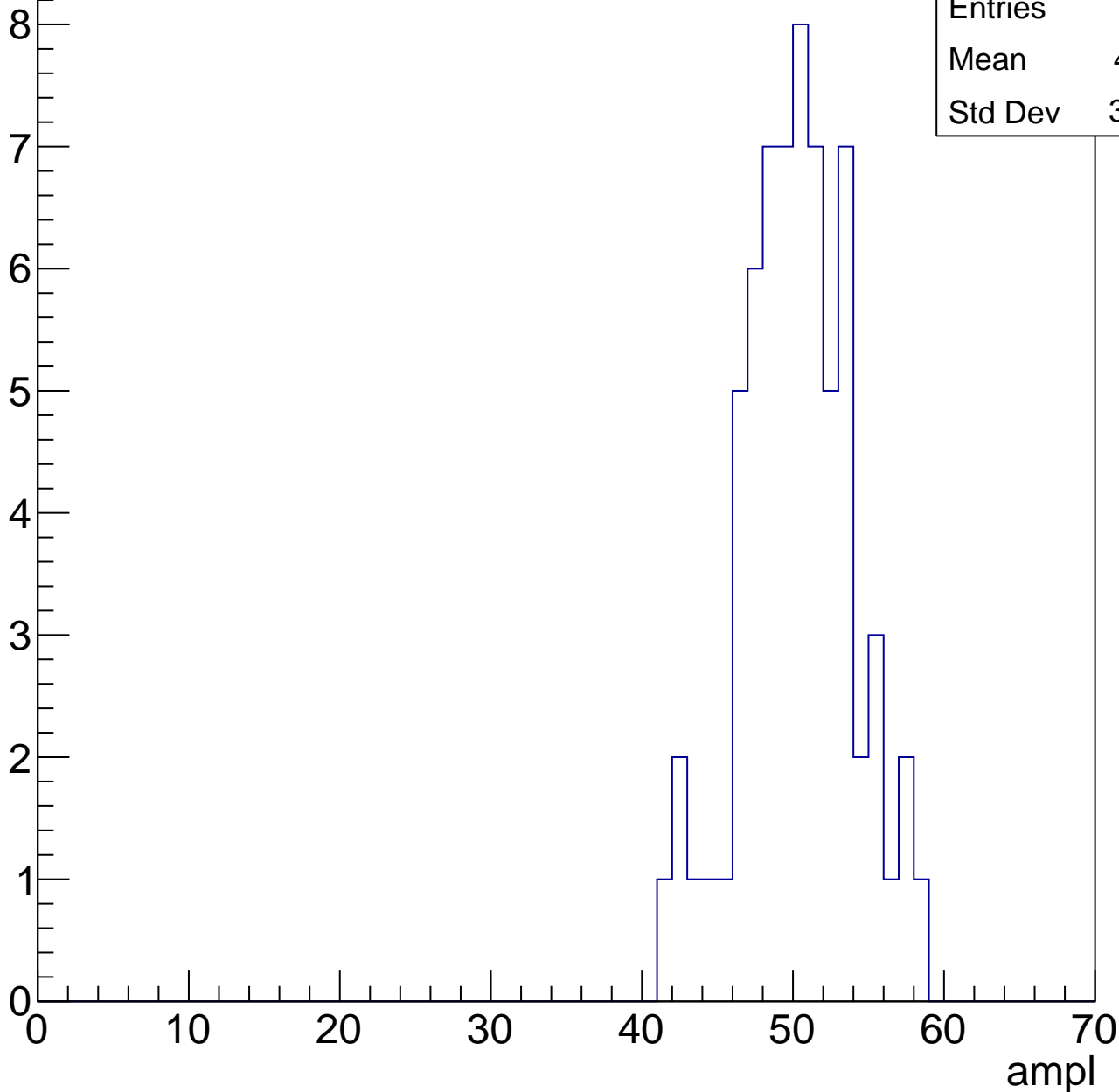


B1L102S, U6-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	49.81
Std Dev	3.625

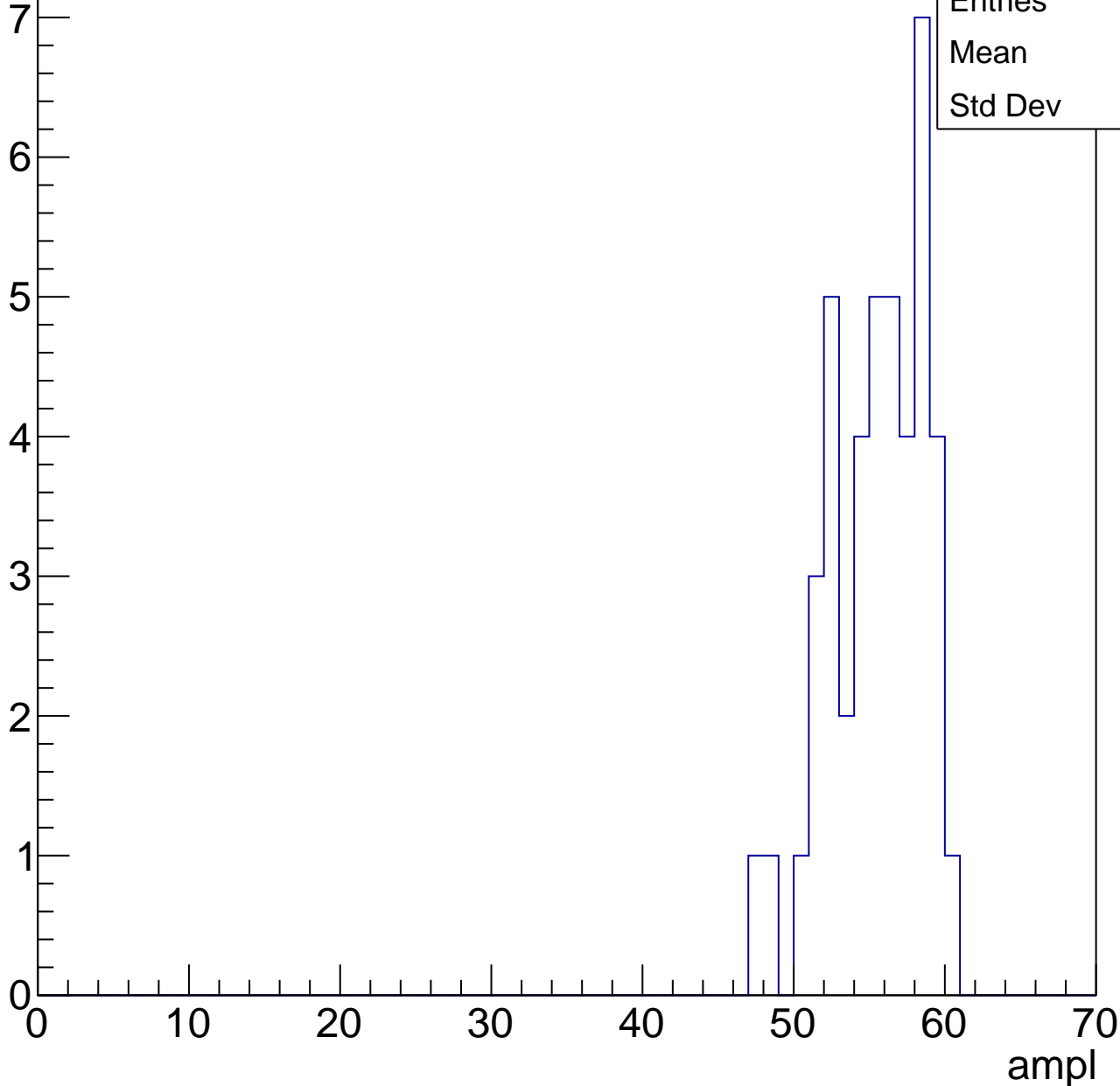


B1L102S, U6-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55
Std Dev	3.11

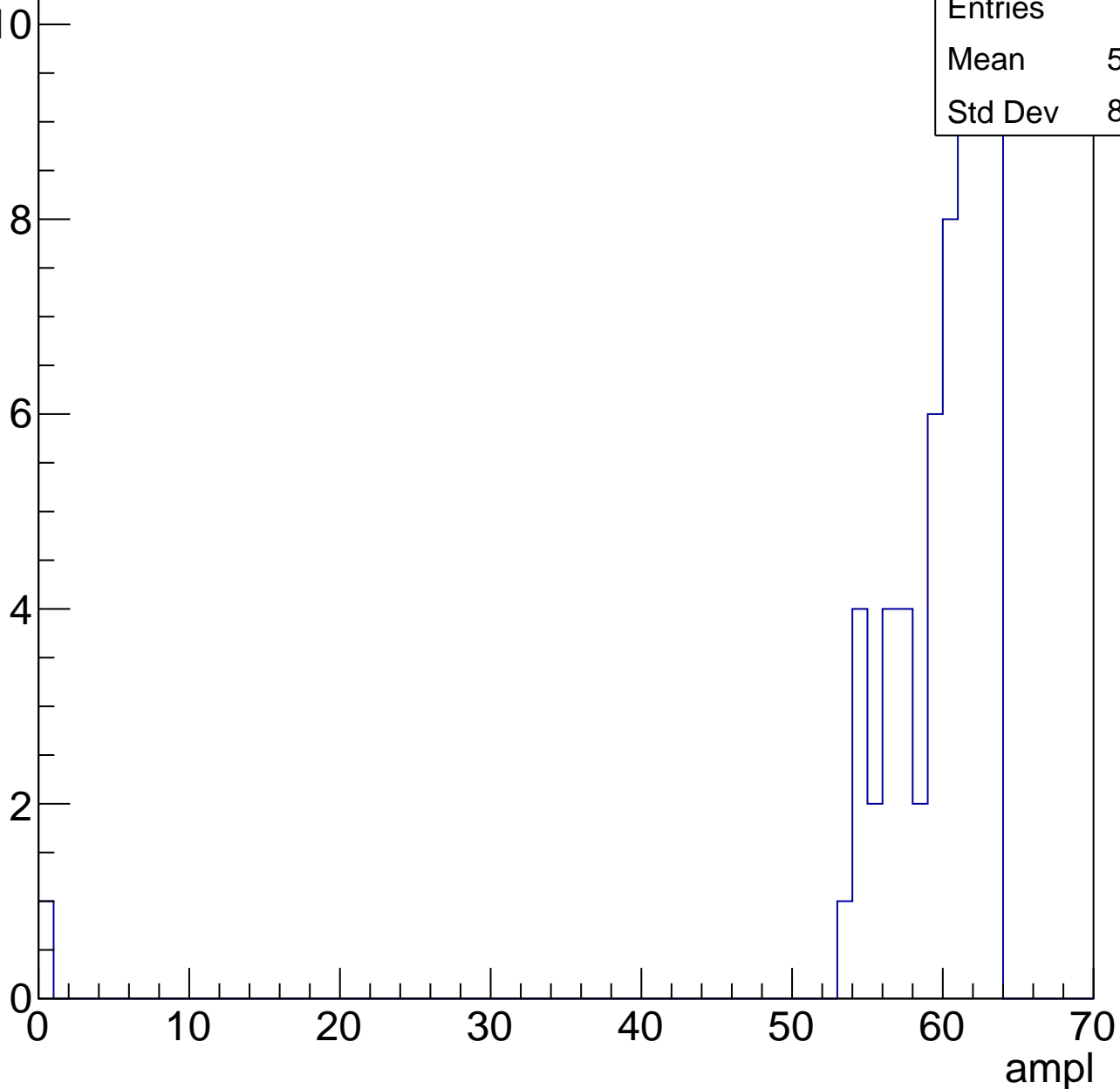


B1L102S, U6-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

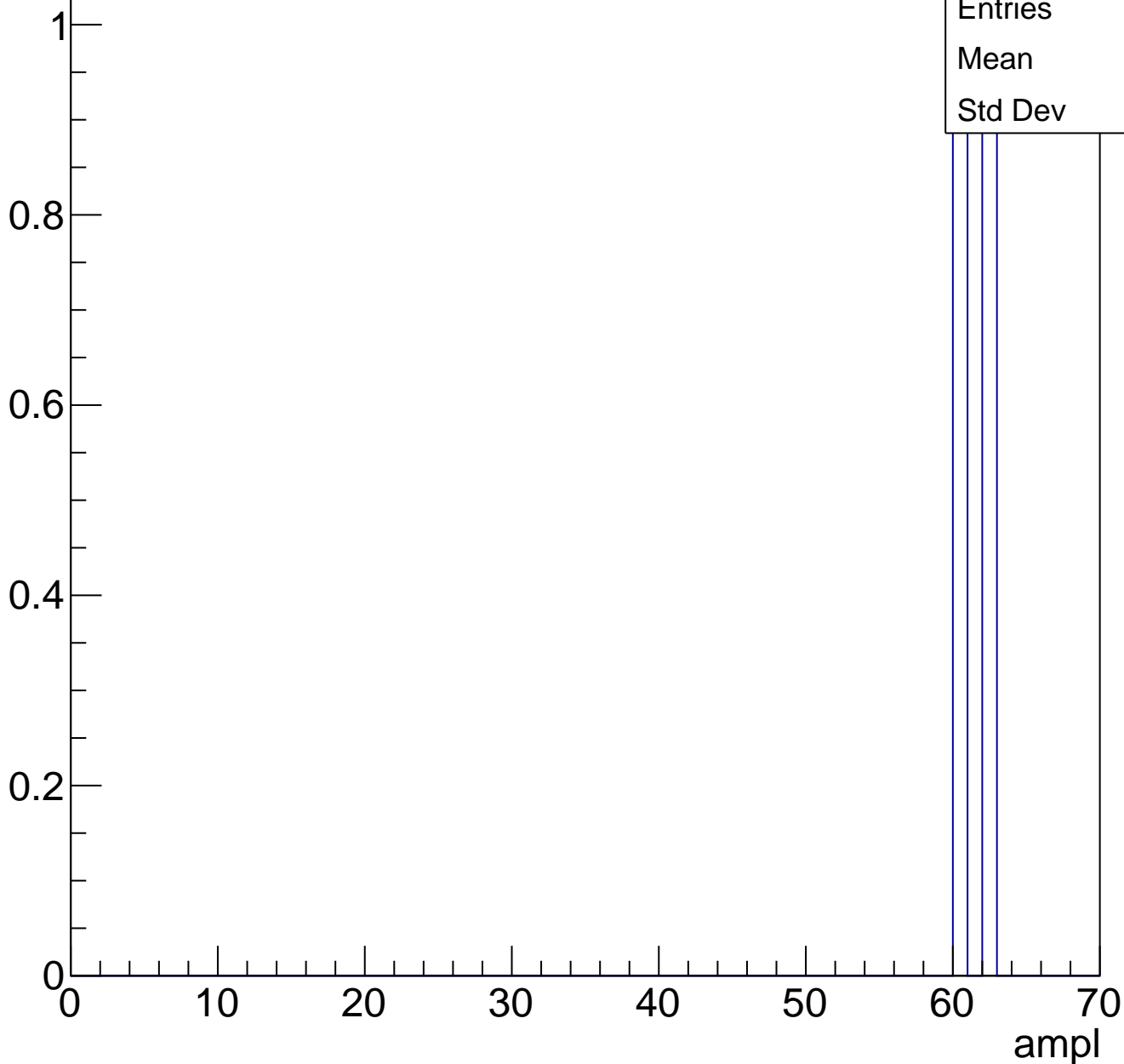
Entries	61
Mean	58.67
Std Dev	8.083



B1L102S, U6-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch58, adc0

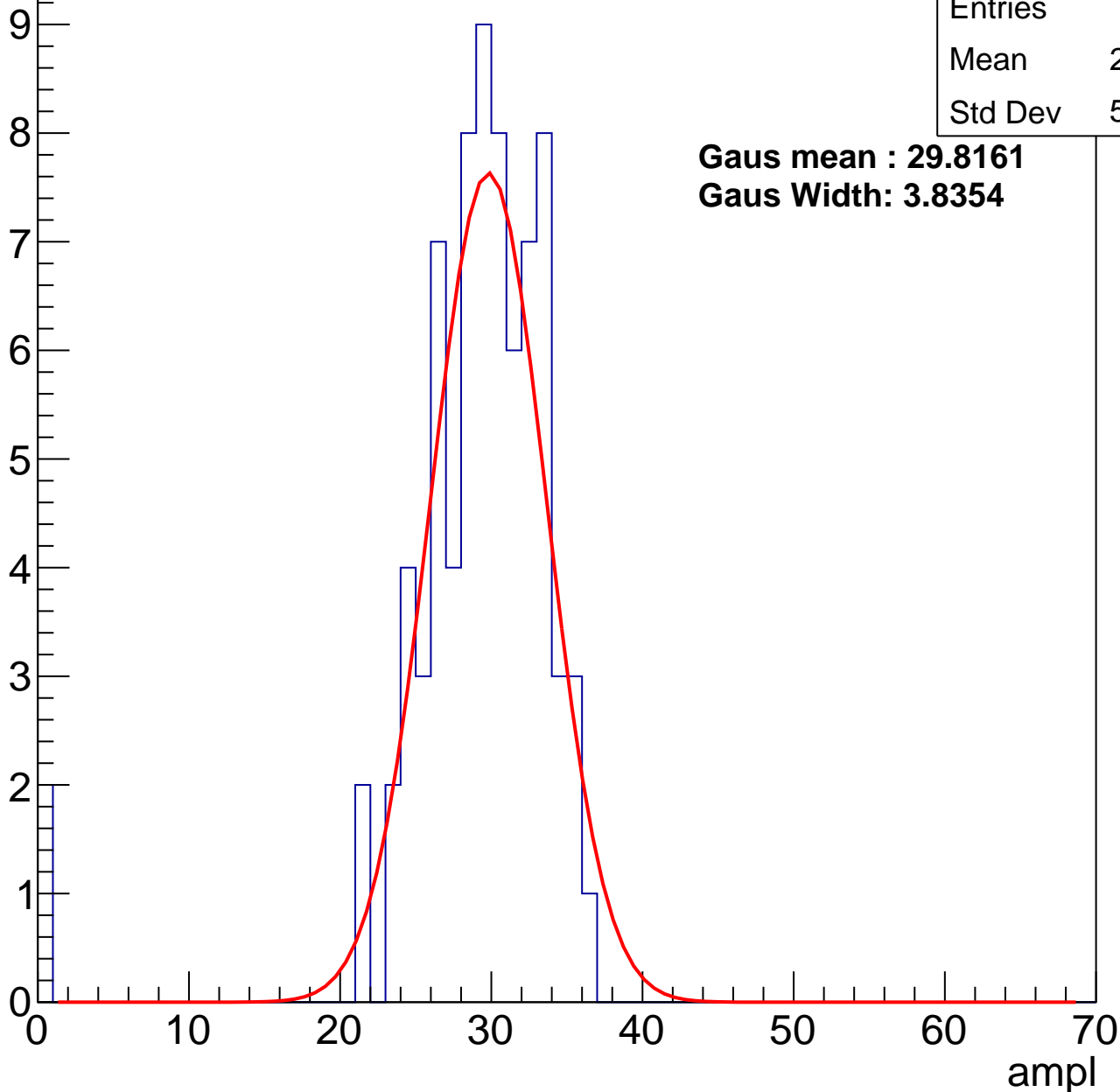
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	28.45
Std Dev	5.754

Gaus mean : 29.8161

Gaus Width: 3.8354



B1L102S, U6-ch58, adc1

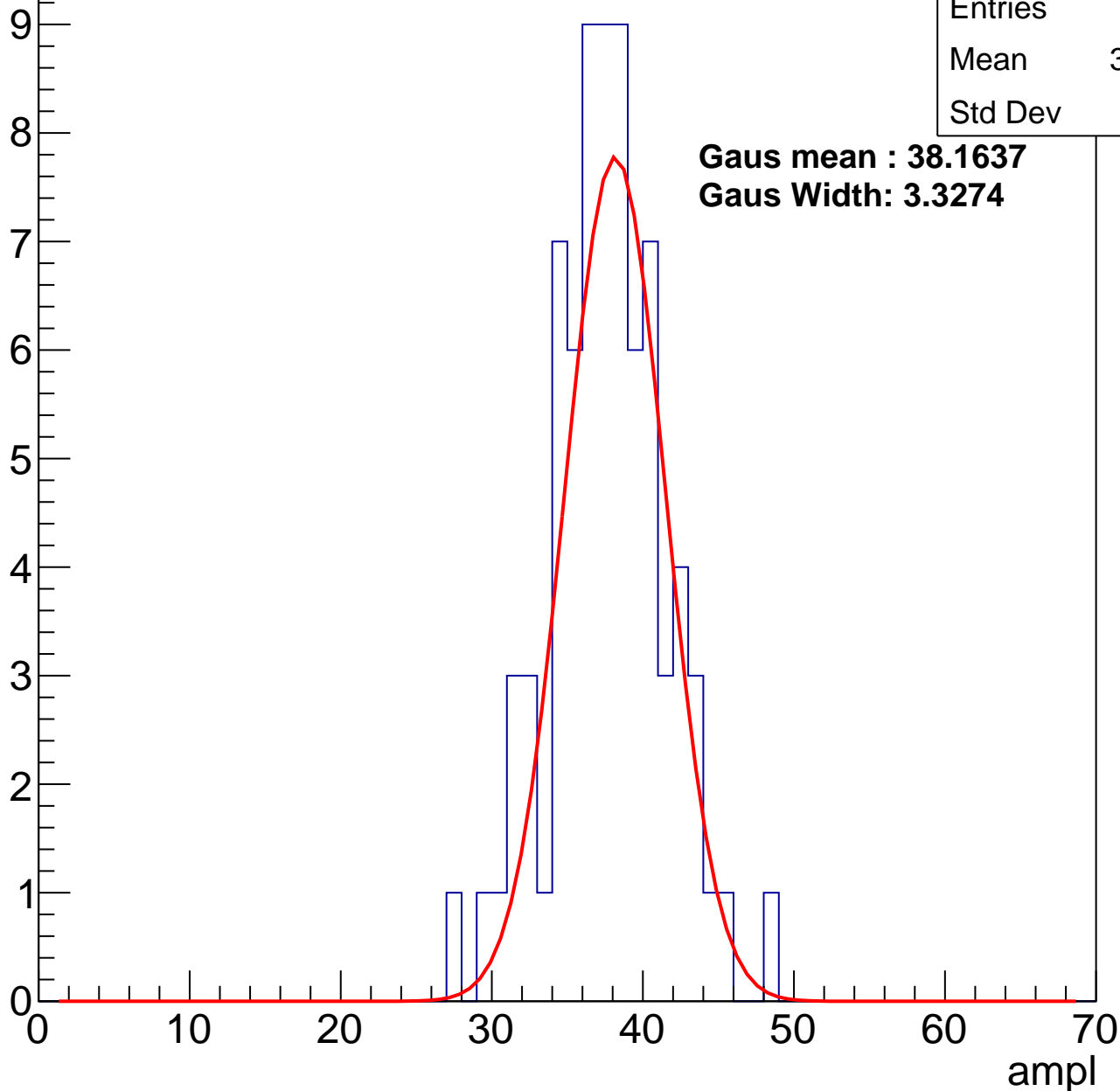
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	37.18
Std Dev	3.79

Gaus mean : 38.1637

Gaus Width: 3.3274



B1L102S, U6-ch58, adc2

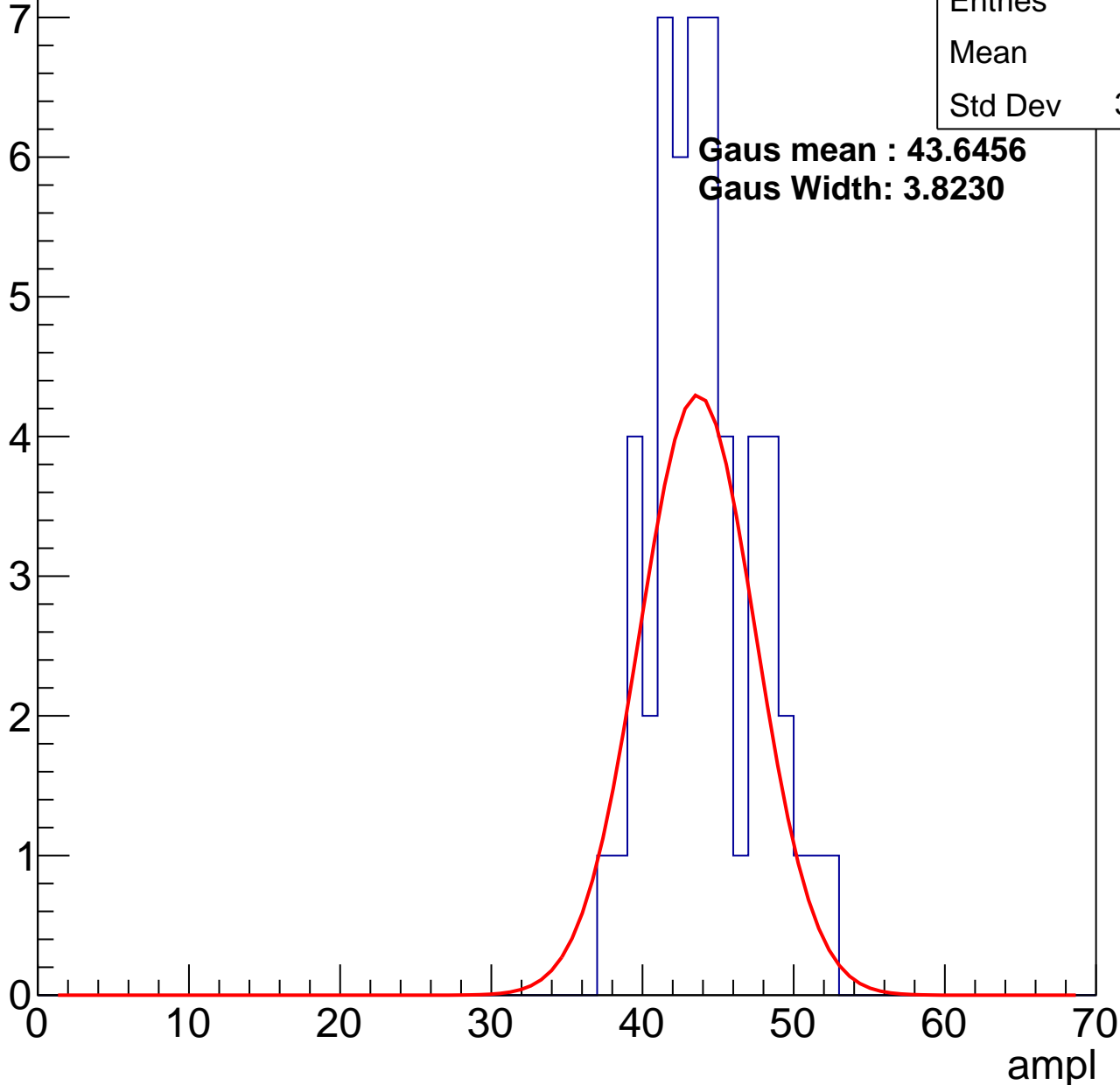
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	43.7
Std Dev	3.401

Gaus mean : 43.6456

Gaus Width: 3.8230

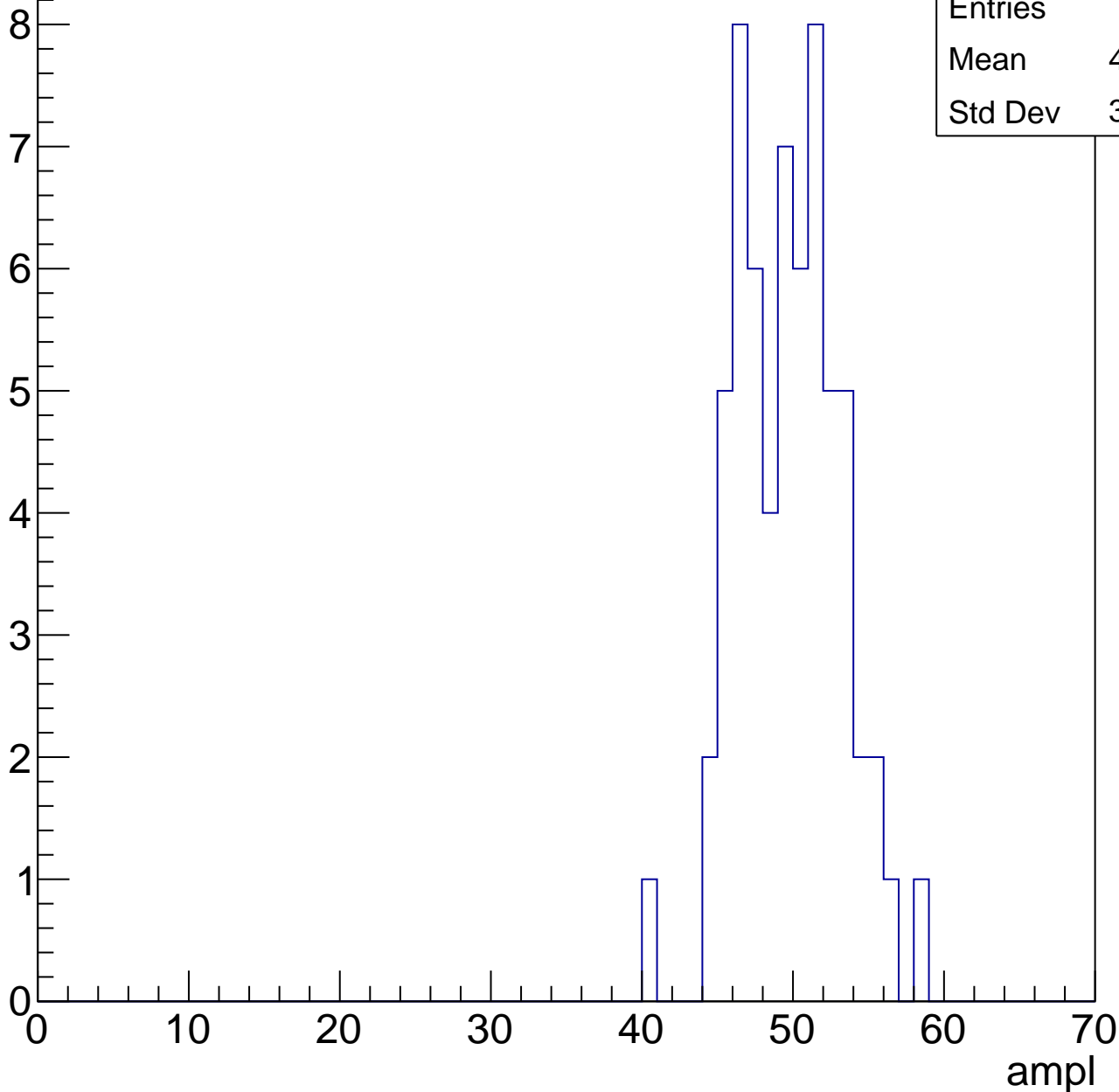


B1L102S, U6-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.25
Std Dev	3.385



B1L102S, U6-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	58
Mean	55.34
Std Dev	3.309

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

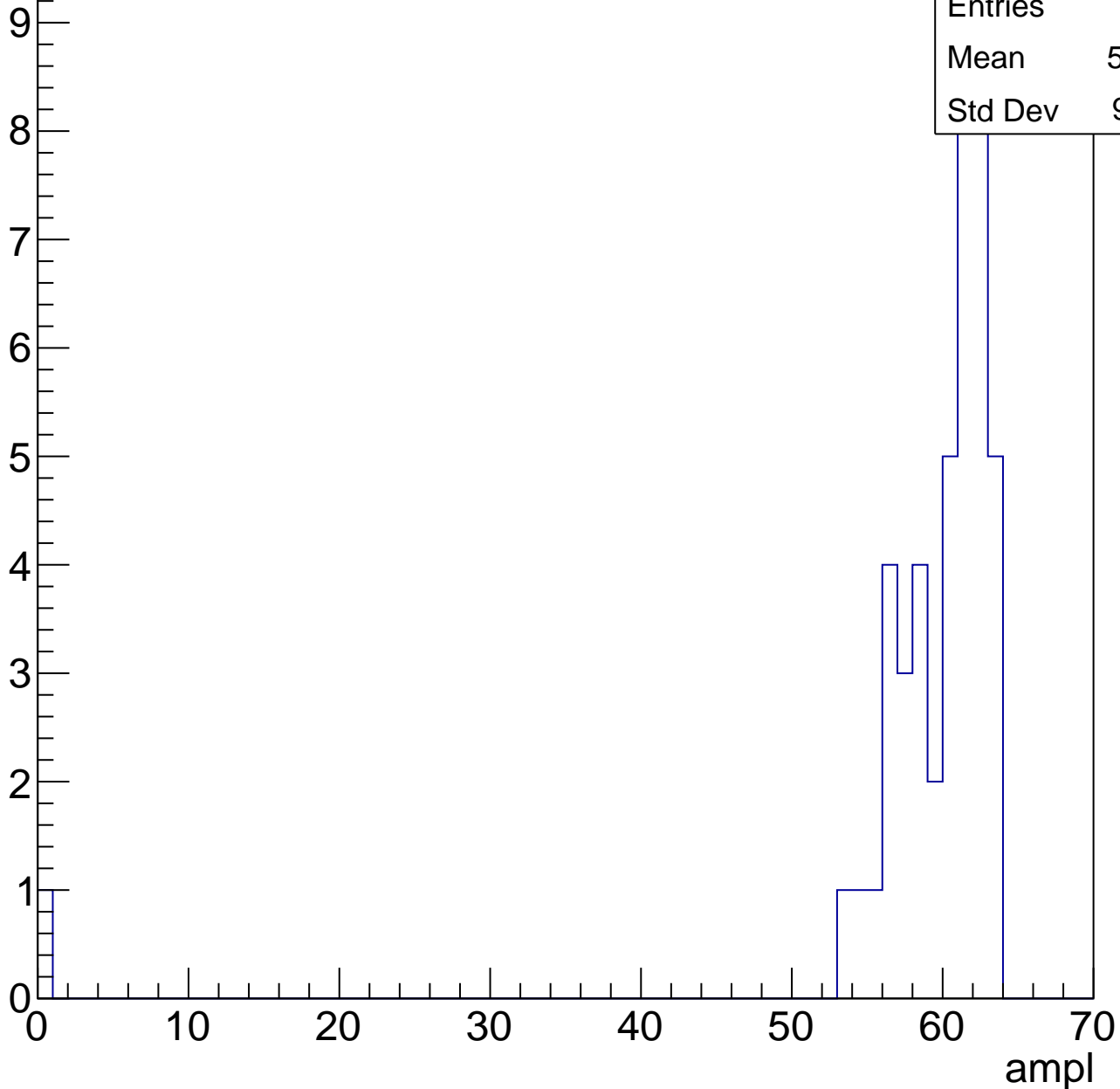
7

B1L102S, U6-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.36
Std Dev	9.281

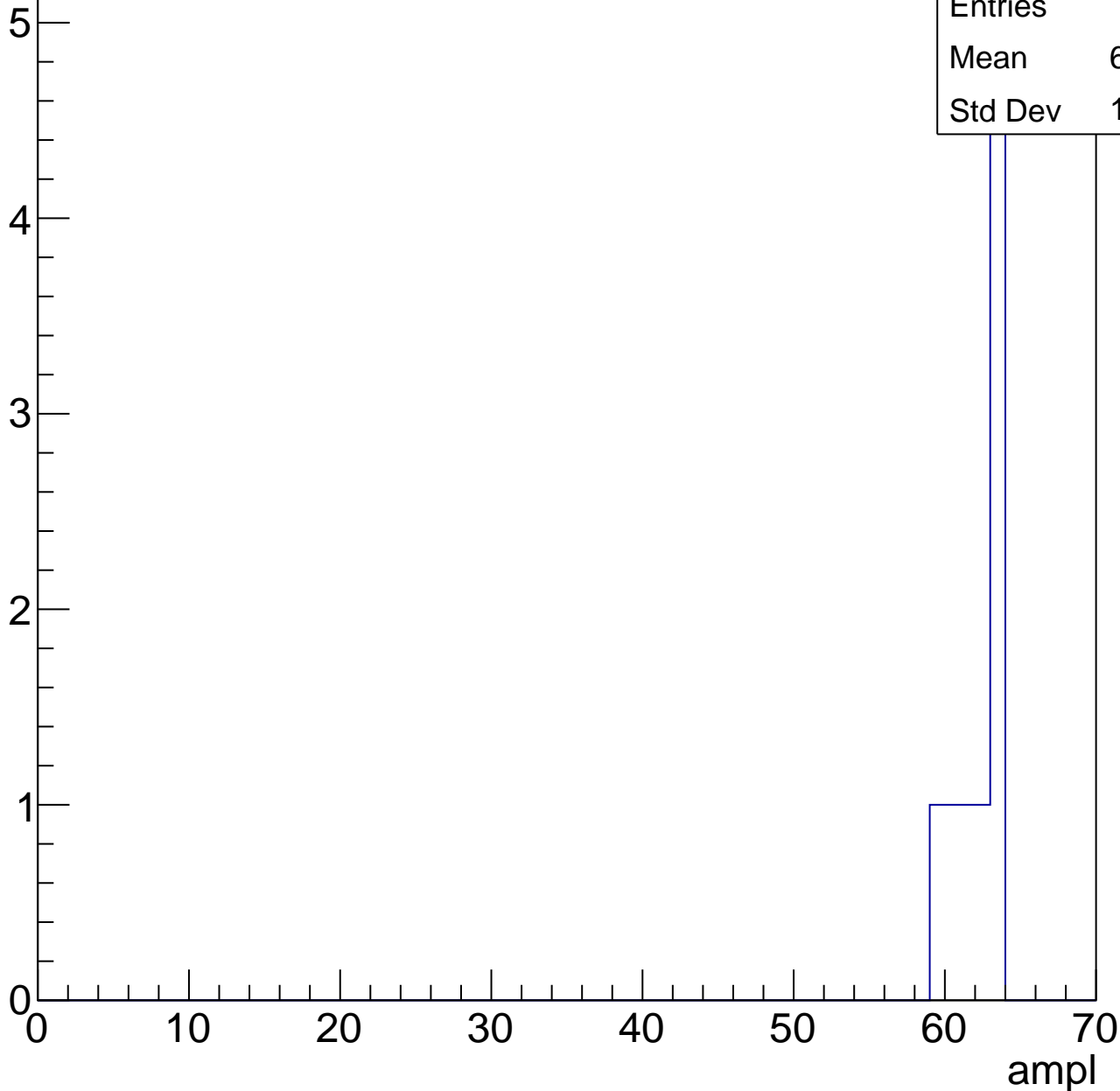


B1L102S, U6-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.89
Std Dev	1.449



B1L102S, U6-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch59, adc0

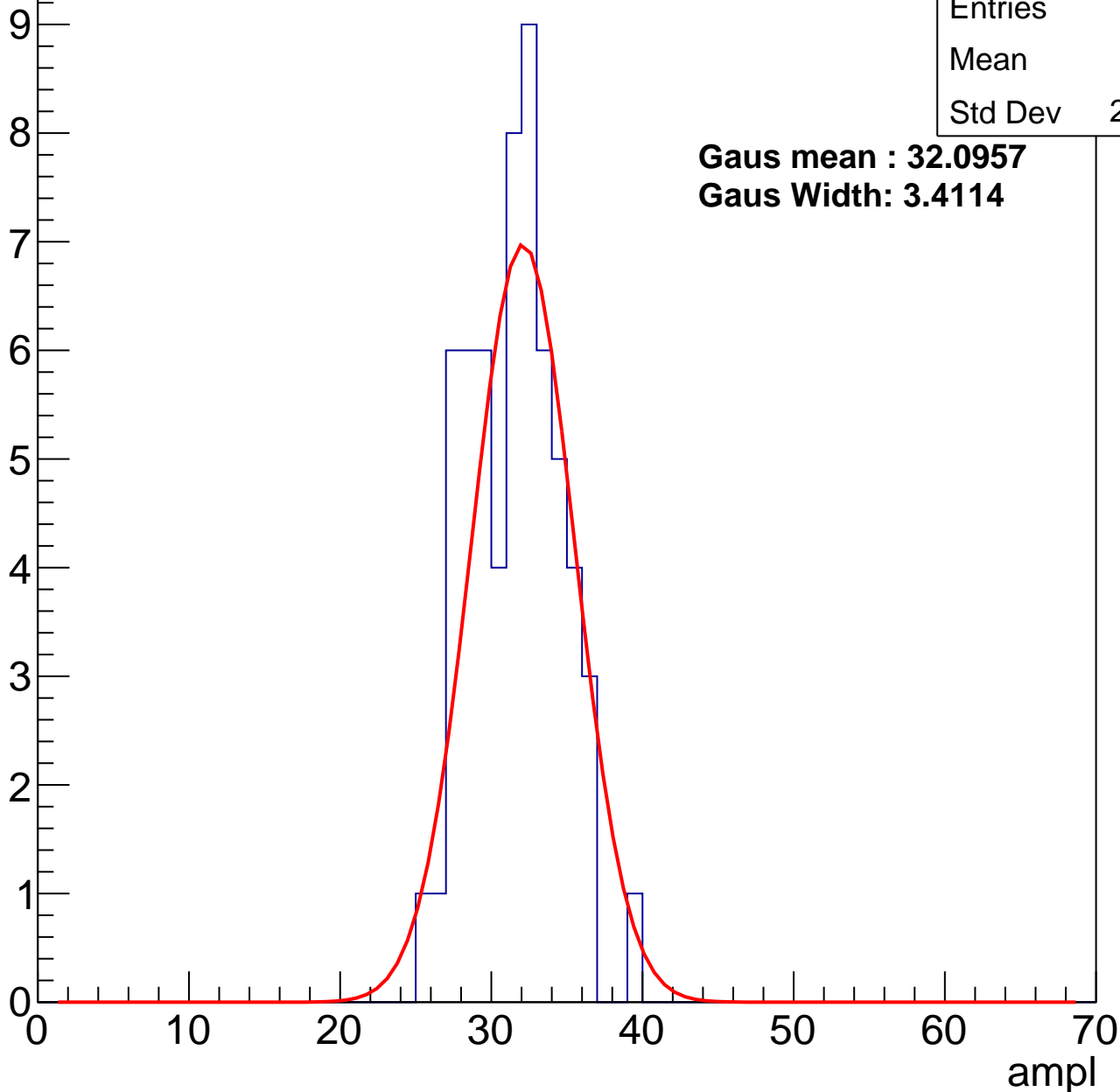
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	31.1
Std Dev	2.942

Gaus mean : 32.0957

Gaus Width: 3.4114



B1L102S, U6-ch59, adc1

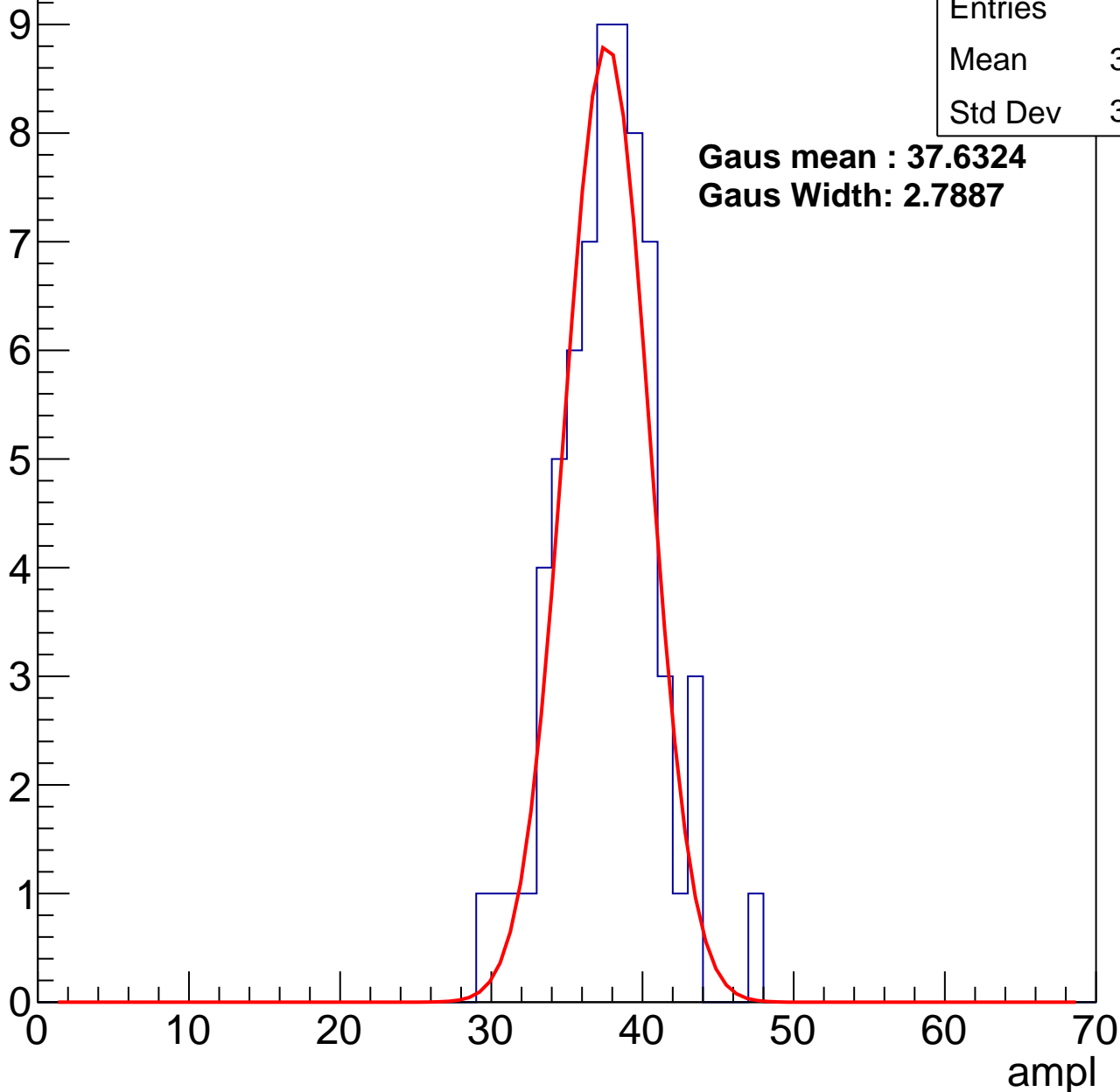
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	37.22
Std Dev	3.222

Gaus mean : 37.6324

Gaus Width: 2.7887



B1L102S, U6-ch59, adc2

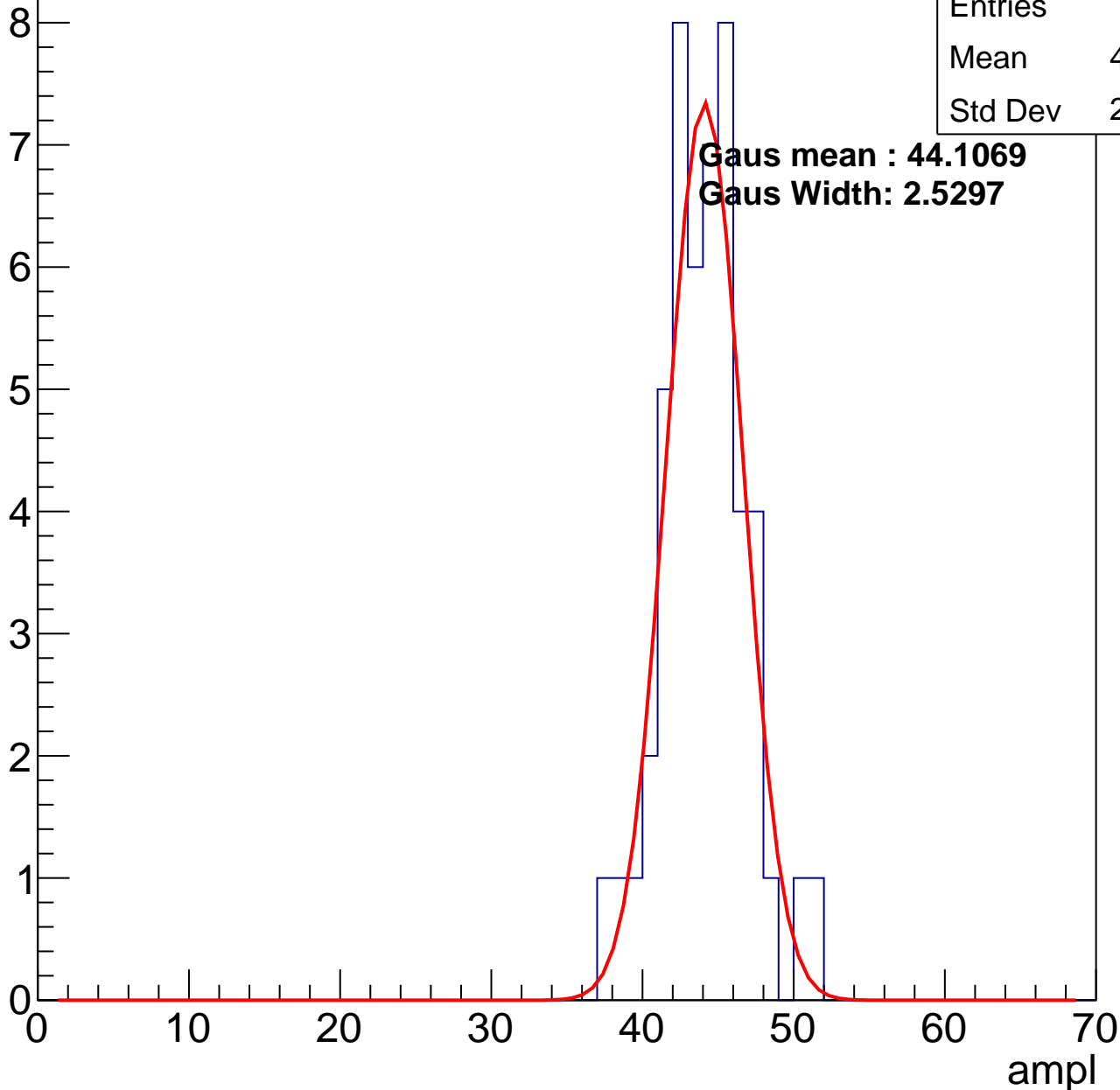
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	43.64
Std Dev	2.755

Gaus mean : 44.1069

Gaus Width: 2.5297

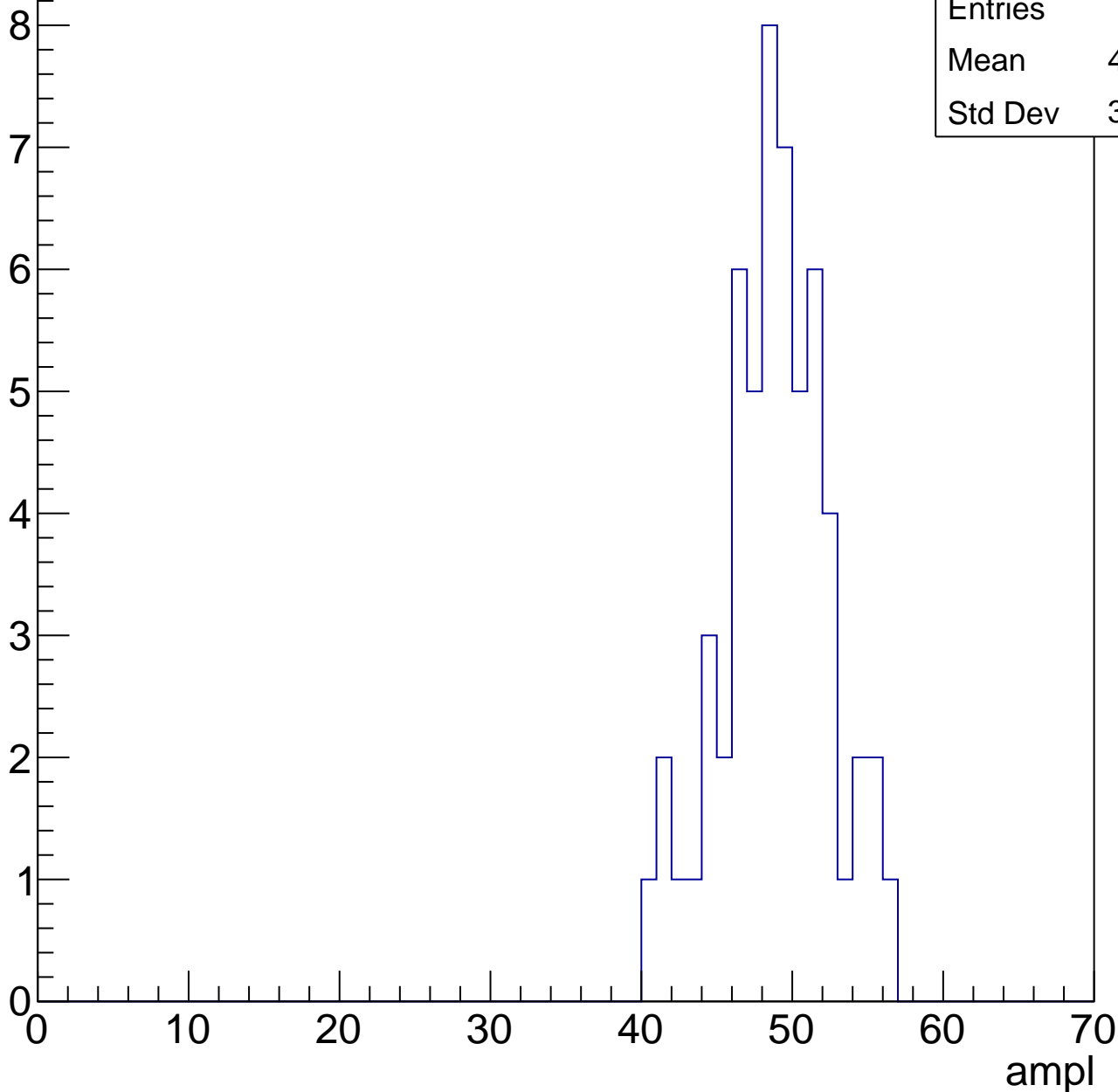


B1L102S, U6-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.39
Std Dev	3.538

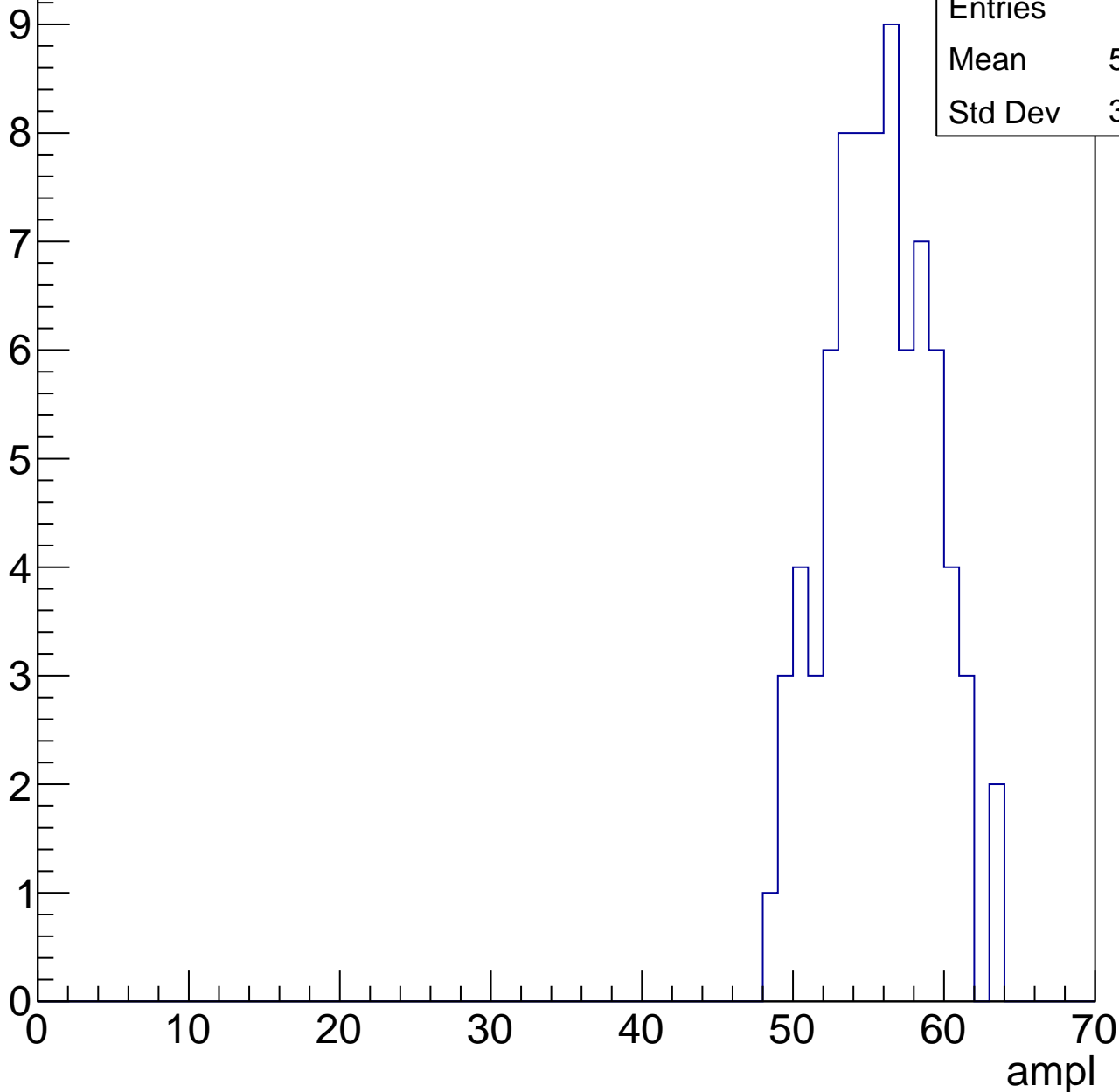


B1L102S, U6-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	55.27
Std Dev	3.437

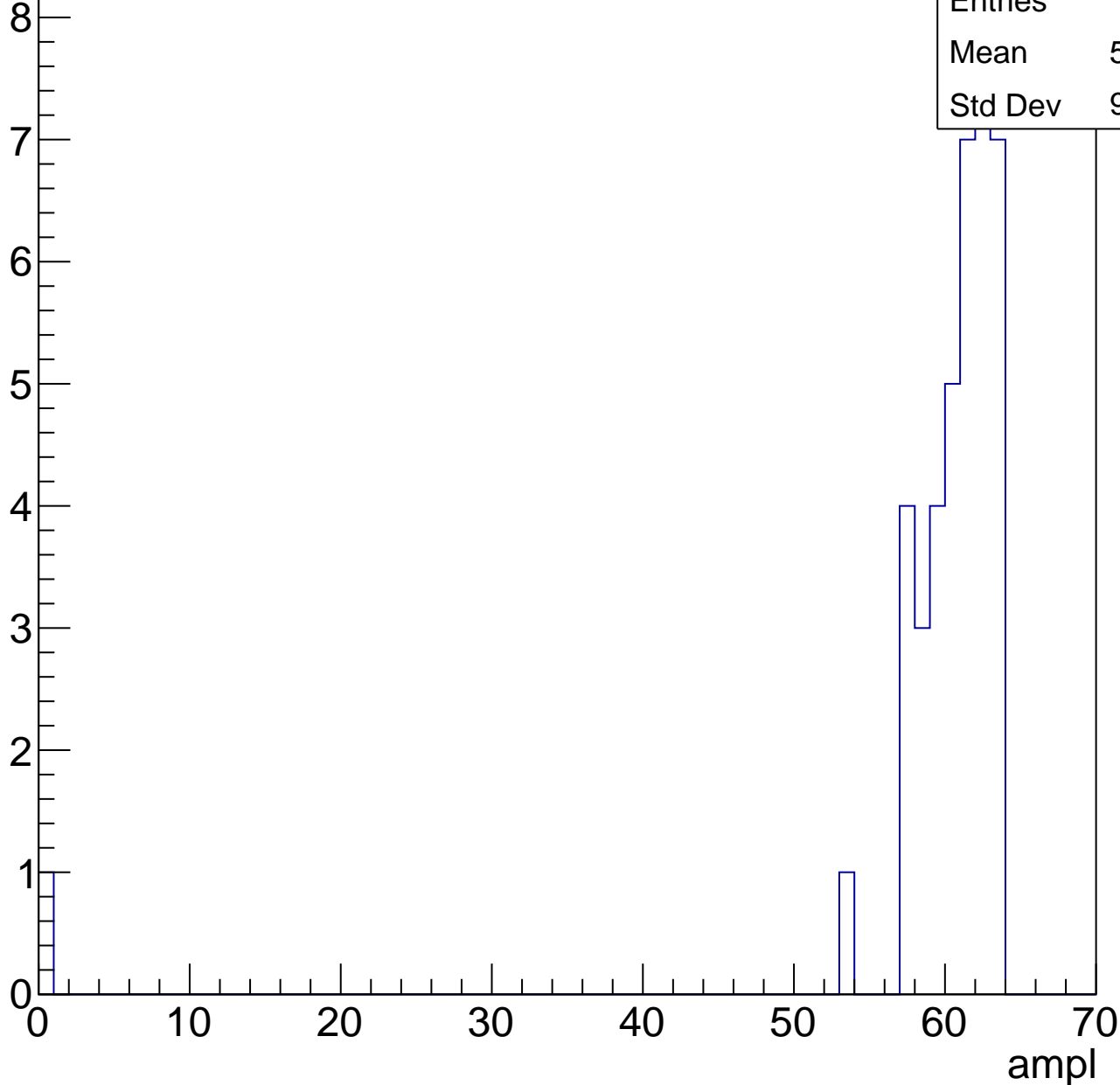


B1L102S, U6-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

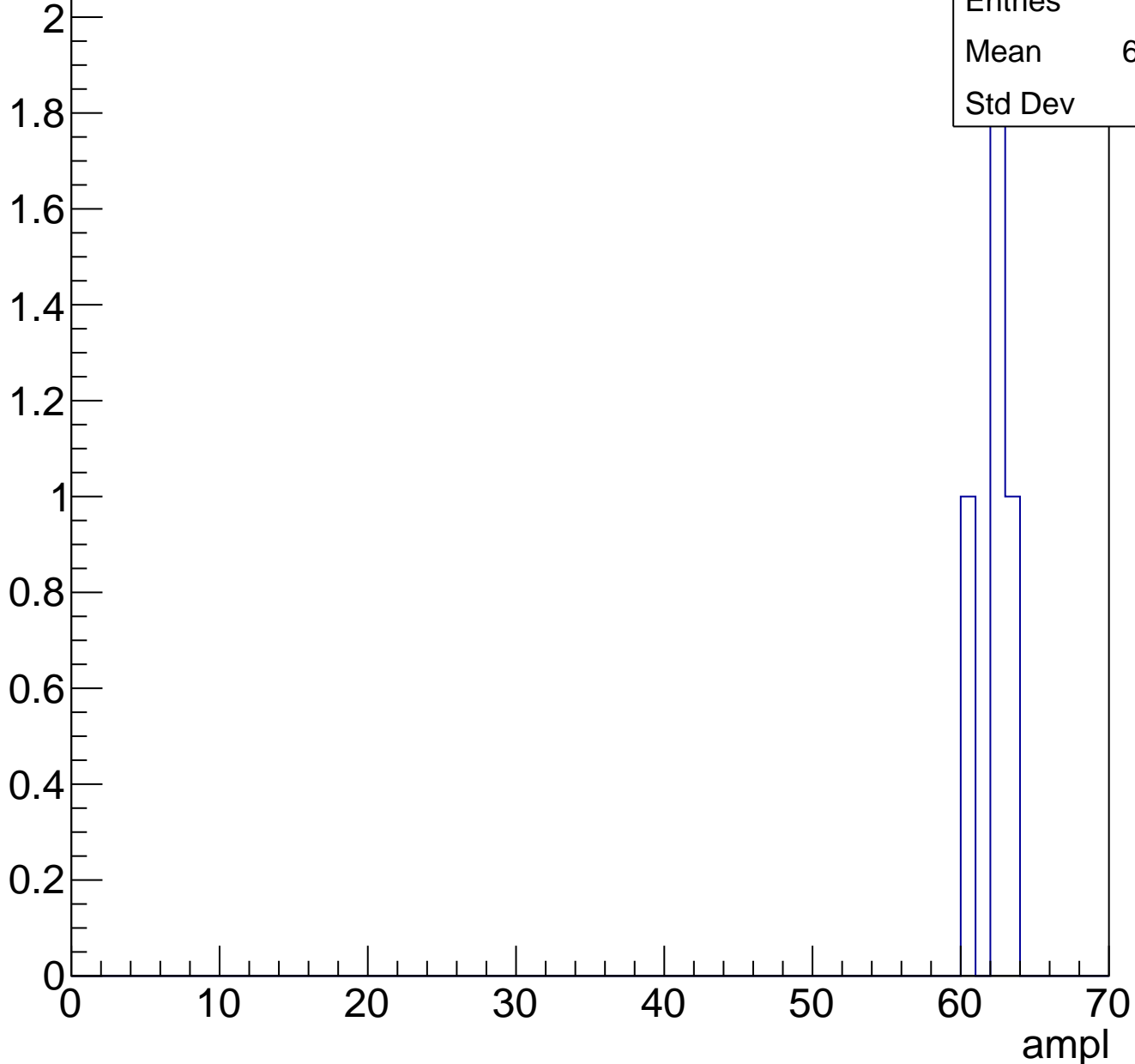
Entries	40
Mean	58.88
Std Dev	9.686



B1L102S, U6-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch60, adc0

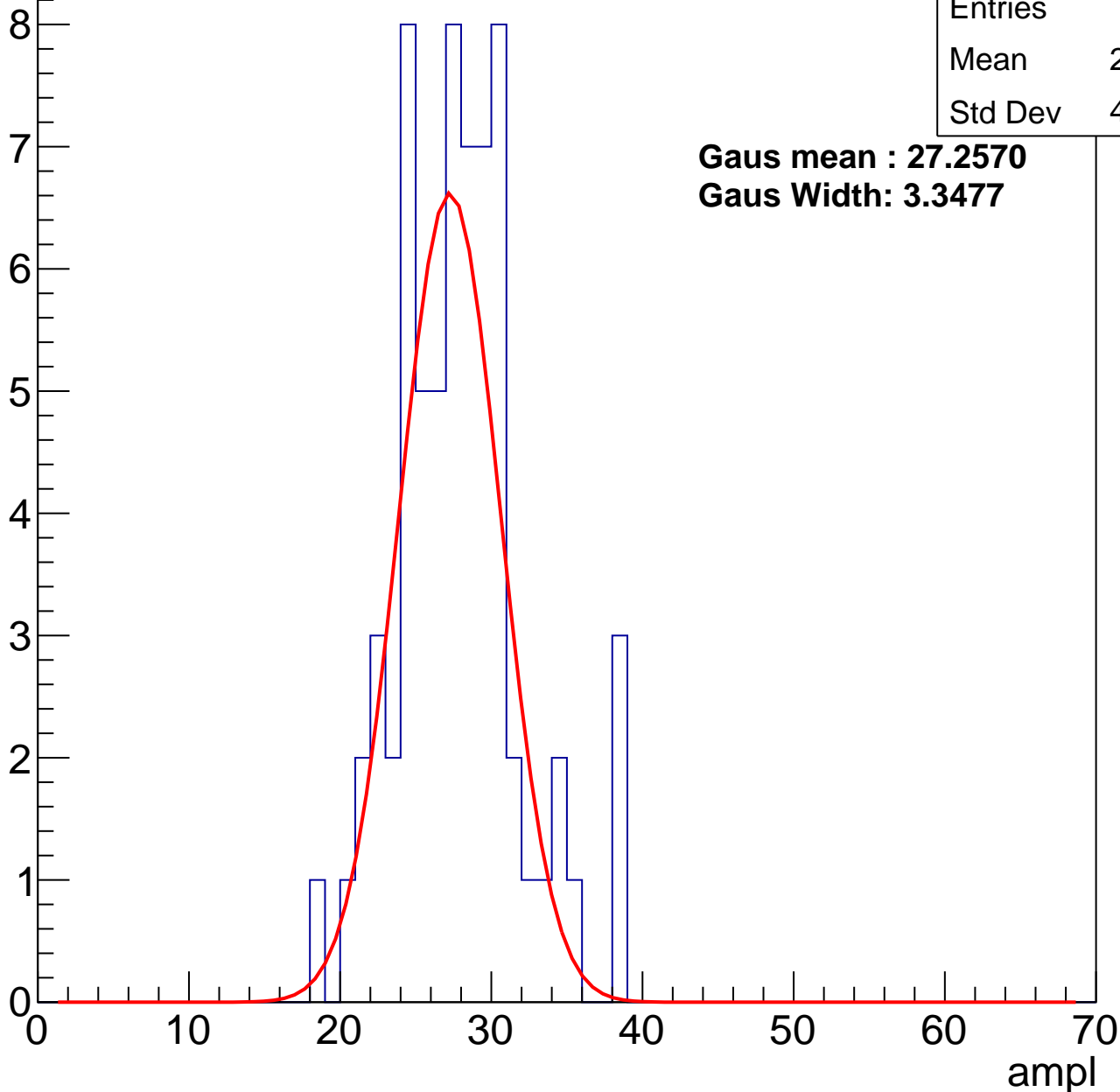
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.43
Std Dev	4.086

Gaus mean : 27.2570

Gaus Width: 3.3477



B1L102S, U6-ch60, adc1

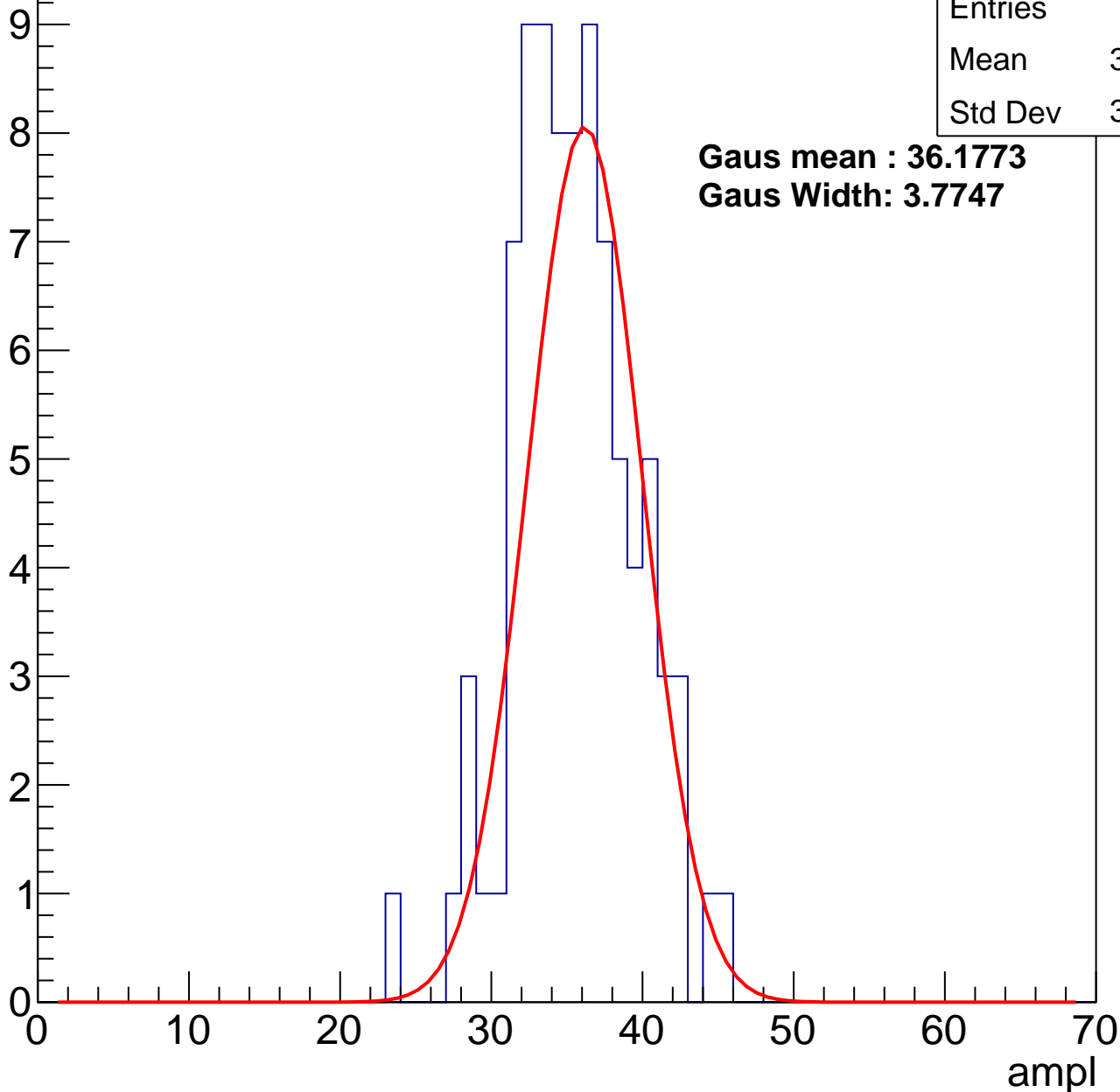
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	35.05
Std Dev	3.976

Gaus mean : 36.1773

Gaus Width: 3.7747



B1L102S, U6-ch60, adc2

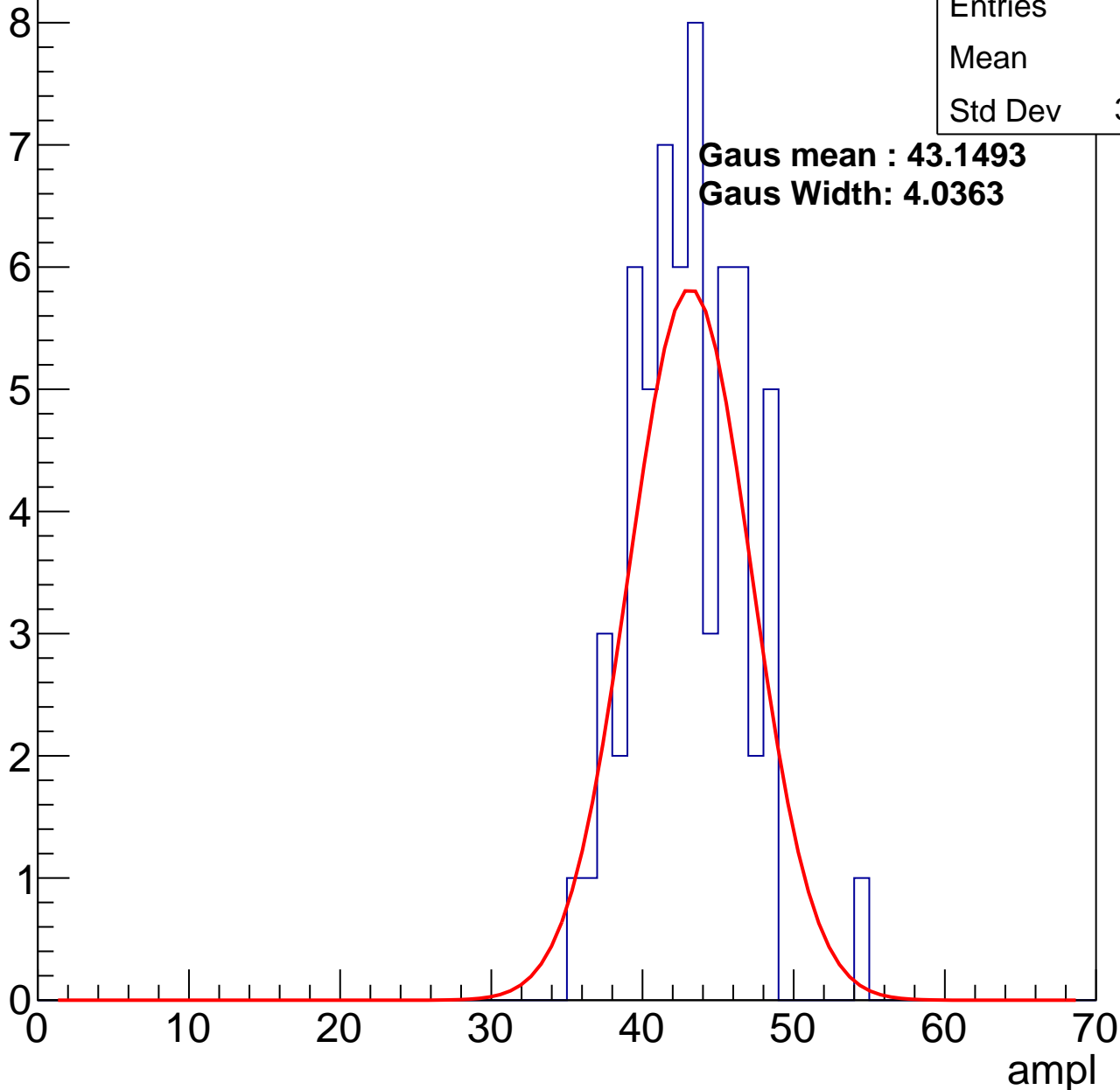
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.6
Std Dev	3.581

Gaus mean : 43.1493

Gaus Width: 4.0363

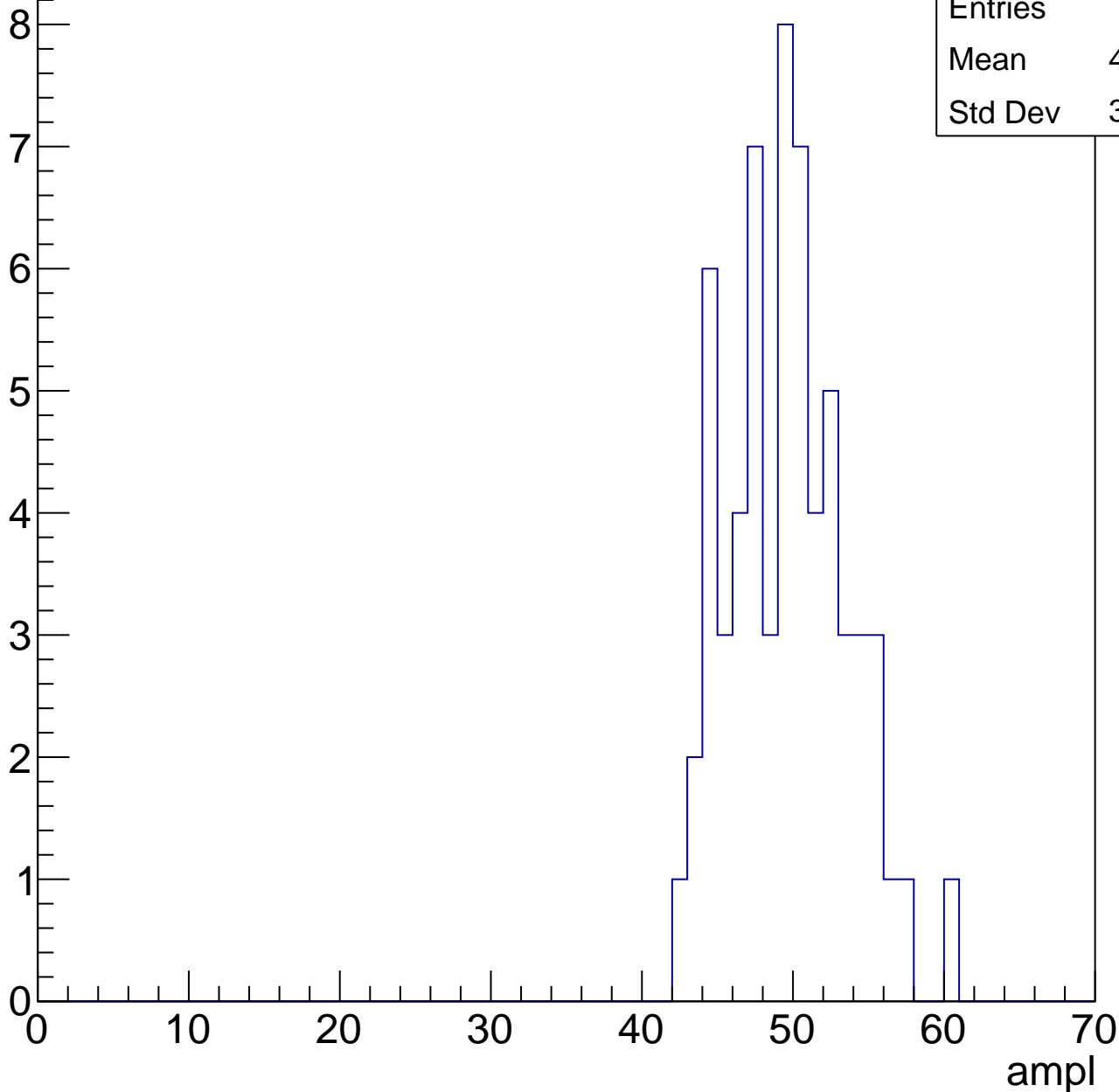


B1L102S, U6-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	49.18
Std Dev	3.846

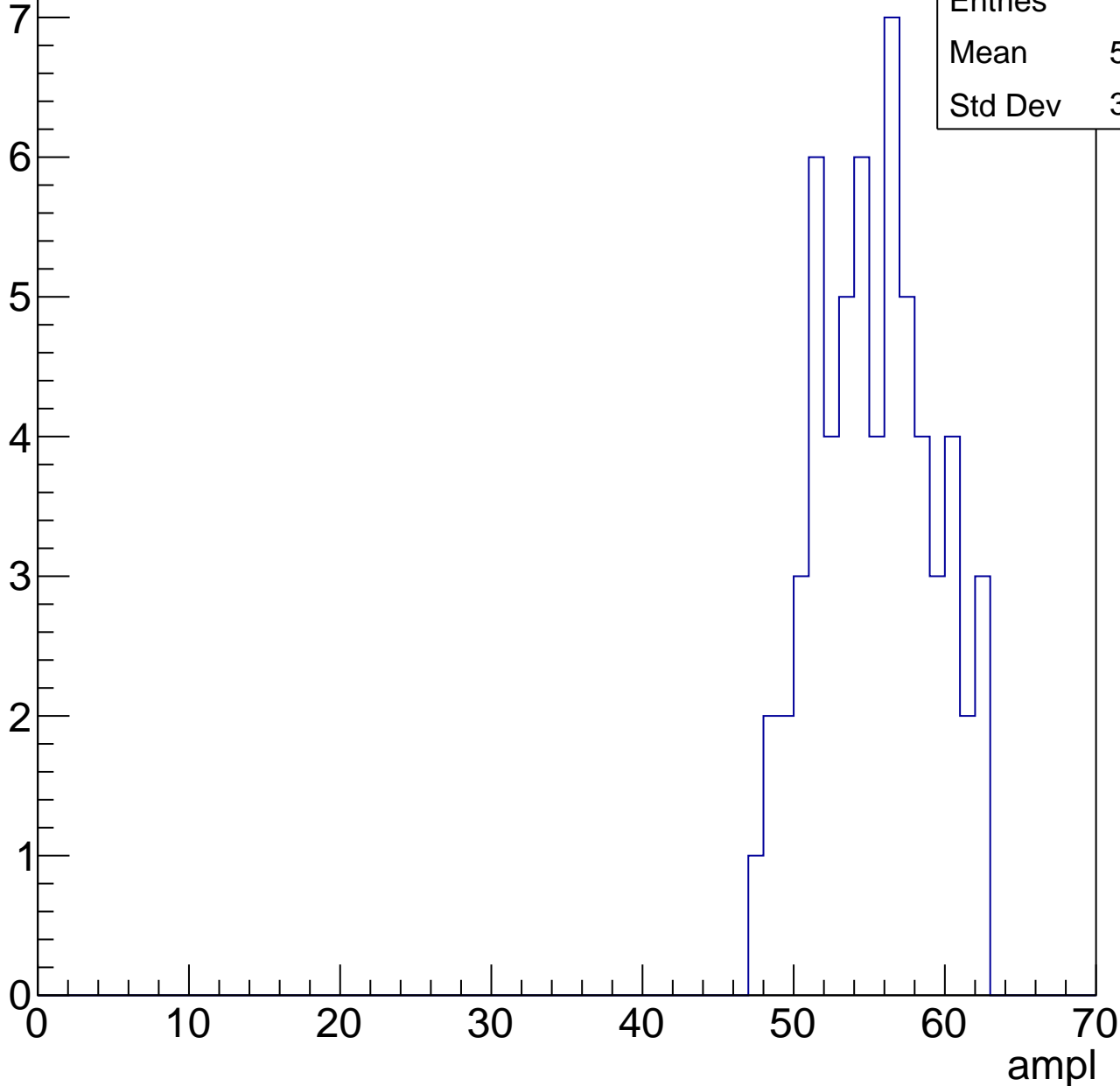


B1L102S, U6-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.89
Std Dev	3.833

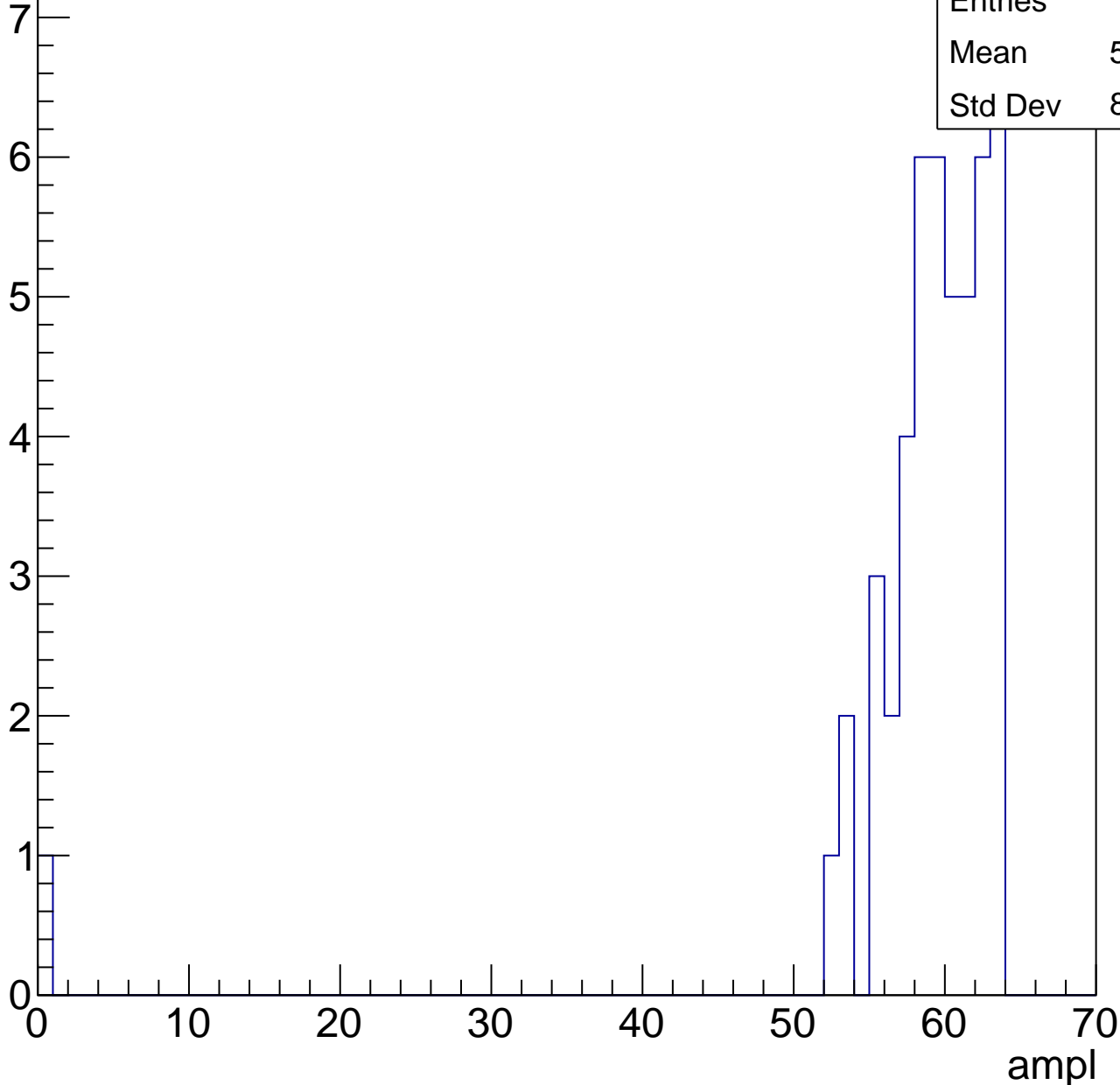


B1L102S, U6-ch60, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

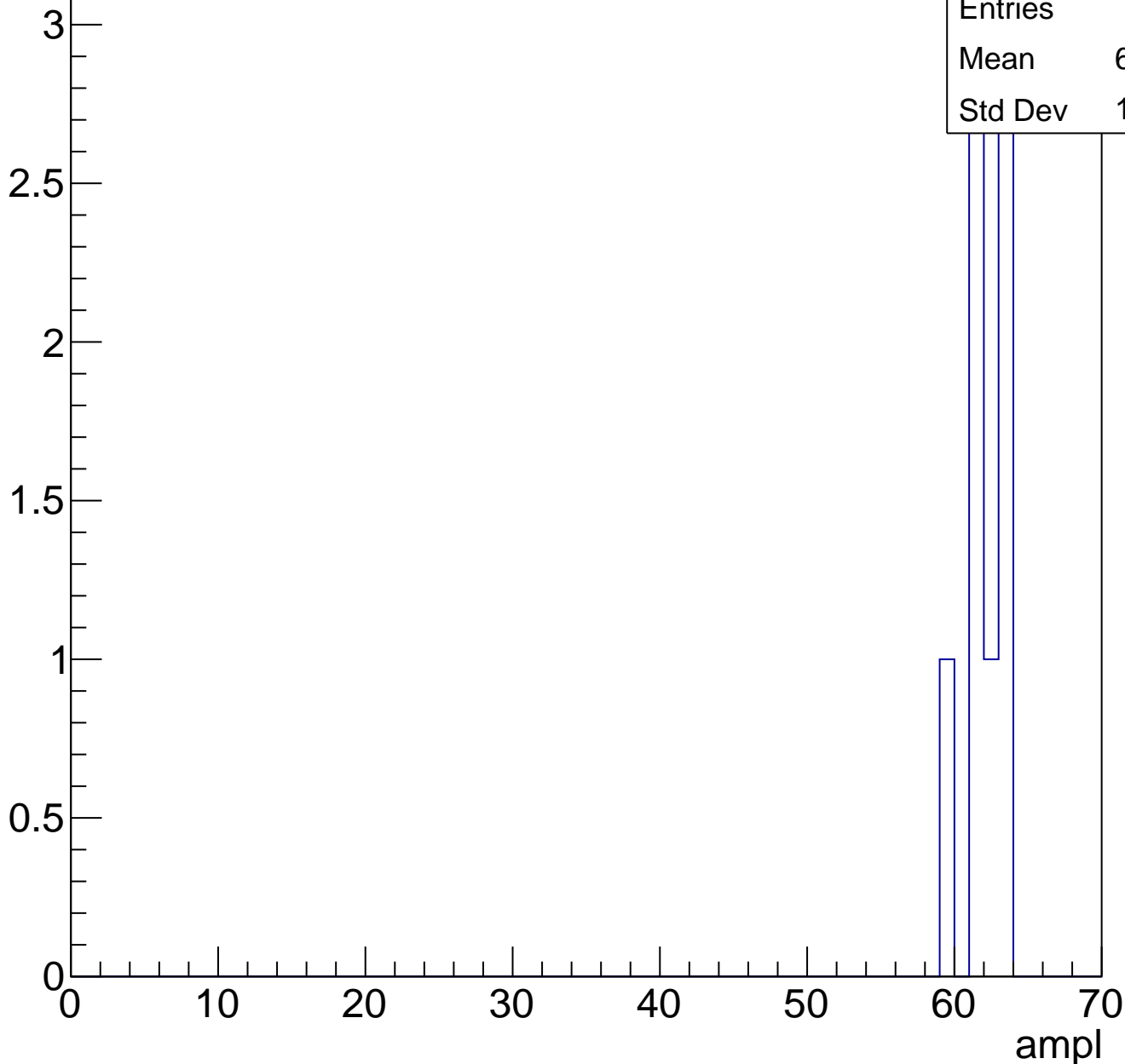
Entries	48
Mean	57.98
Std Dev	8.934



B1L102S, U6-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch61, adc0

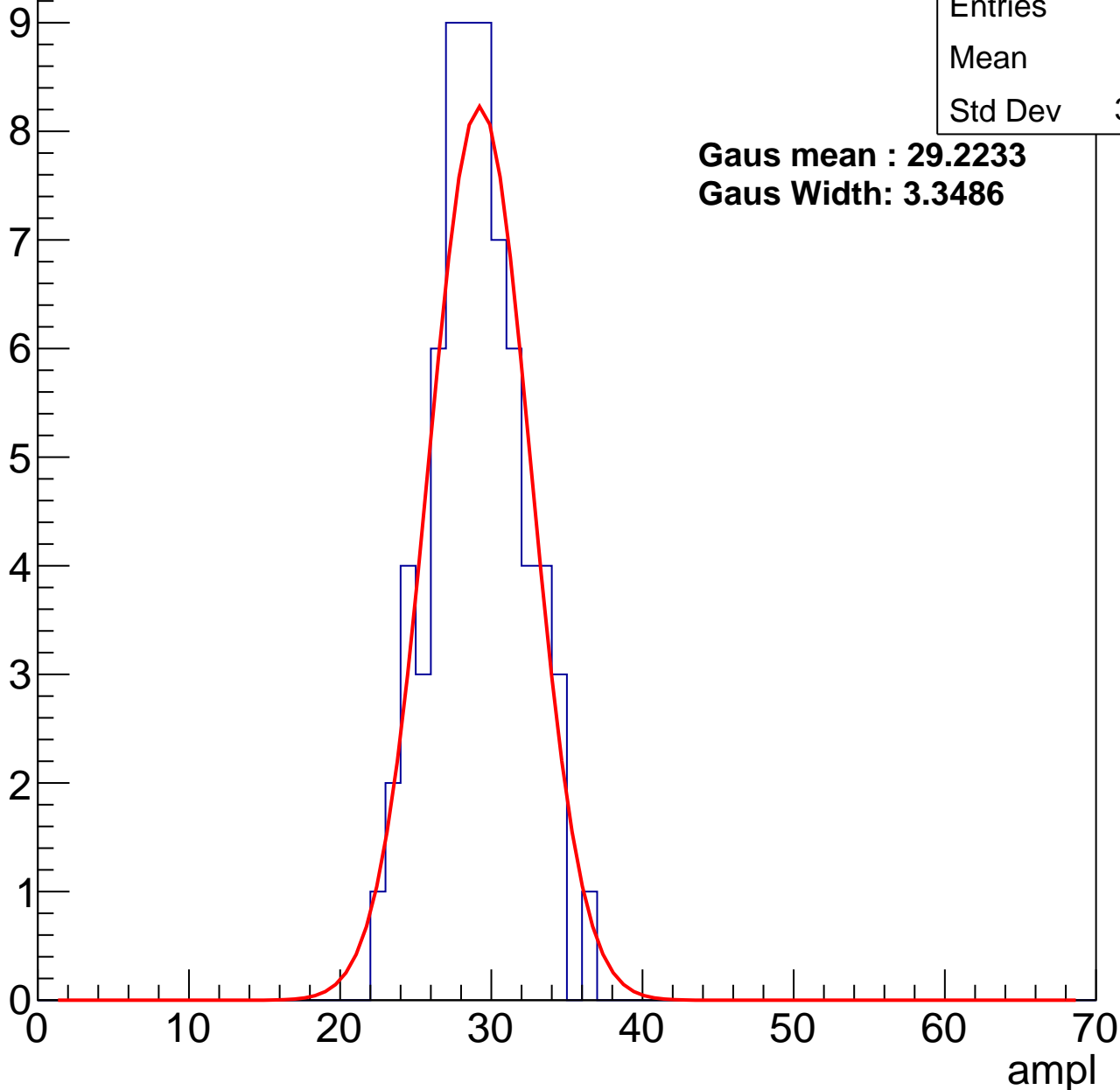
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	28.6
Std Dev	3.001

Gaus mean : 29.2233

Gaus Width: 3.3486



B1L102S, U6-ch61, adc1

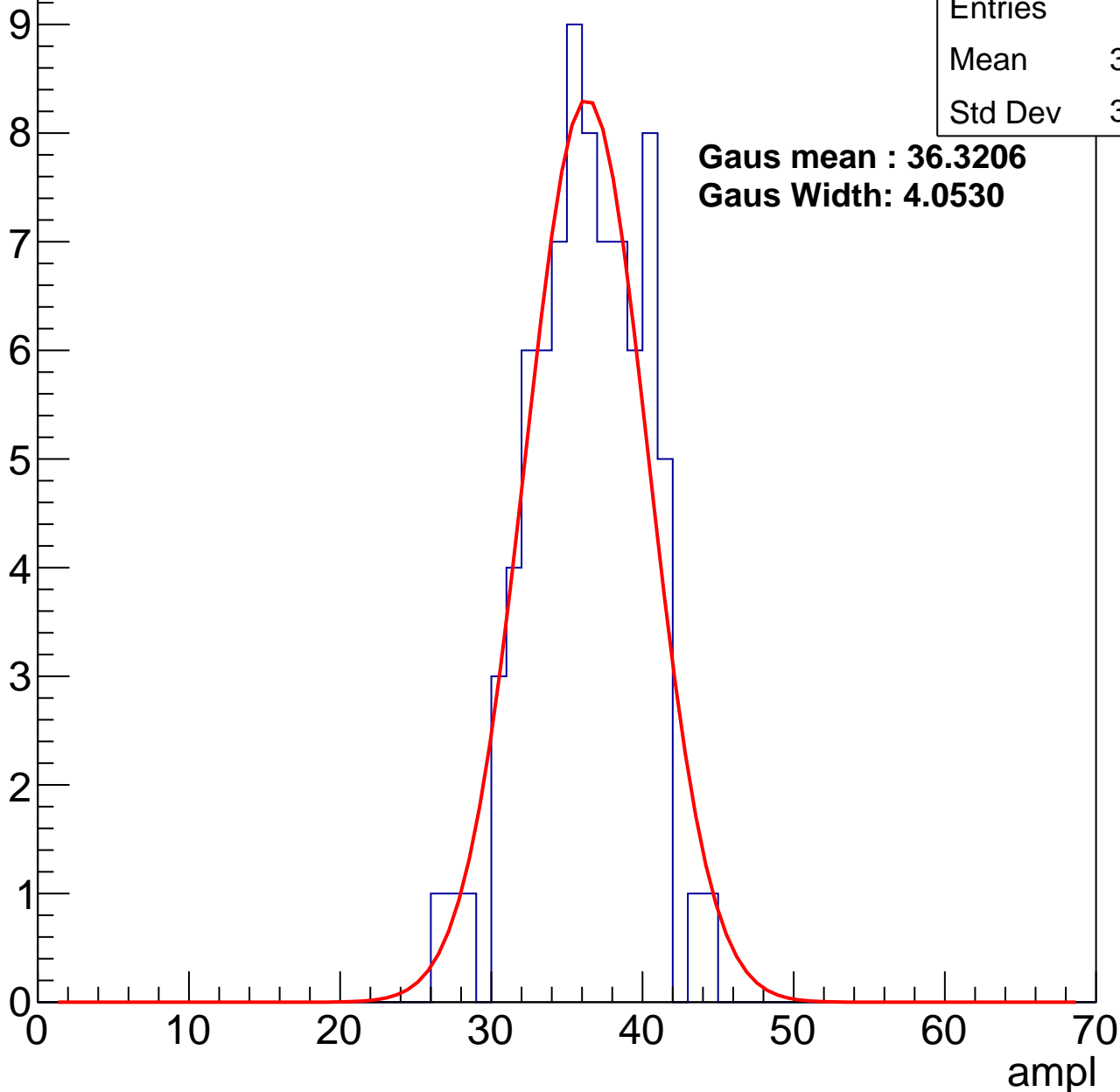
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	35.77
Std Dev	3.669

Gaus mean : 36.3206

Gaus Width: 4.0530



B1L102S, U6-ch61, adc2

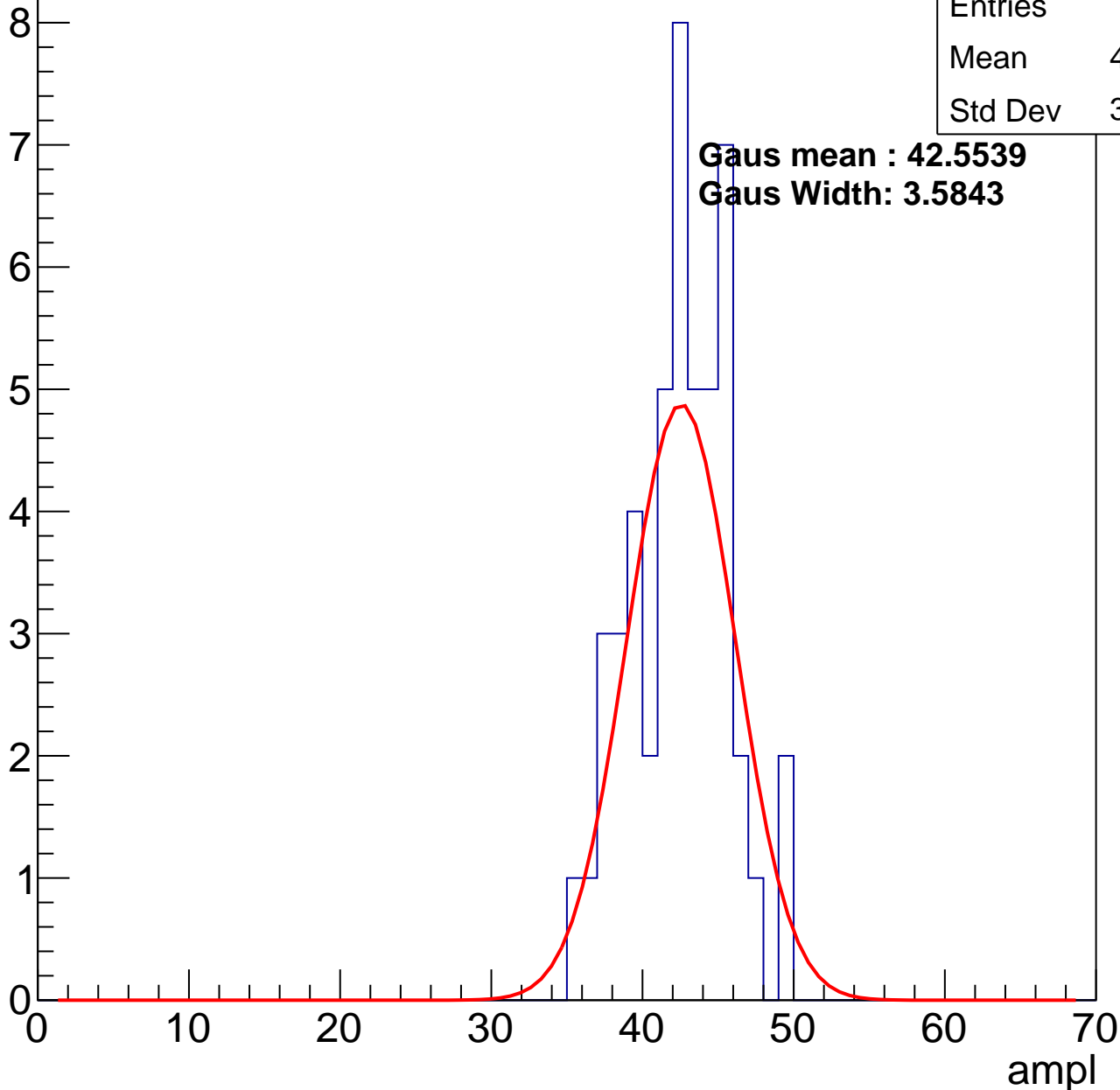
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	42.04
Std Dev	3.194

Gaus mean : 42.5539

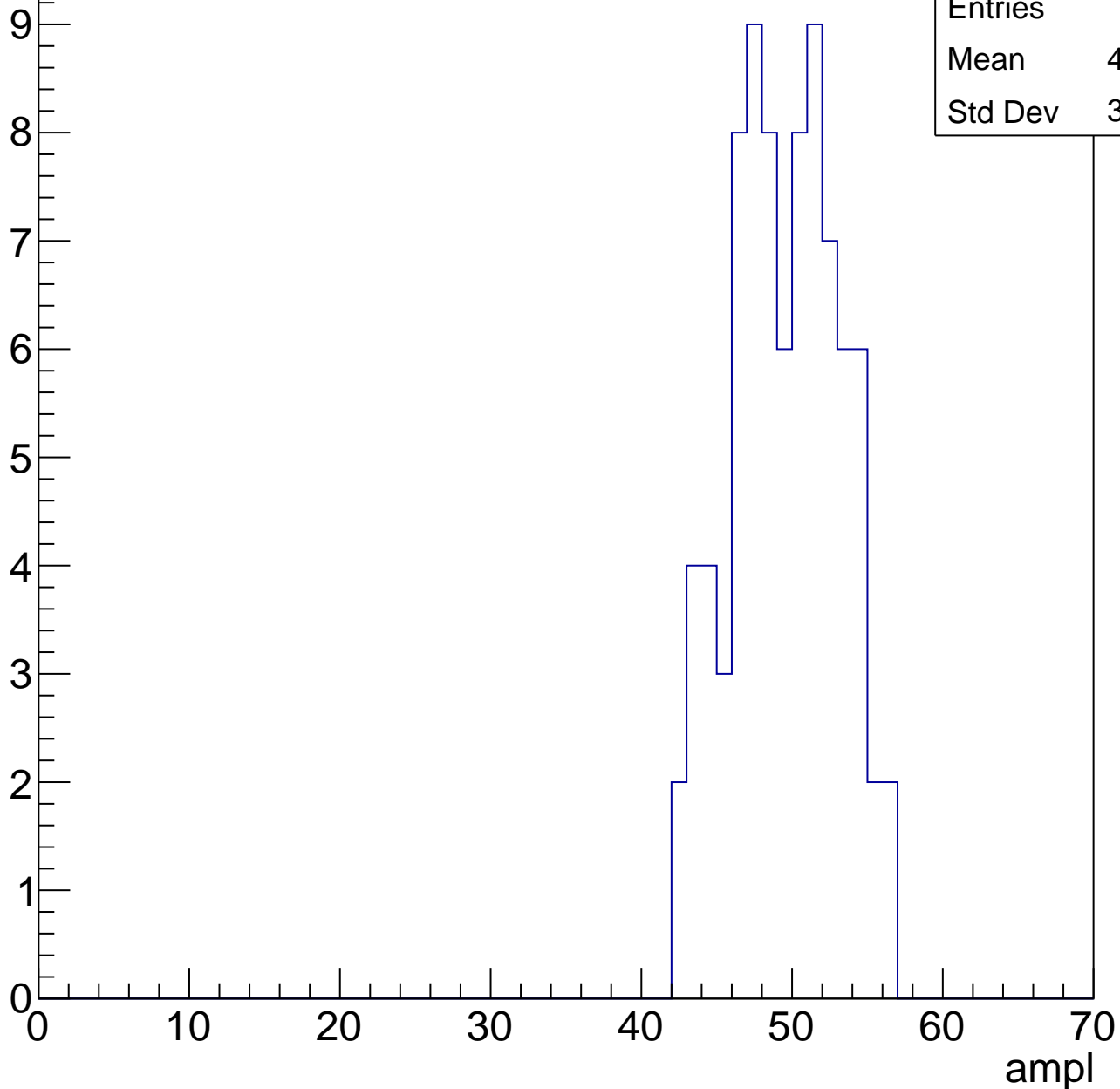
Gaus Width: 3.5843



B1L102S, U6-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

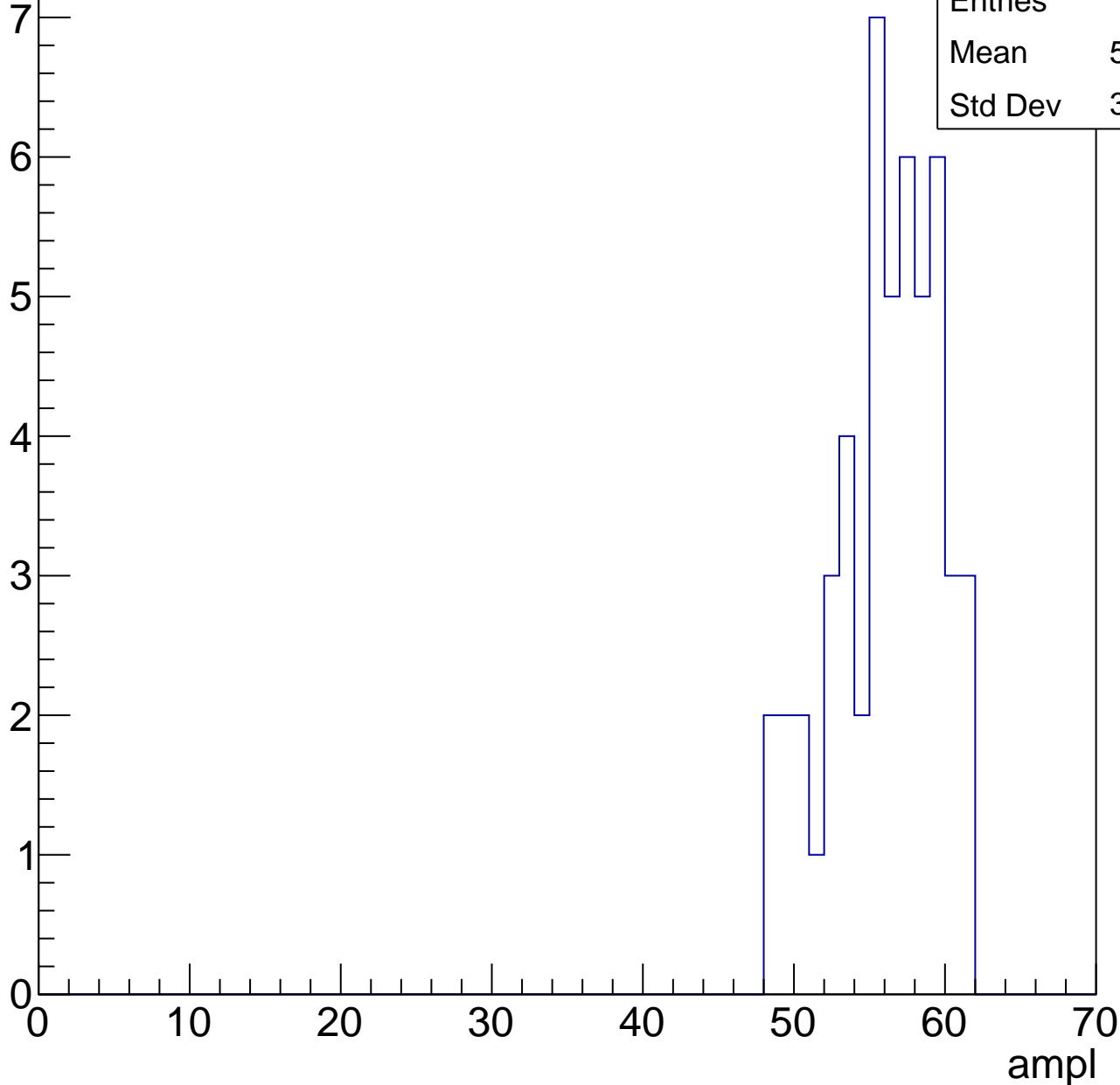


Entries	84
Mean	49.08
Std Dev	3.499

B1L102S, U6-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	51
Mean	55.59
Std Dev	3.476

B1L102S, U6-ch61, adc5

calib_packv5_042523_0143.root, FC#11, port A2

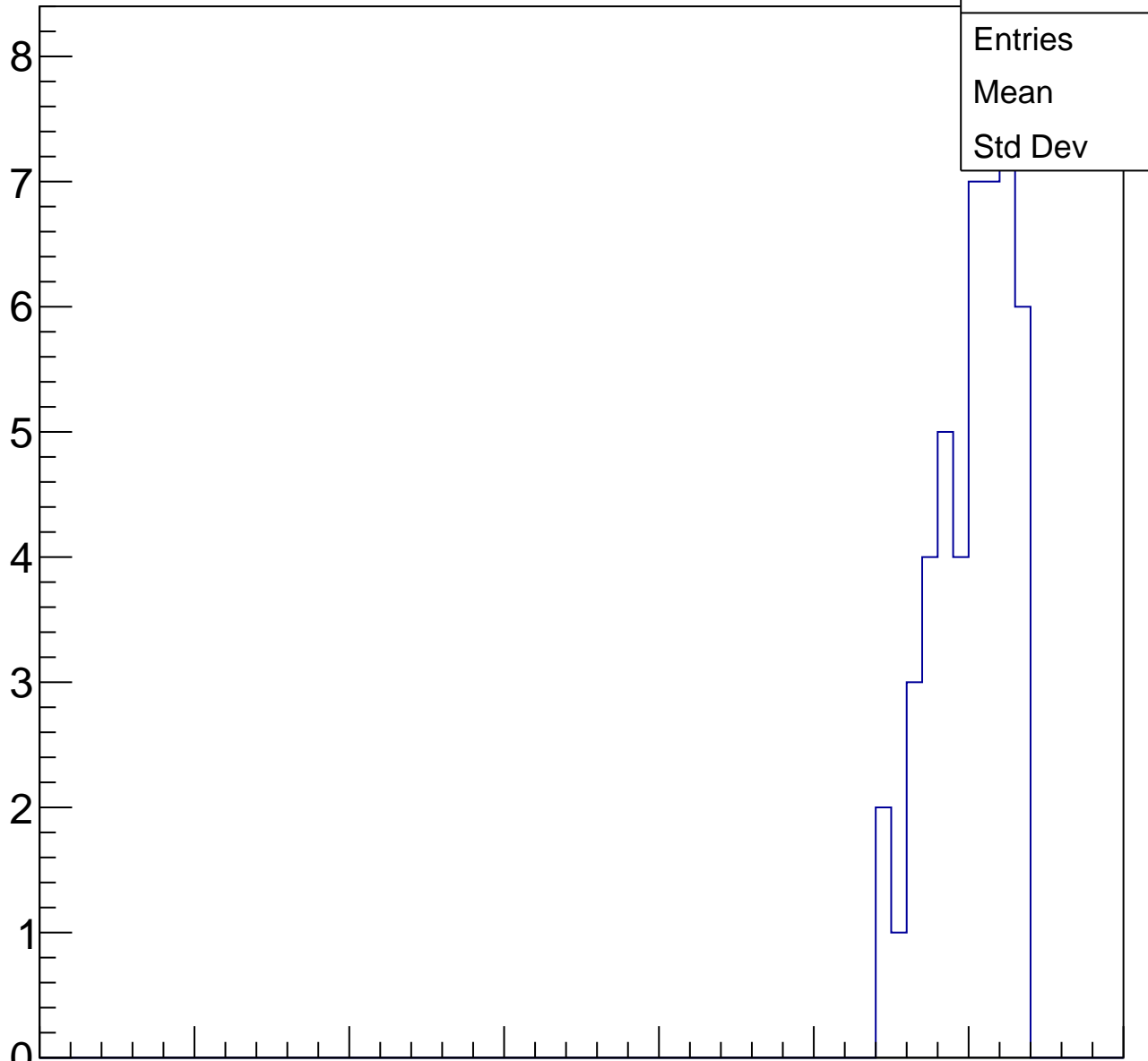
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.7
Std Dev	2.5

ampl

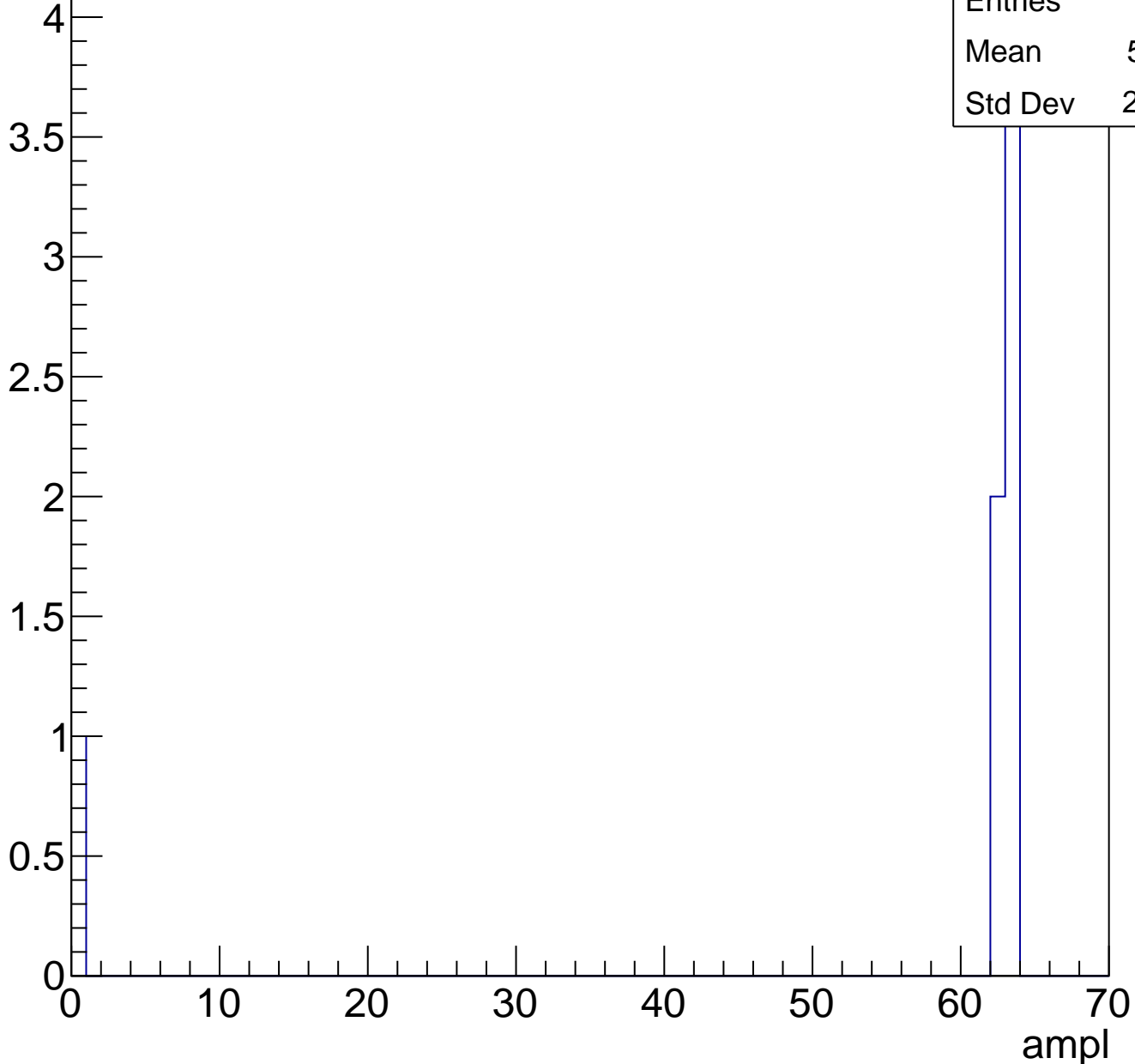
0 10 20 30 40 50 60 70



B1L102S, U6-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	53.71
Std Dev	21.93

B1L102S, U6-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch62, adc0

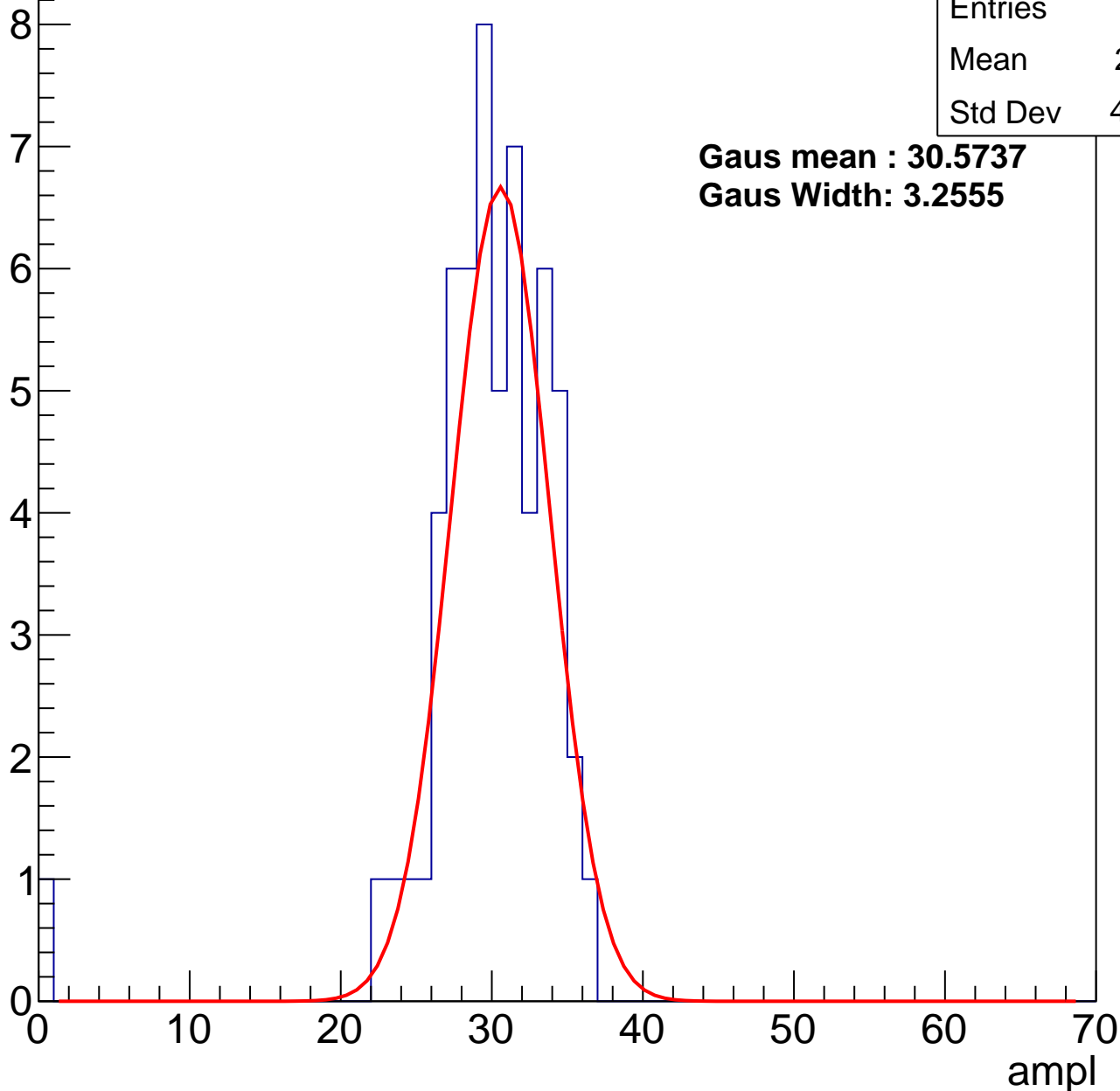
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.31
Std Dev	4.938

Gaus mean : 30.5737

Gaus Width: 3.2555



B1L102S, U6-ch62, adc1

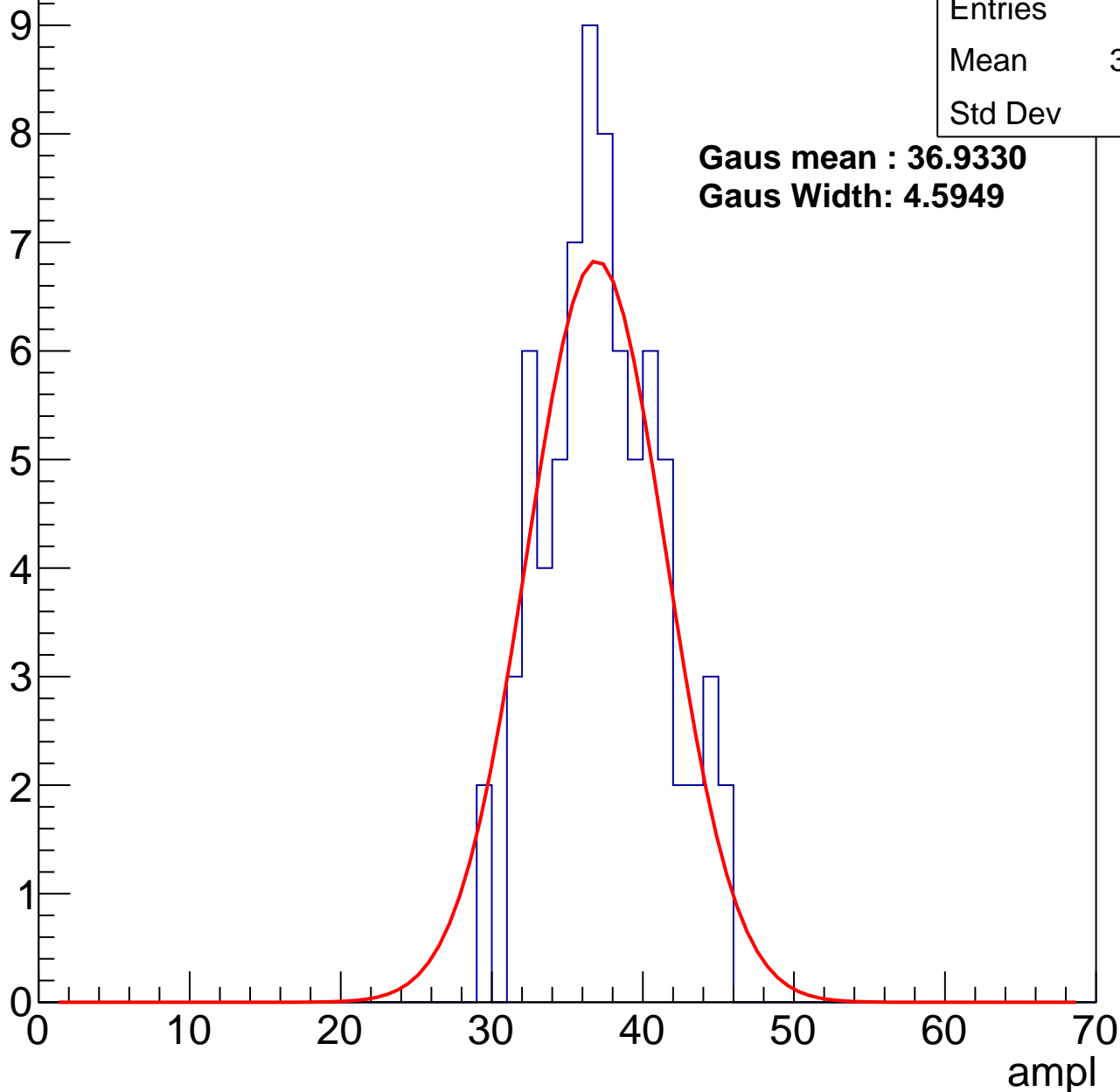
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	36.93
Std Dev	3.81

Gaus mean : 36.9330

Gaus Width: 4.5949



B1L102S, U6-ch62, adc2

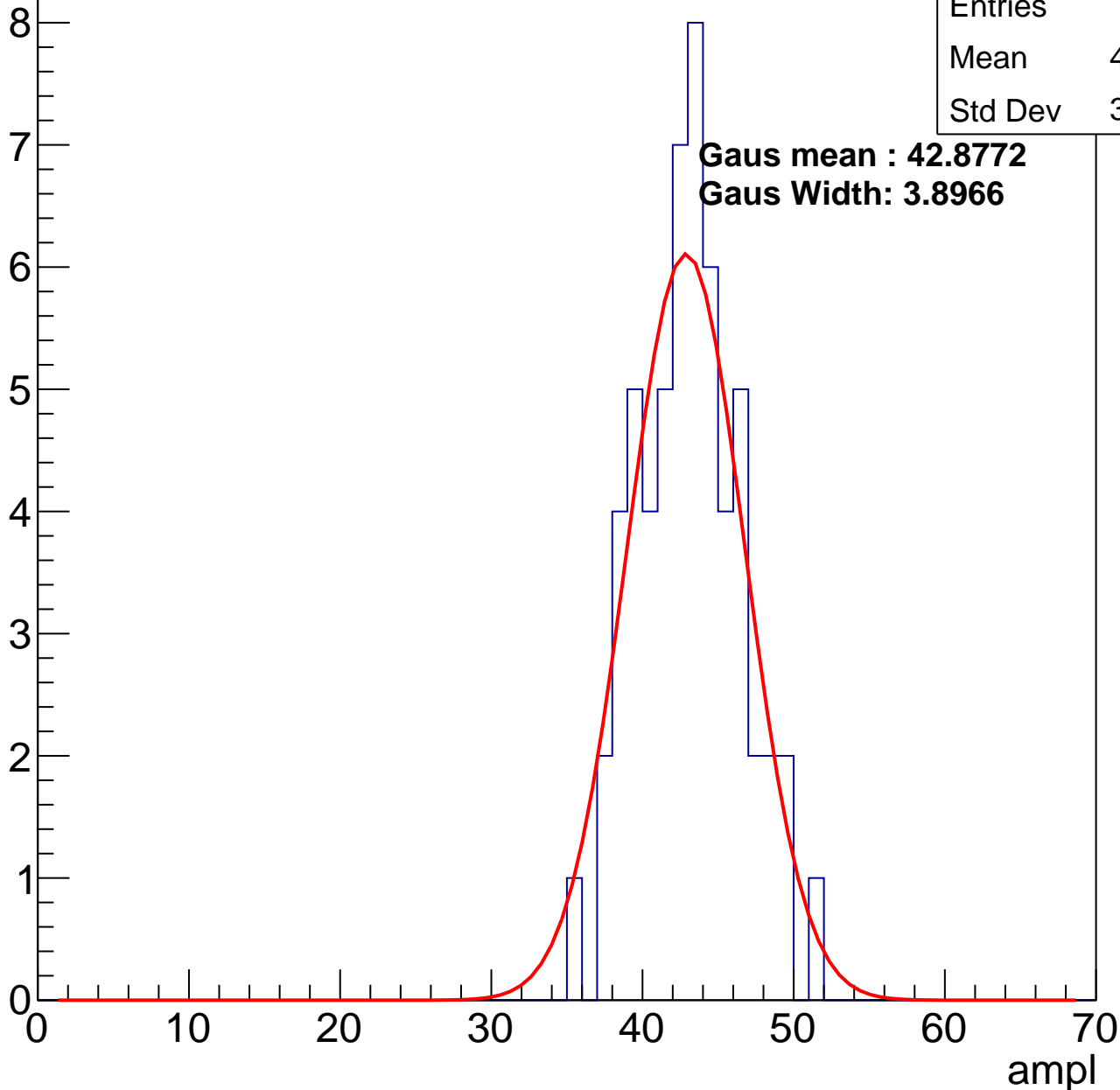
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.62
Std Dev	3.362

Gaus mean : 42.8772

Gaus Width: 3.8966

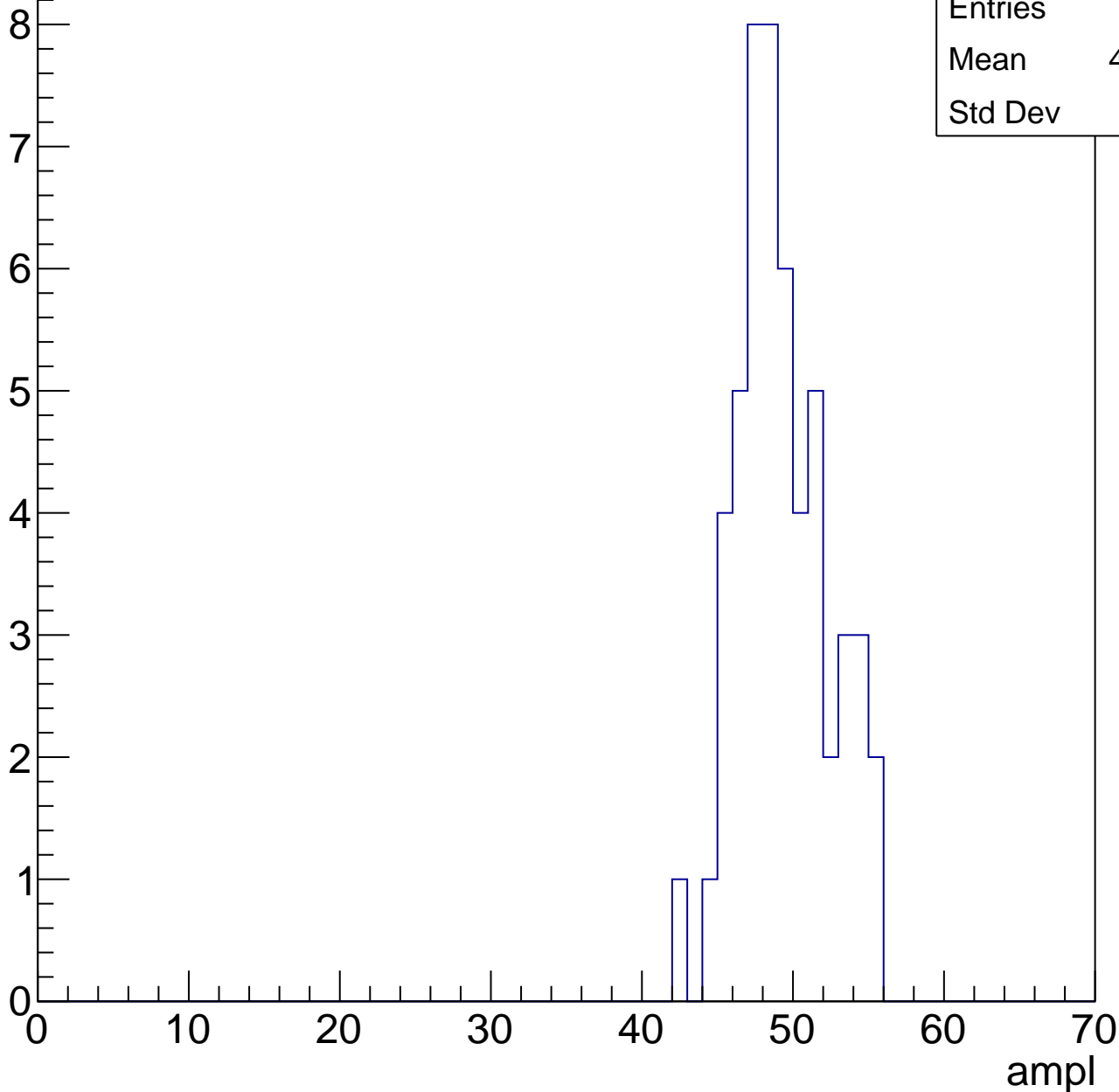


B1L102S, U6-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	48.85
Std Dev	2.97

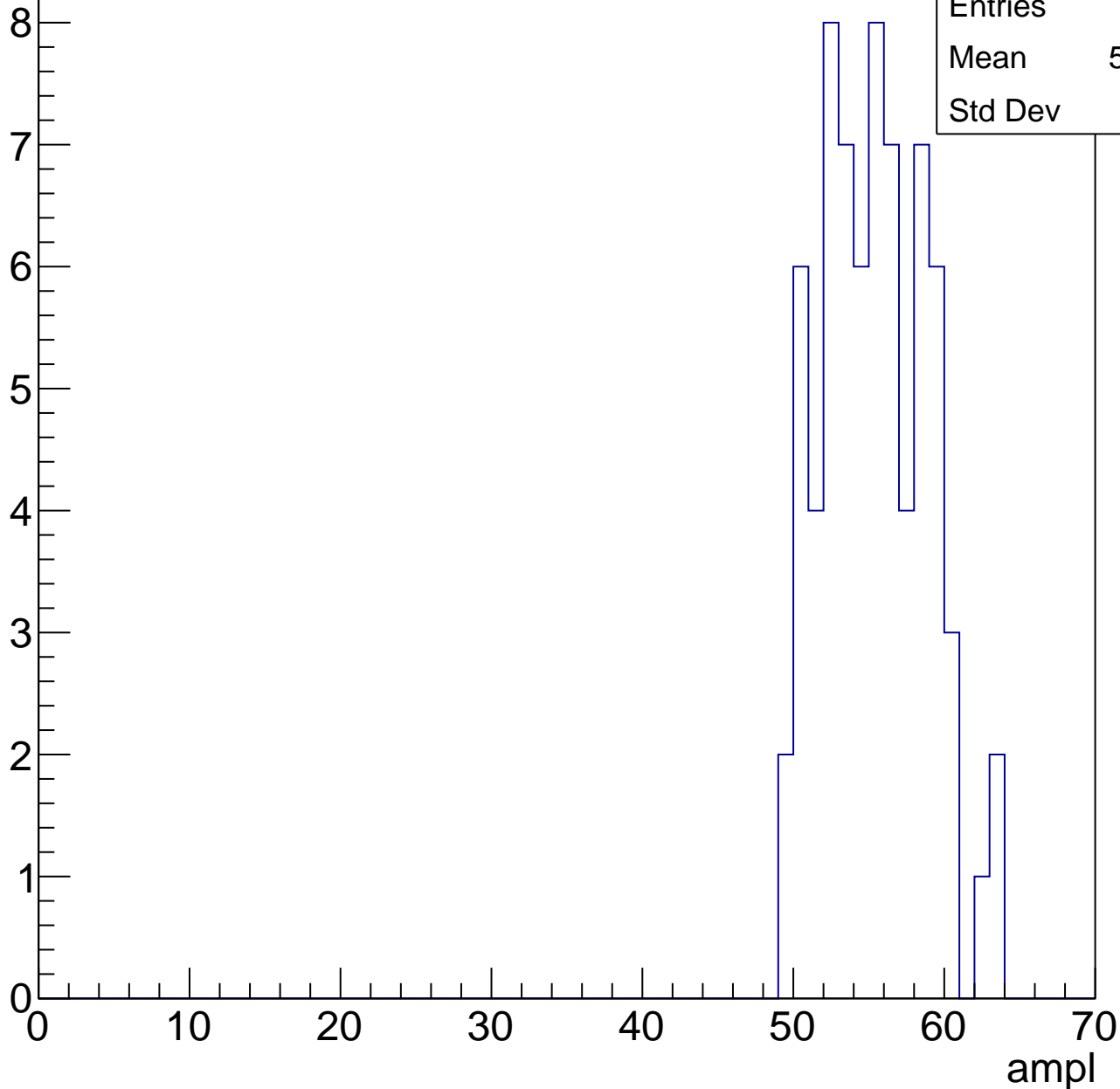


B1L102S, U6-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.94
Std Dev	3.41

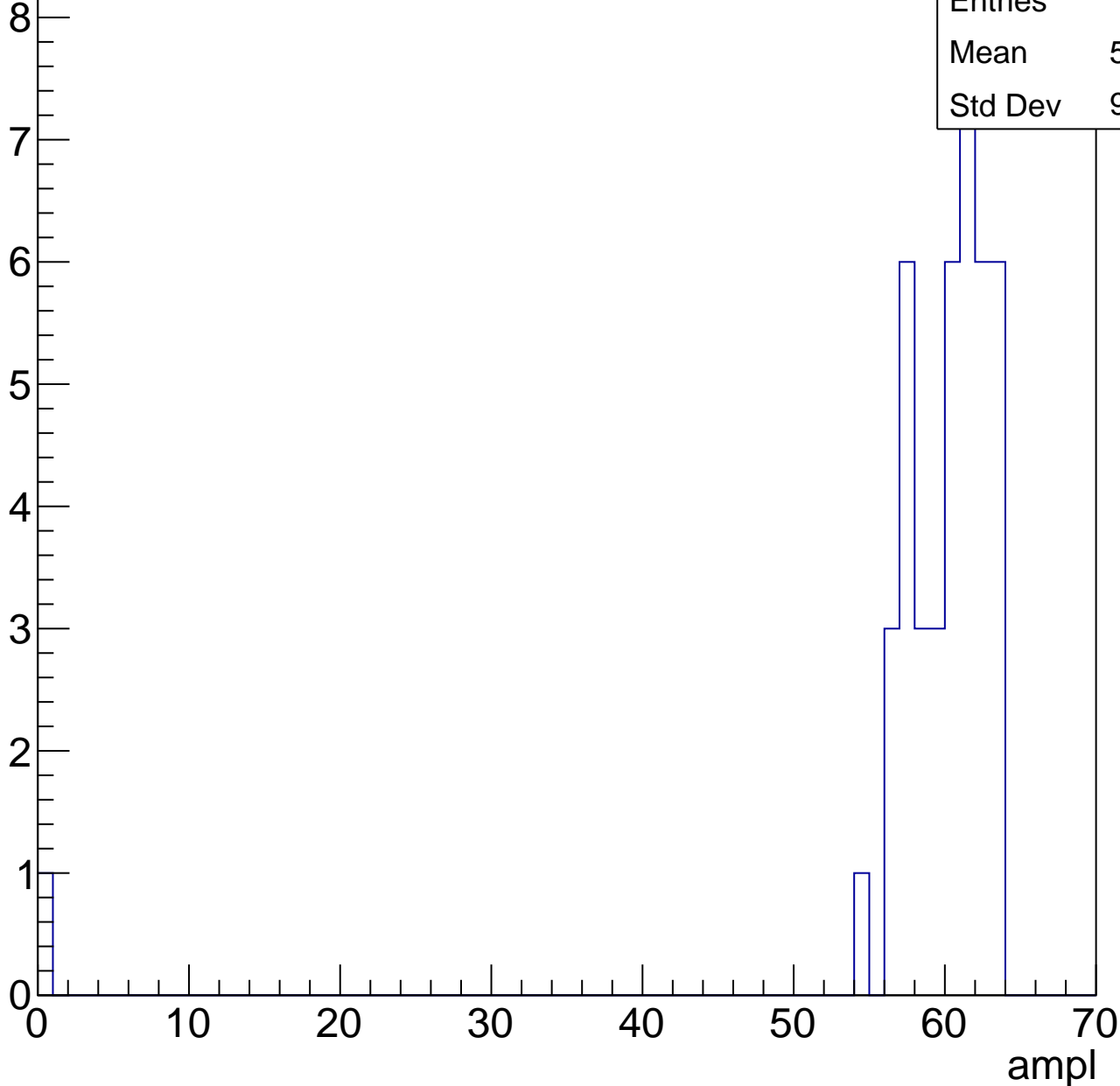


B1L102S, U6-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

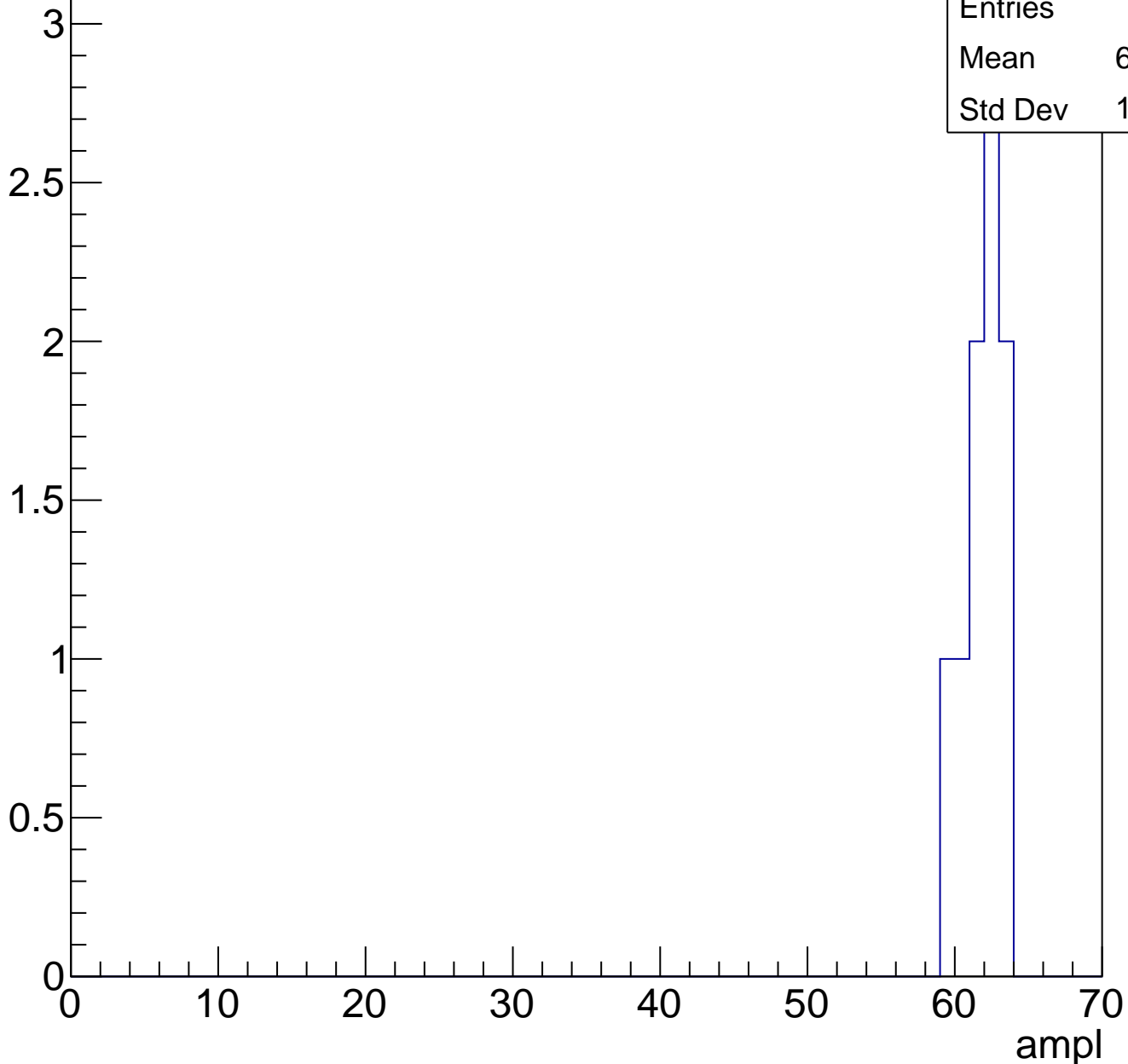
Entries	43
Mean	58.44
Std Dev	9.319



B1L102S, U6-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	9
Mean	61.44
Std Dev	1.257

B1L102S, U6-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch63, adc0

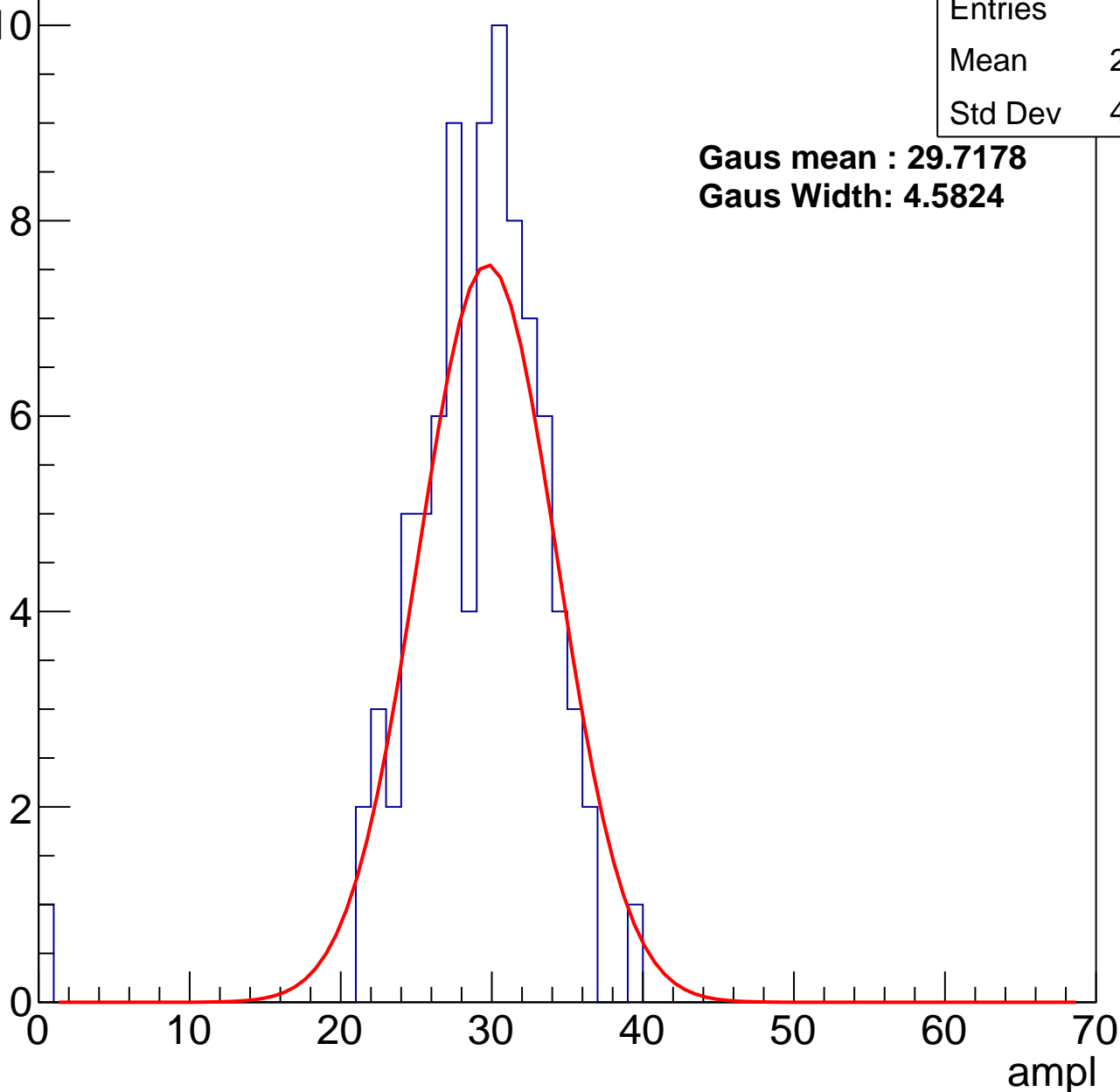
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	28.66
Std Dev	4.894

Gaus mean : 29.7178

Gaus Width: 4.5824



B1L102S, U6-ch63, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	36.81
Std Dev	2.789

Gaus mean : 37.7253

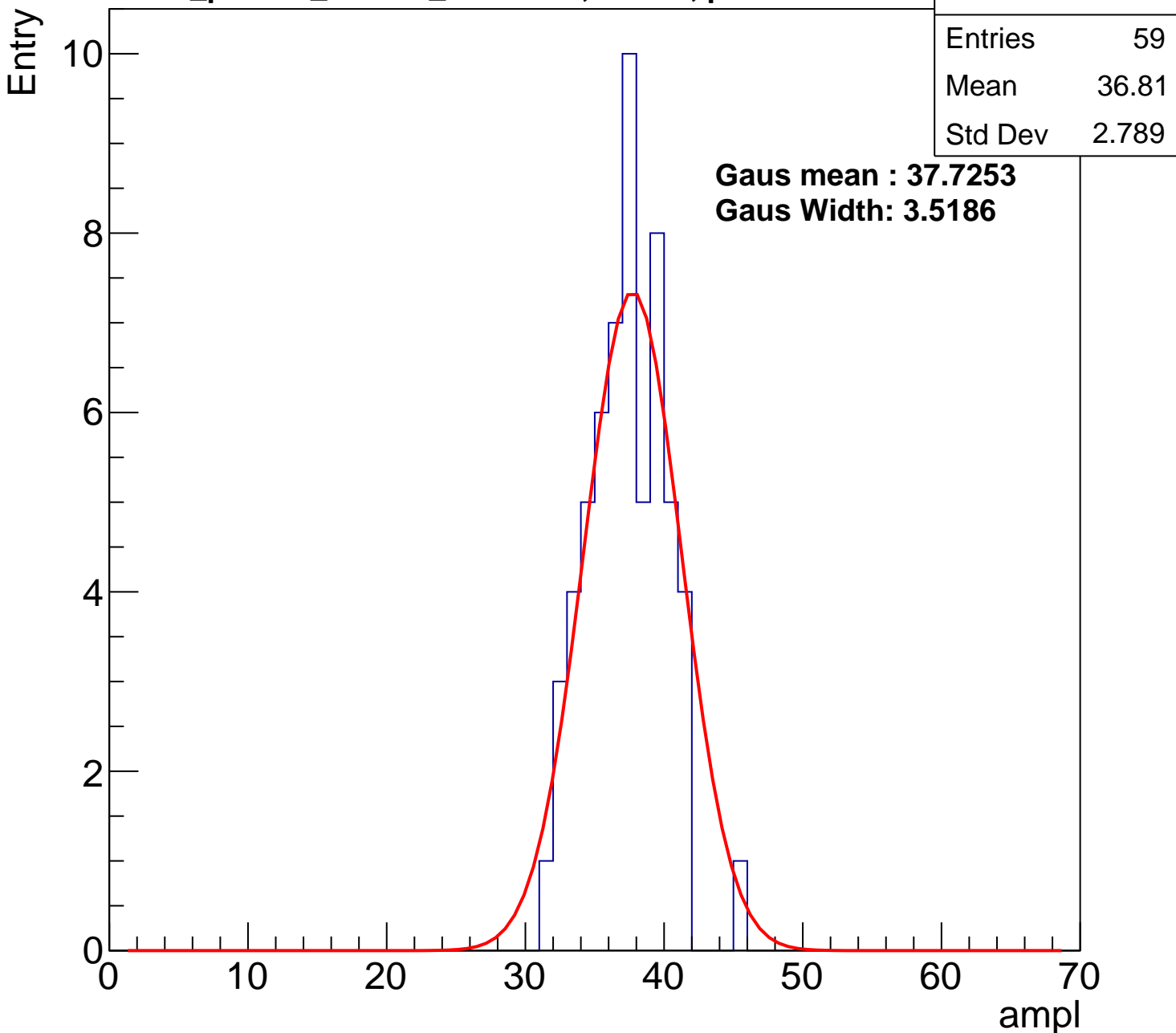
Gaus Width: 3.5186

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch63, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	71
Mean	43.28
Std Dev	3.596

Gaus mean : 44.3811

Gaus Width: 3.4239

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

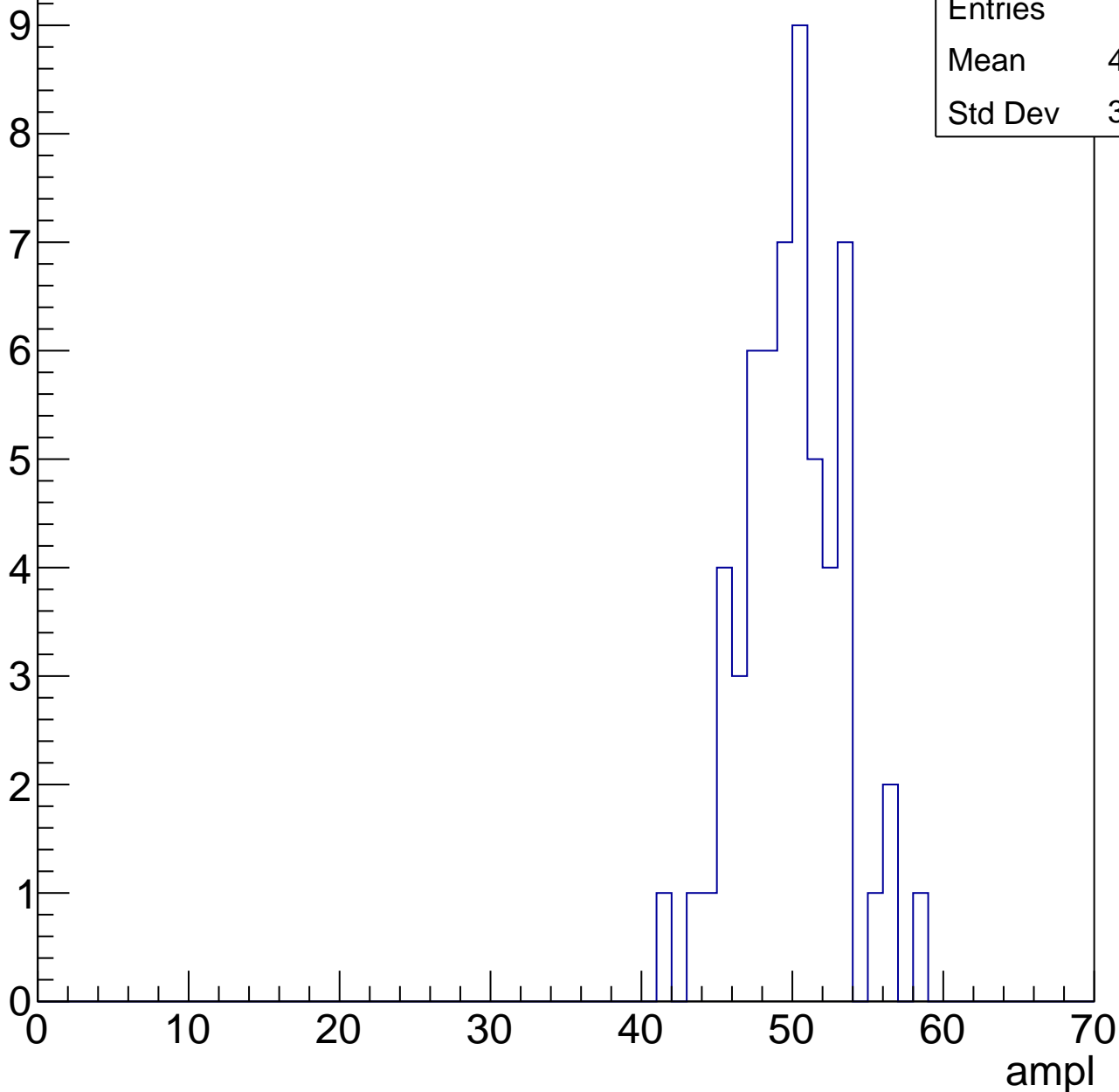
70

B1L102S, U6-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.45
Std Dev	3.286



B1L102S, U6-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

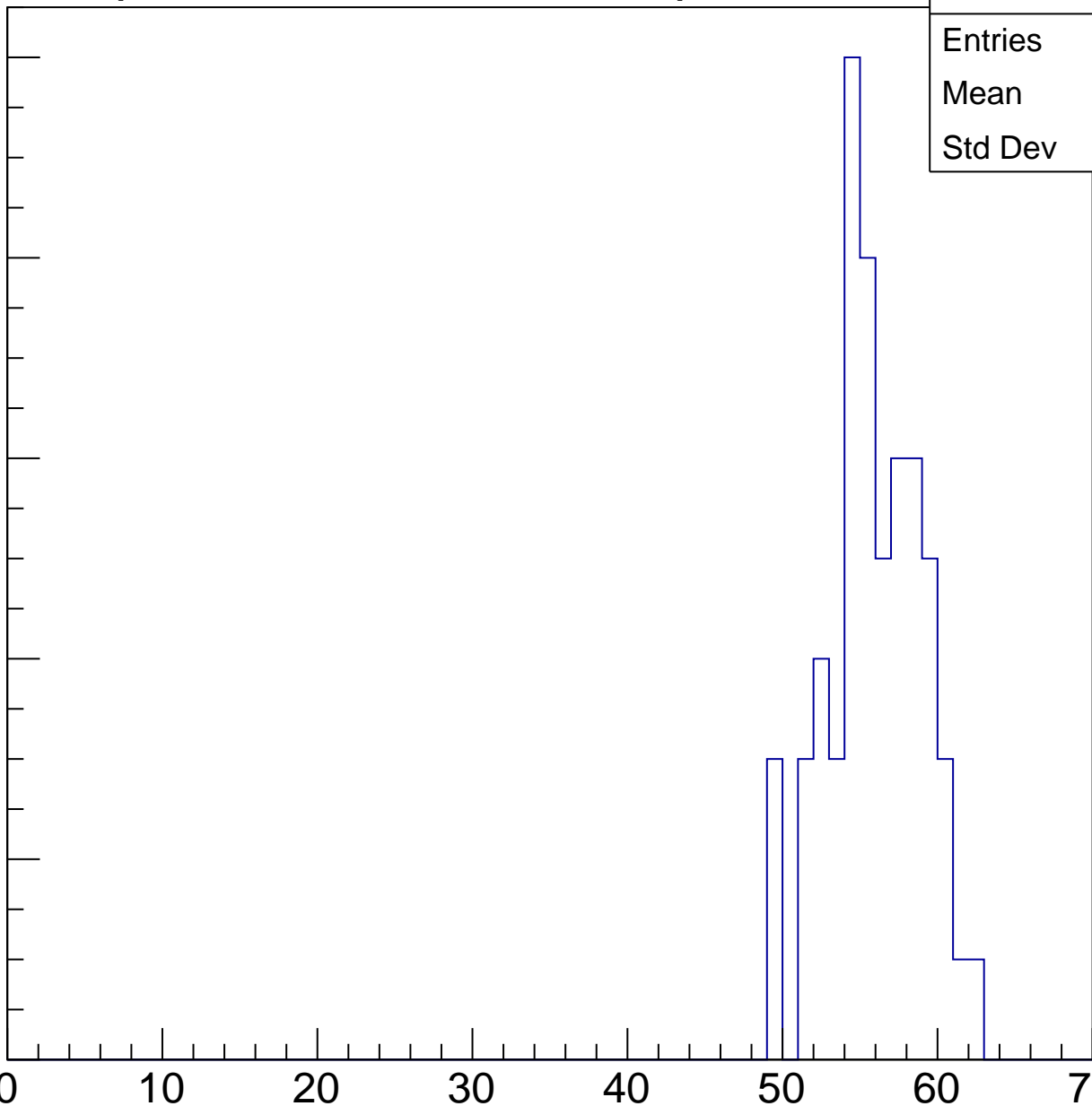
Entries	58
Mean	55.43
Std Dev	3.012

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

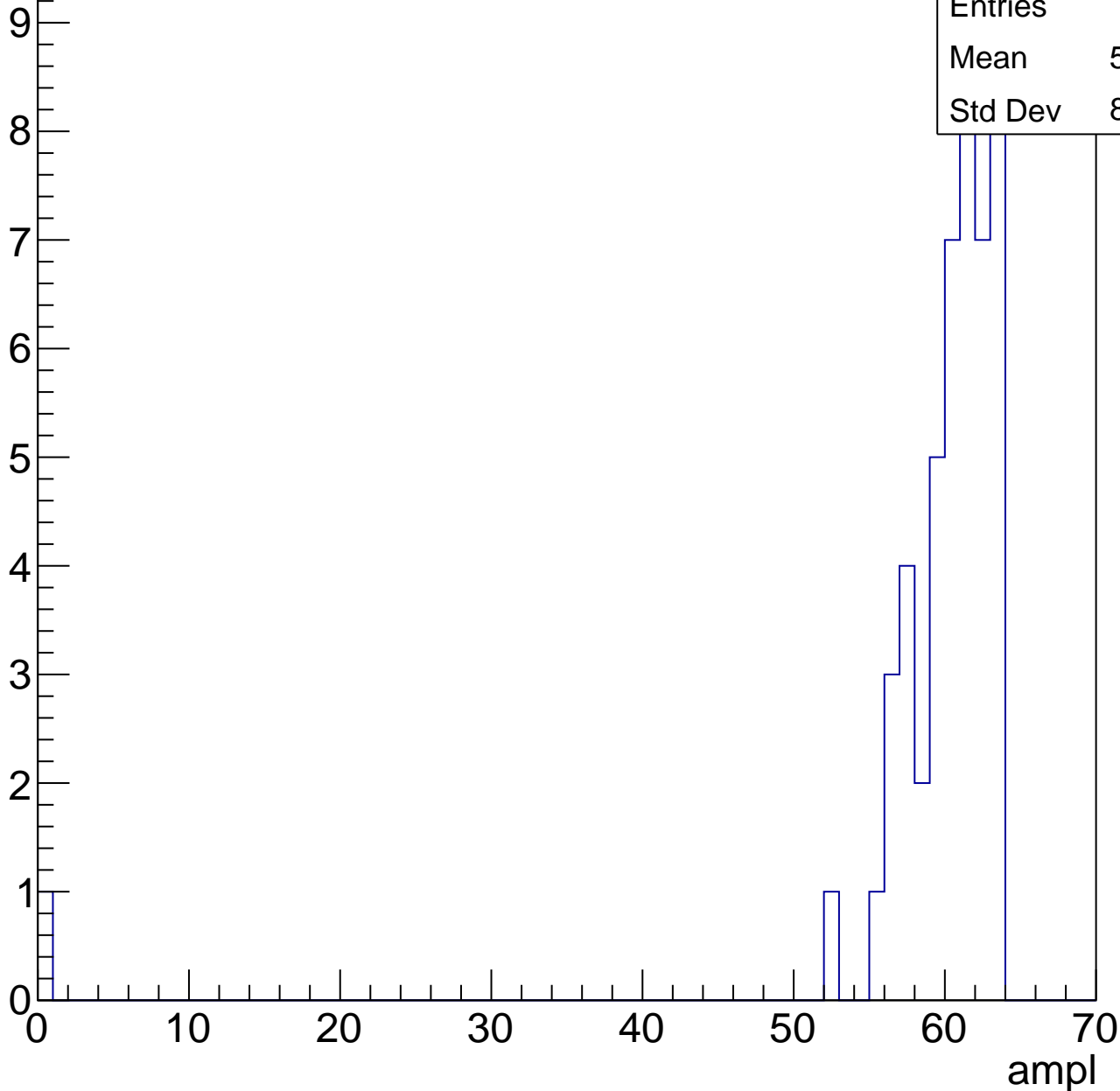


B1L102S, U6-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

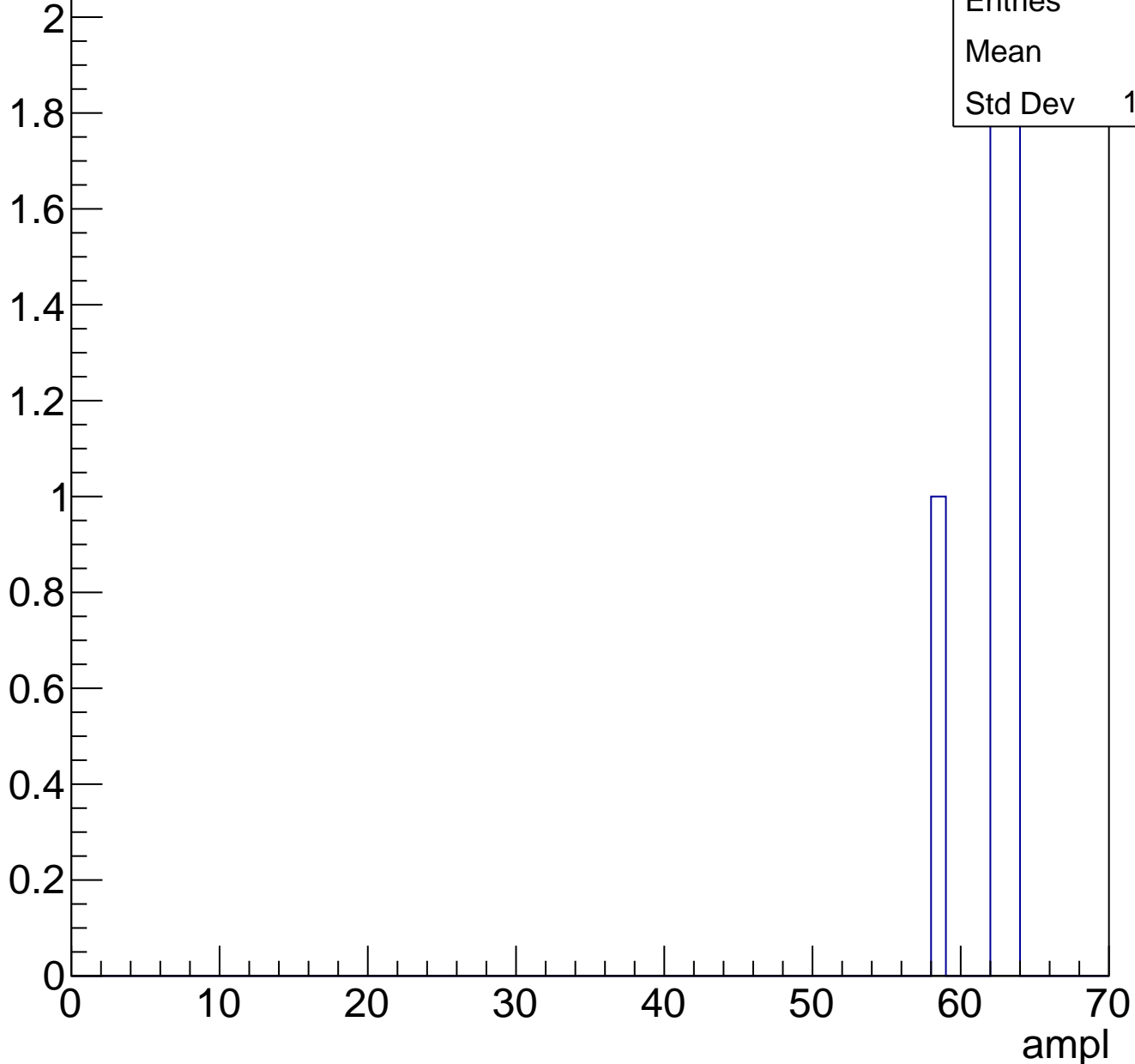
Entries	48
Mean	58.77
Std Dev	8.924



B1L102S, U6-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch64, adc0

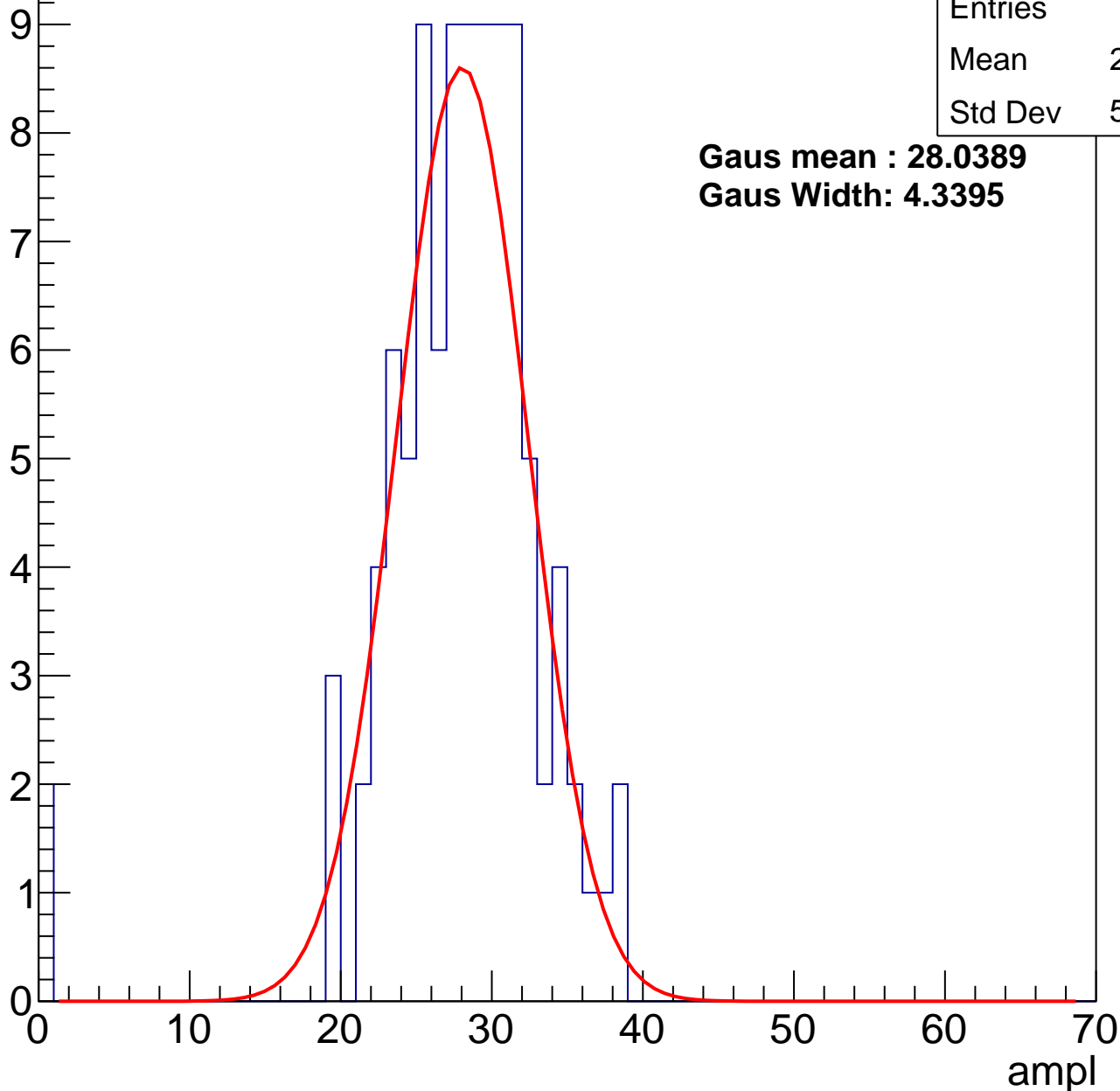
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	99
Mean	27.39
Std Dev	5.689

Gaus mean : 28.0389

Gaus Width: 4.3395



B1L102S, U6-ch64, adc1

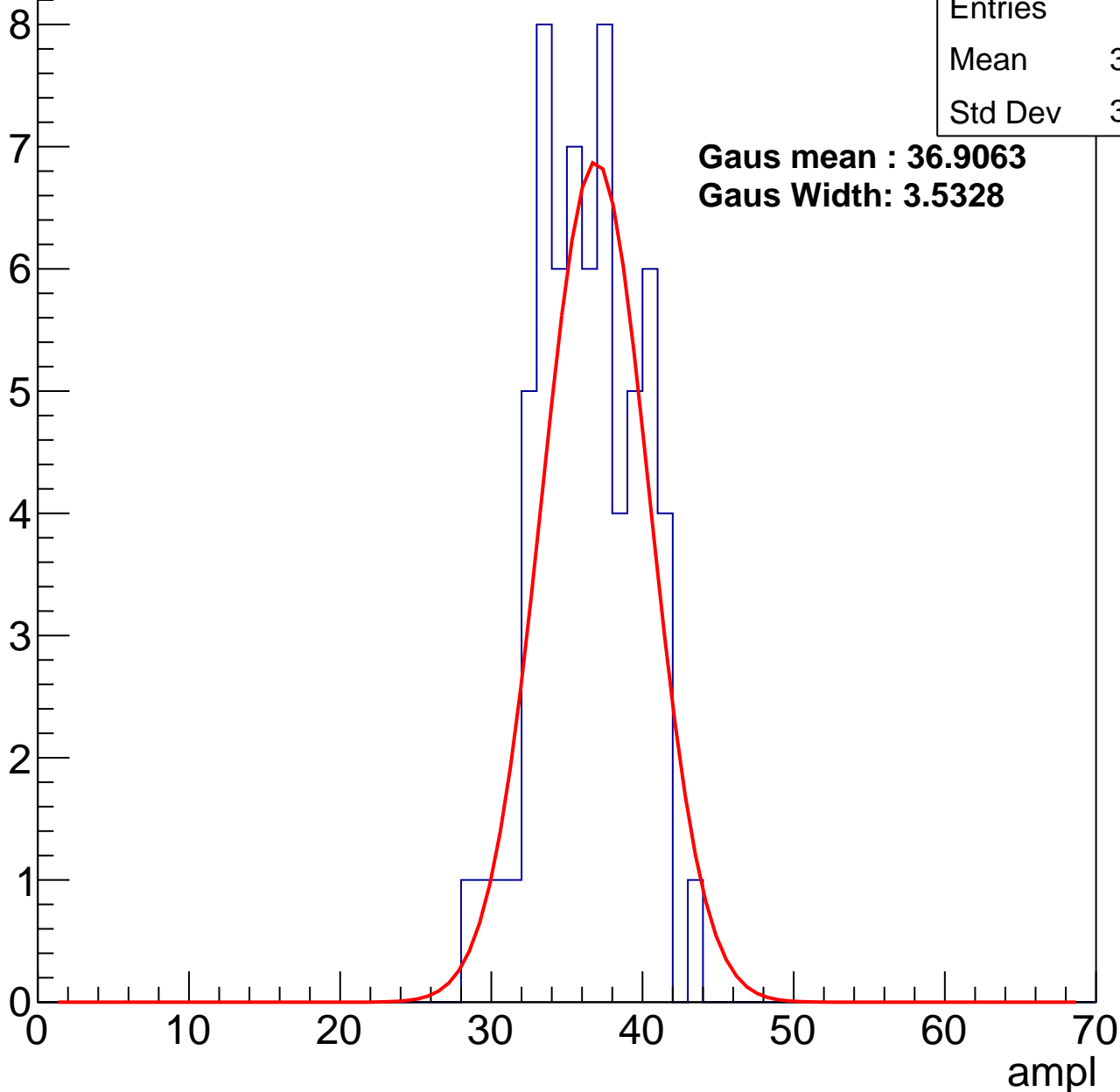
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.89
Std Dev	3.236

Gaus mean : 36.9063

Gaus Width: 3.5328



B1L102S, U6-ch64, adc2

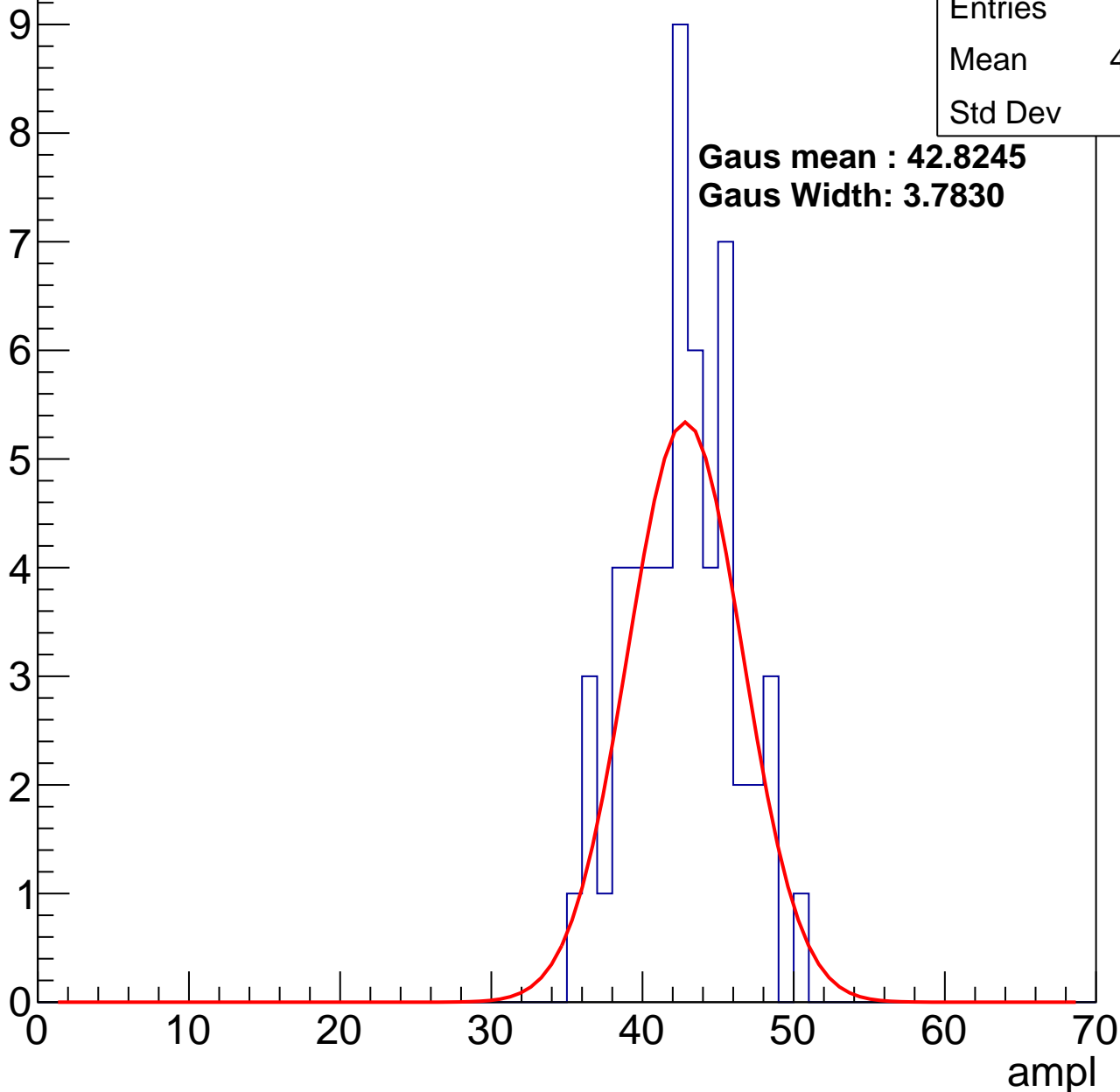
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.16
Std Dev	3.41

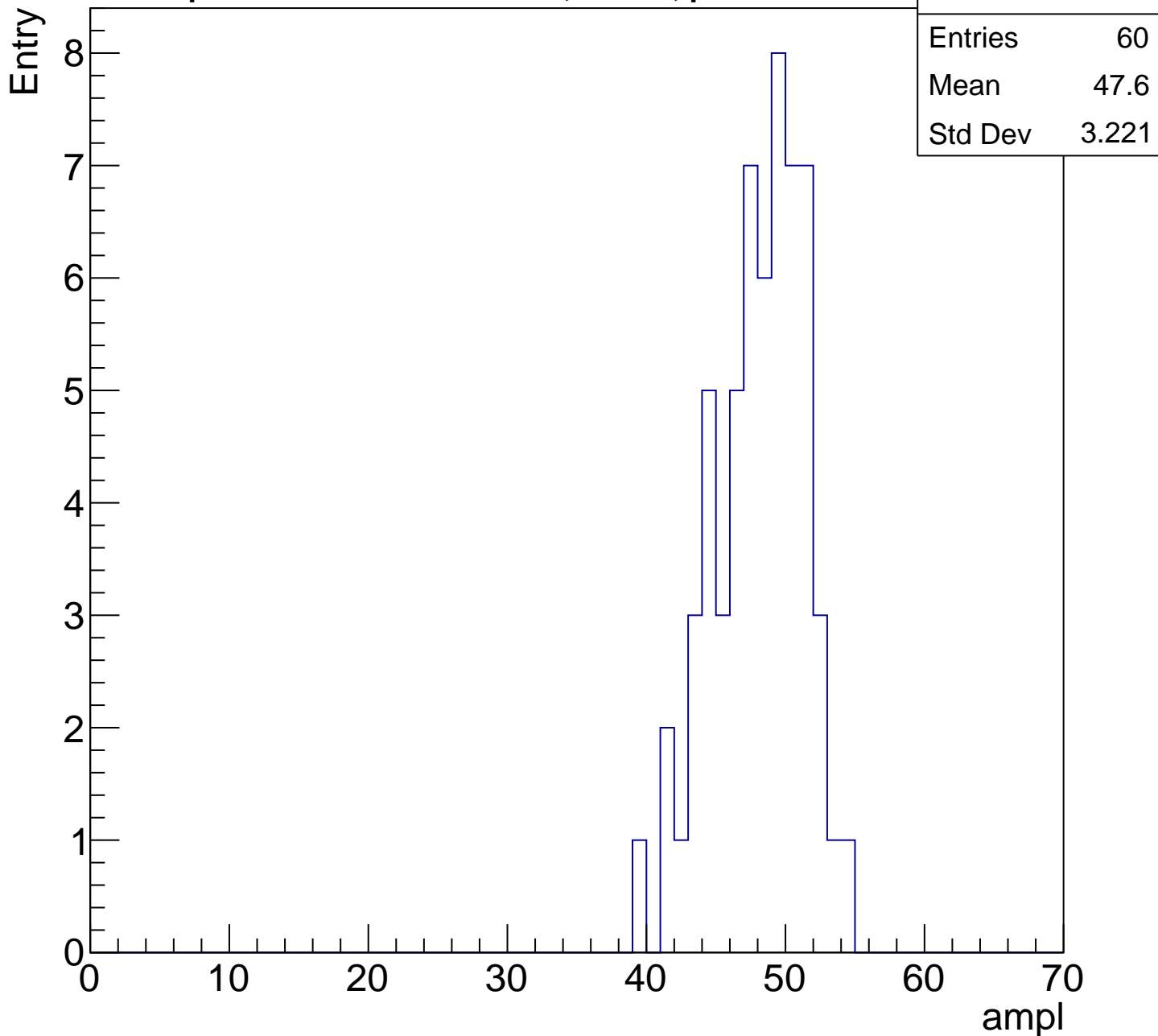
Gaus mean : 42.8245

Gaus Width: 3.7830



B1L102S, U6-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

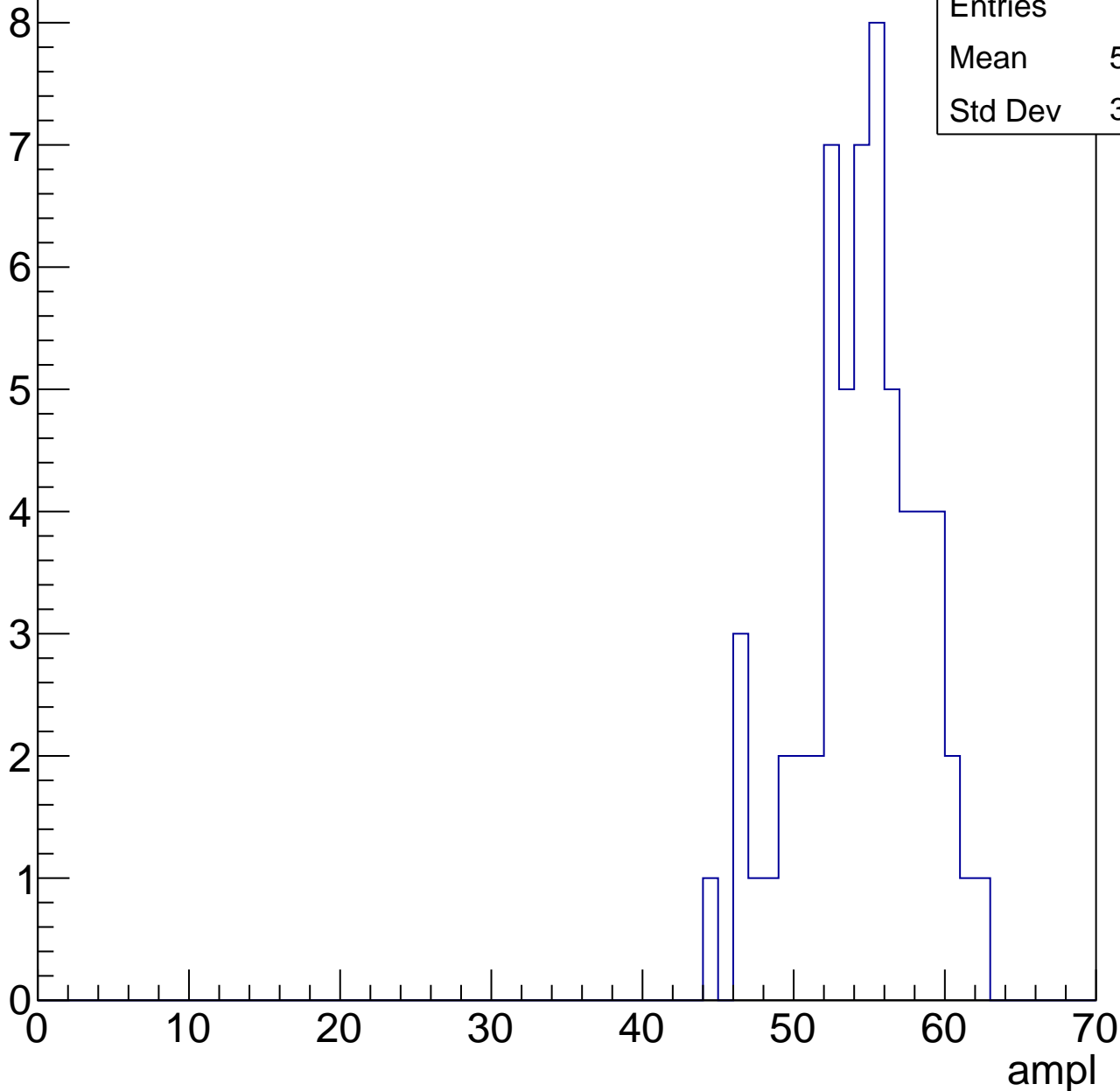


B1L102S, U6-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.05
Std Dev	3.922



B1L102S, U6-ch64, adc5

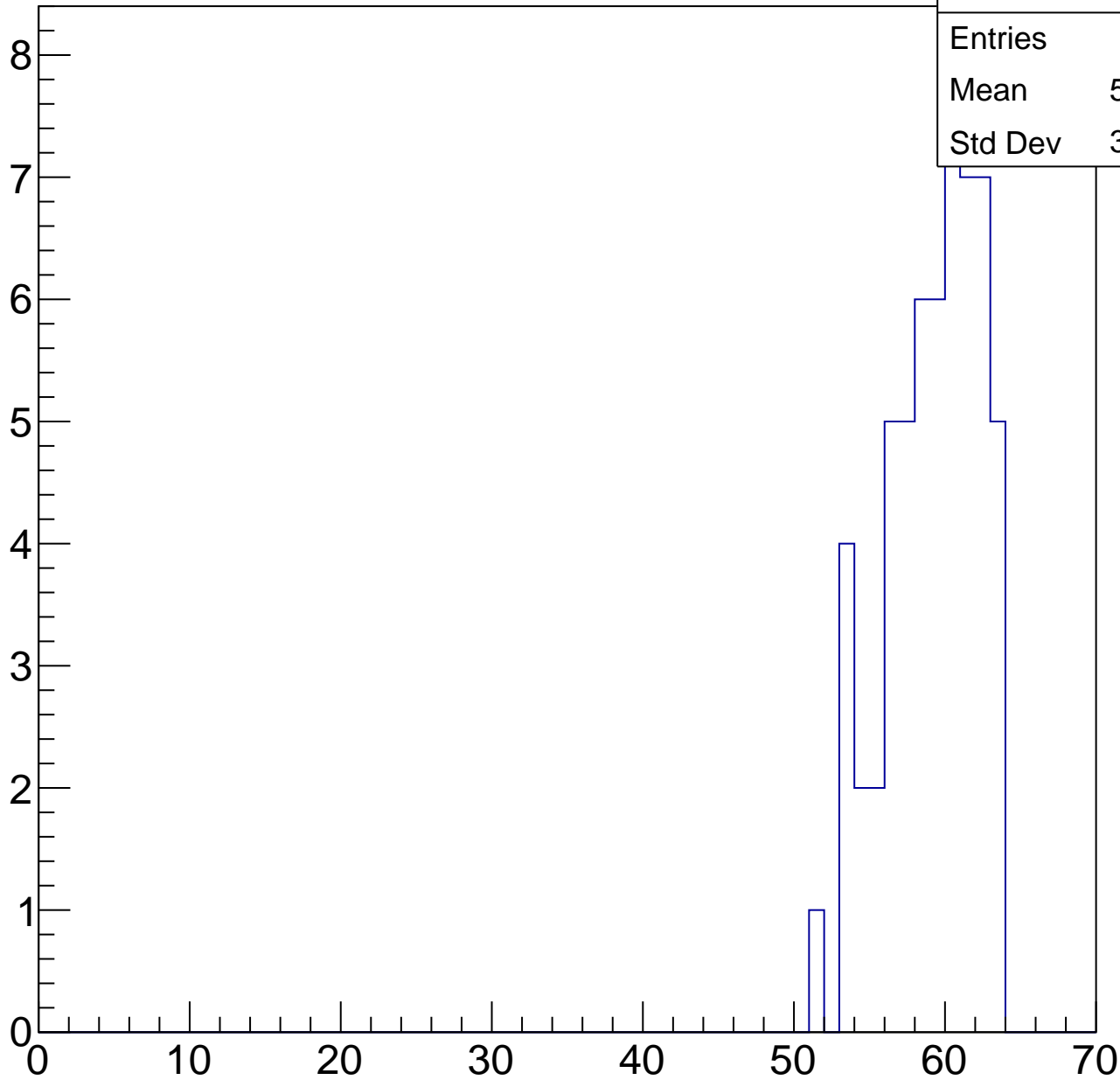
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	58
Mean	58.69
Std Dev	3.035

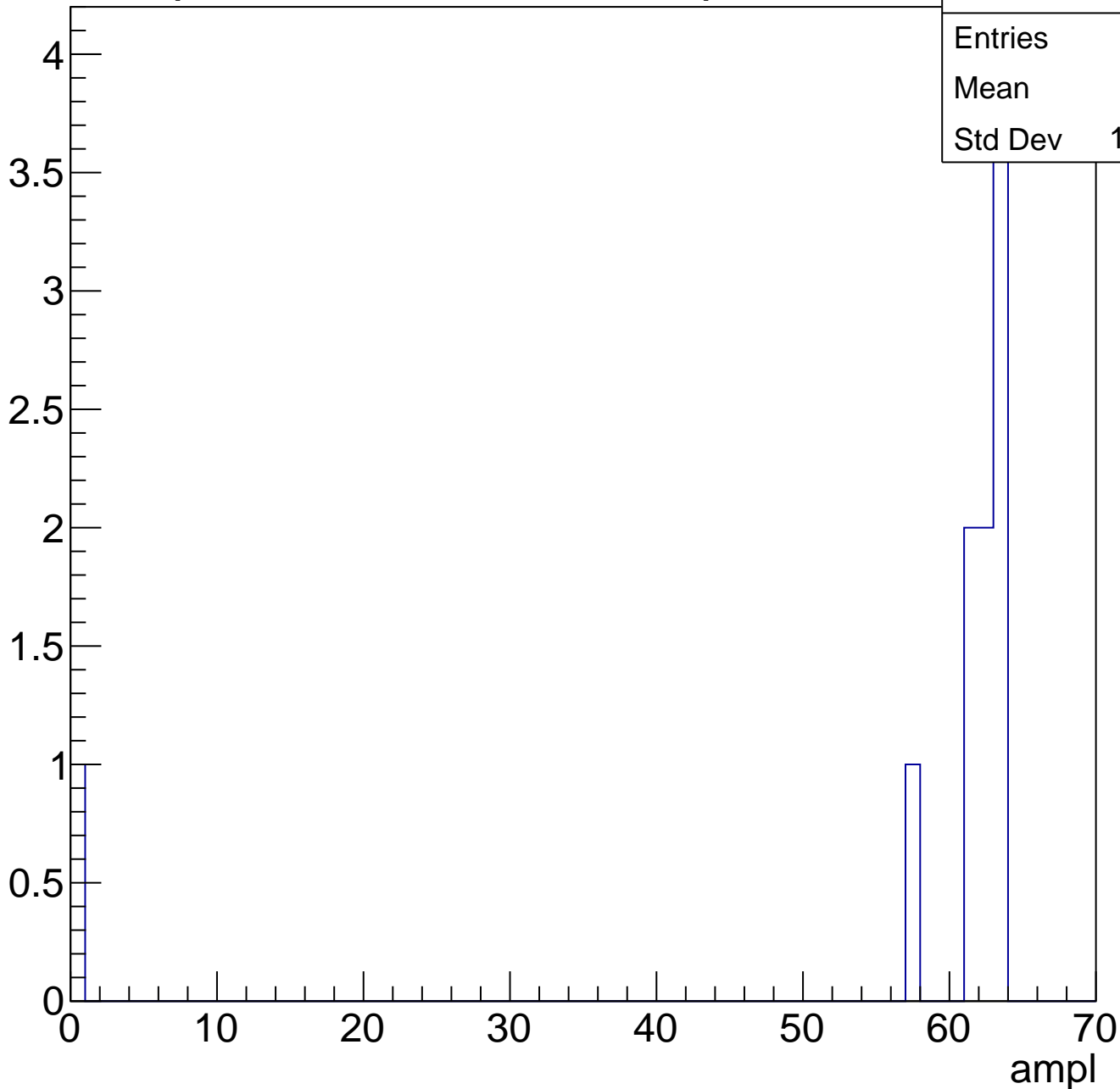
ampl



B1L102S, U6-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

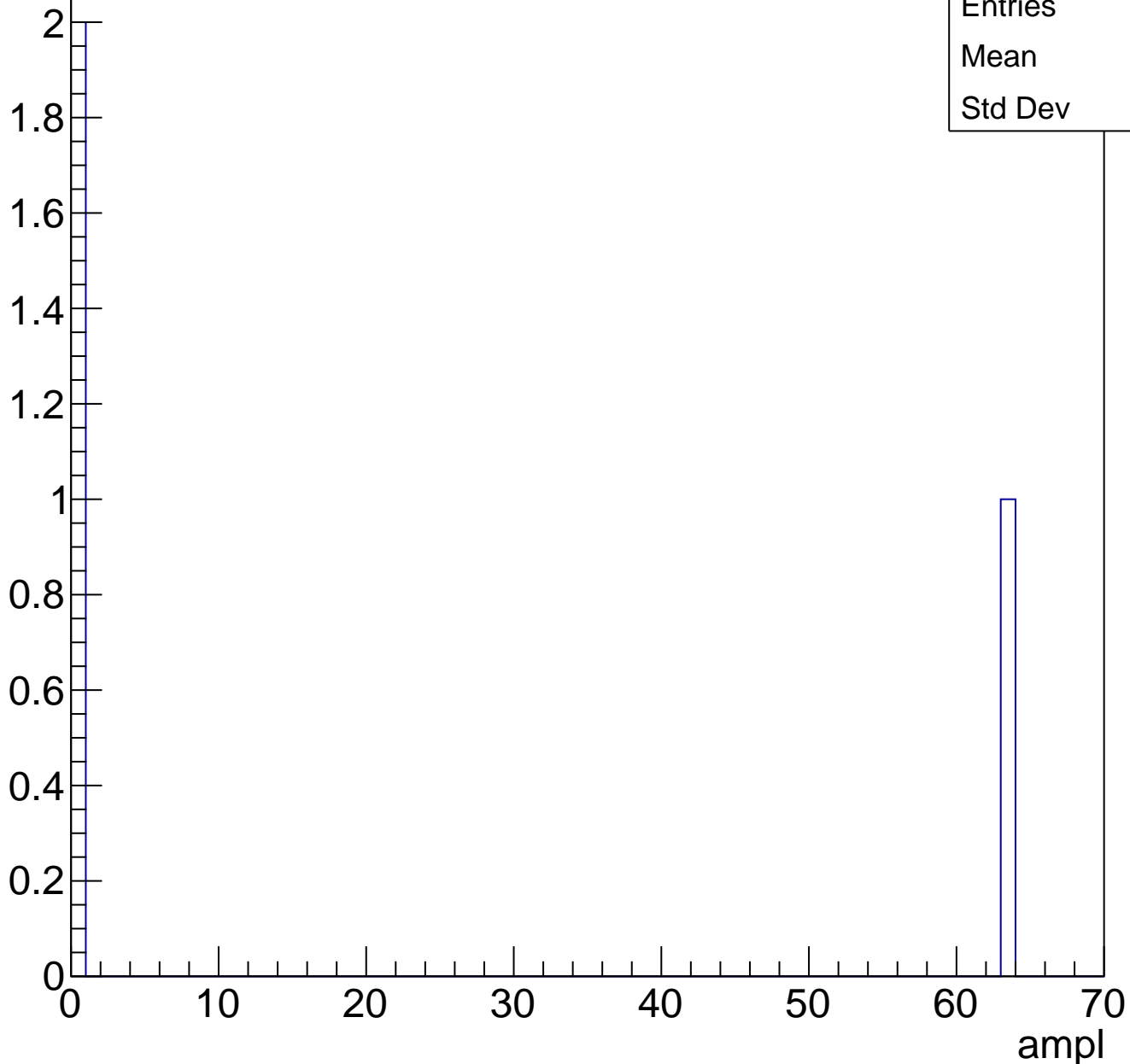


Entries	10
Mean	55.5
Std Dev	18.58

B1L102S, U6-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U6-ch65, adc0

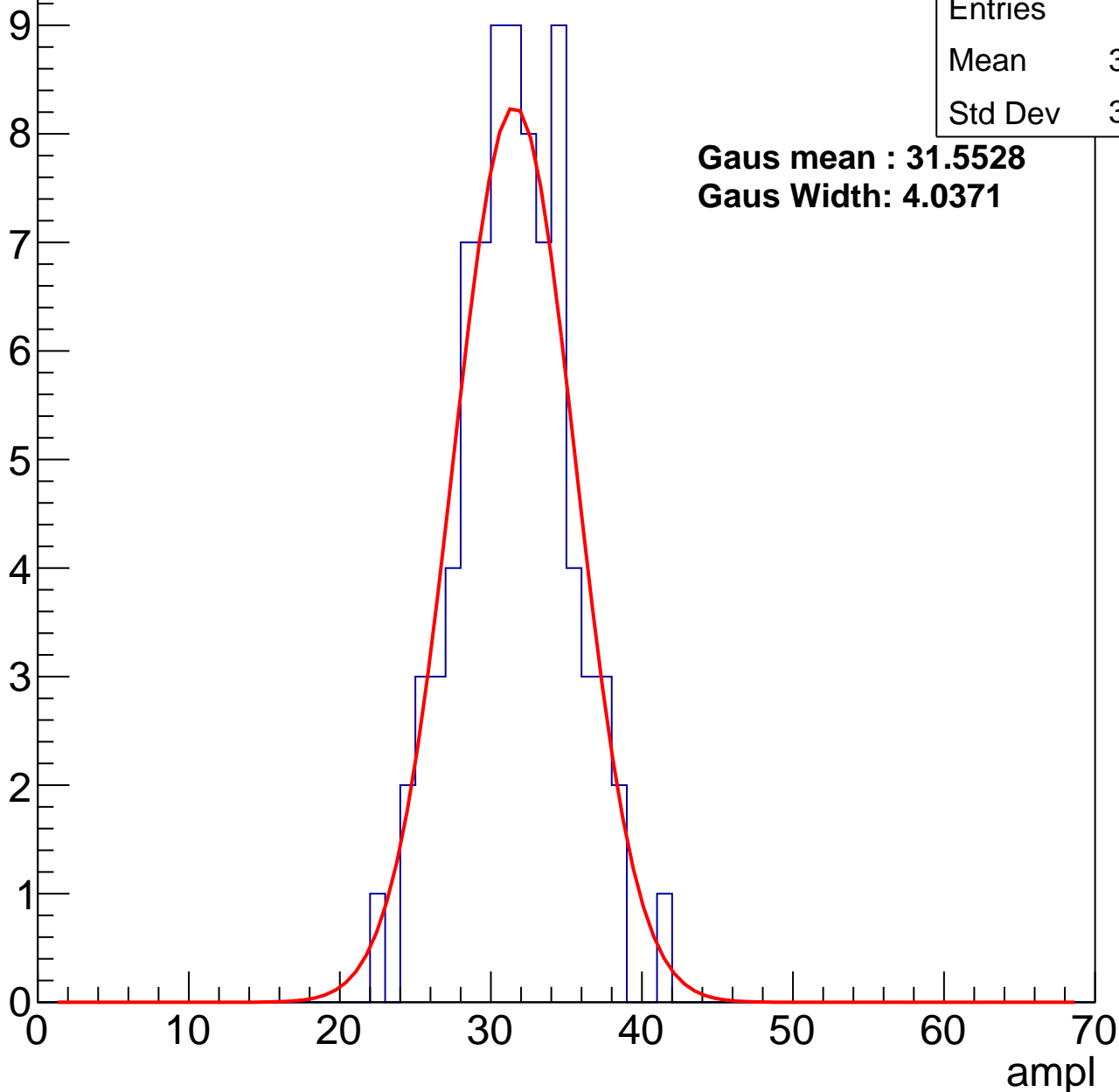
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	31.07
Std Dev	3.642

Gaus mean : 31.5528

Gaus Width: 4.0371



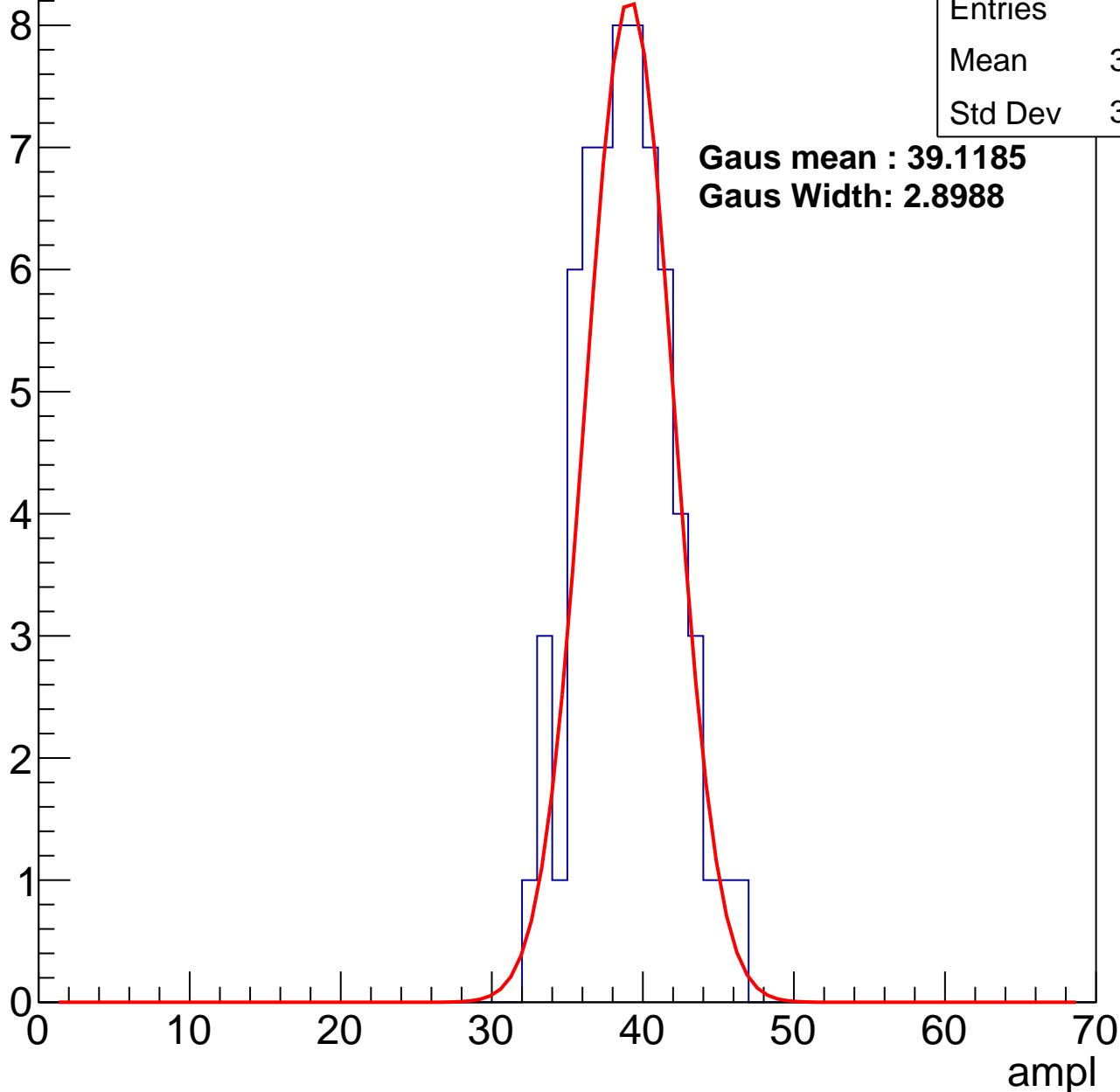
B1L102S, U6-ch65, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	38.44
Std Dev	3.015

Gaus mean : 39.1185
Gaus Width: 2.8988



B1L102S, U6-ch65, adc2

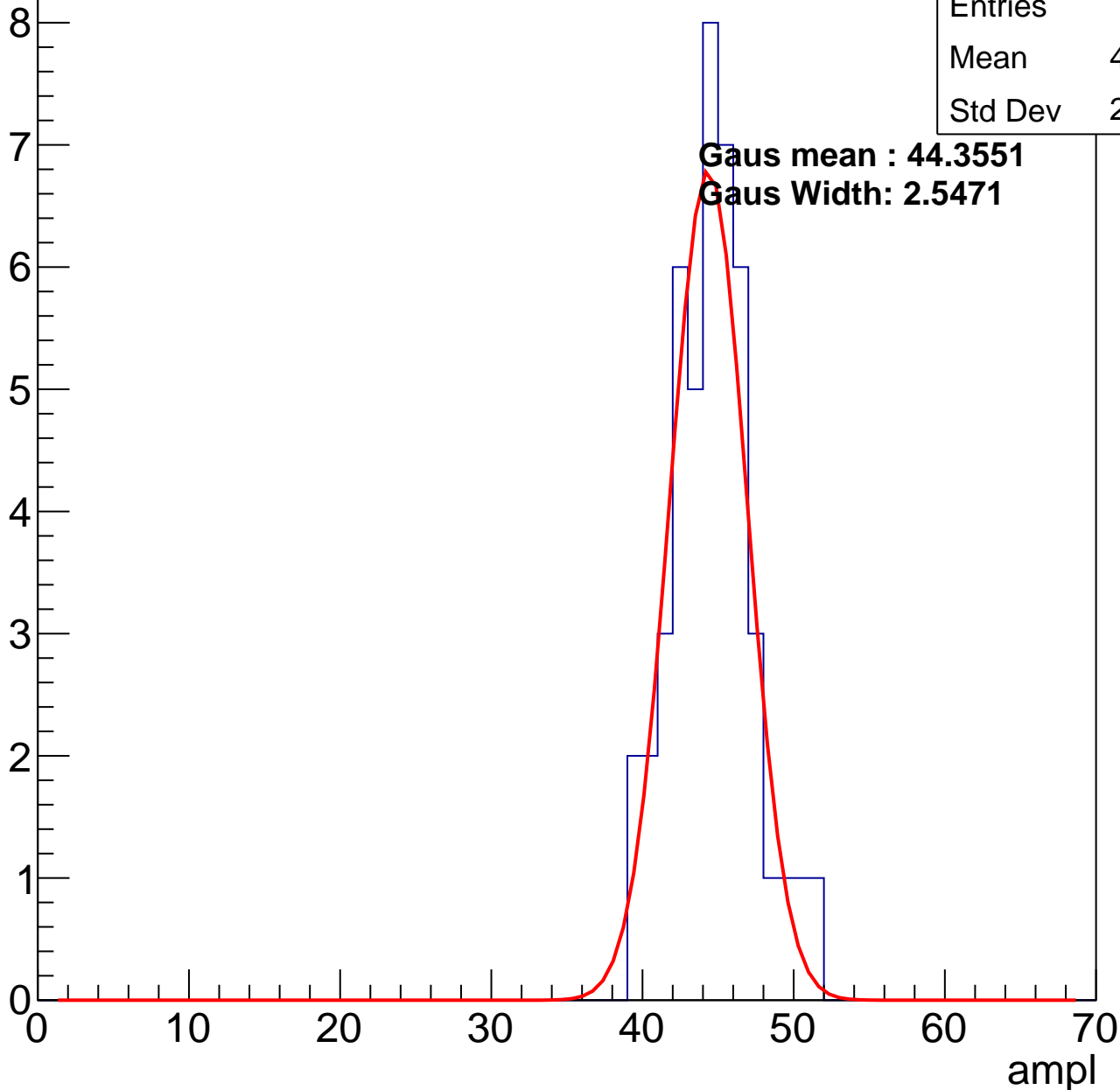
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	44.13
Std Dev	2.643

Gaus mean : 44.3551

Gaus Width: 2.5471

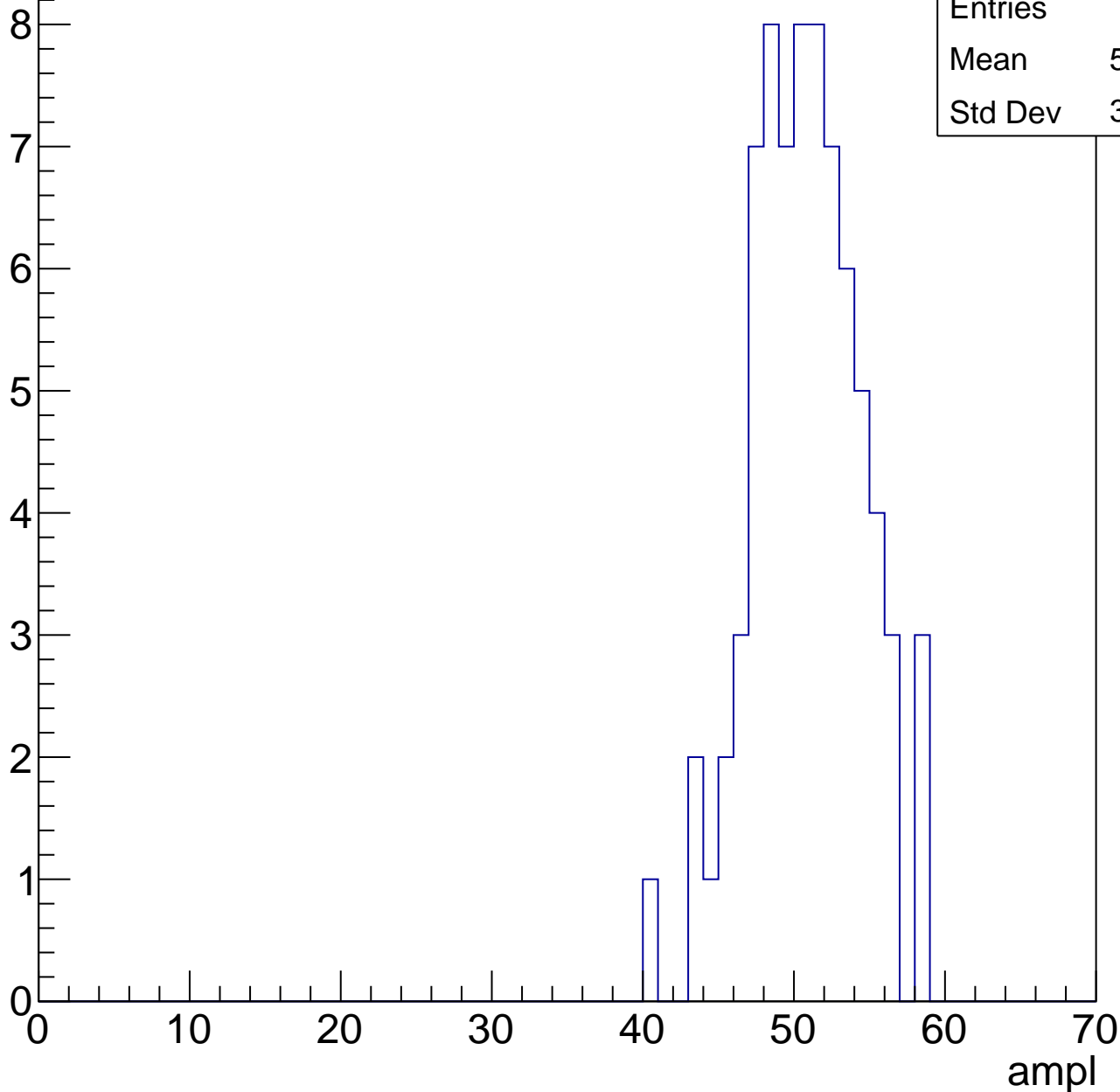


B1L102S, U6-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	50.35
Std Dev	3.642

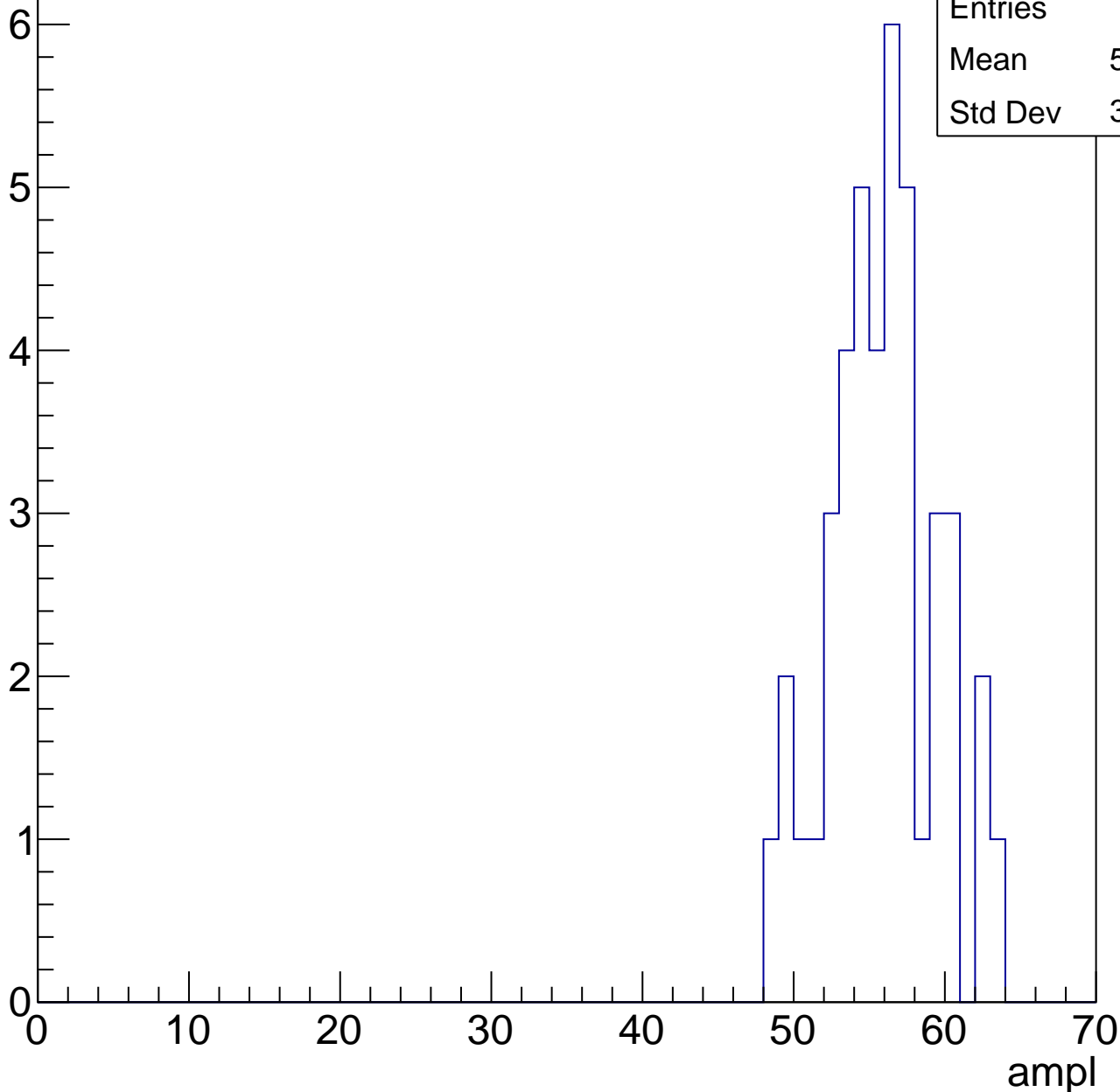


B1L102S, U6-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	55.43
Std Dev	3.526

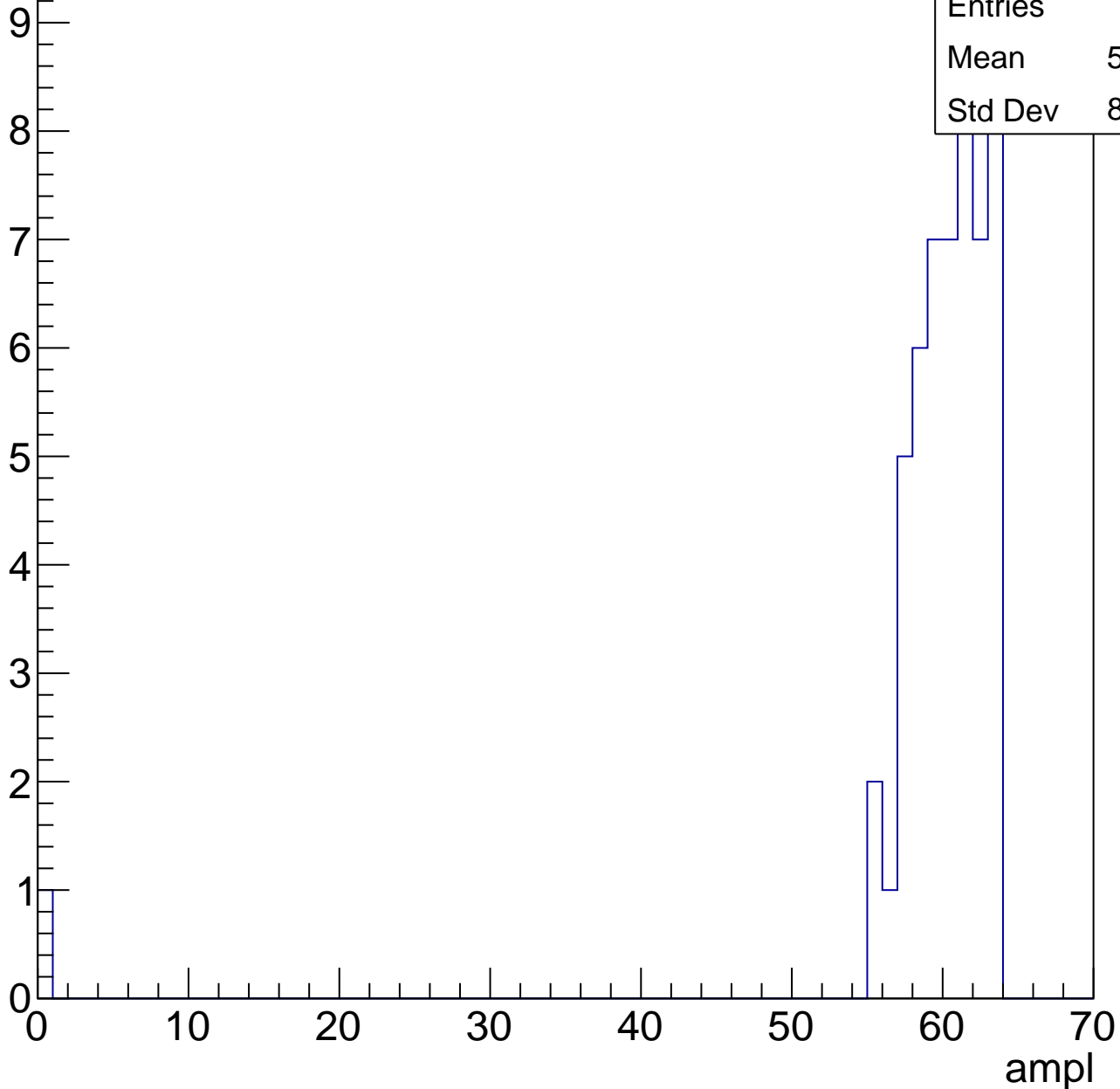


B1L102S, U6-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.93
Std Dev	8.386



B1L102S, U6-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch66, adc0

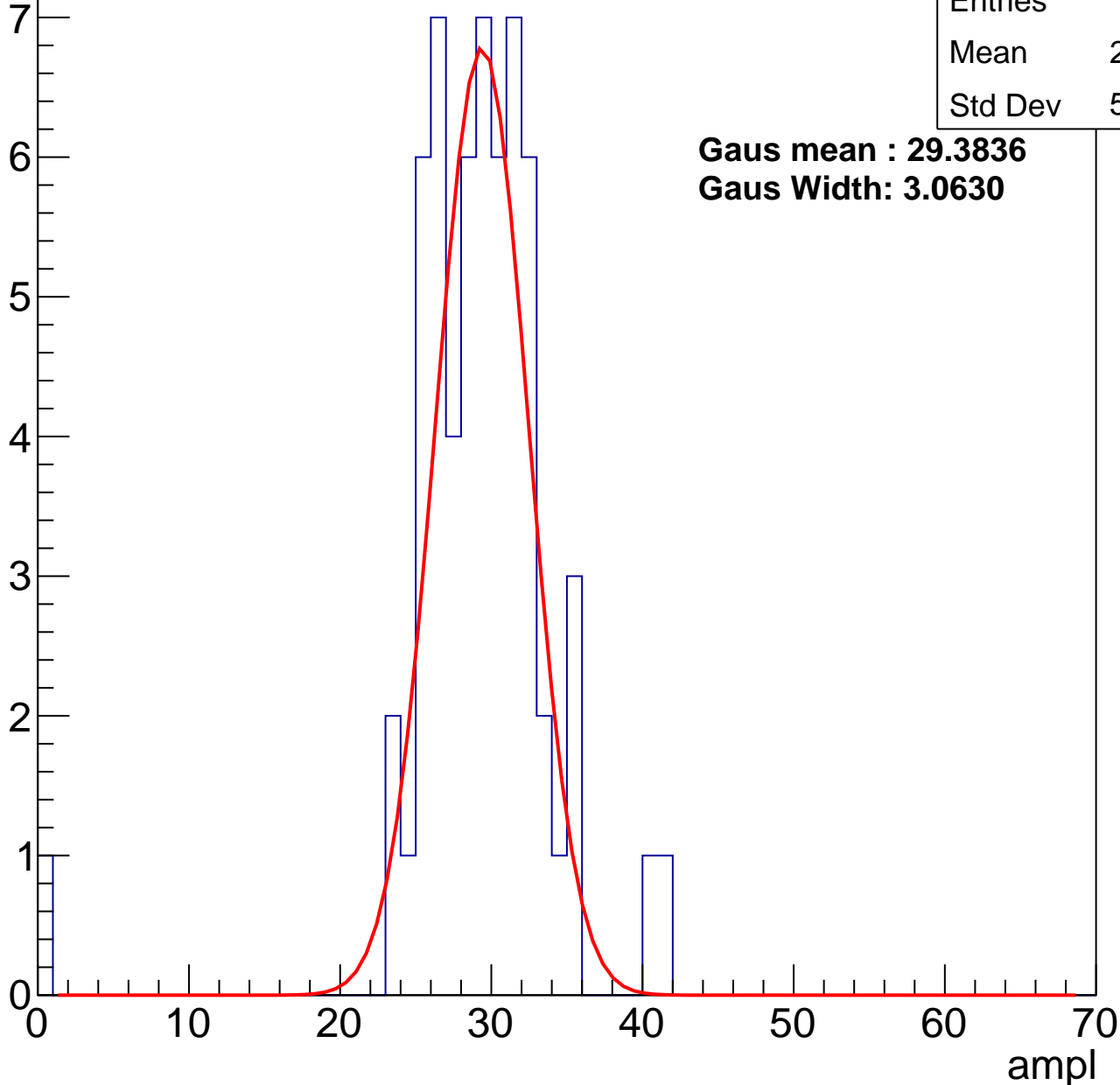
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.79
Std Dev	5.176

Gaus mean : 29.3836

Gaus Width: 3.0630



B1L102S, U6-ch66, adc1

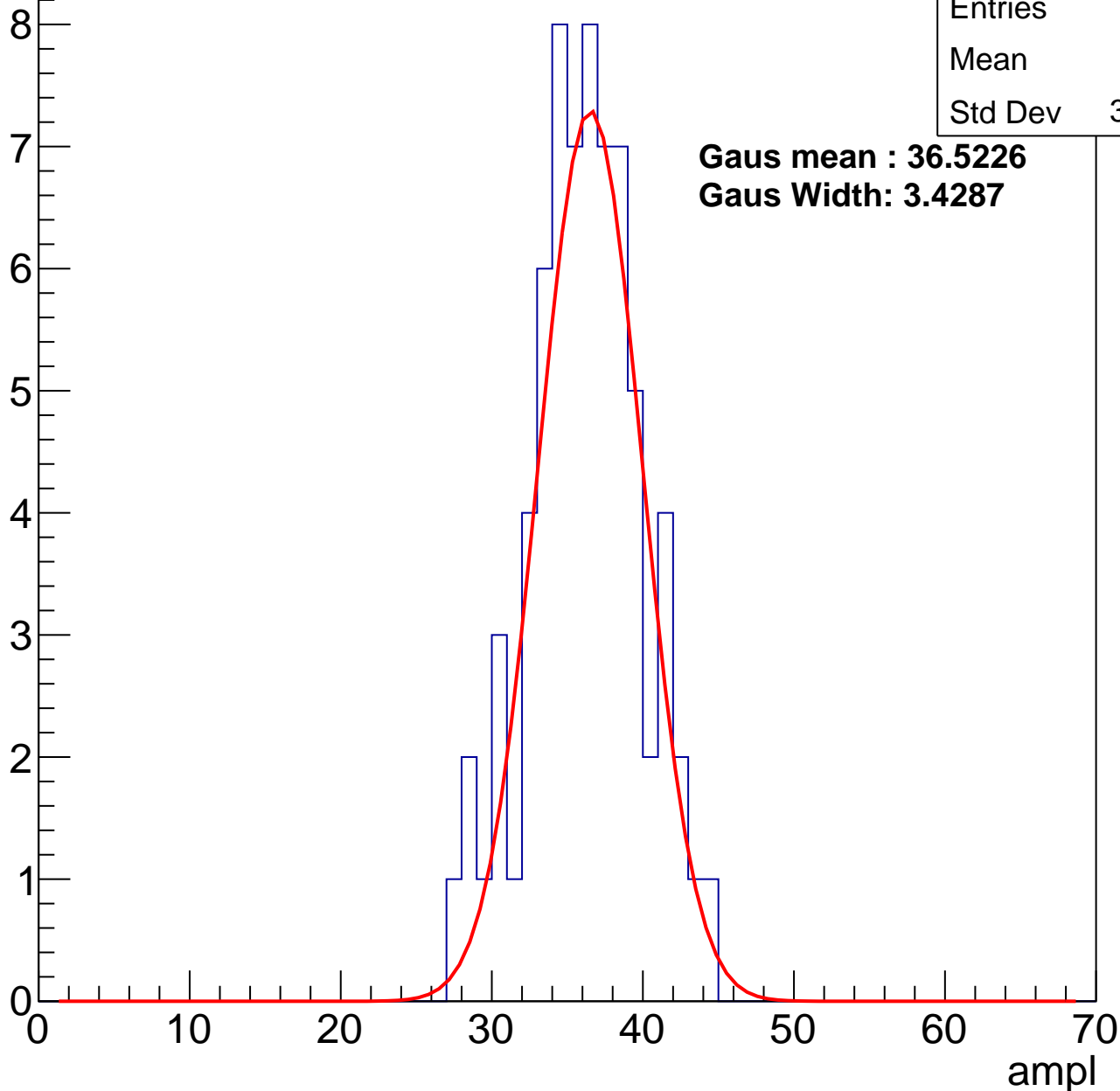
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.7
Std Dev	3.662

Gaus mean : 36.5226

Gaus Width: 3.4287



B1L102S, U6-ch66, adc2

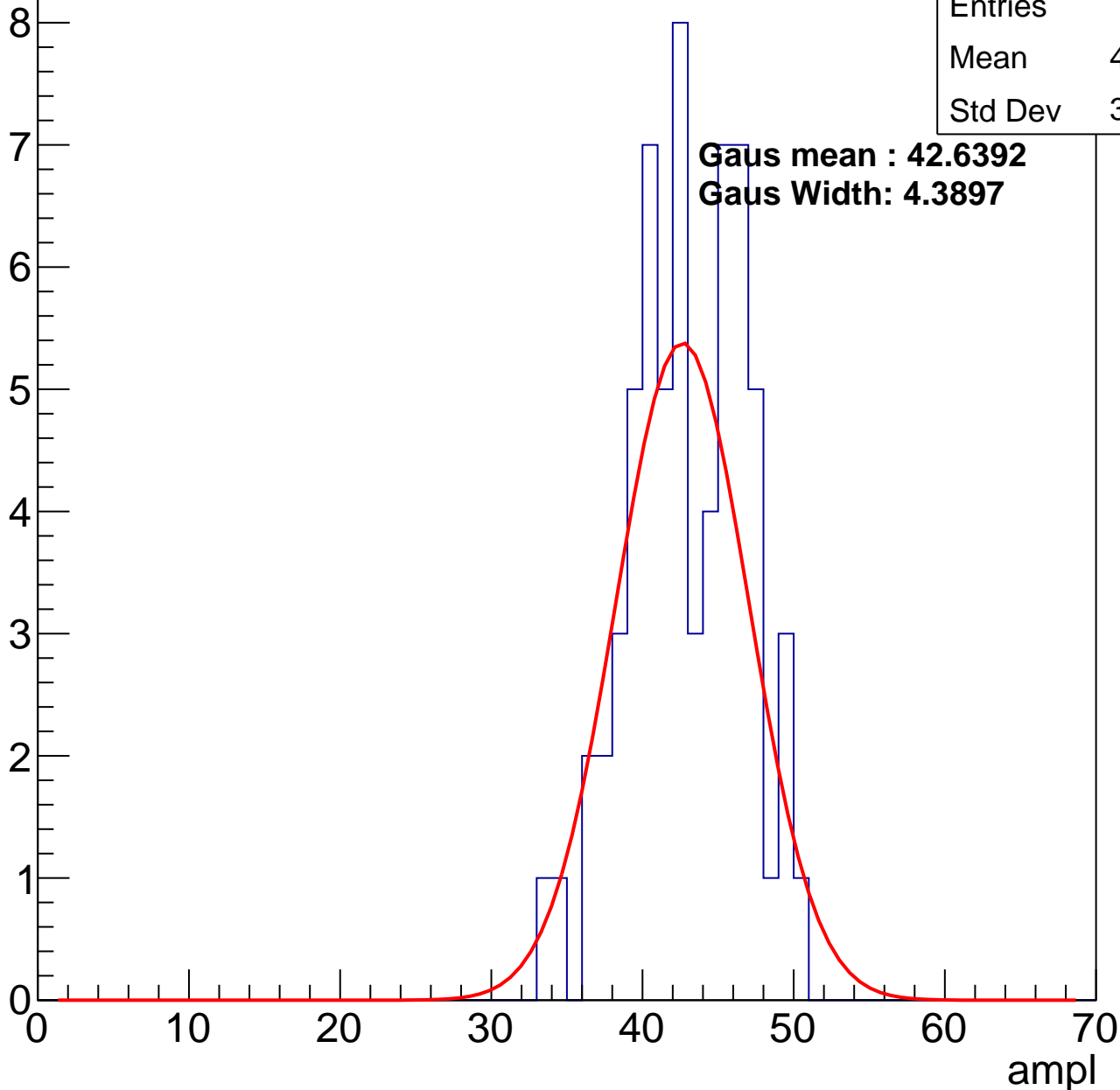
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.54
Std Dev	3.803

Gaus mean : 42.6392

Gaus Width: 4.3897

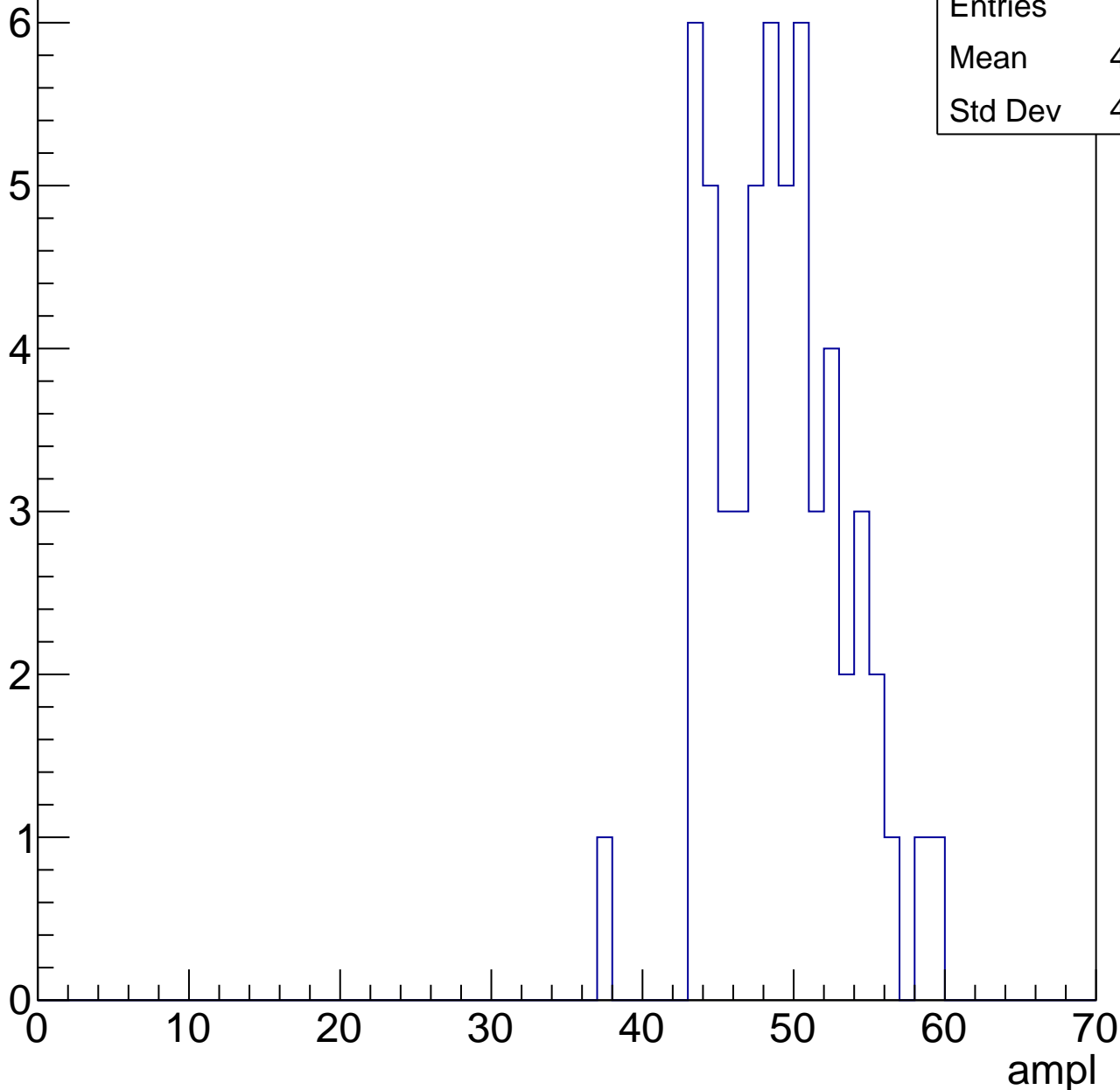


B1L102S, U6-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.56
Std Dev	4.267

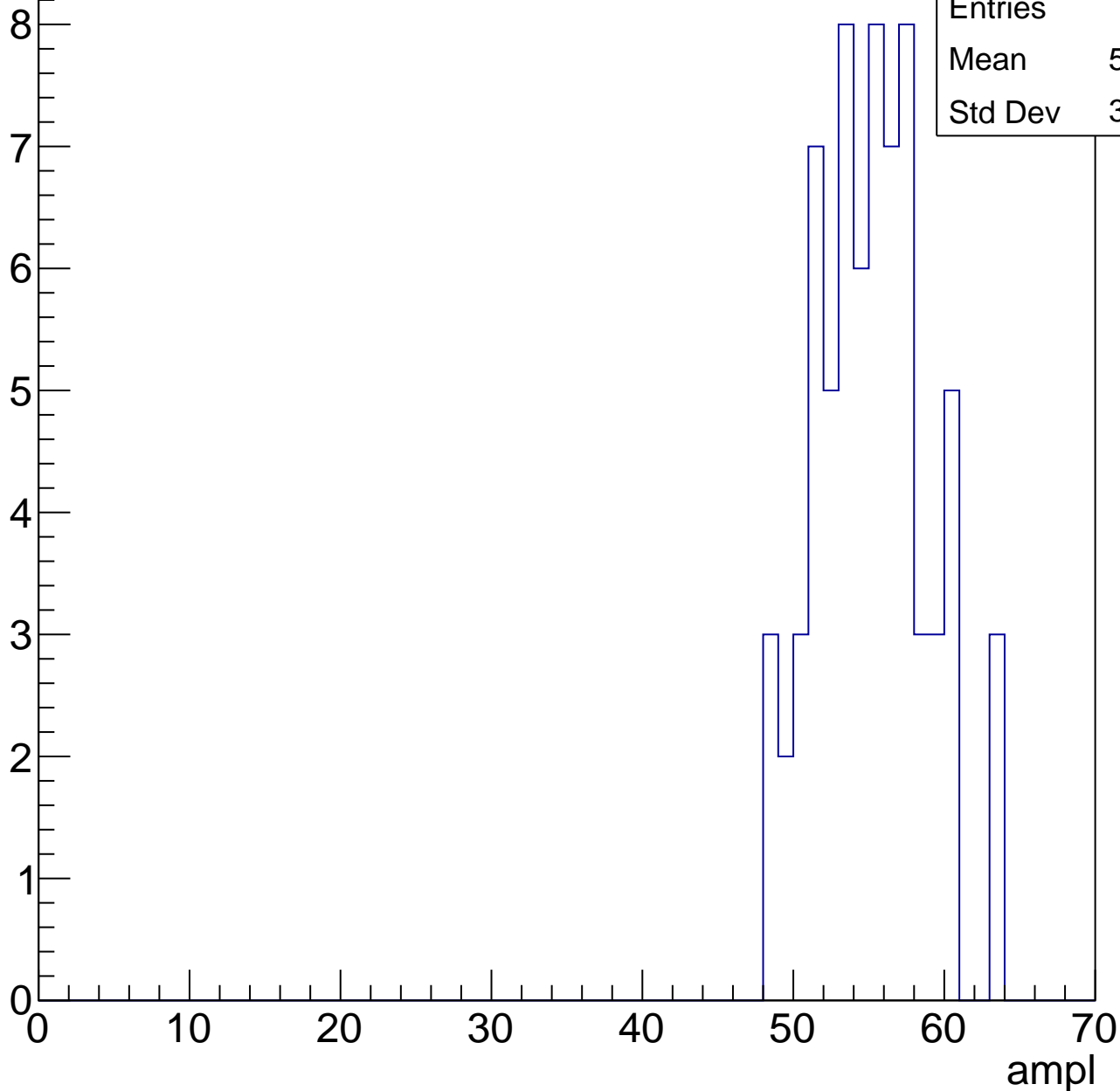


B1L102S, U6-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.72
Std Dev	3.589

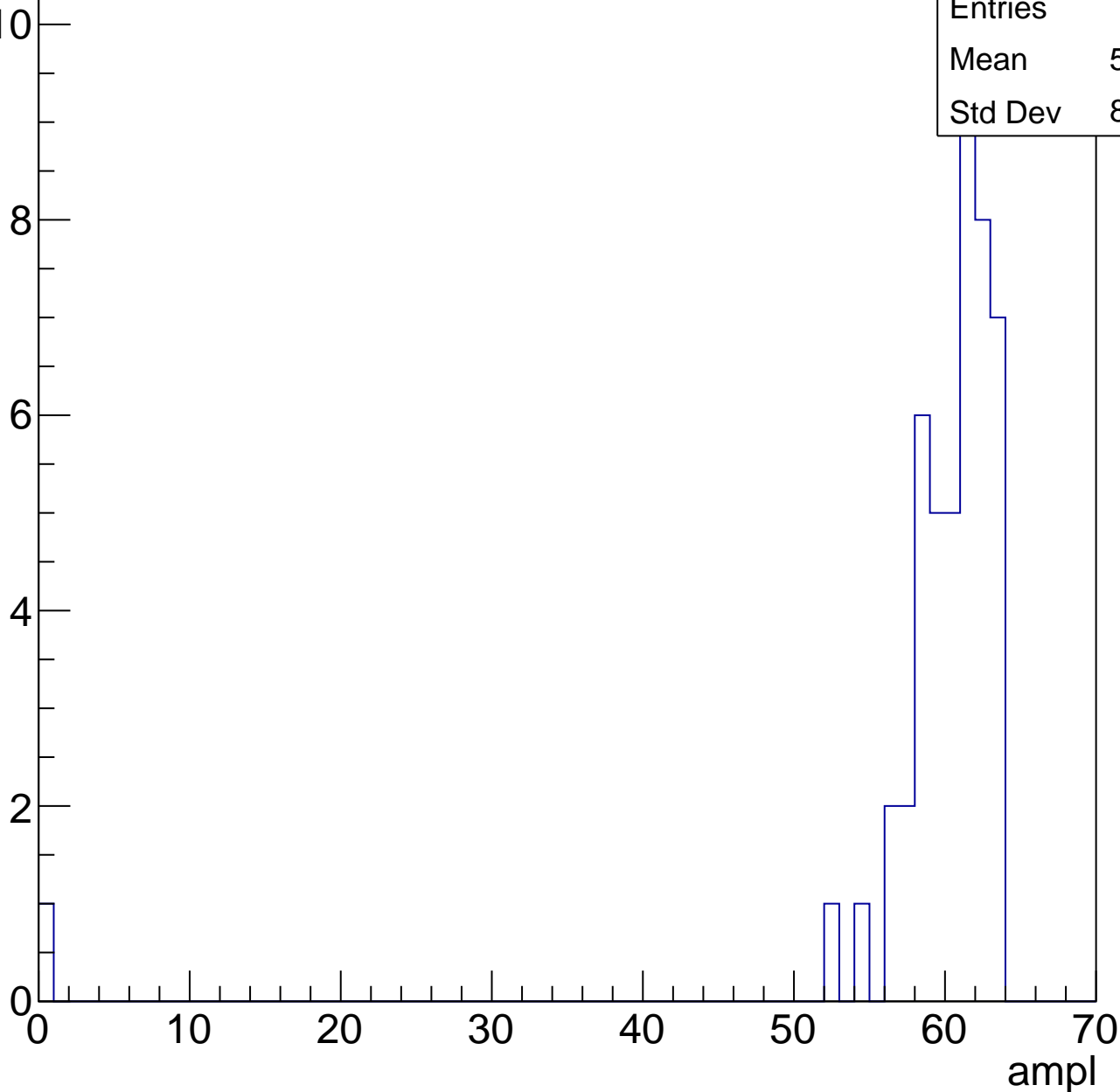


B1L102S, U6-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

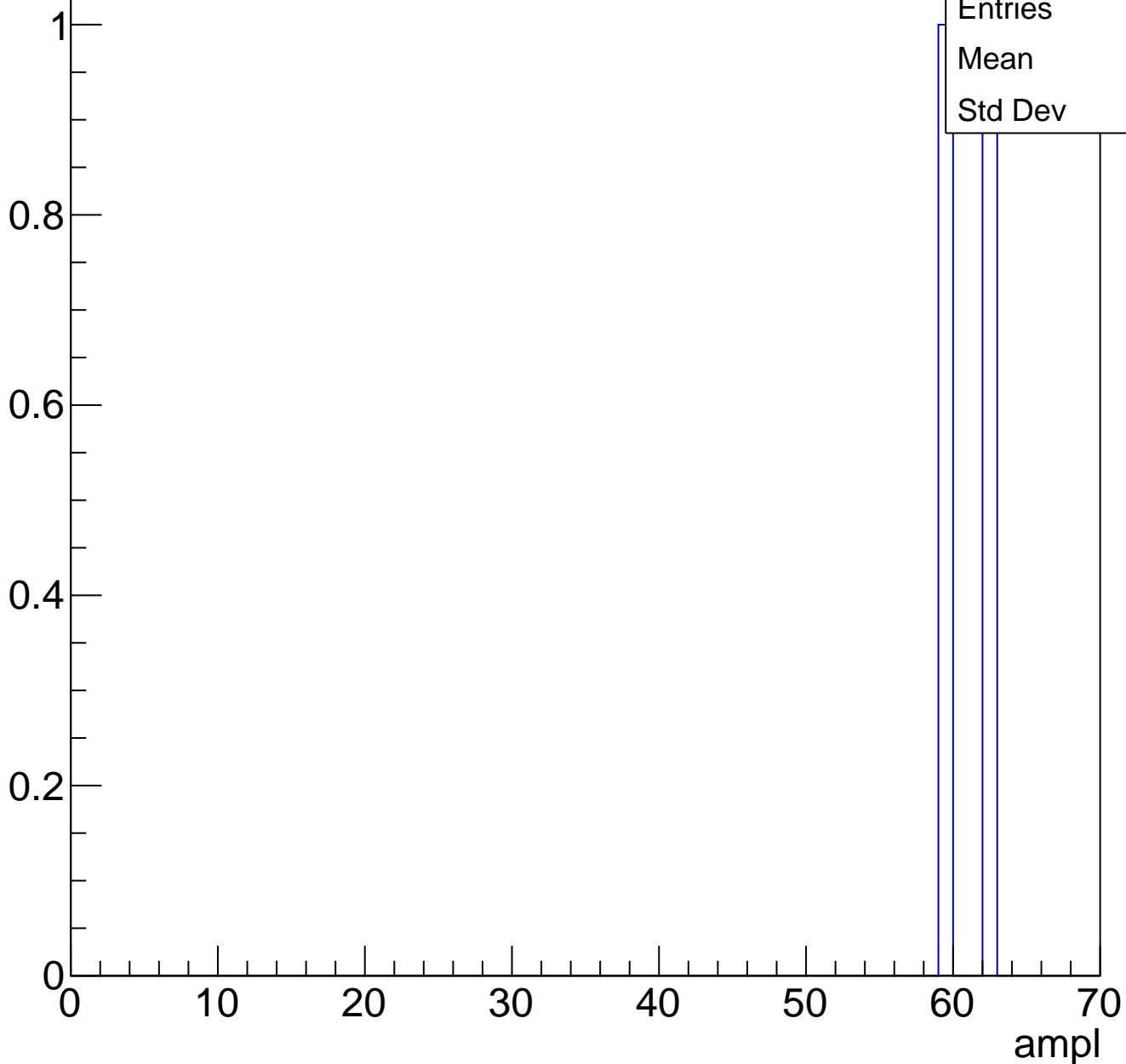
Entries	48
Mean	58.79
Std Dev	8.914



B1L102S, U6-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch66, adc7

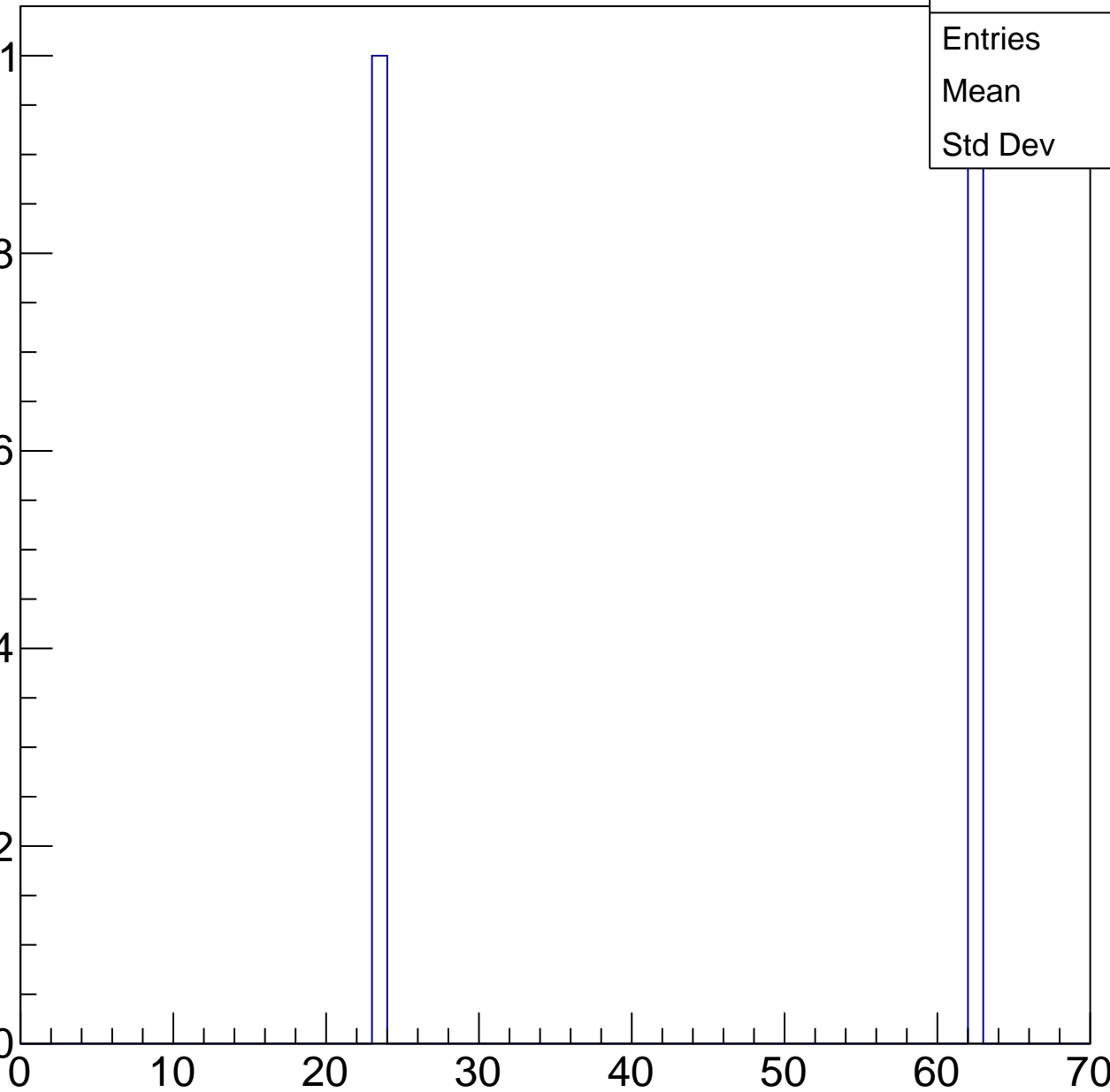
calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	19.5

ampl



B1L102S, U6-ch67, adc0

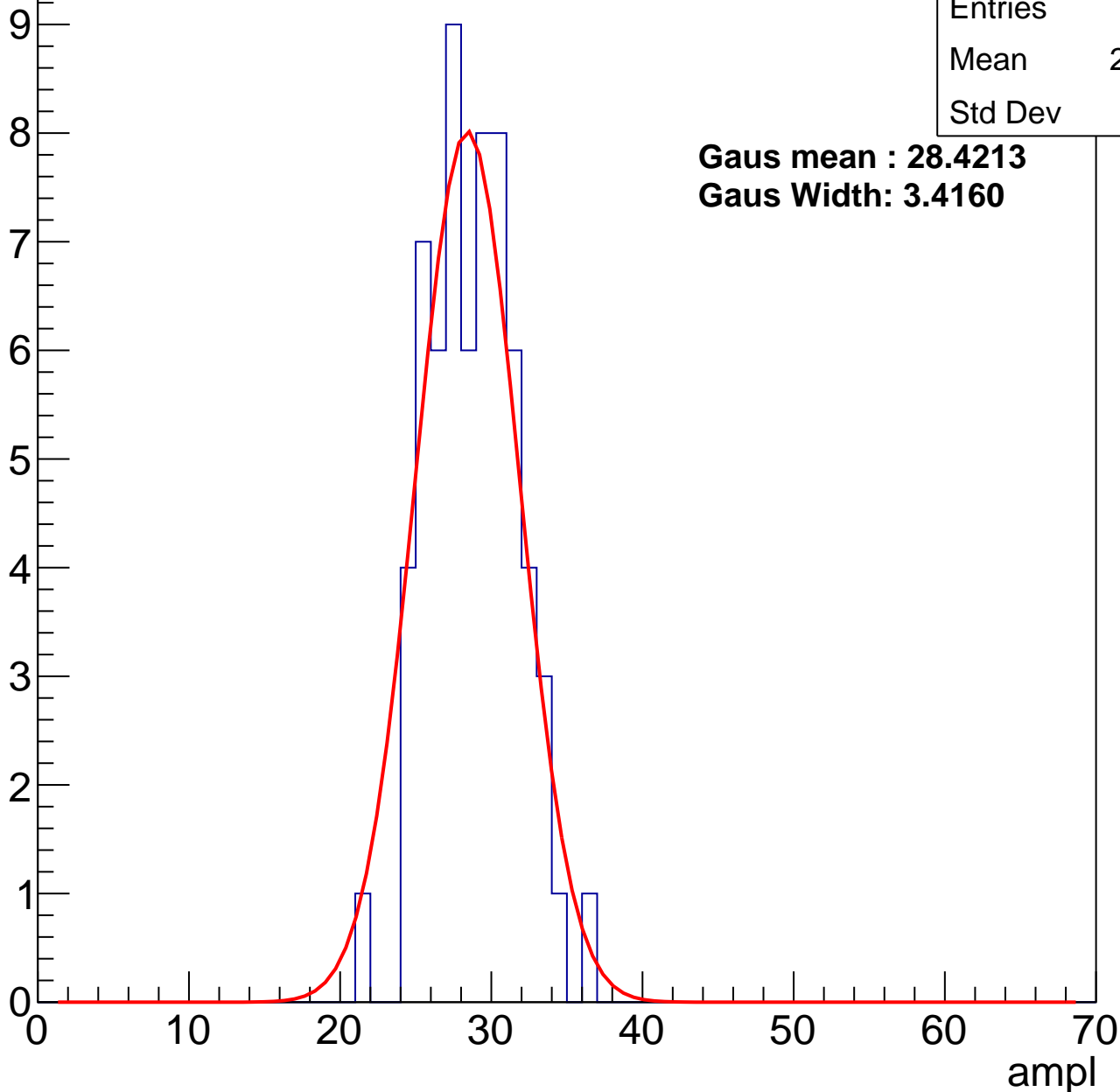
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.34
Std Dev	2.89

Gaus mean : 28.4213

Gaus Width: 3.4160



B1L102S, U6-ch67, adc1

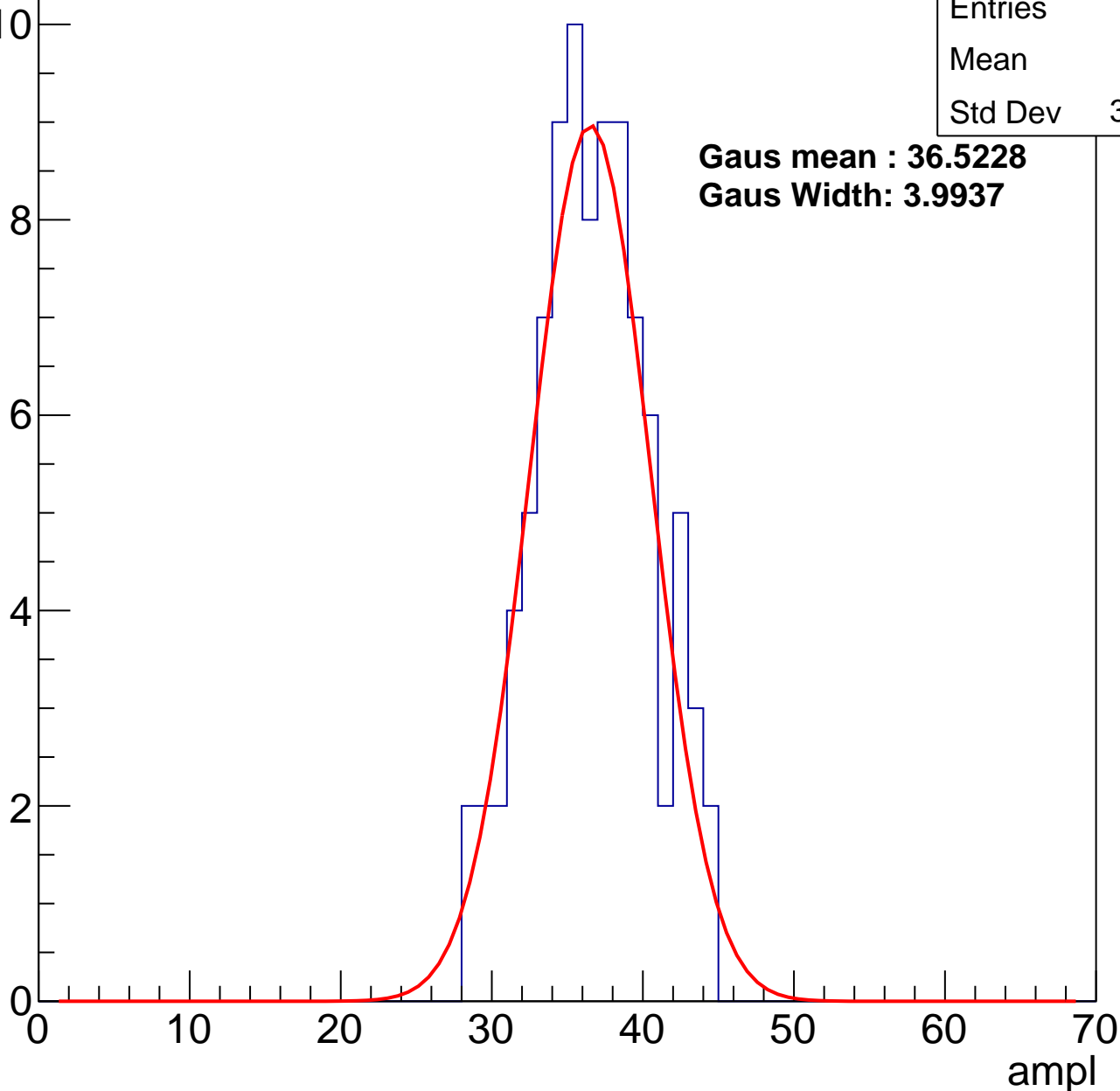
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	92
Mean	36.2
Std Dev	3.748

Gaus mean : 36.5228

Gaus Width: 3.9937



B1L102S, U6-ch67, adc2

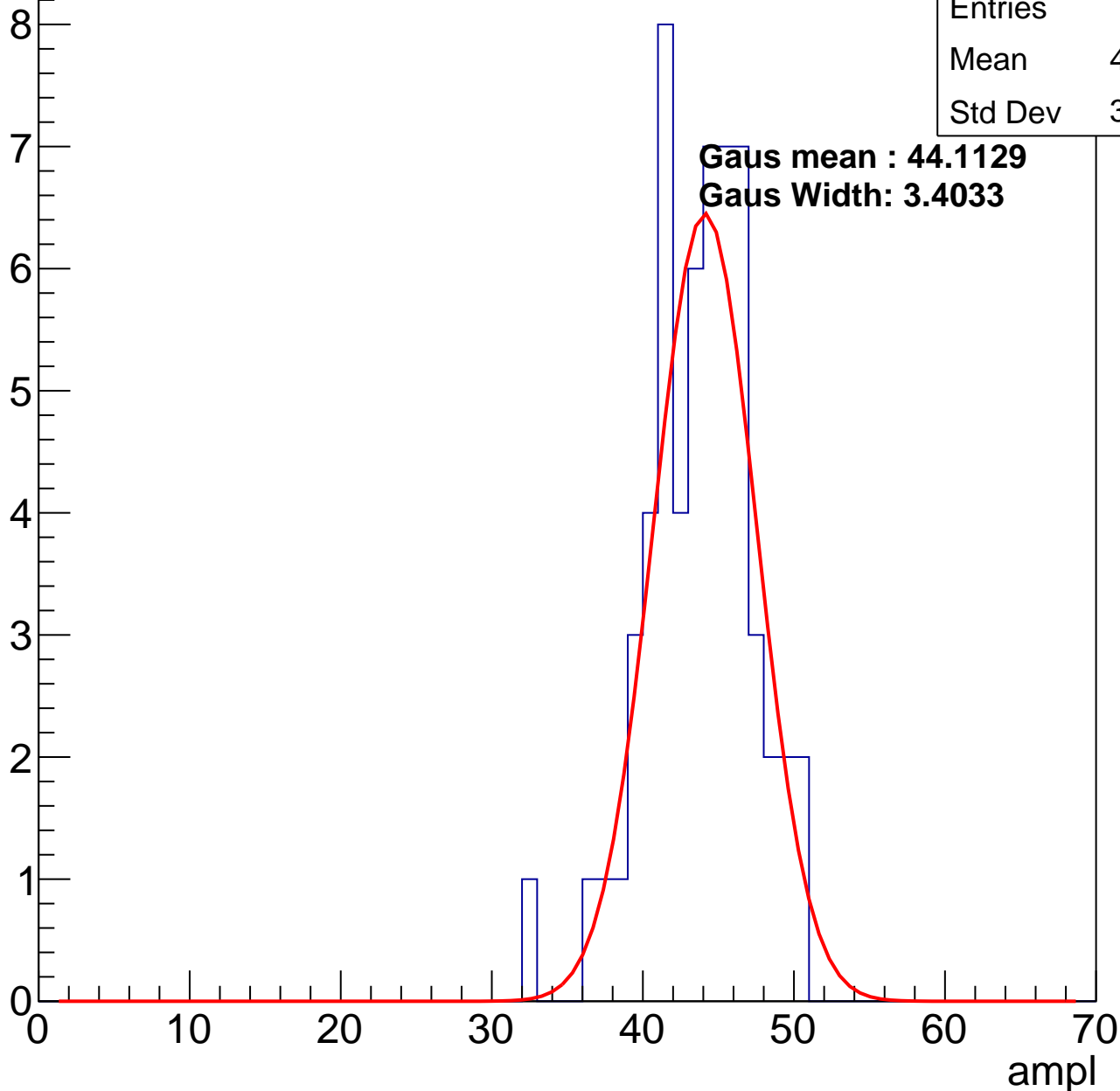
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	43.29
Std Dev	3.474

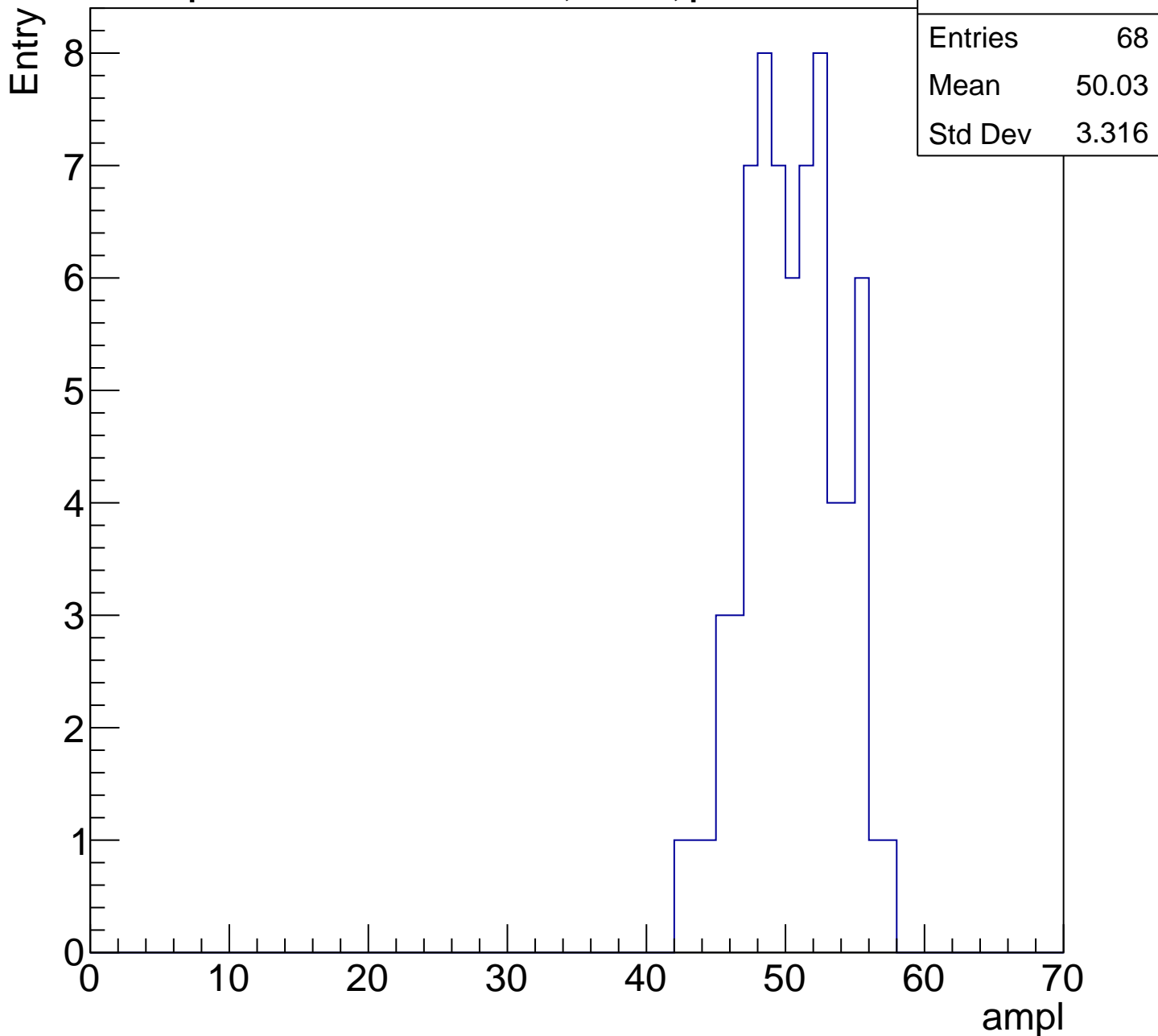
Gaus mean : 44.1129

Gaus Width: 3.4033



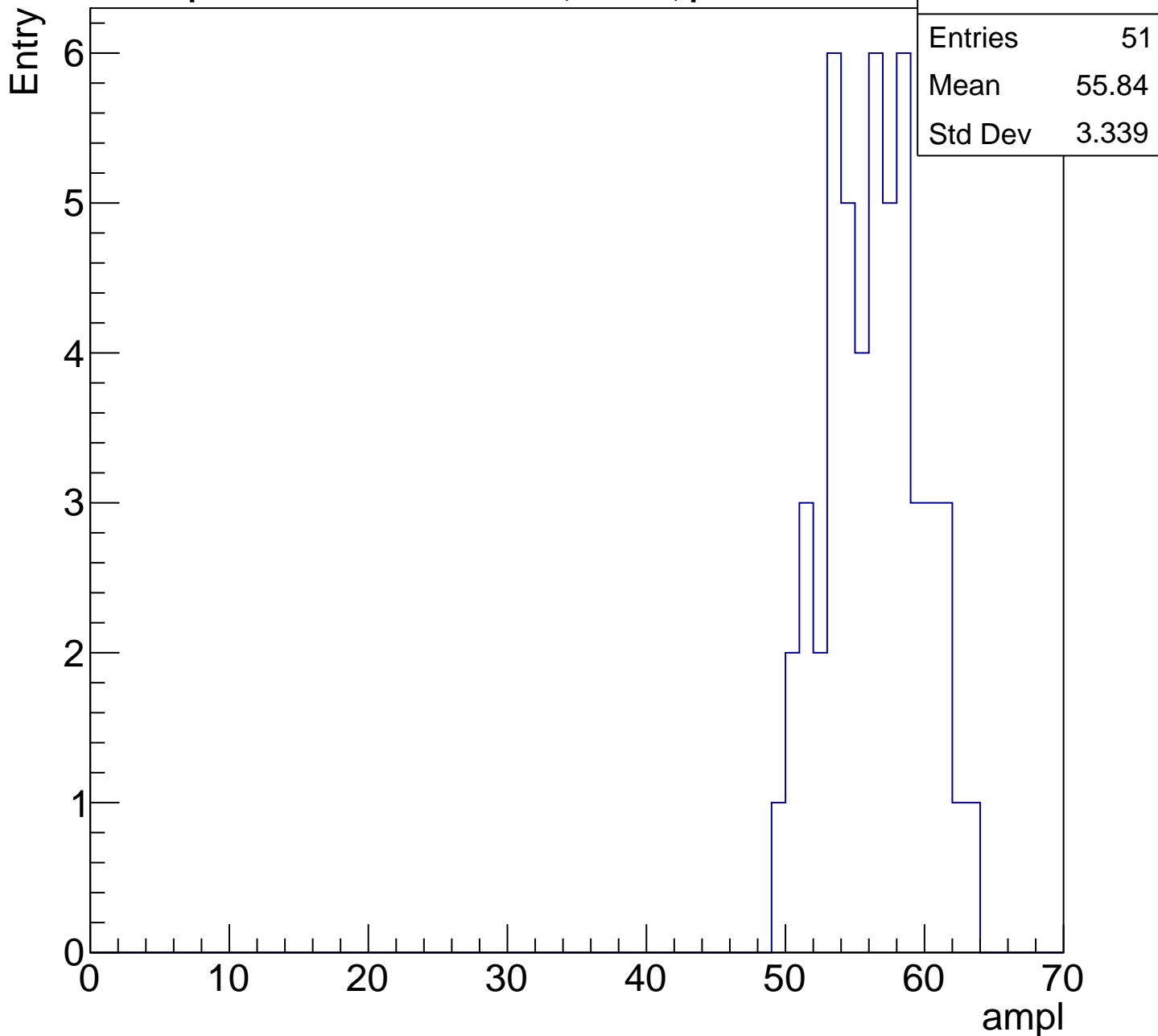
B1L102S, U6-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2



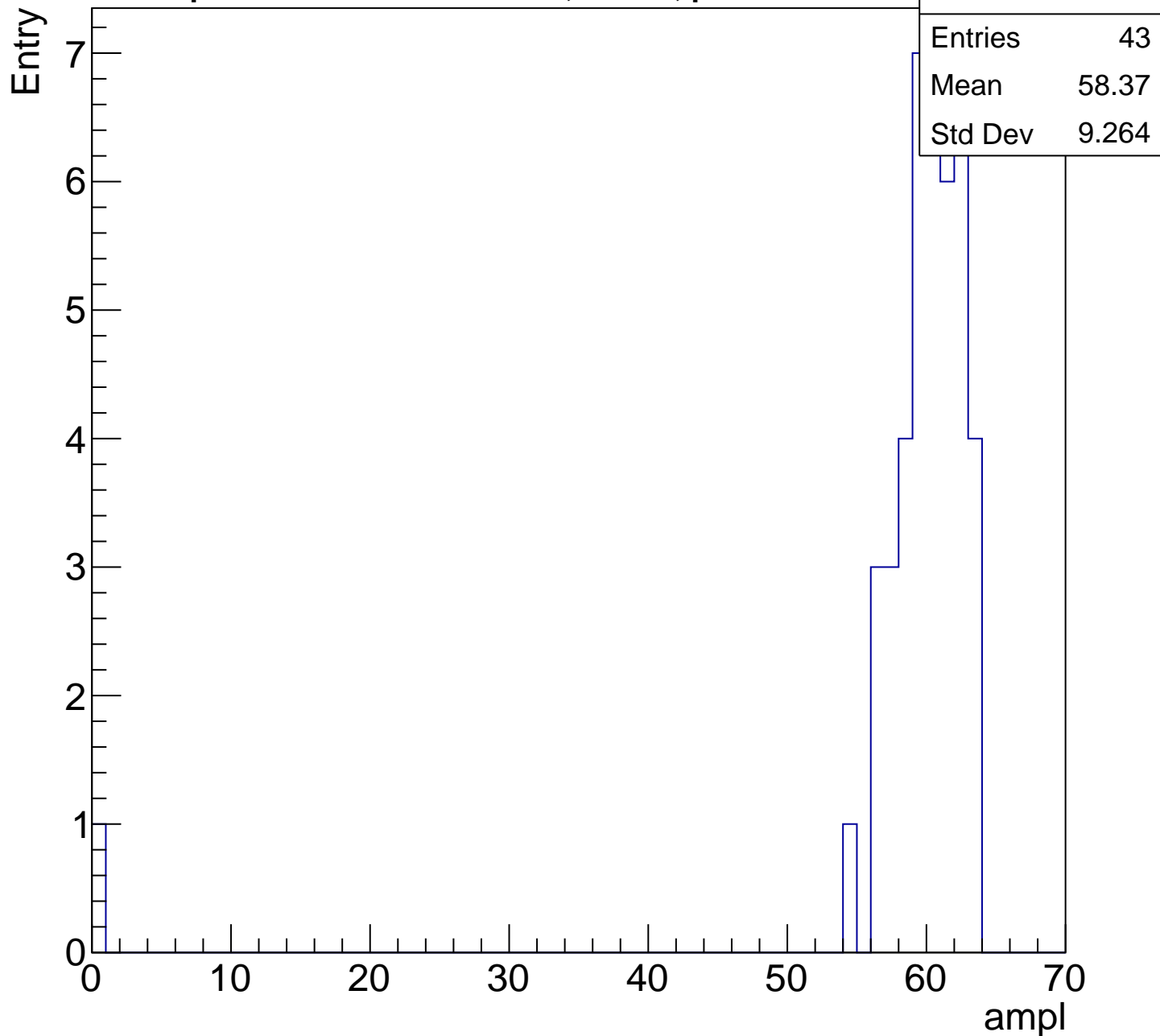
B1L102S, U6-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

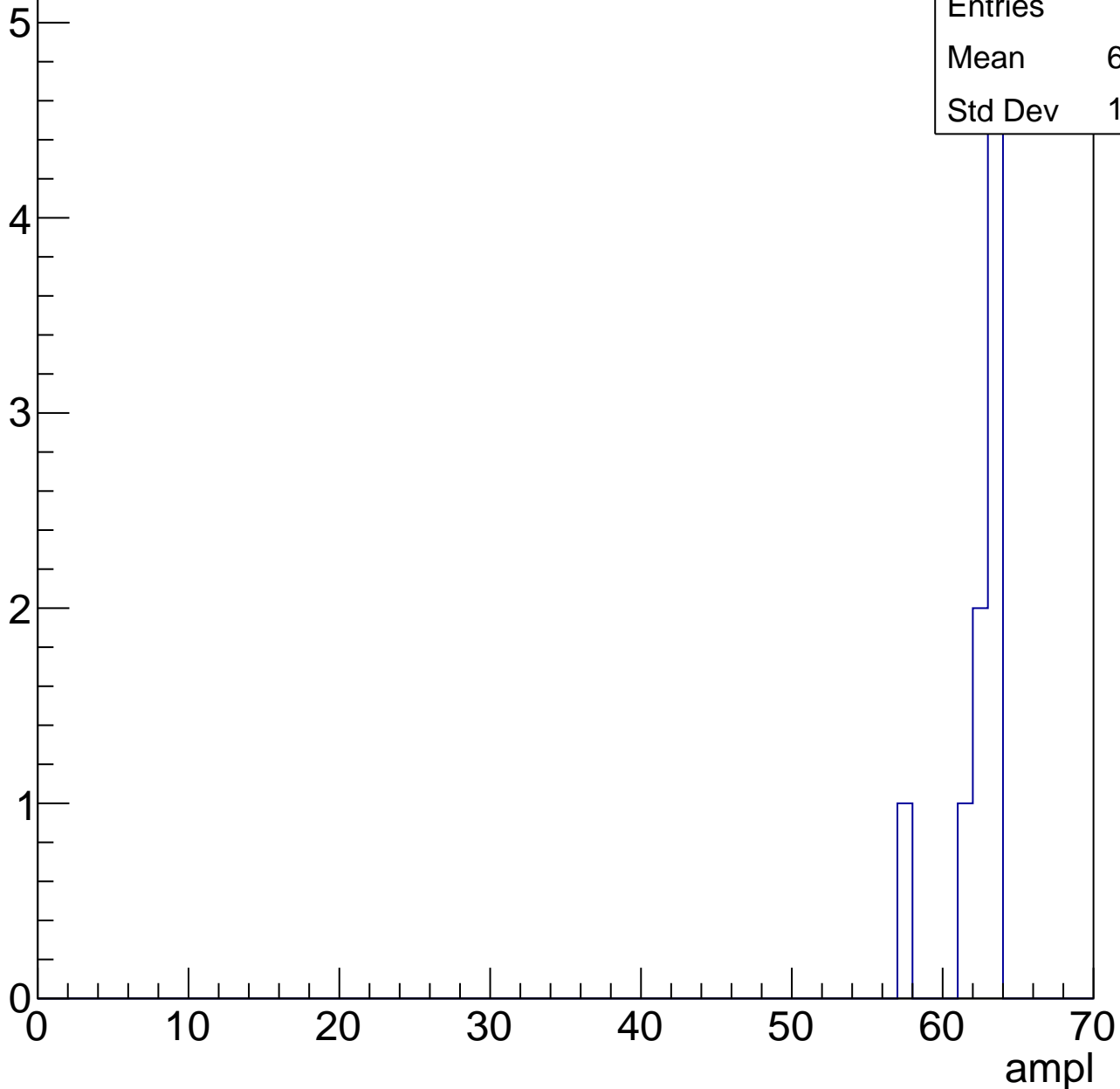


B1L102S, U6-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.89
Std Dev	1.853



B1L102S, U6-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch68, adc0

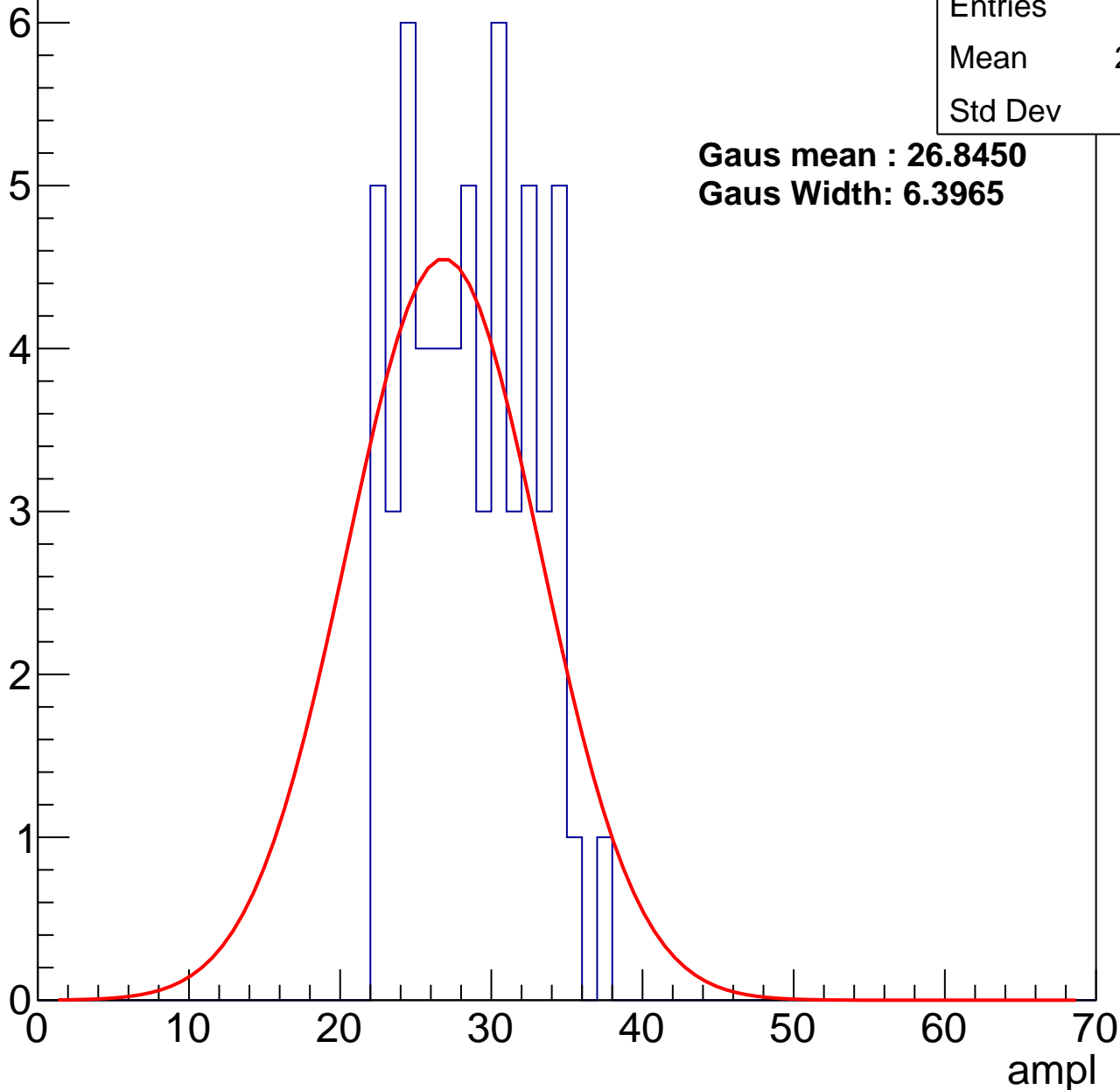
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.21
Std Dev	3.99

Gaus mean : 26.8450

Gaus Width: 6.3965



B1L102S, U6-ch68, adc1

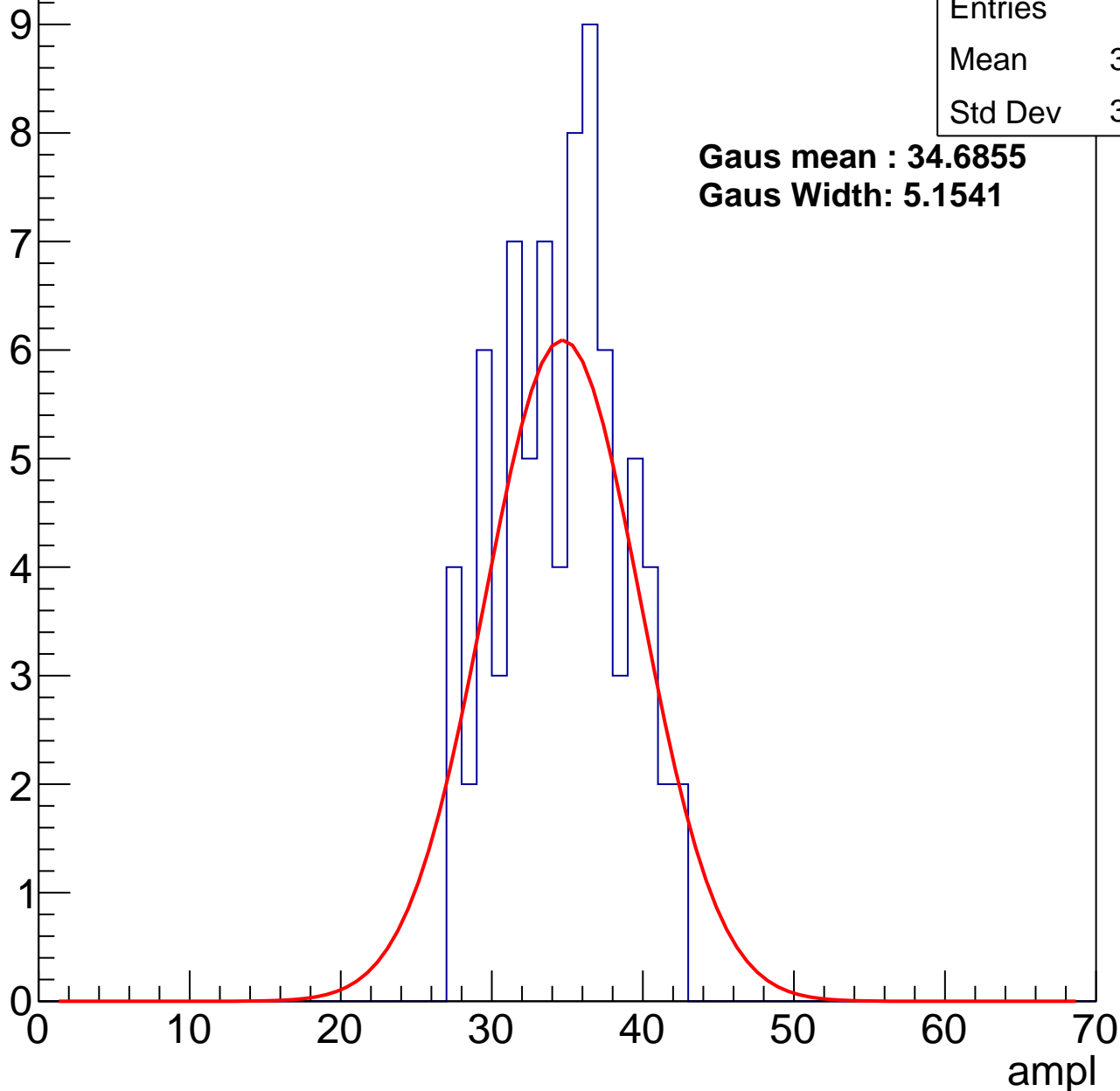
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.19
Std Dev	3.938

Gaus mean : 34.6855

Gaus Width: 5.1541



B1L102S, U6-ch68, adc2

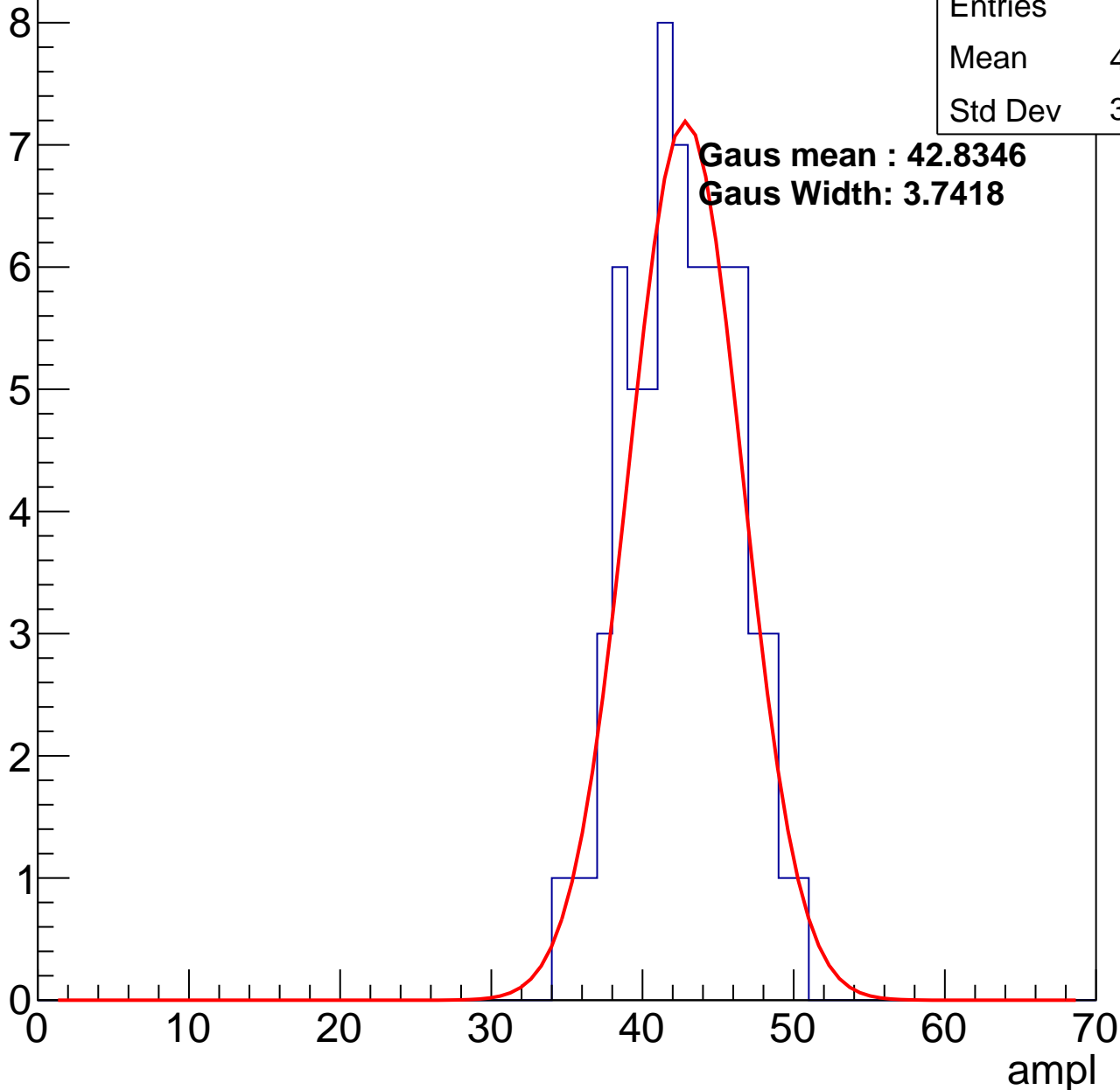
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.22
Std Dev	3.542

Gaus mean : 42.8346

Gaus Width: 3.7418

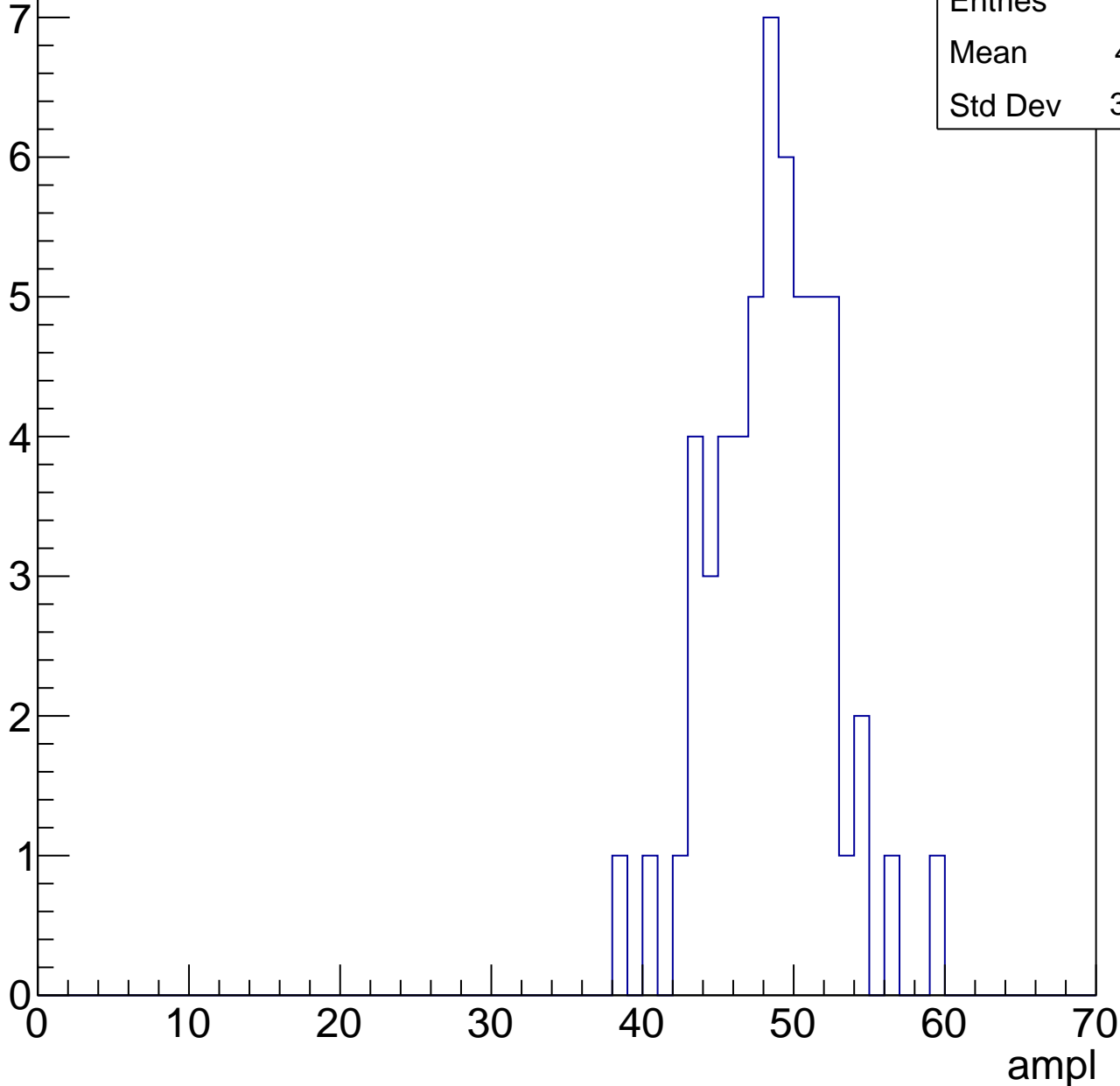


B1L102S, U6-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.11
Std Dev	3.867

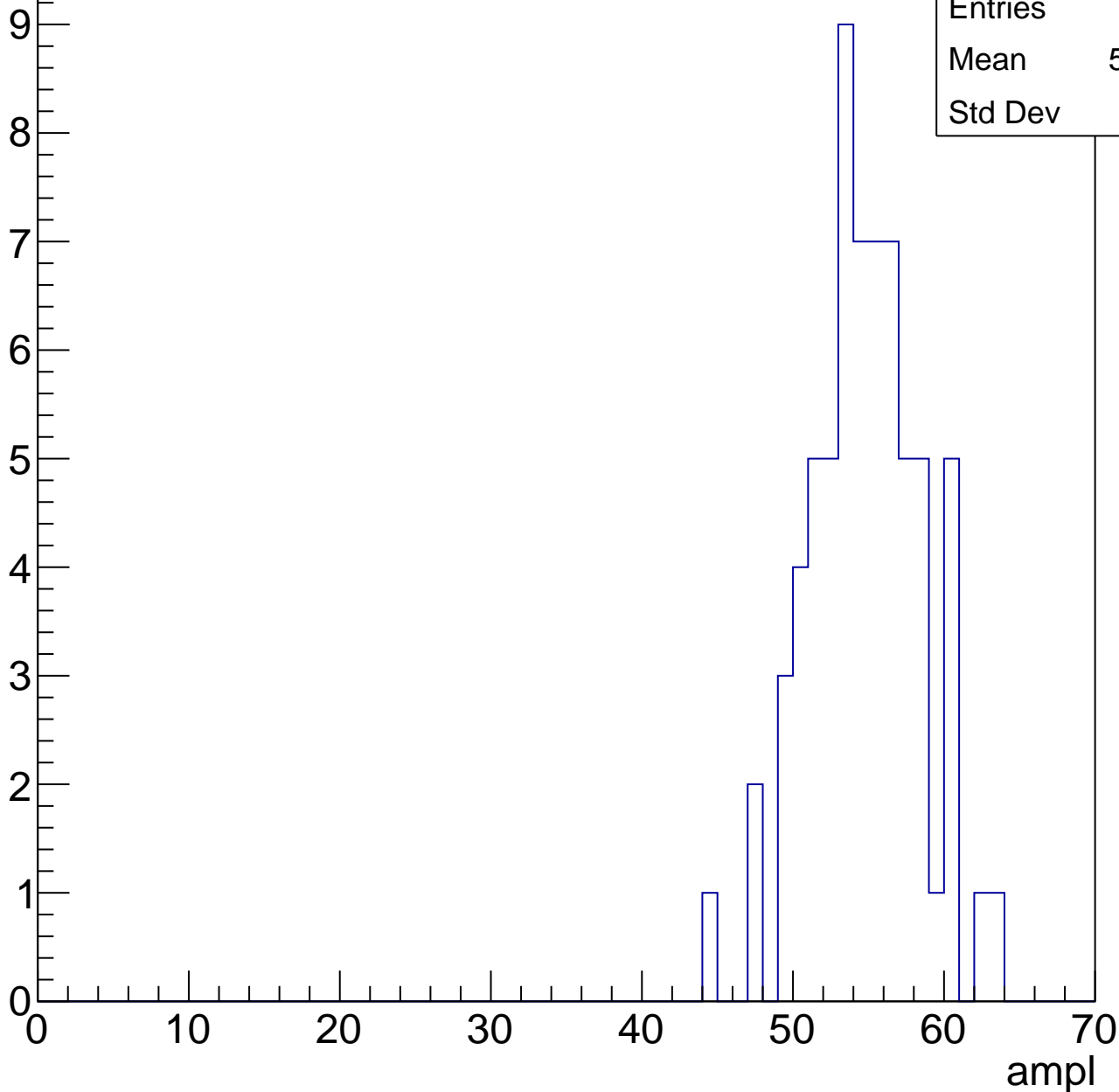


B1L102S, U6-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.28
Std Dev	3.67

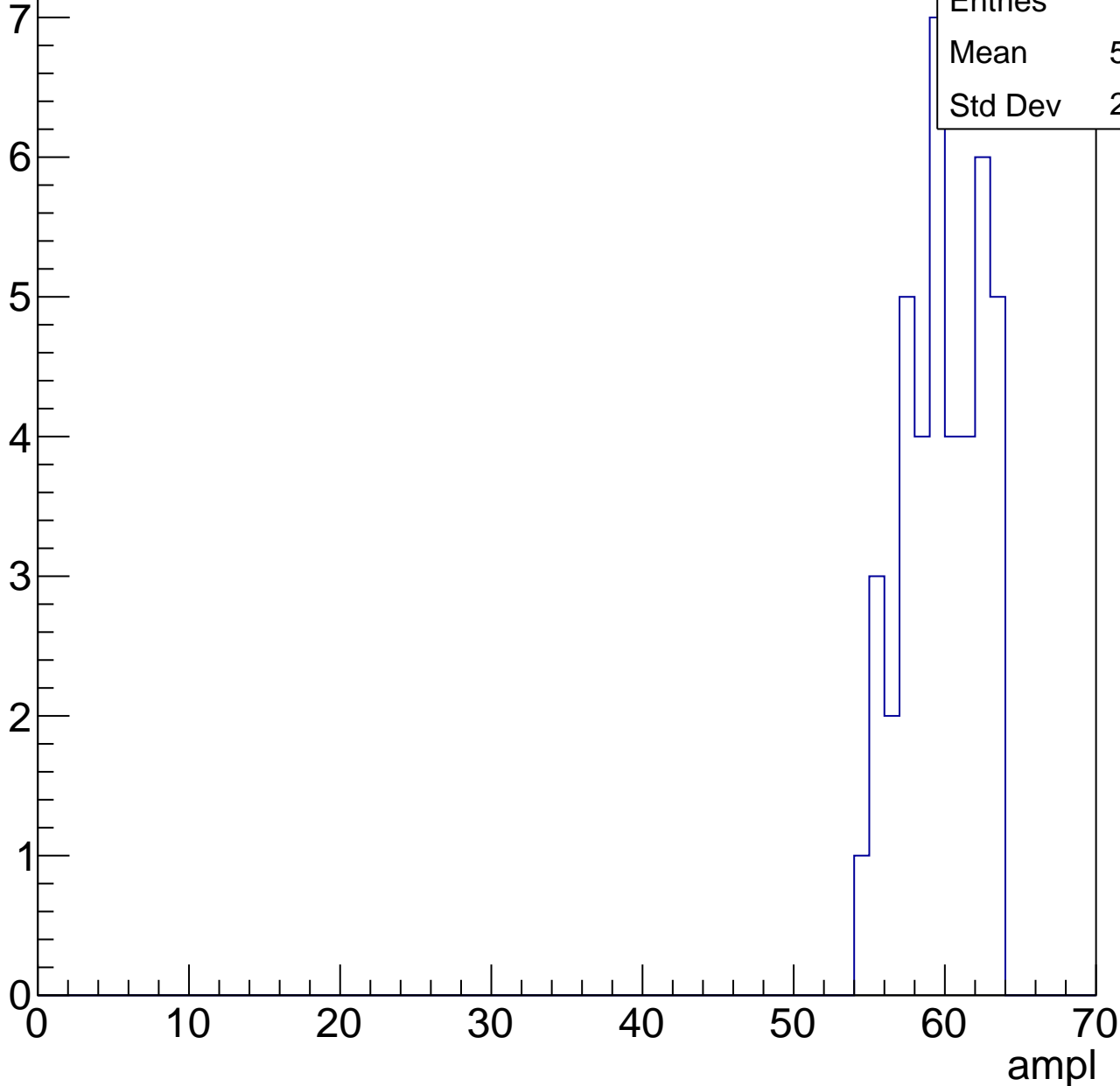


B1L102S, U6-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	59.32
Std Dev	2.542

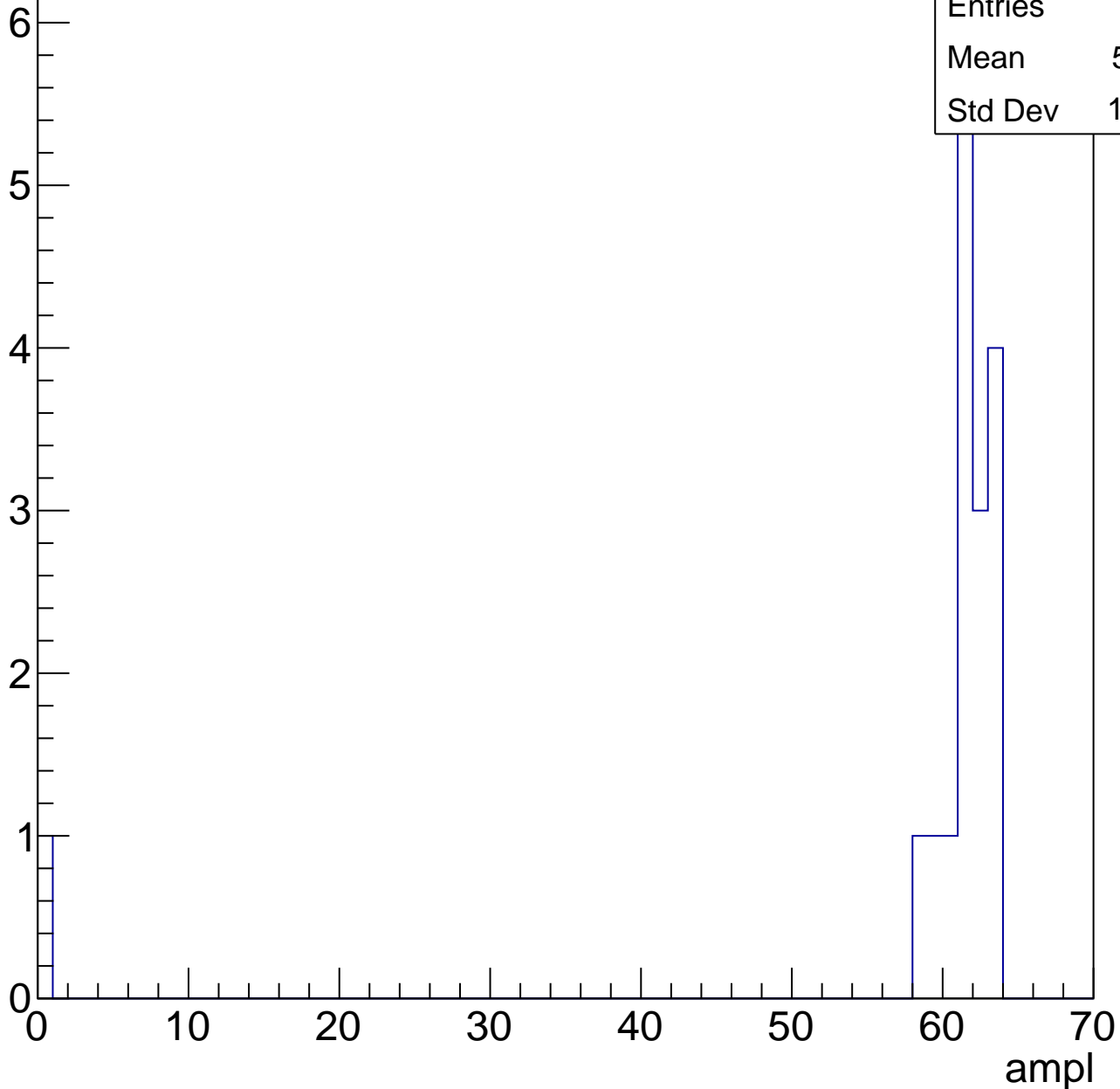


B1L102S, U6-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.71
Std Dev	14.49



B1L102S, U6-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch69, adc0

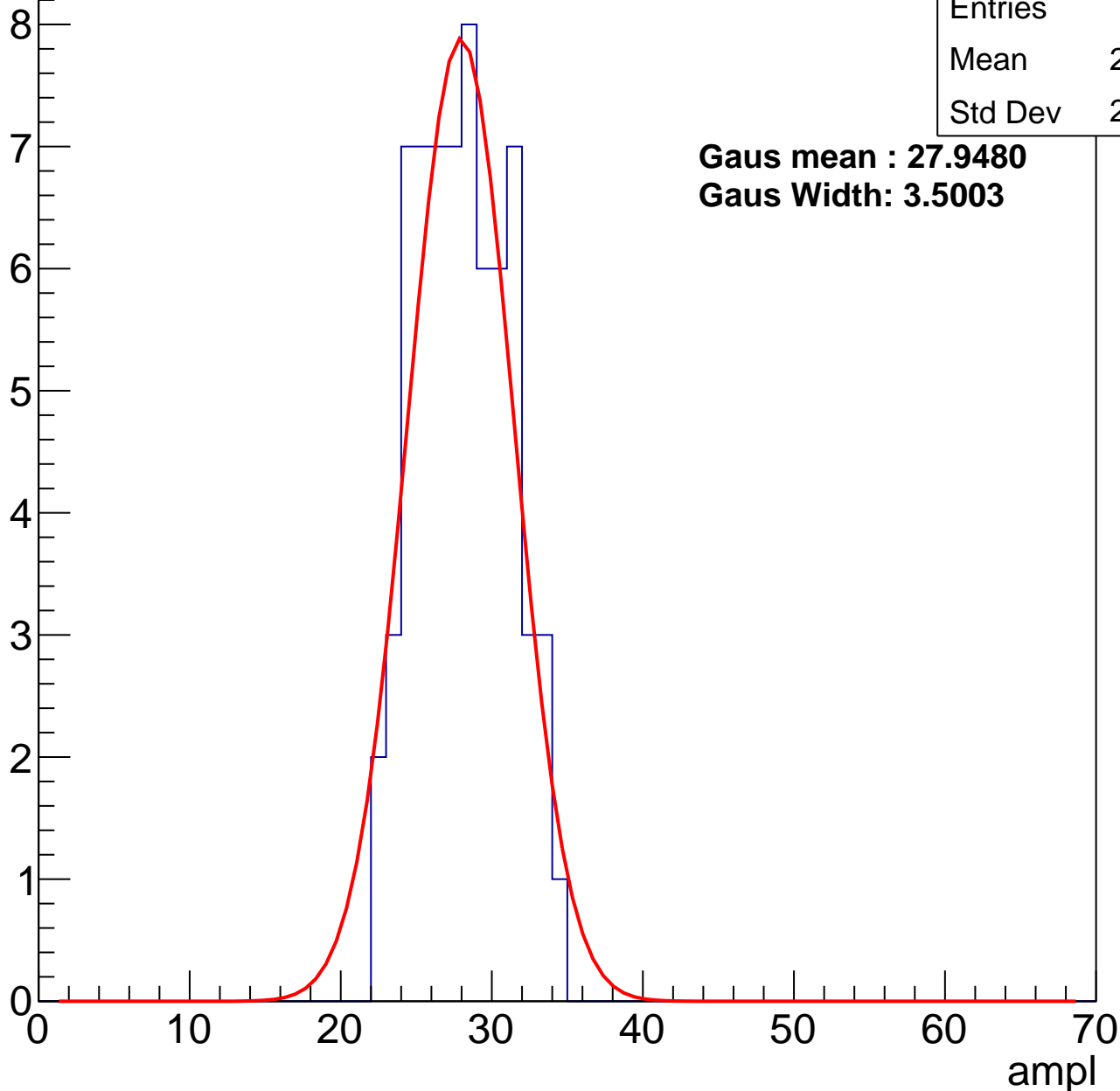
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.63
Std Dev	2.992

Gaus mean : 27.9480

Gaus Width: 3.5003



B1L102S, U6-ch69, adc1

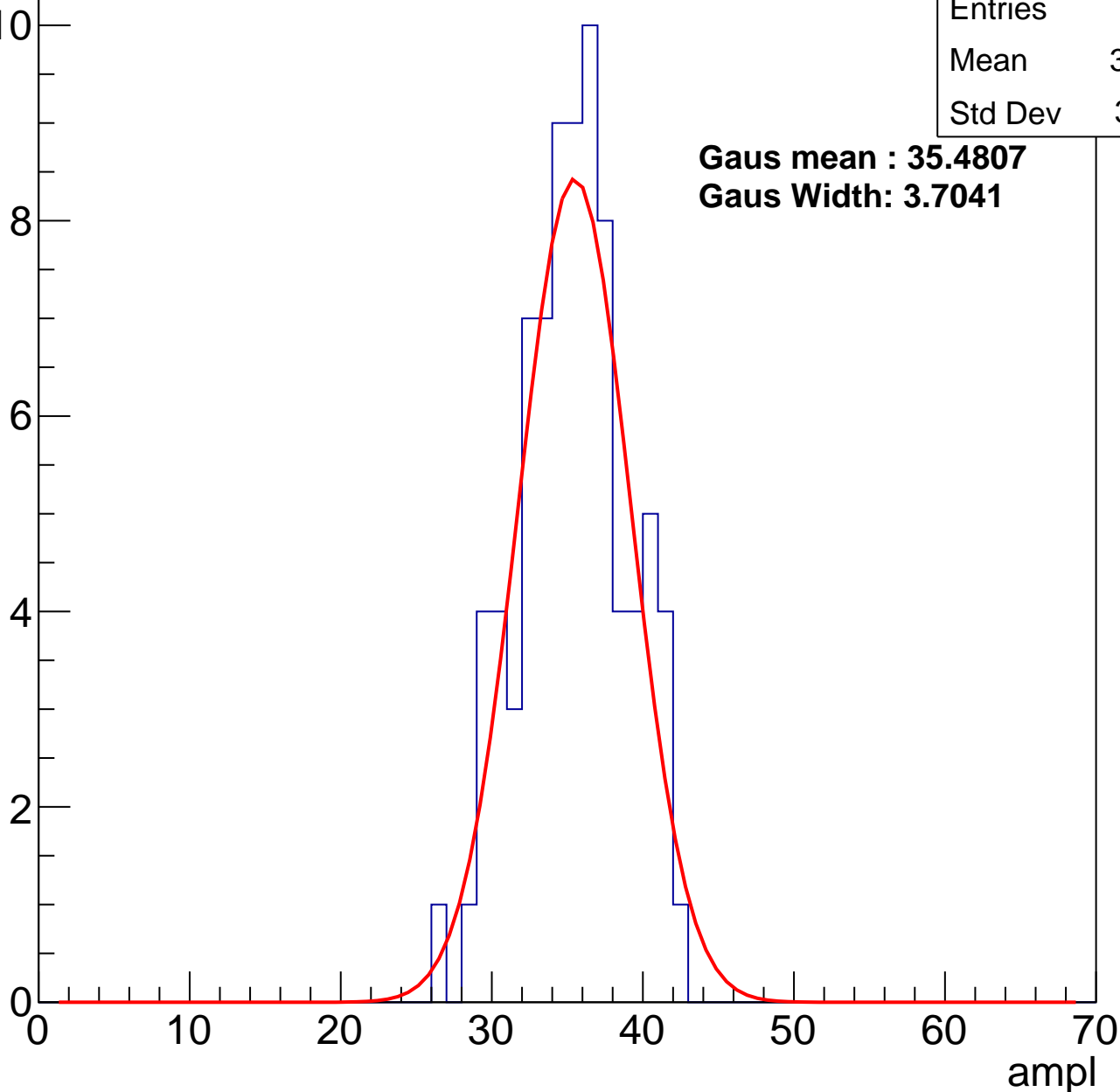
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	34.93
Std Dev	3.481

Gaus mean : 35.4807

Gaus Width: 3.7041



B1L102S, U6-ch69, adc2

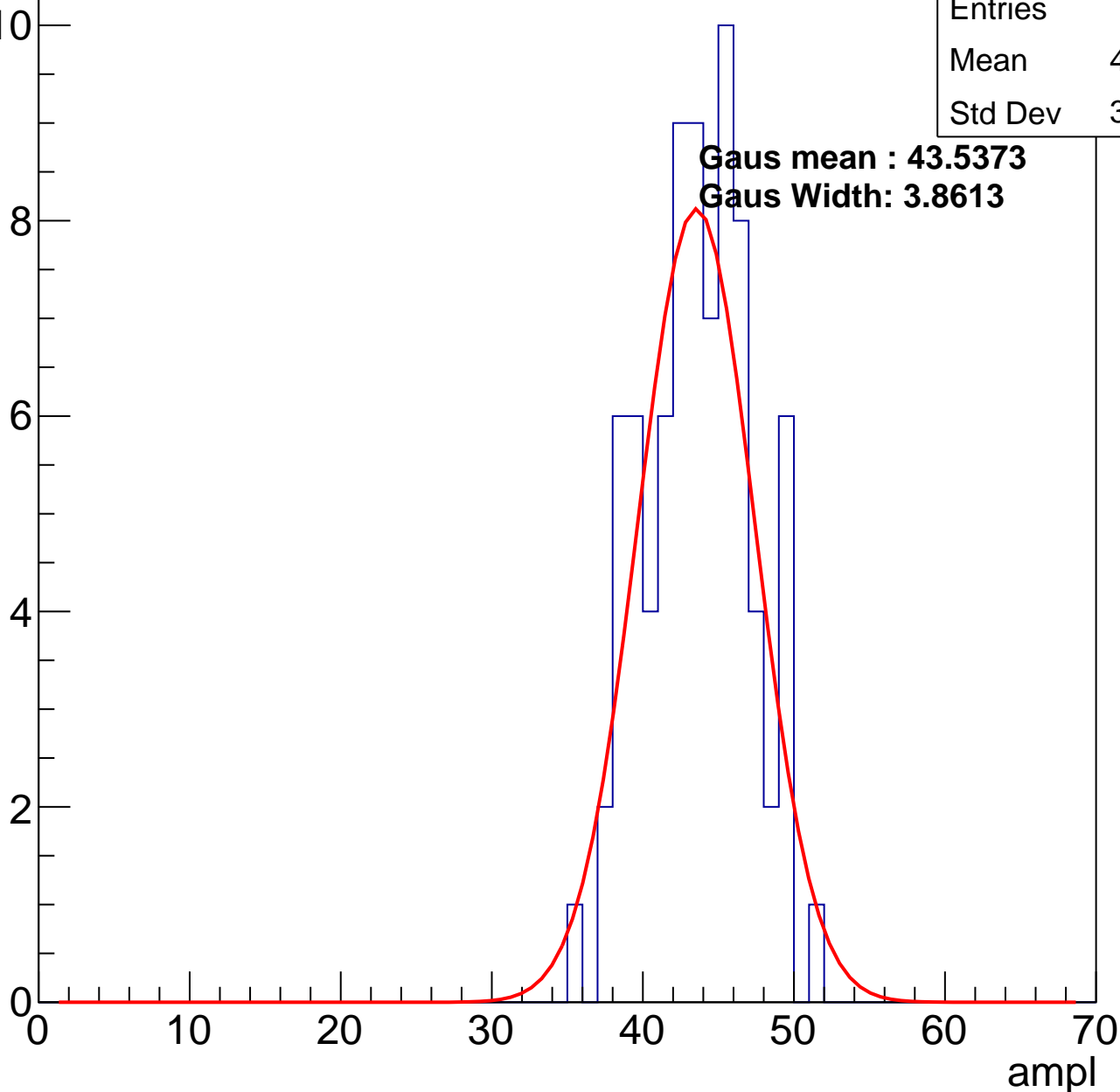
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	43.17
Std Dev	3.445

Gaus mean : 43.5373

Gaus Width: 3.8613

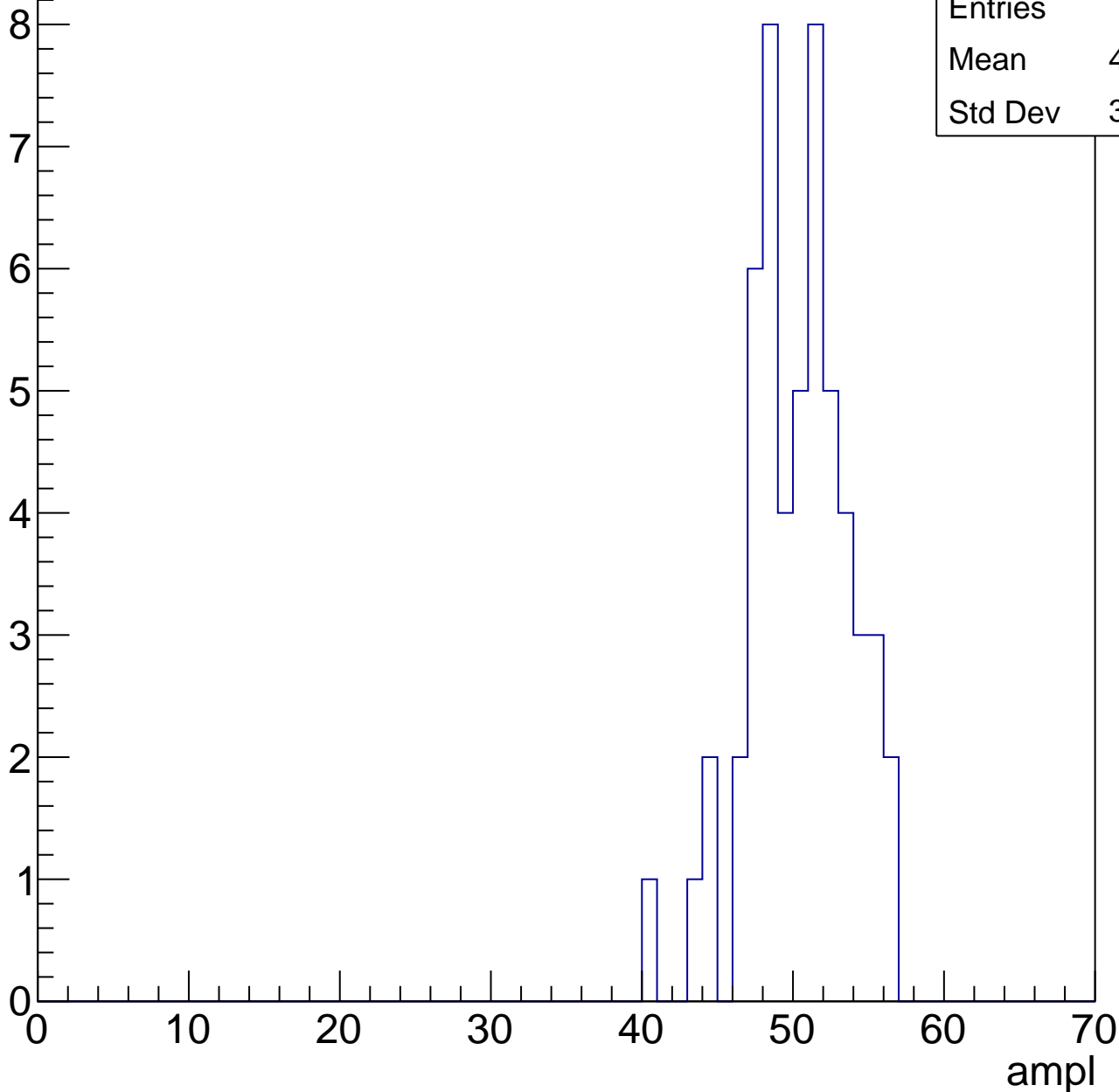


B1L102S, U6-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

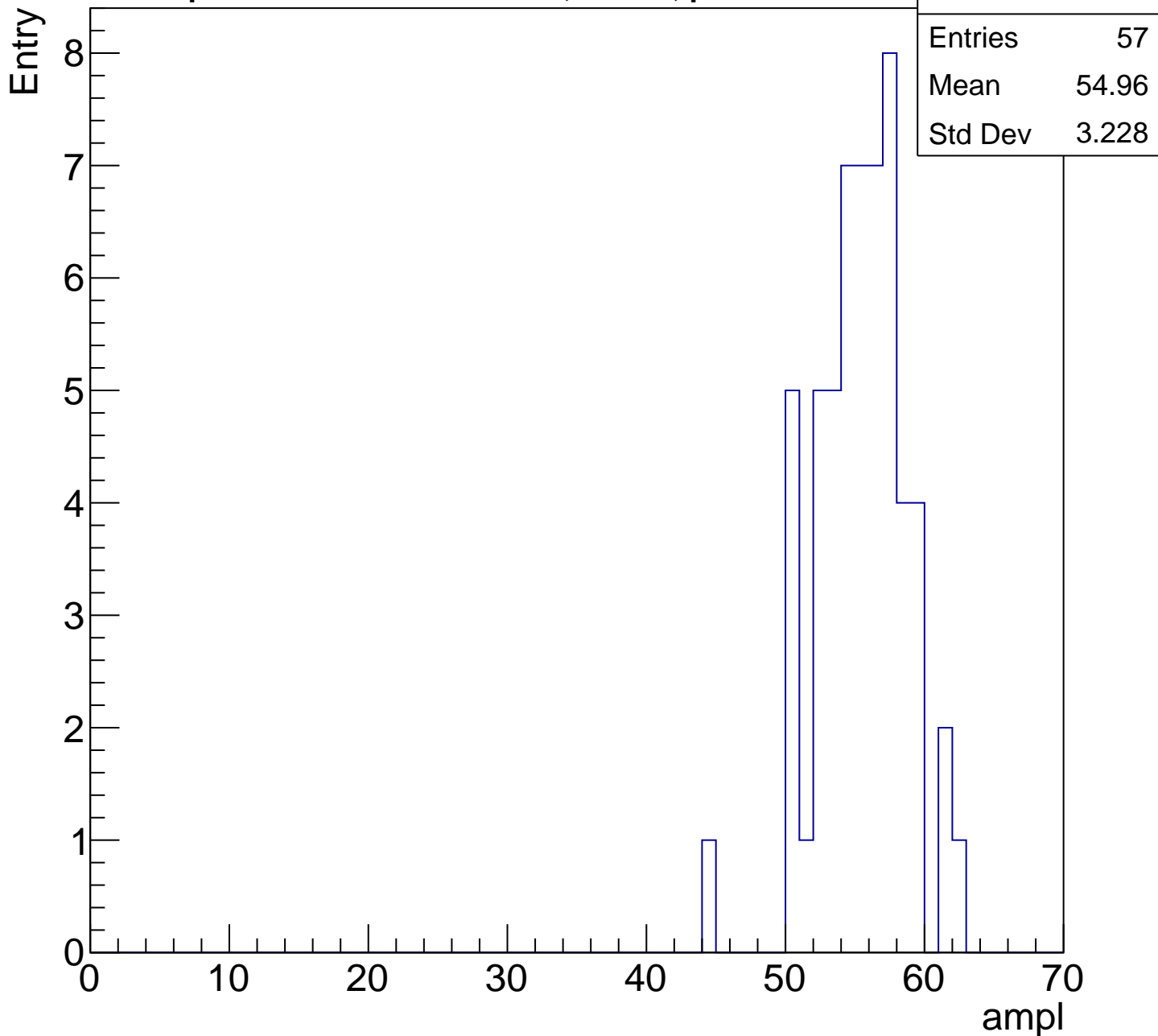
Entry

Entries	54
Mean	49.89
Std Dev	3.337



B1L102S, U6-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

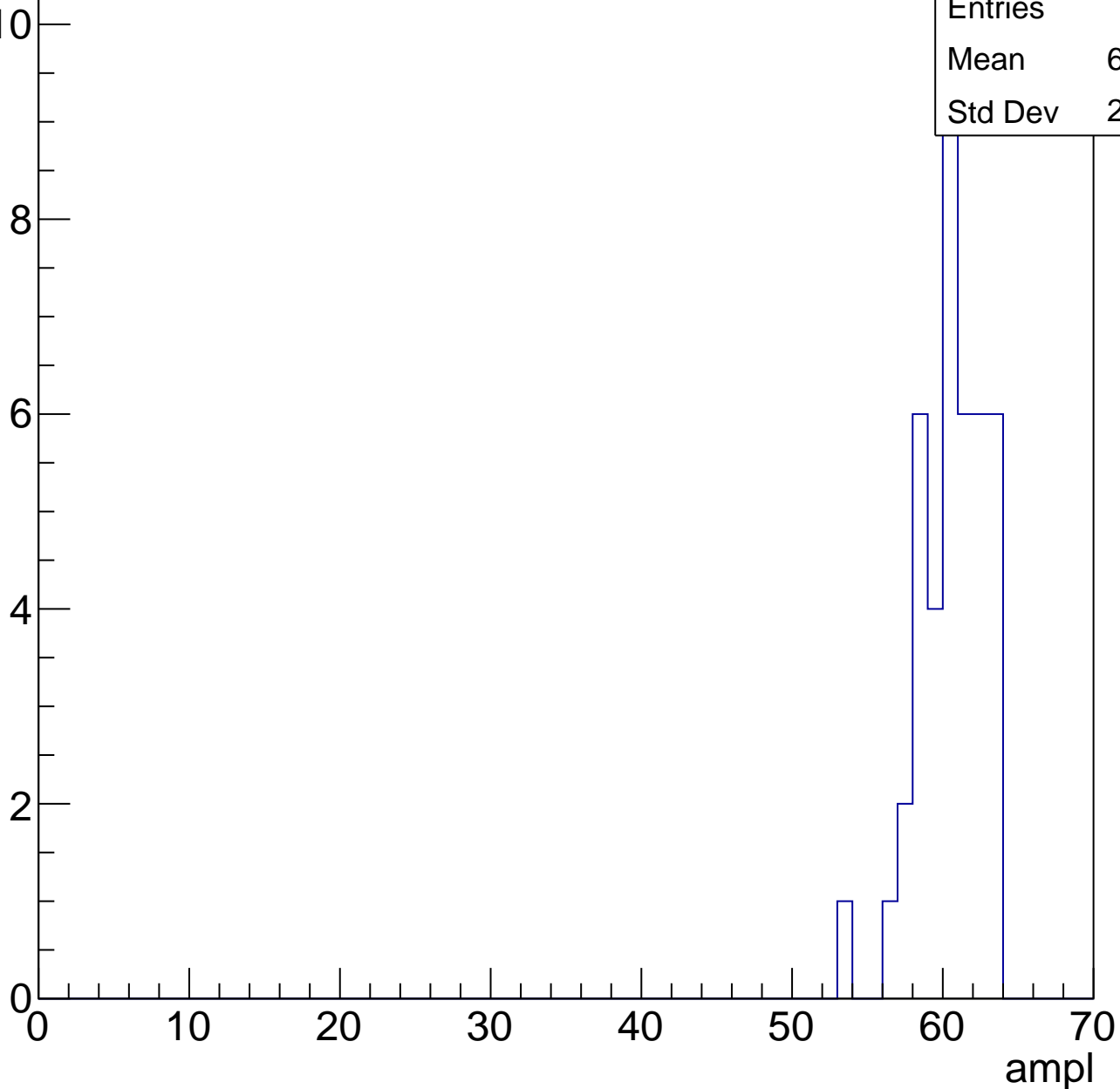


B1L102S, U6-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

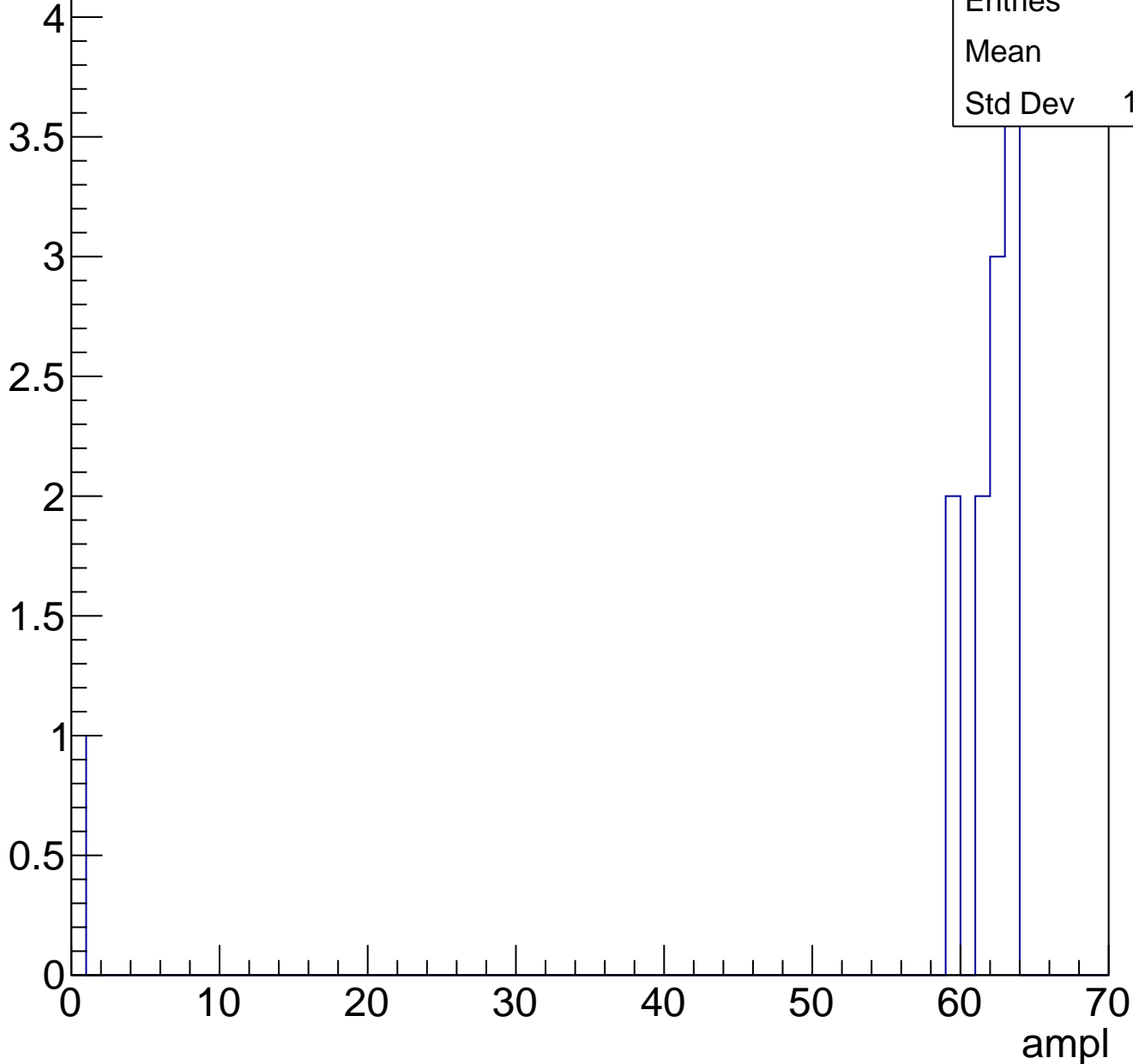
Entries	42
Mean	60.07
Std Dev	2.154



B1L102S, U6-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch70, adc0

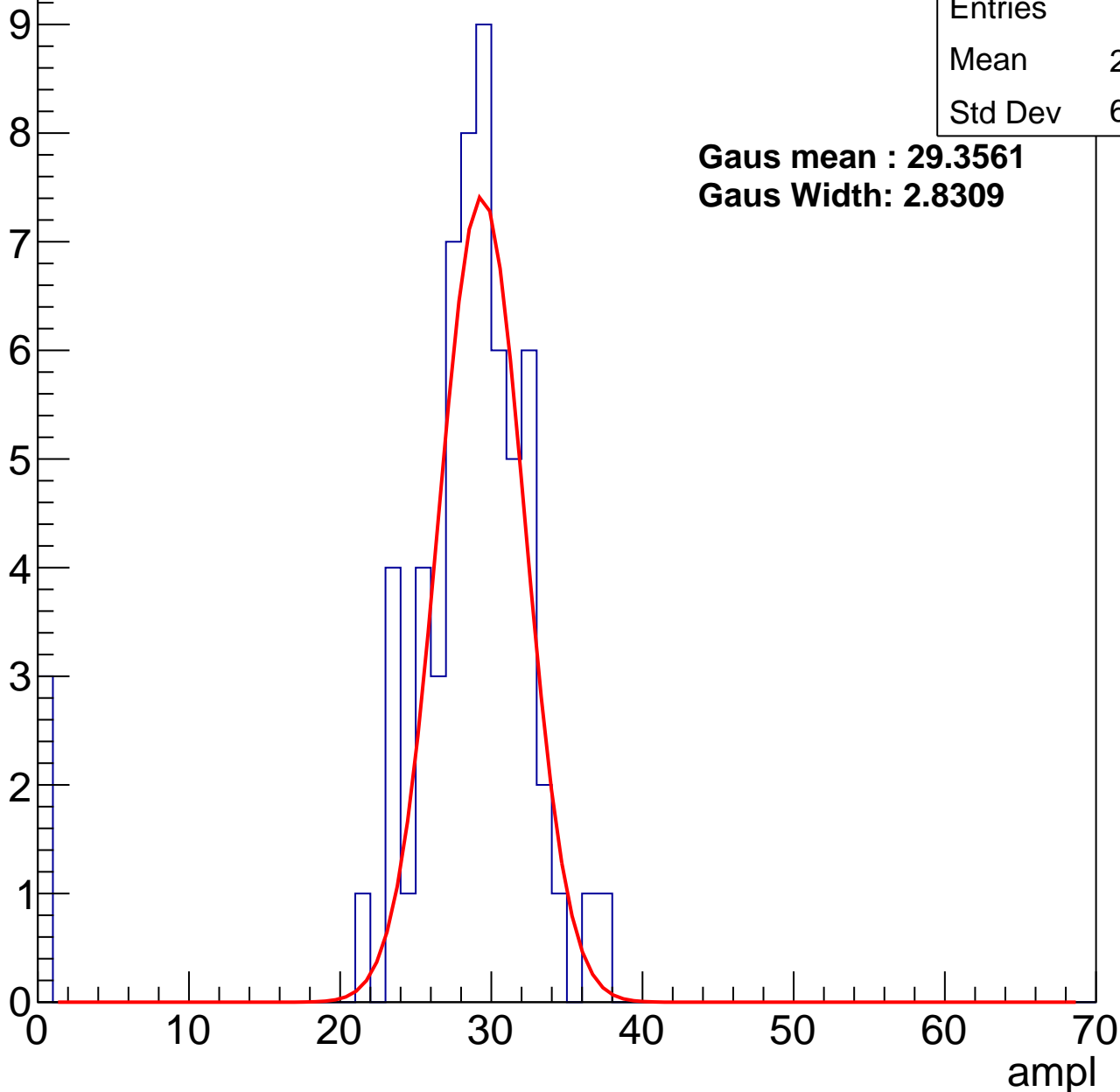
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.24
Std Dev	6.888

Gaus mean : 29.3561

Gaus Width: 2.8309



B1L102S, U6-ch70, adc1

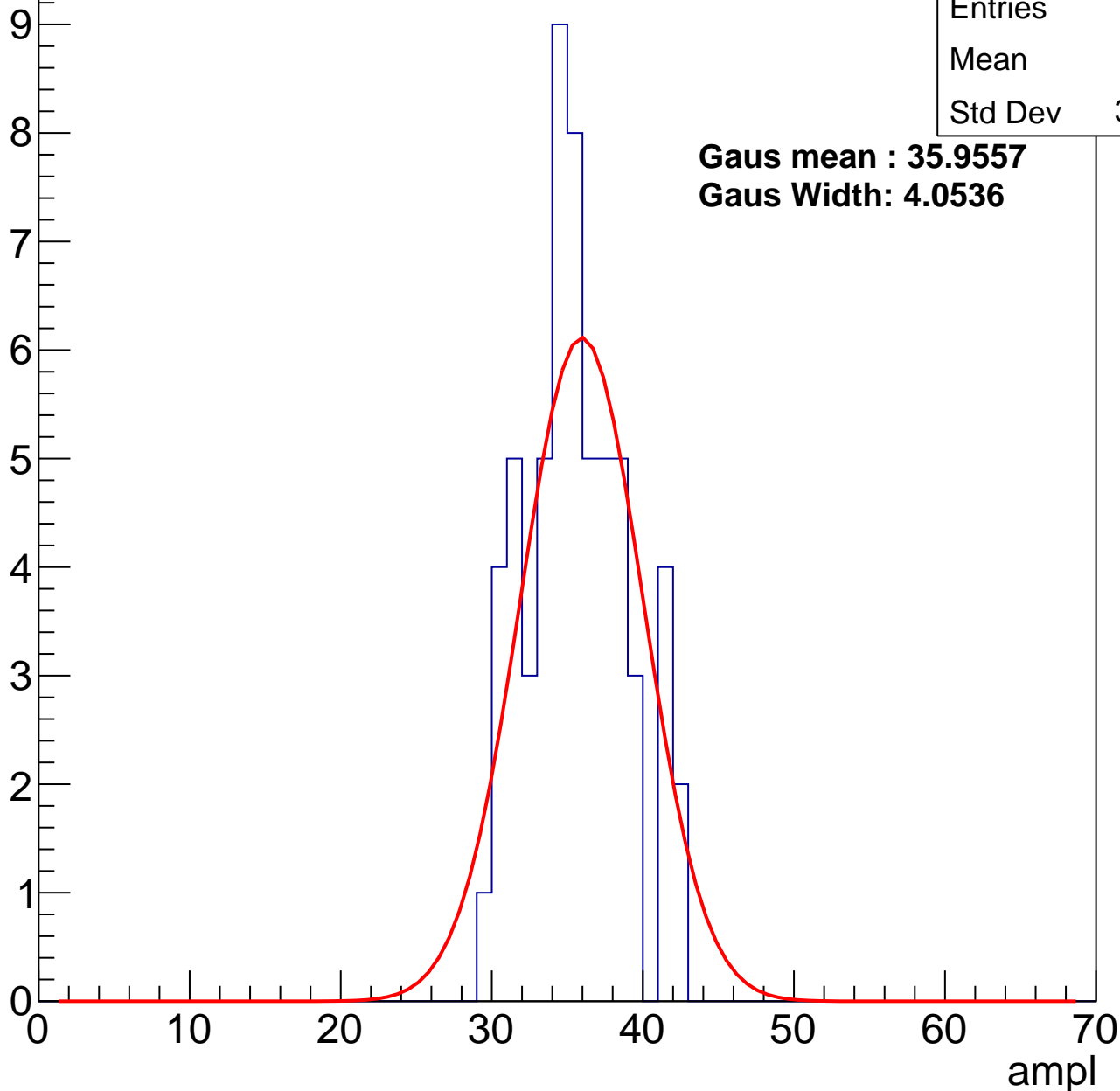
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.1
Std Dev	3.271

Gaus mean : 35.9557

Gaus Width: 4.0536



B1L102S, U6-ch70, adc2

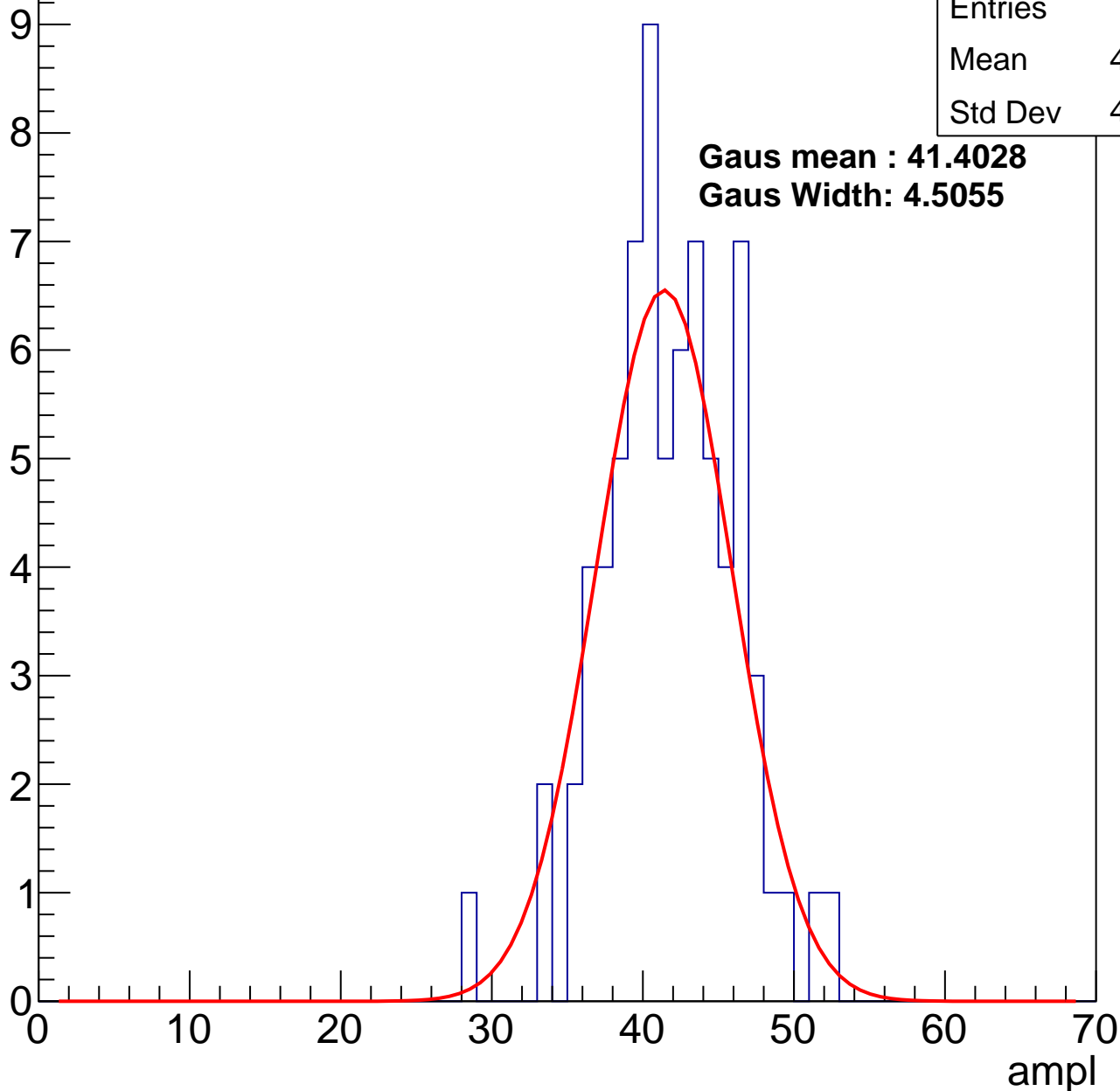
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.33
Std Dev	4.269

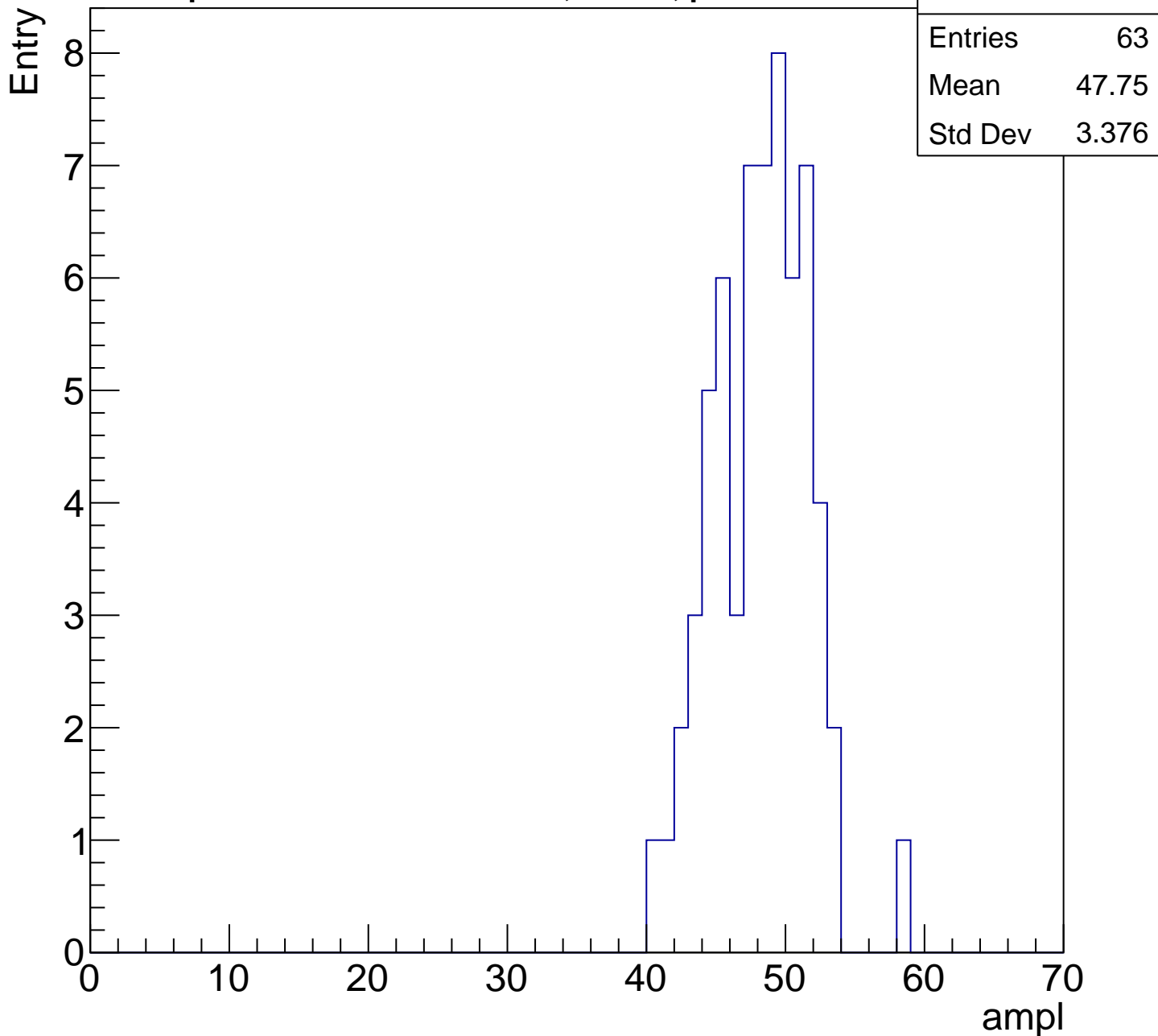
Gaus mean : 41.4028

Gaus Width: 4.5055



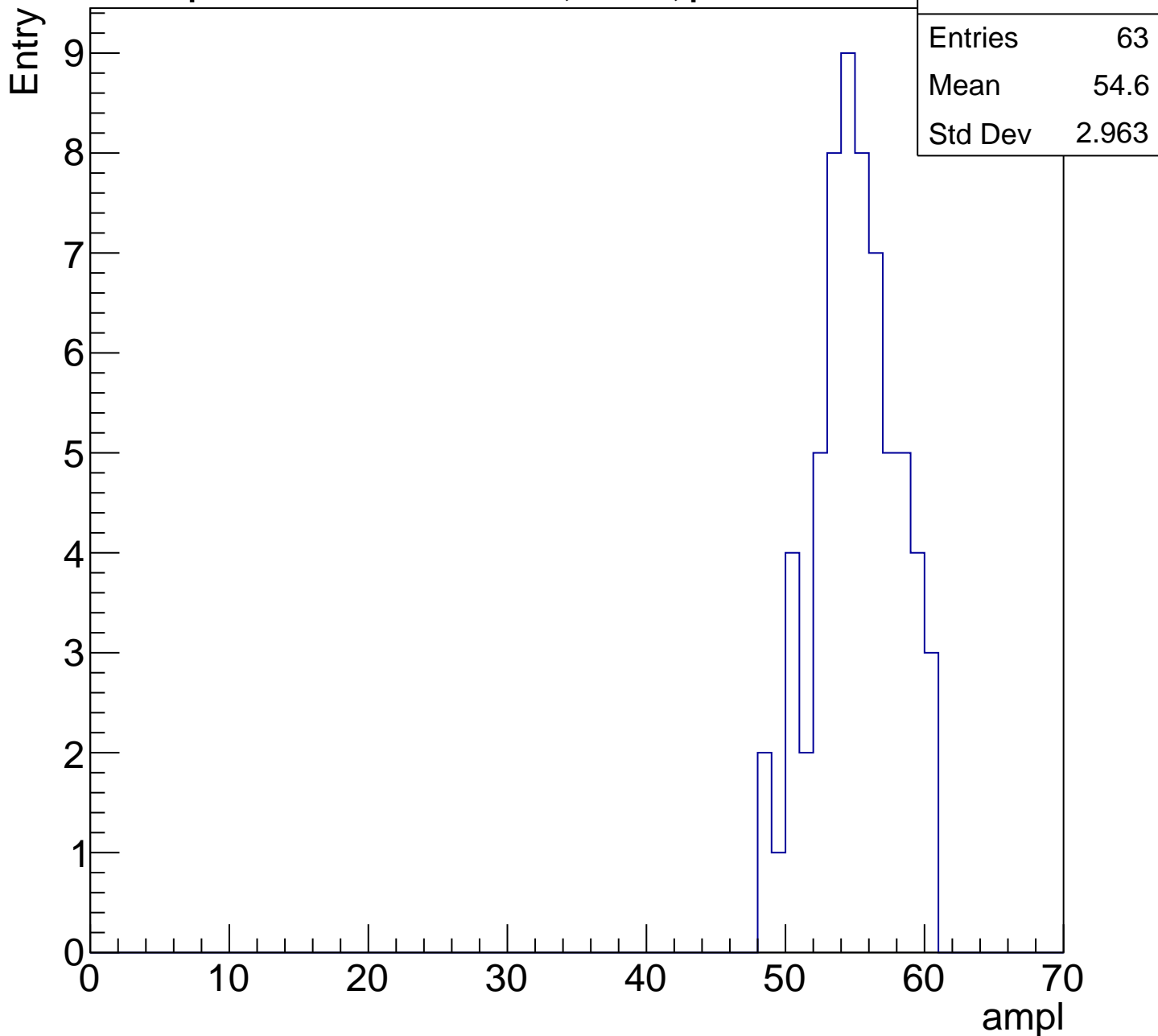
B1L102S, U6-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

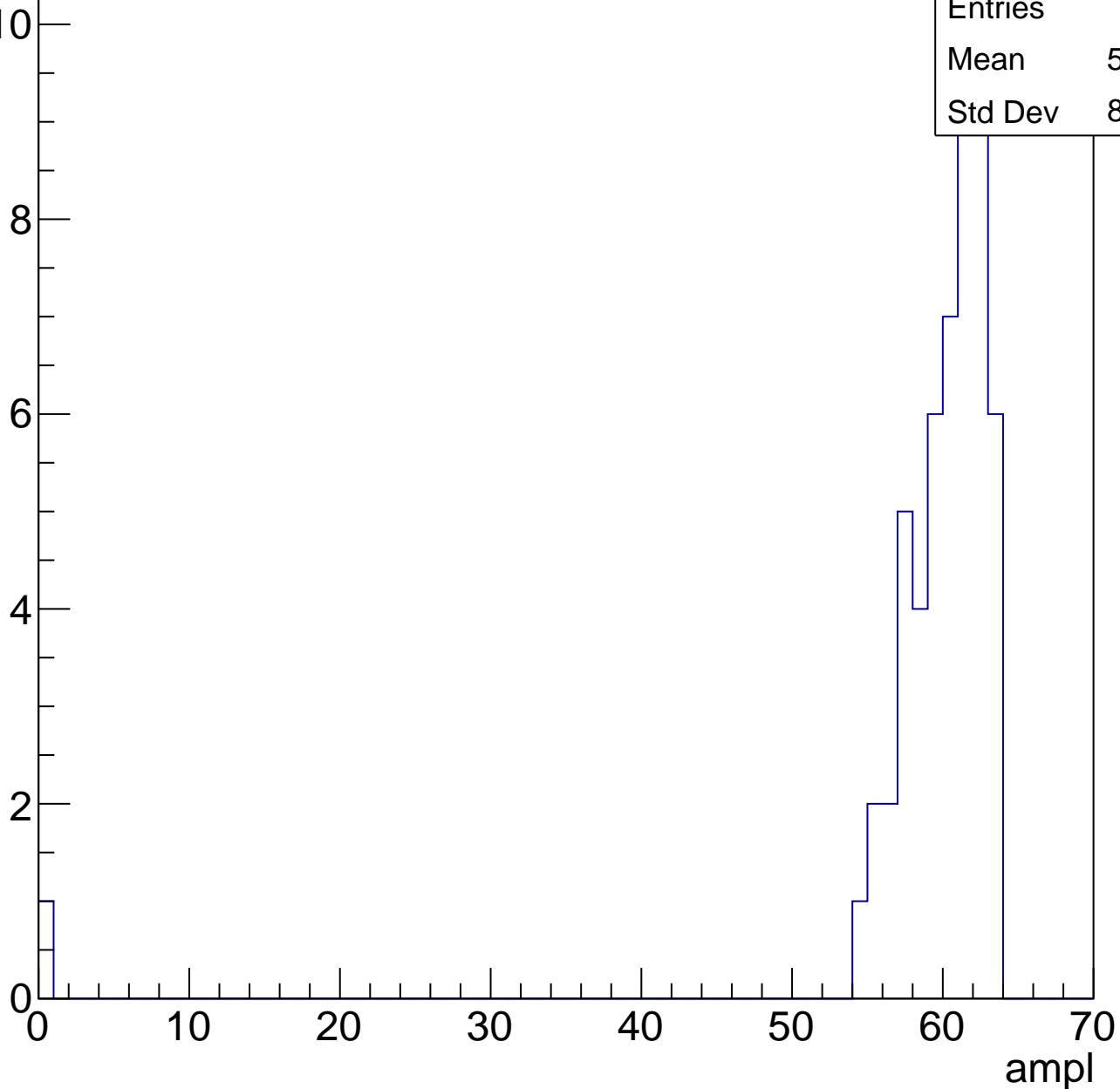


B1L102S, U6-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

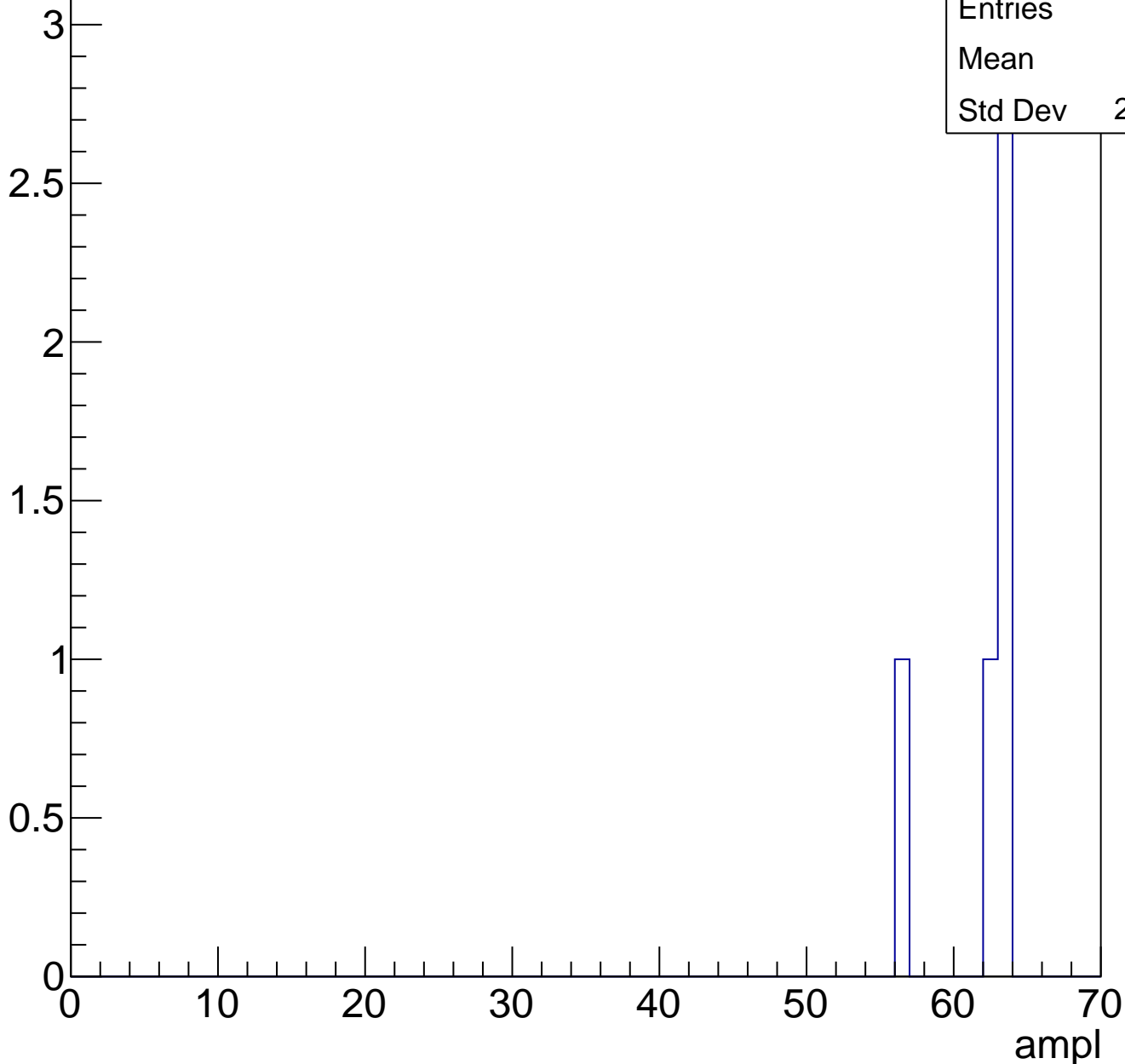
Entries	53
Mean	58.74
Std Dev	8.468



B1L102S, U6-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch71, adc0

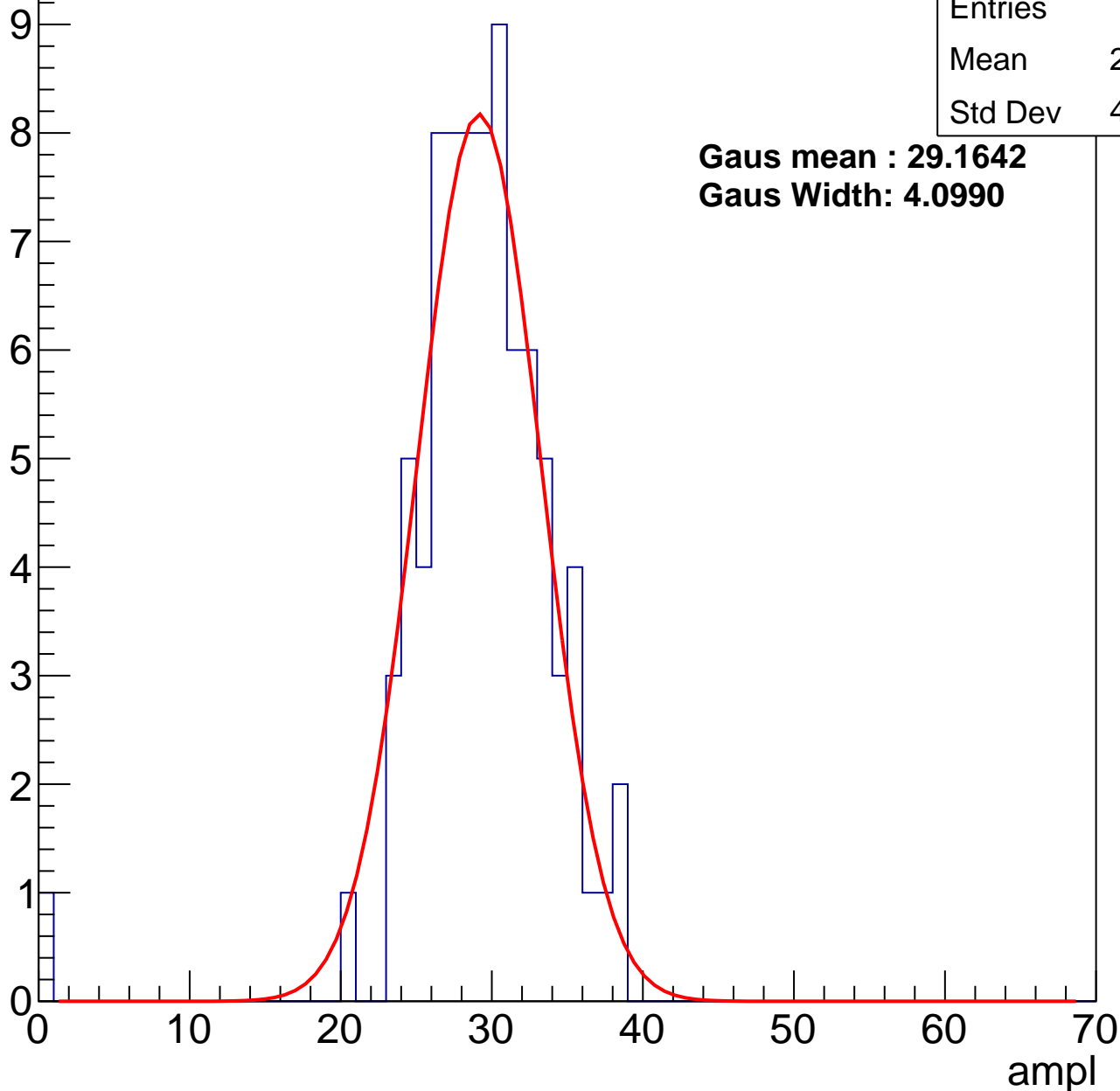
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.83
Std Dev	4.896

Gaus mean : 29.1642

Gaus Width: 4.0990



B1L102S, U6-ch71, adc1

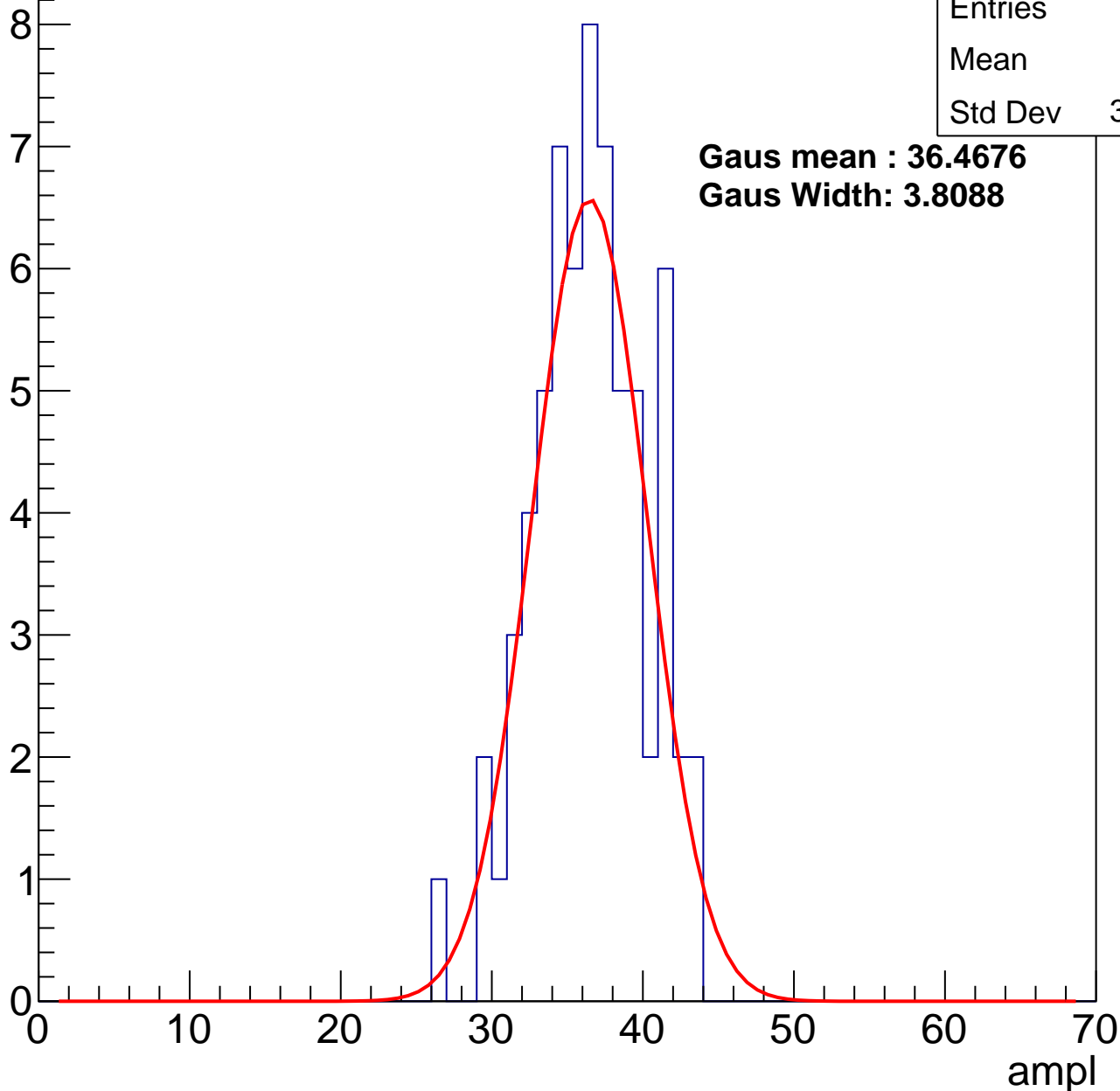
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36
Std Dev	3.643

Gaus mean : 36.4676

Gaus Width: 3.8088



B1L102S, U6-ch71, adc2

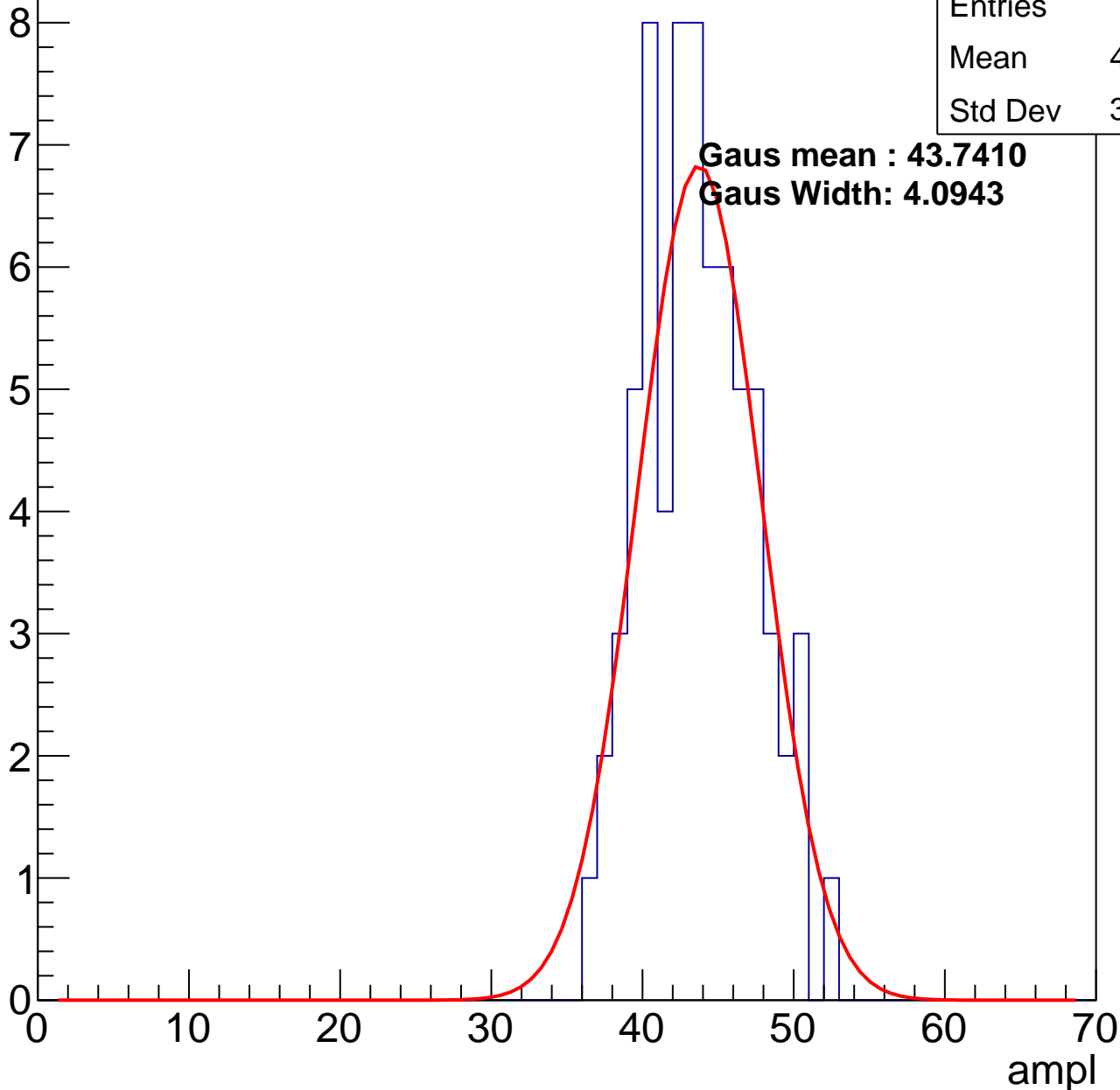
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	43.23
Std Dev	3.582

Gaus mean : 43.7410

Gaus Width: 4.0943

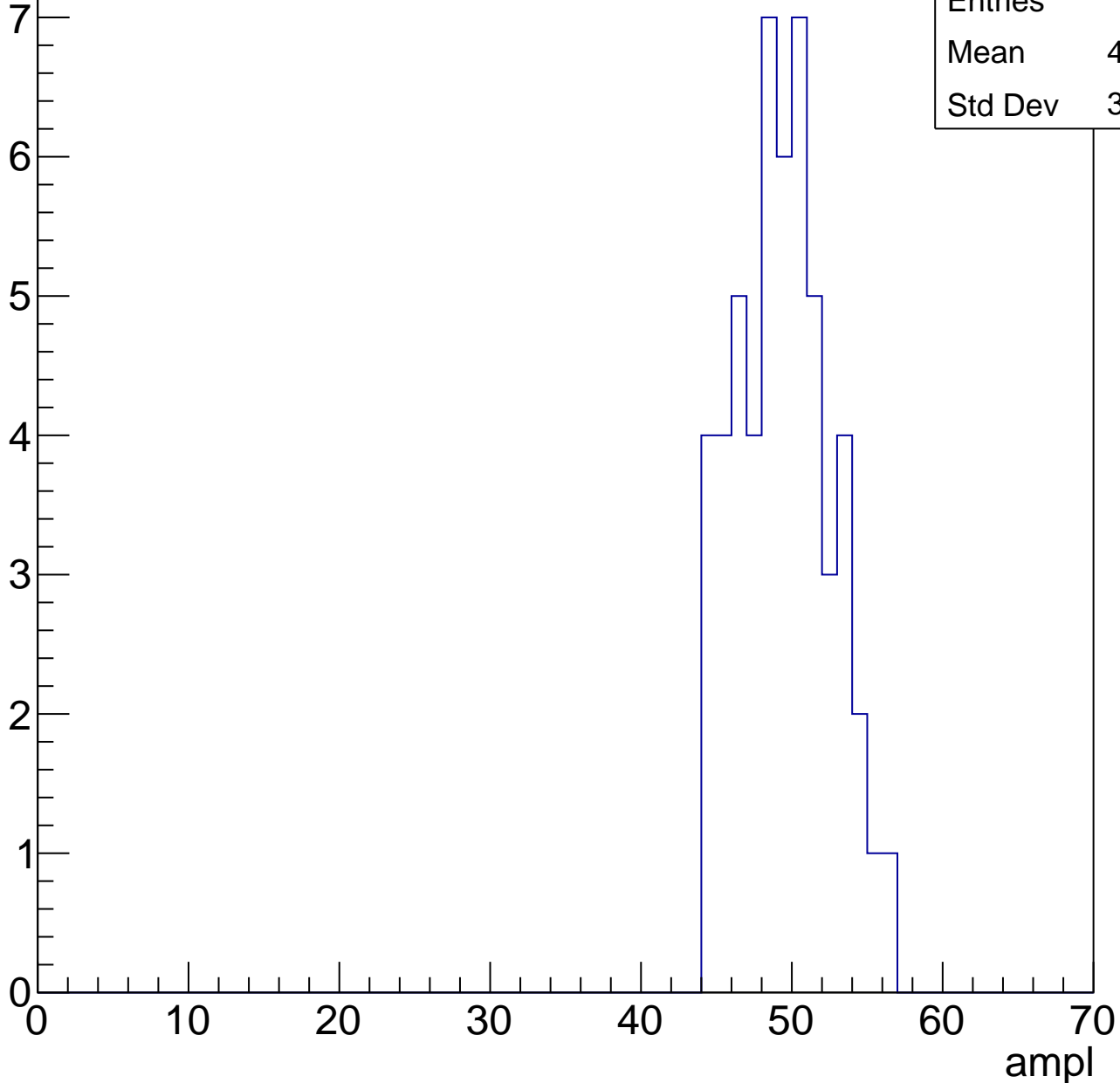


B1L102S, U6-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.98
Std Dev	3.025

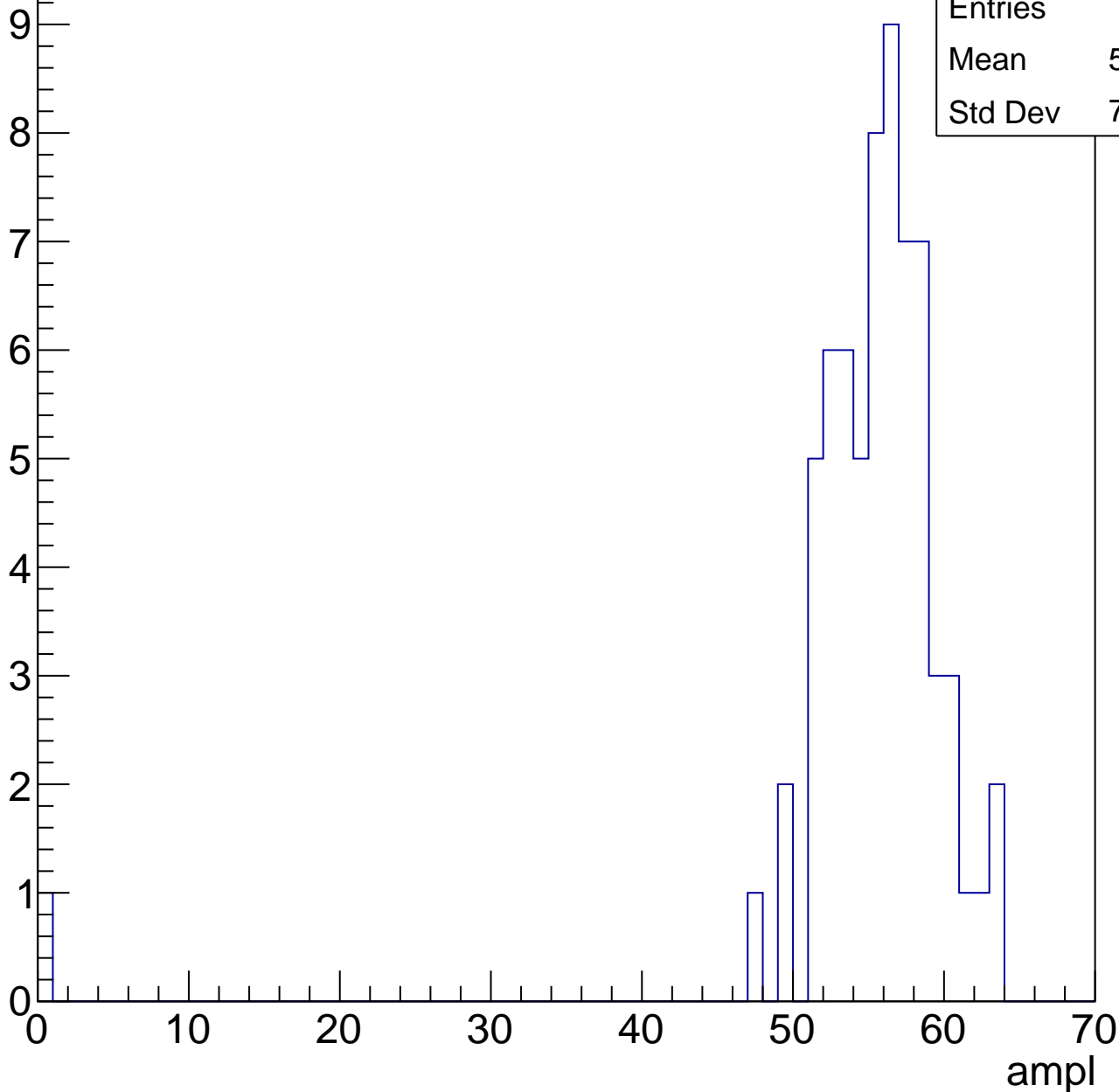


B1L102S, U6-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	54.55
Std Dev	7.478



B1L102S, U6-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.73
Std Dev	2.46

ampl

0

10

20

30

40

50

60

70

B1L102S, U6-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.56
Std Dev	1.499

B1L102S, U6-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U6-ch72, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	28.1
Std Dev	3.33

Gaus mean : 28.8064

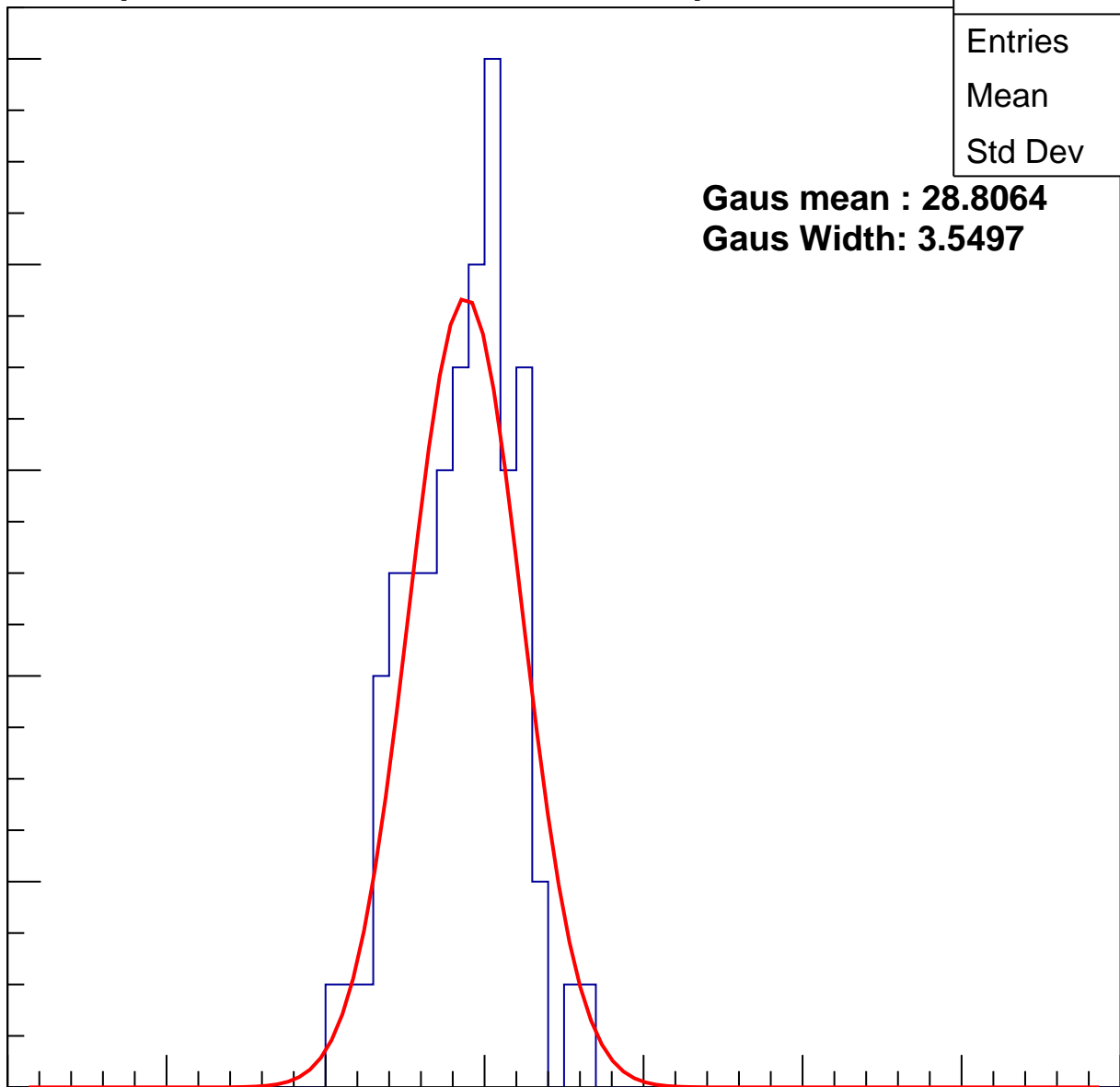
Gaus Width: 3.5497

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch72, adc1

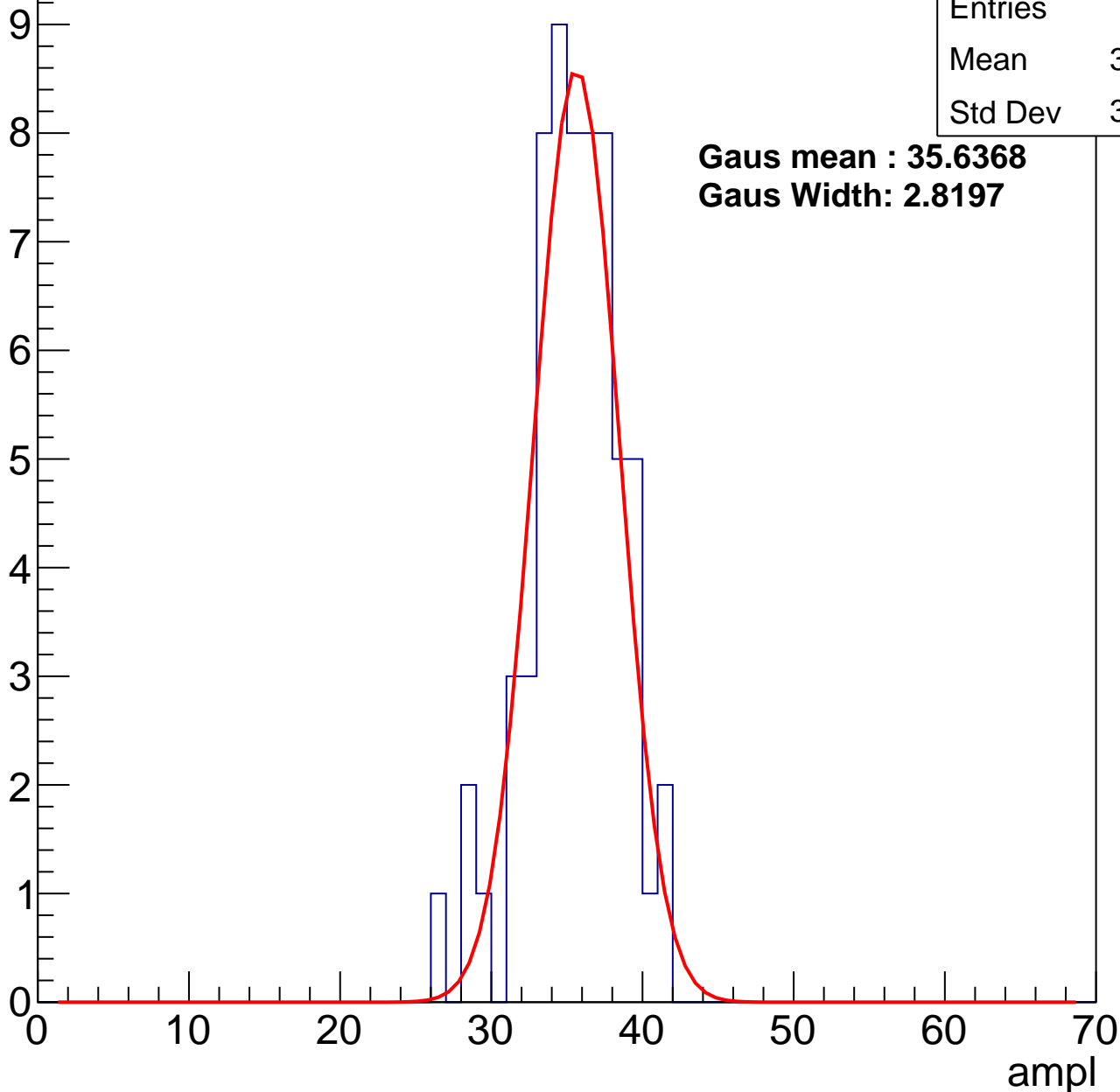
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.02
Std Dev	3.044

Gaus mean : 35.6368

Gaus Width: 2.8197



B1L102S, U6-ch72, adc2

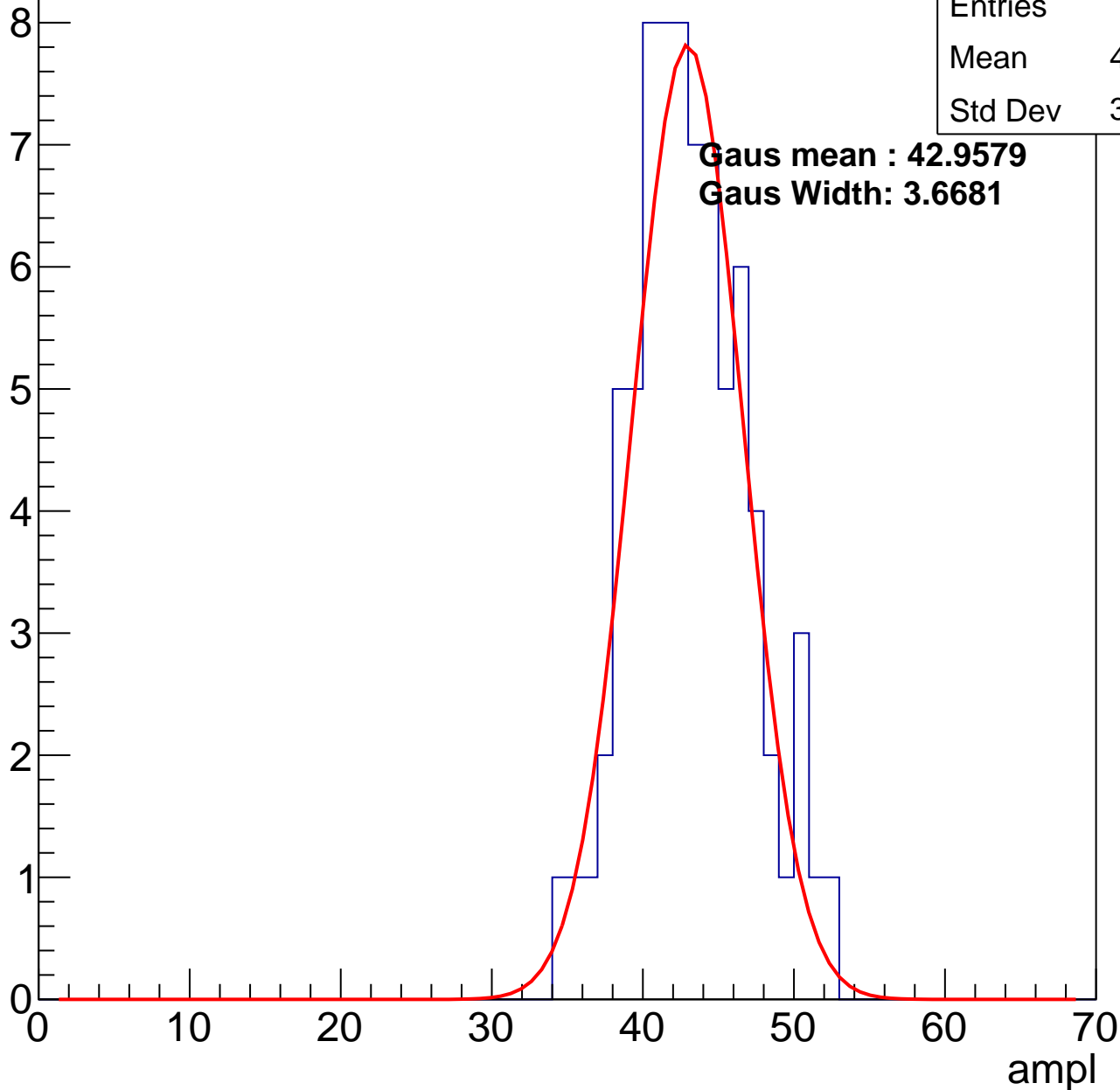
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.68
Std Dev	3.802

Gaus mean : 42.9579

Gaus Width: 3.6681

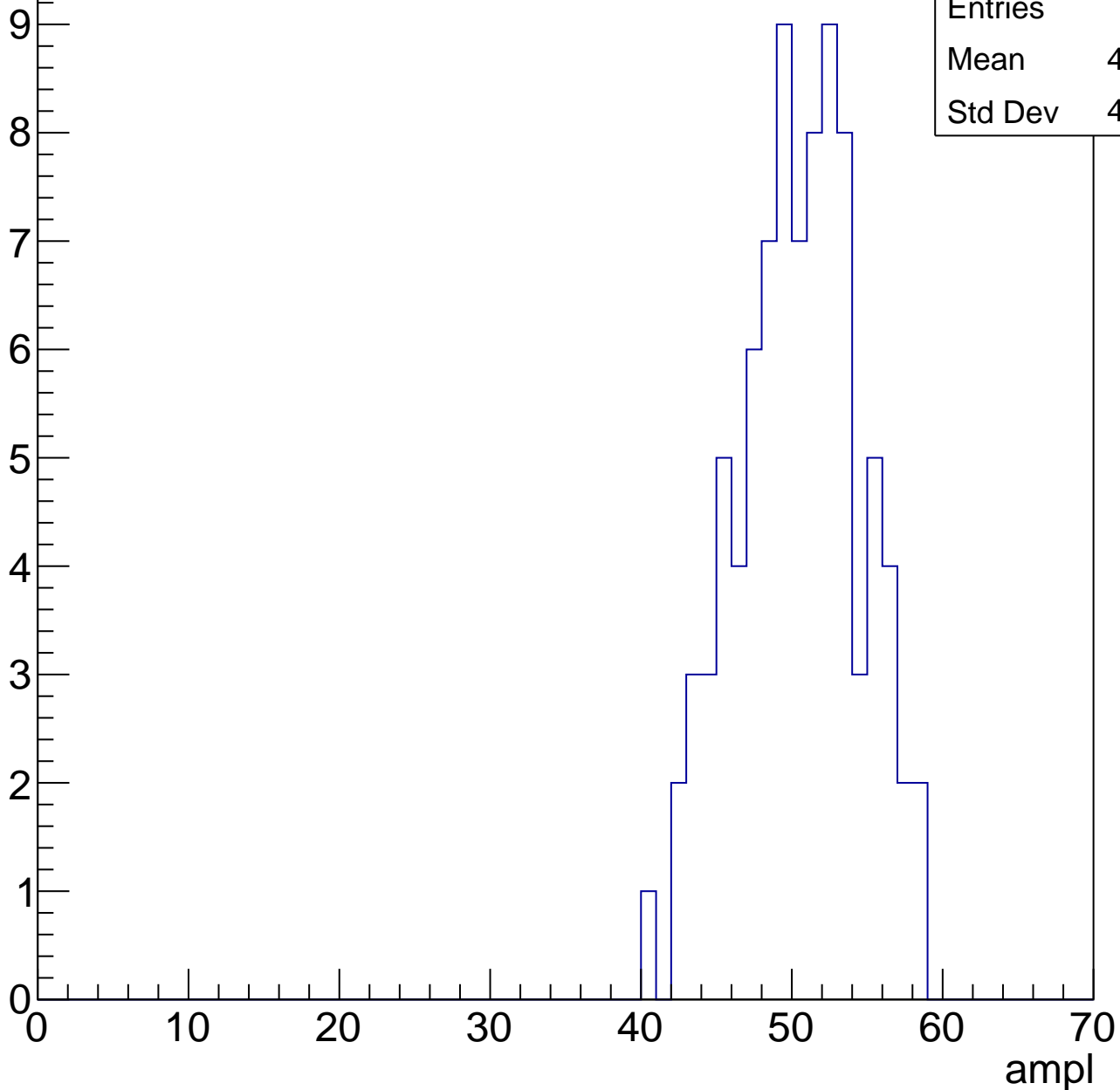


B1L102S, U6-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	49.93
Std Dev	4.019

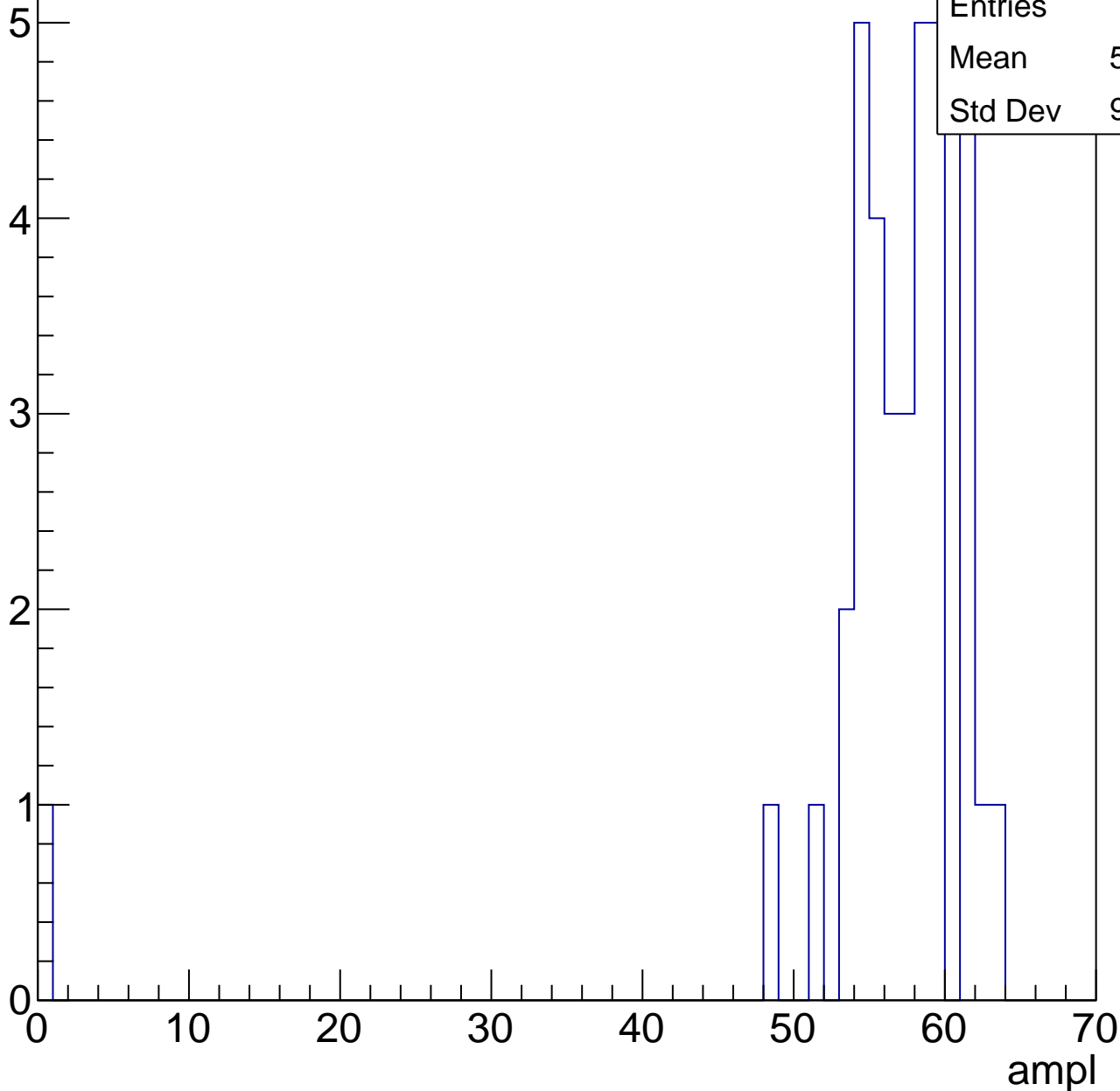


B1L102S, U6-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	37
Mean	55.38
Std Dev	9.769



B1L102S, U6-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

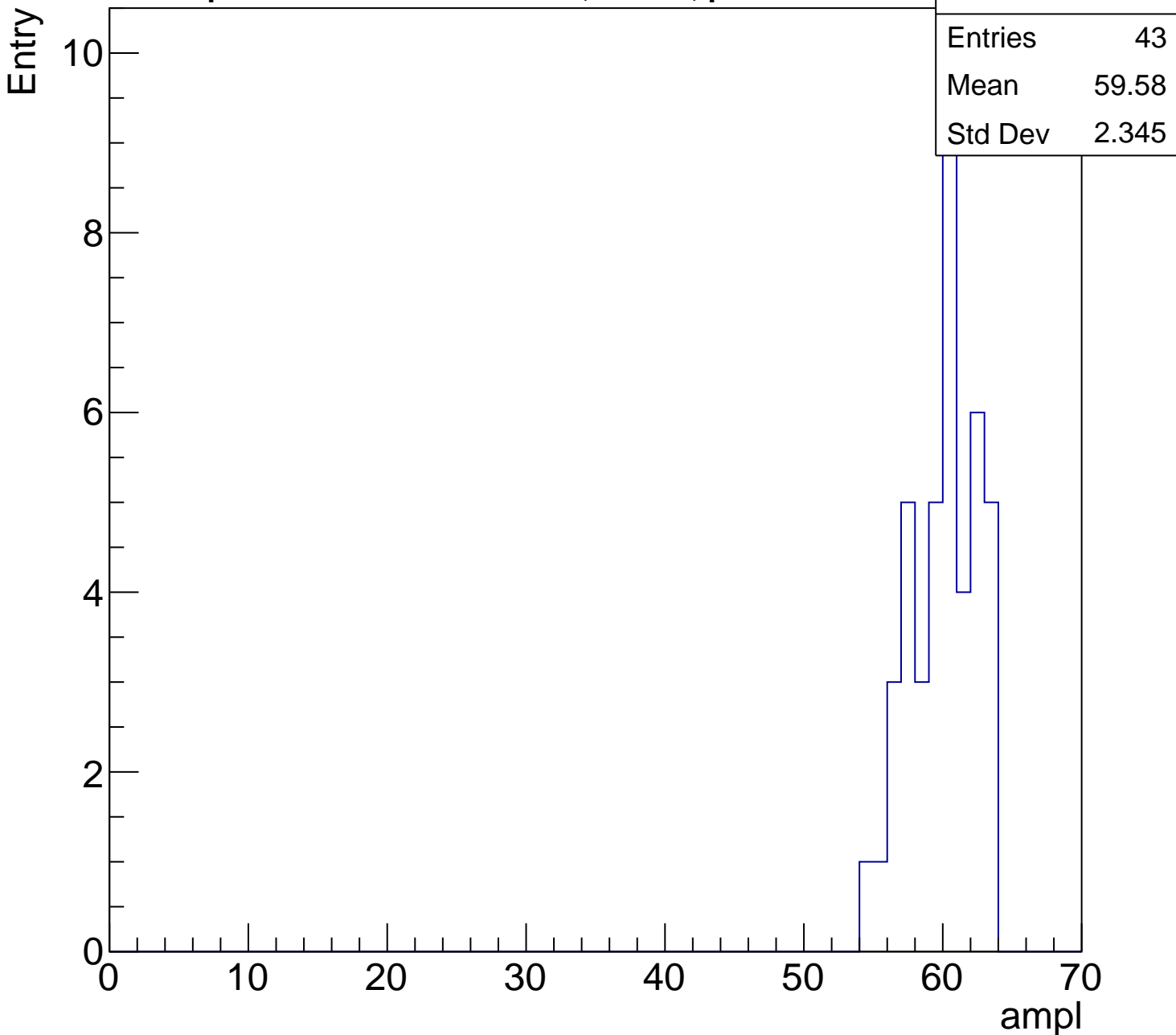
43

Mean

59.58

Std Dev

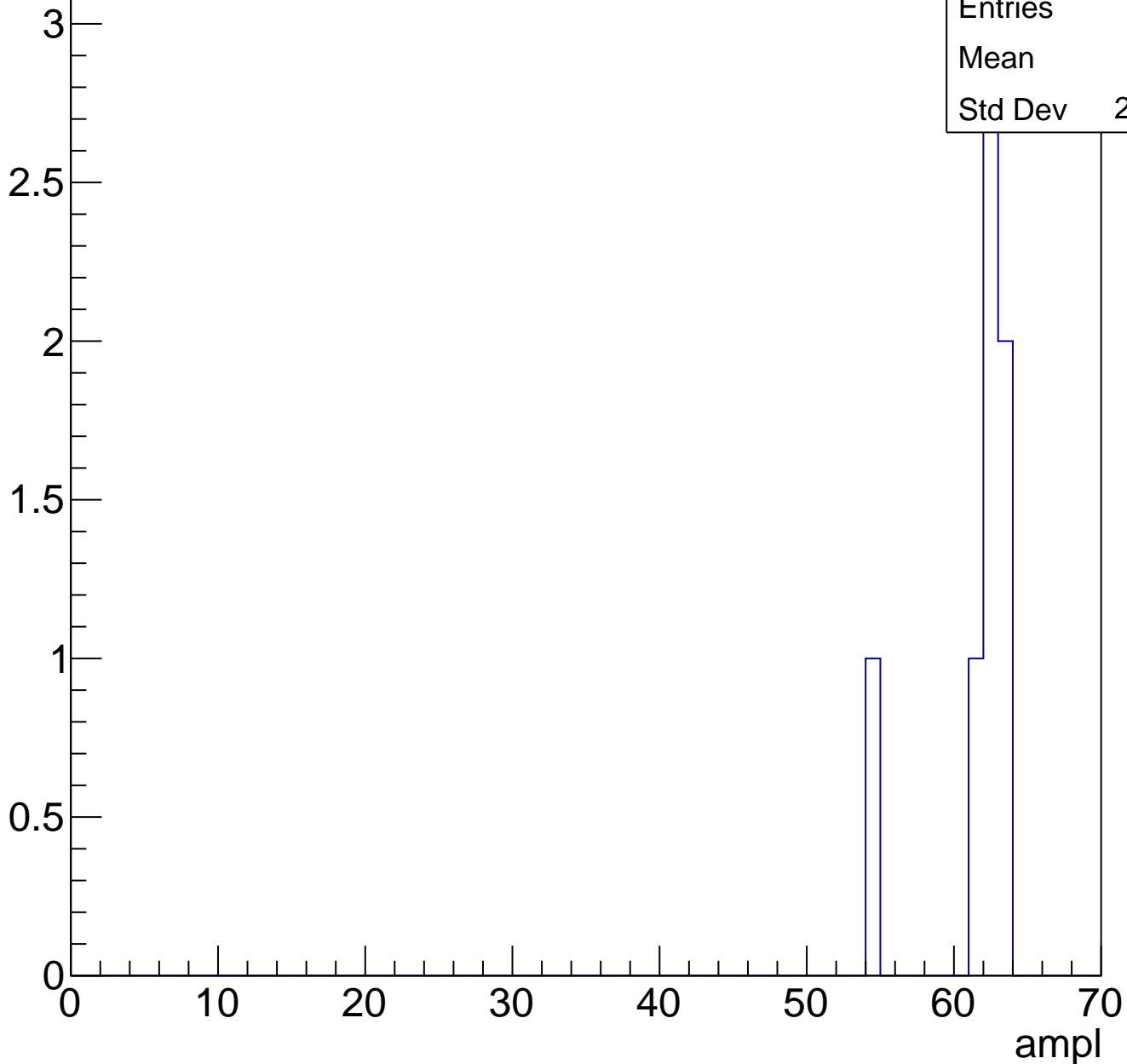
2.345



B1L102S, U6-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch73, adc0

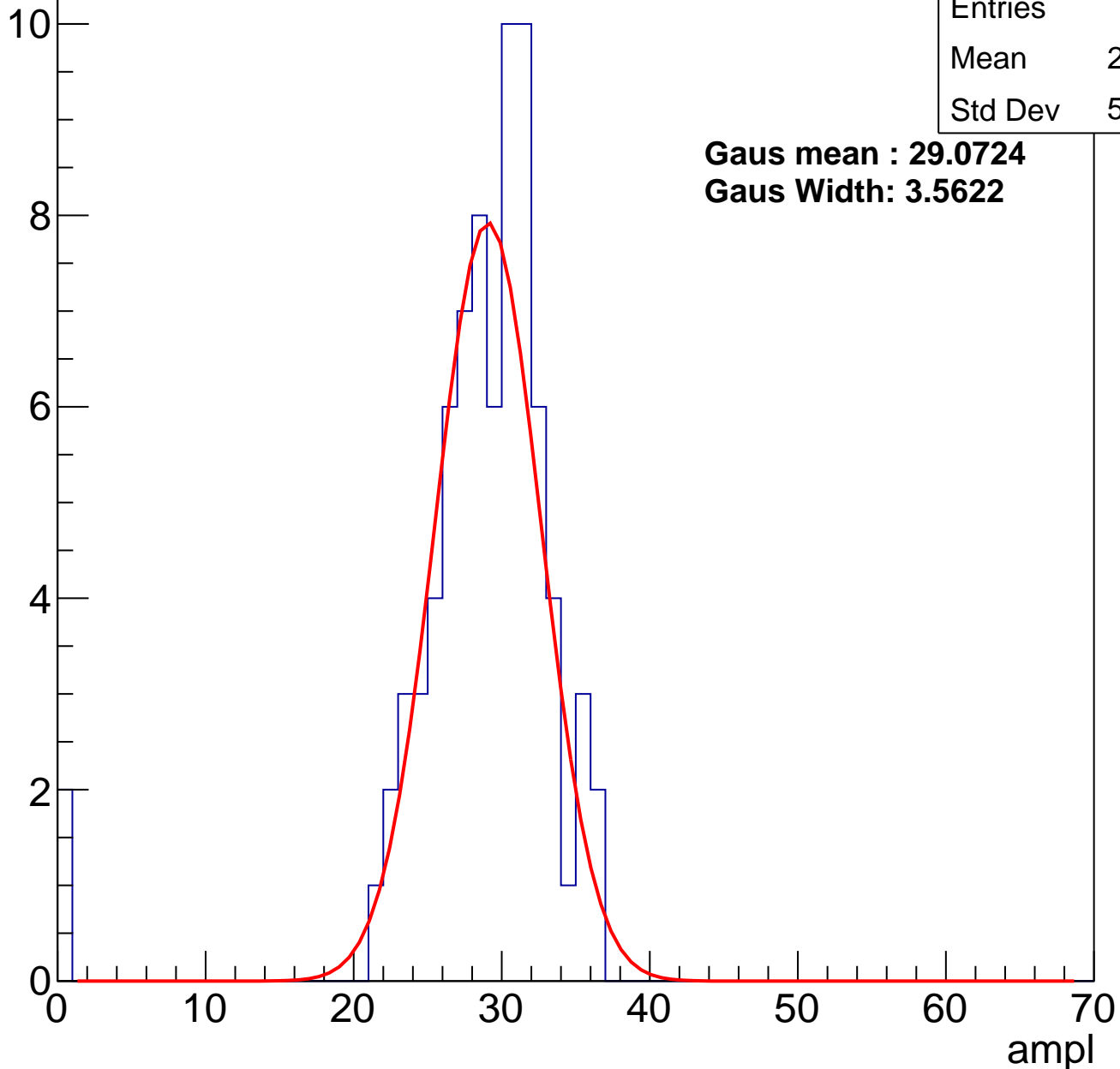
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.13
Std Dev	5.685

Gaus mean : 29.0724

Gaus Width: 3.5622

Entry



B1L102S, U6-ch73, adc1

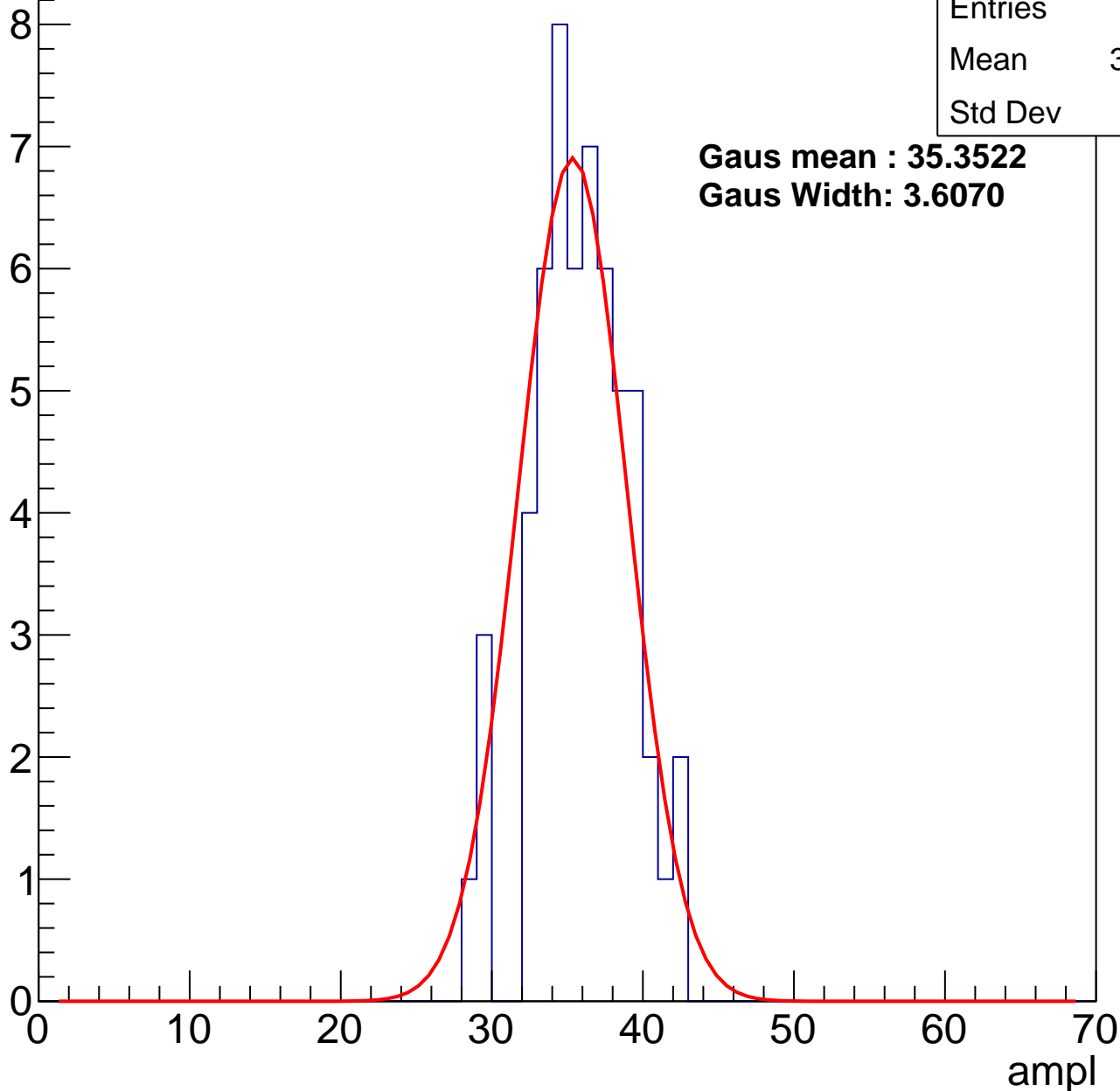
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.48
Std Dev	3.14

Gaus mean : 35.3522

Gaus Width: 3.6070



B1L102S, U6-ch73, adc2

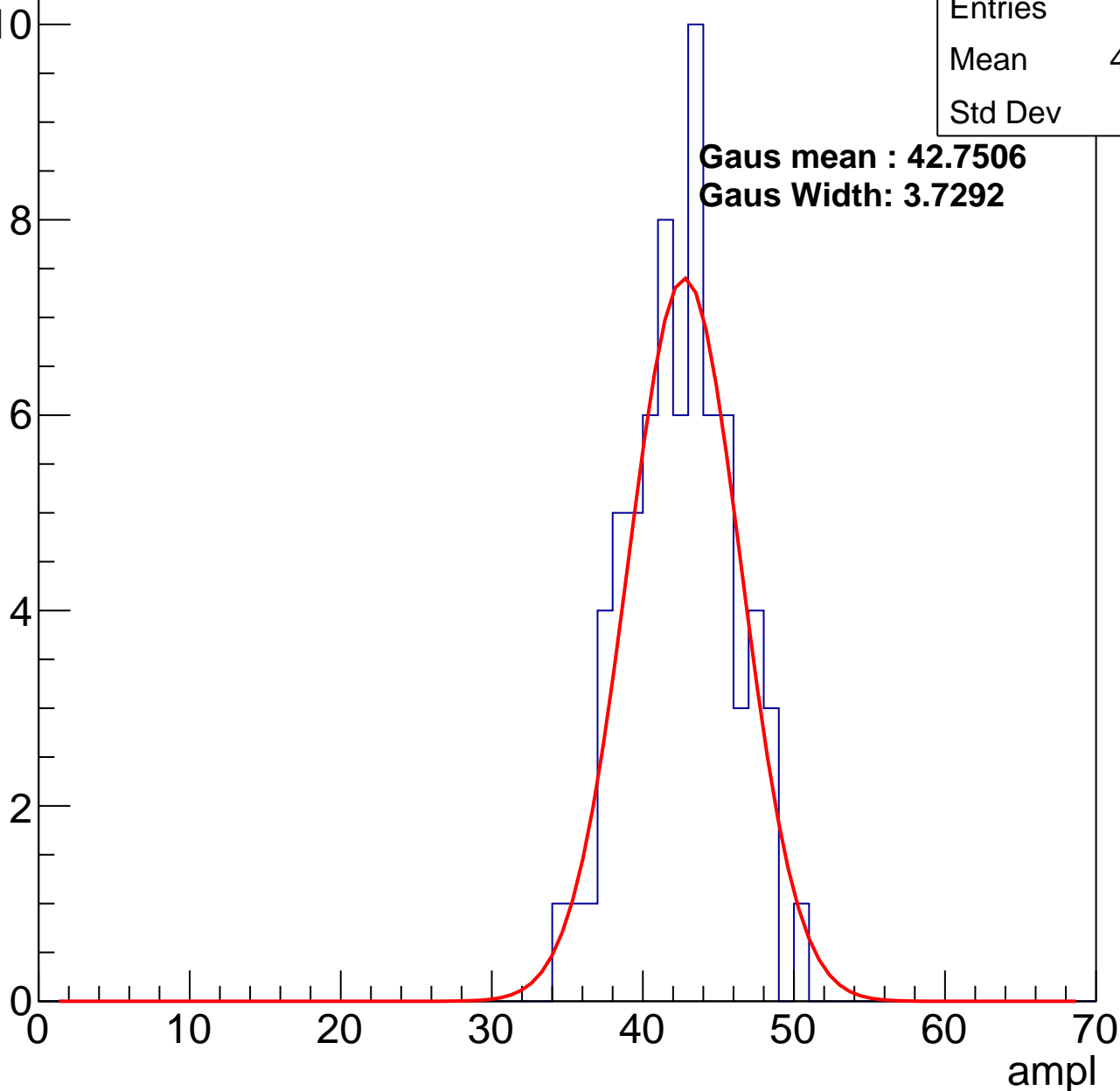
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	42.03
Std Dev	3.41

Gaus mean : 42.7506

Gaus Width: 3.7292

Entry

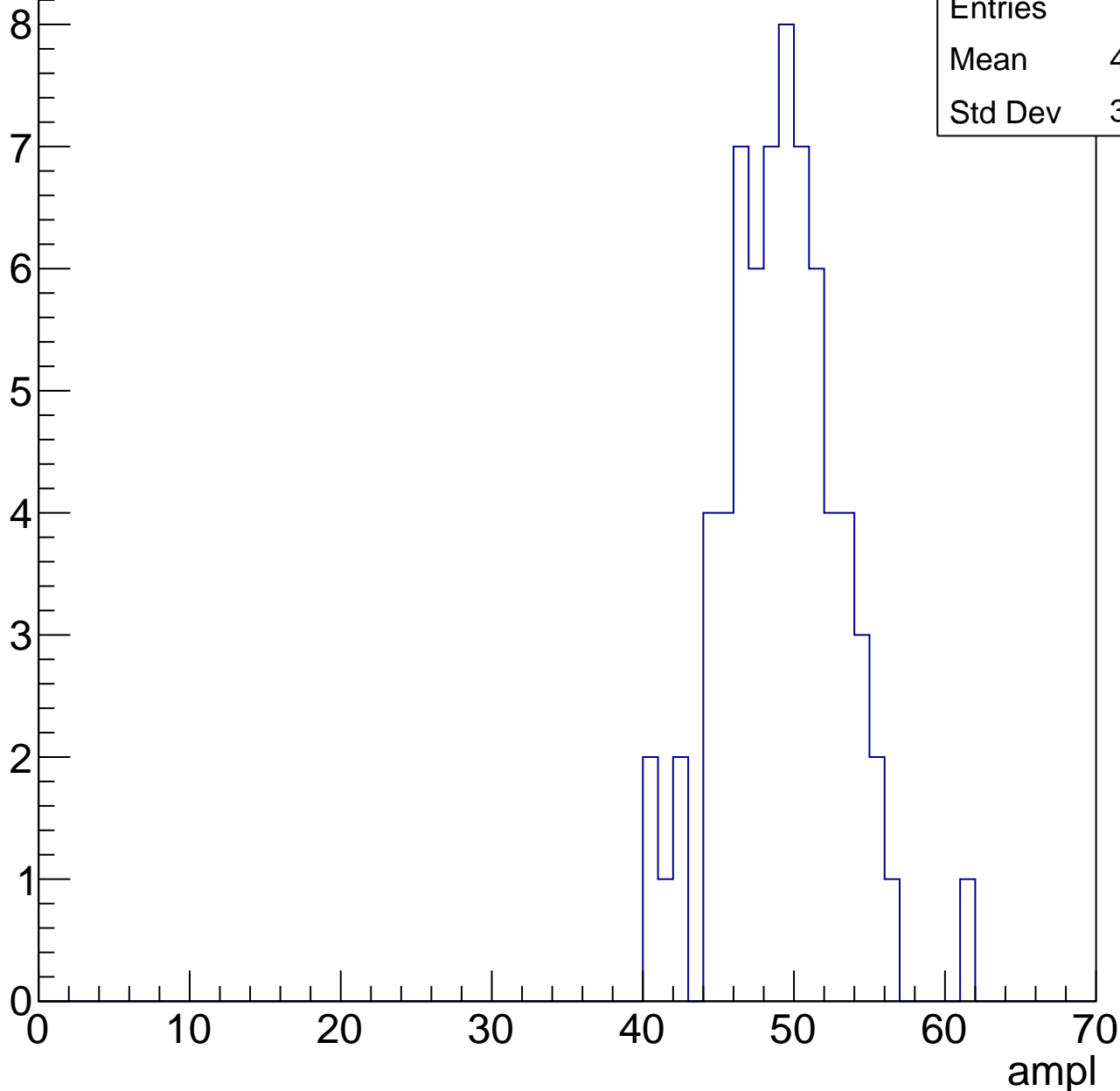


B1L102S, U6-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

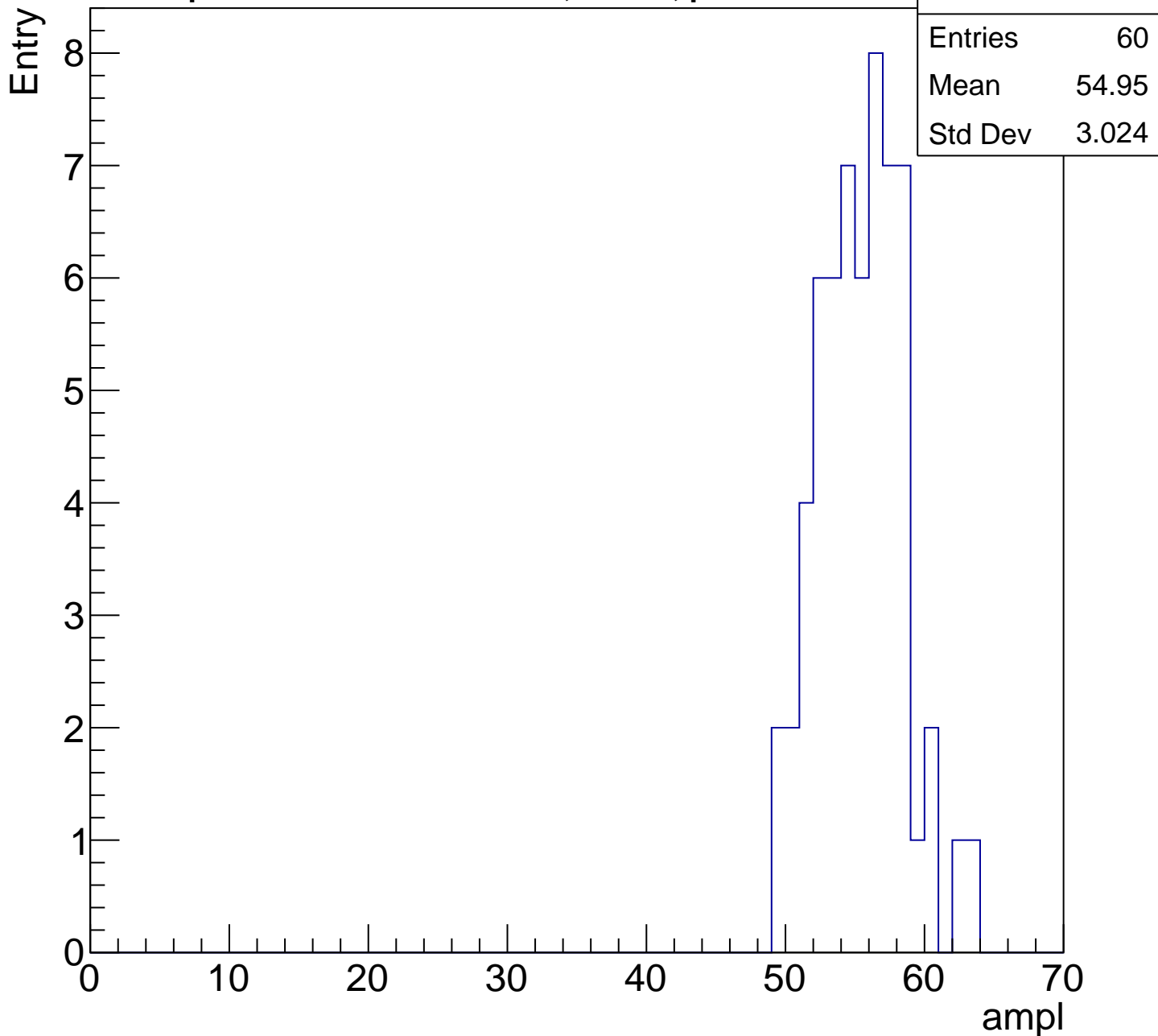
Entry

Entries	69
Mean	48.67
Std Dev	3.892



B1L102S, U6-ch73, adc4

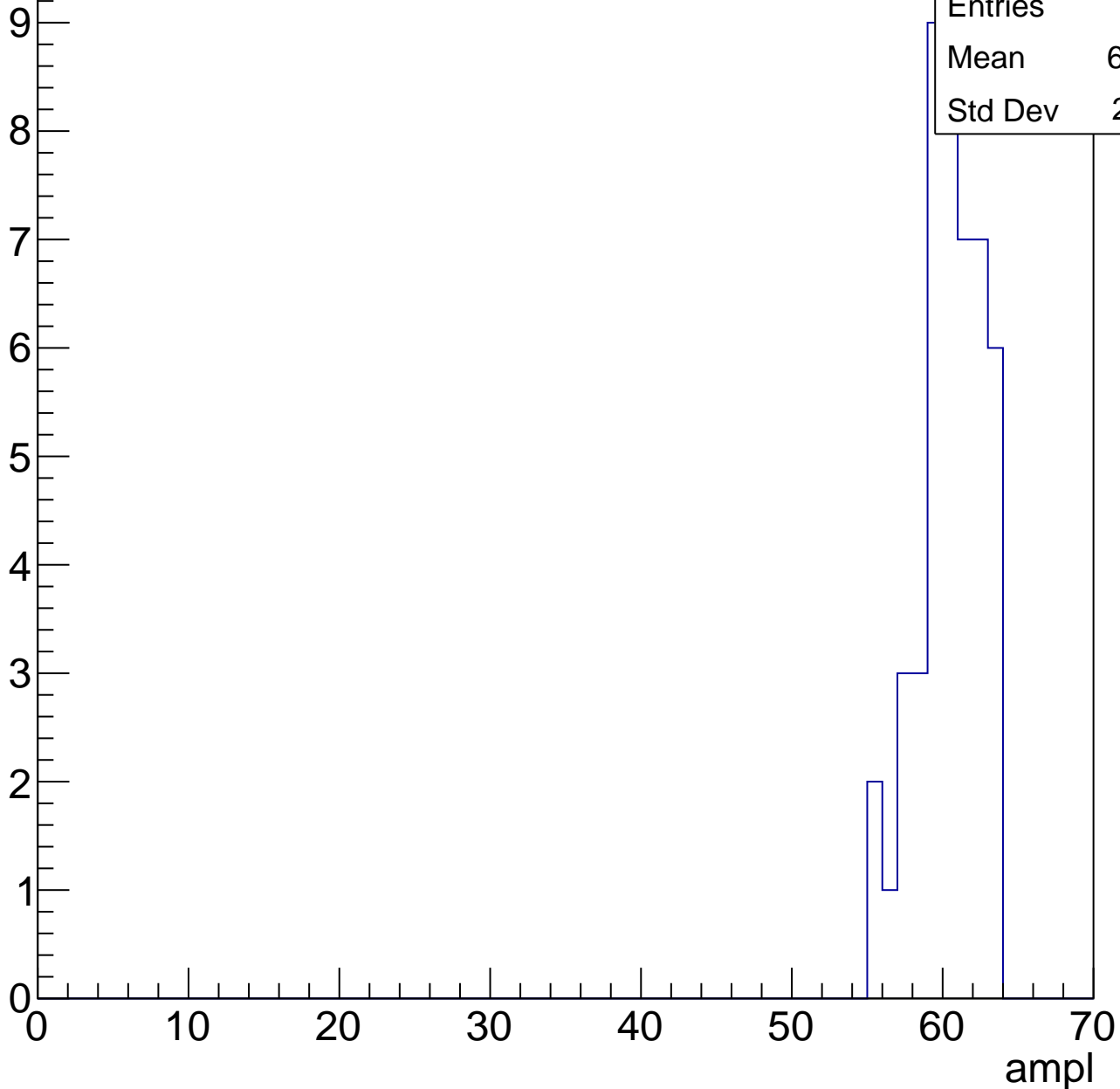
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

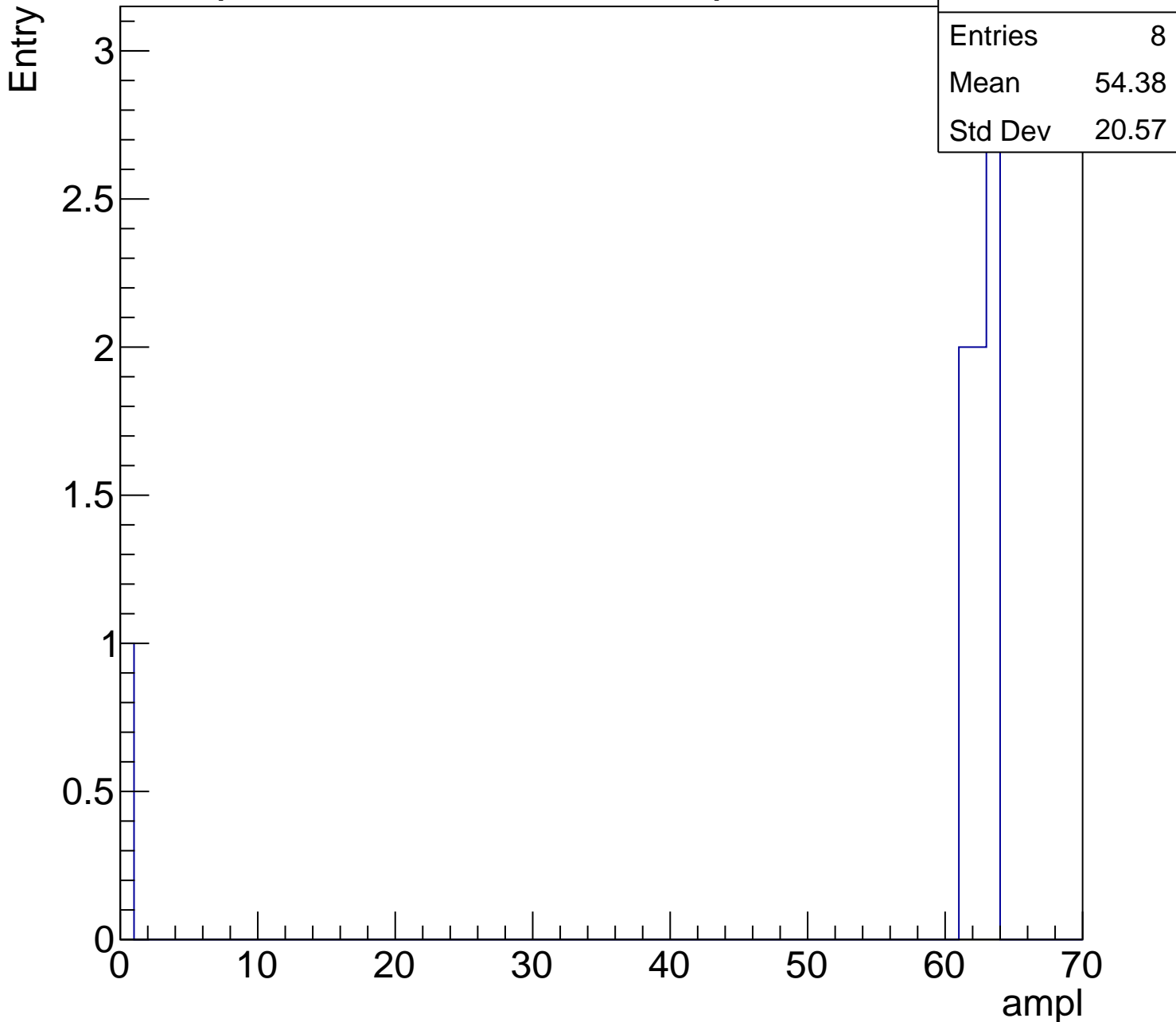
8

Mean

54.38

Std Dev

20.57



B1L102S, U6-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch74, adc0

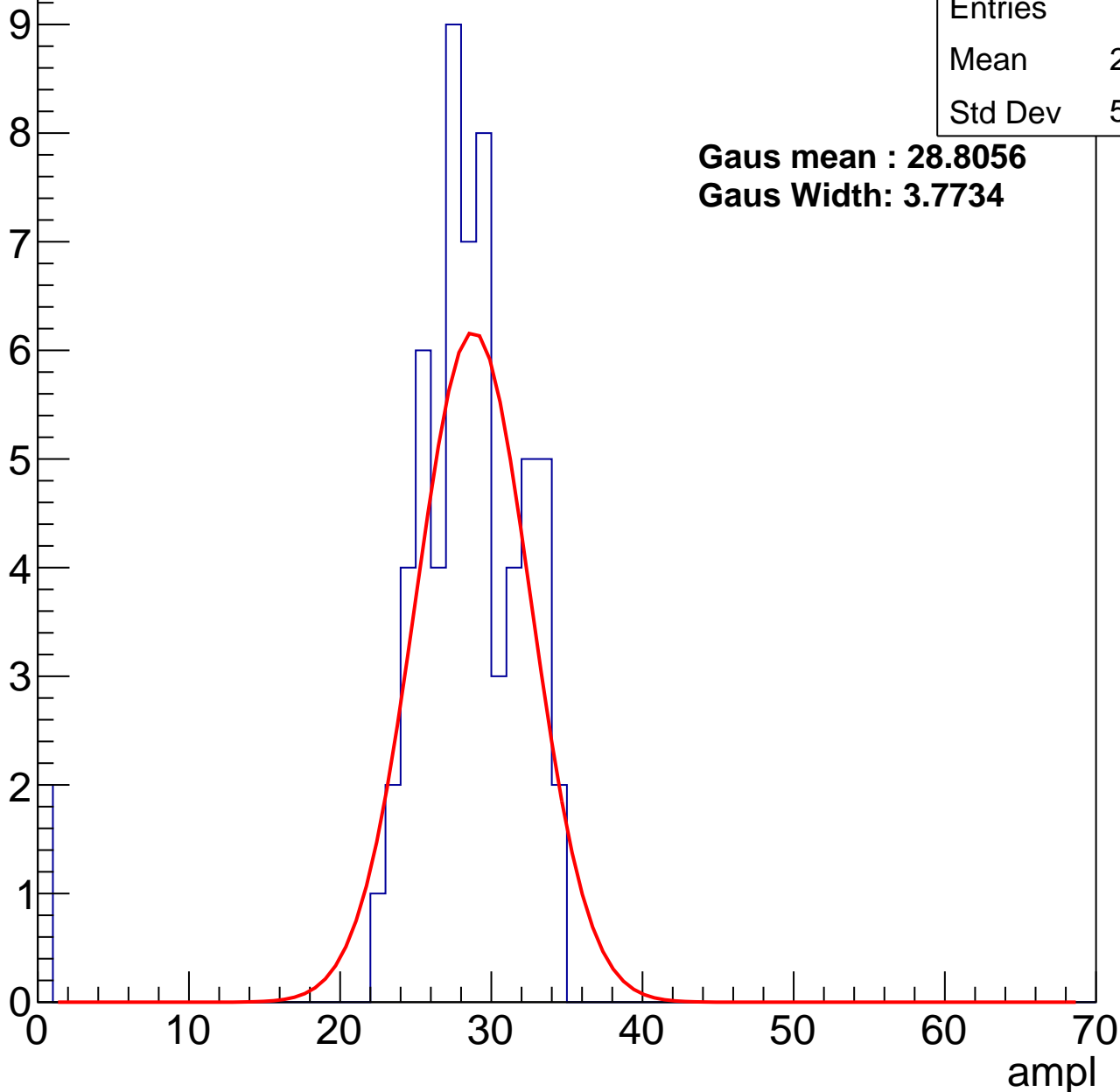
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	27.35
Std Dev	5.826

Gaus mean : 28.8056

Gaus Width: 3.7734



B1L102S, U6-ch74, adc1

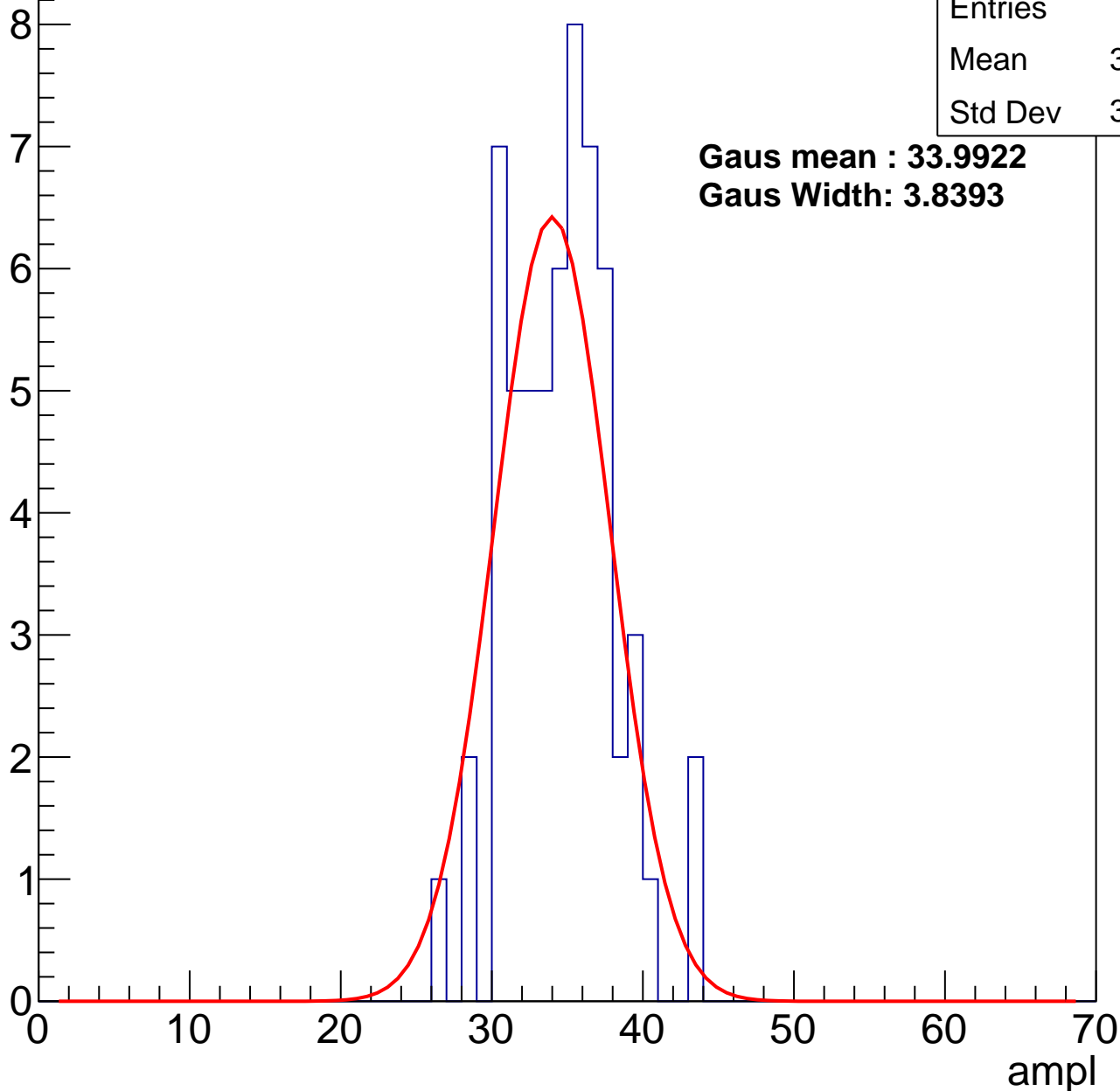
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.15
Std Dev	3.444

Gaus mean : 33.9922

Gaus Width: 3.8393



B1L102S, U6-ch74, adc2

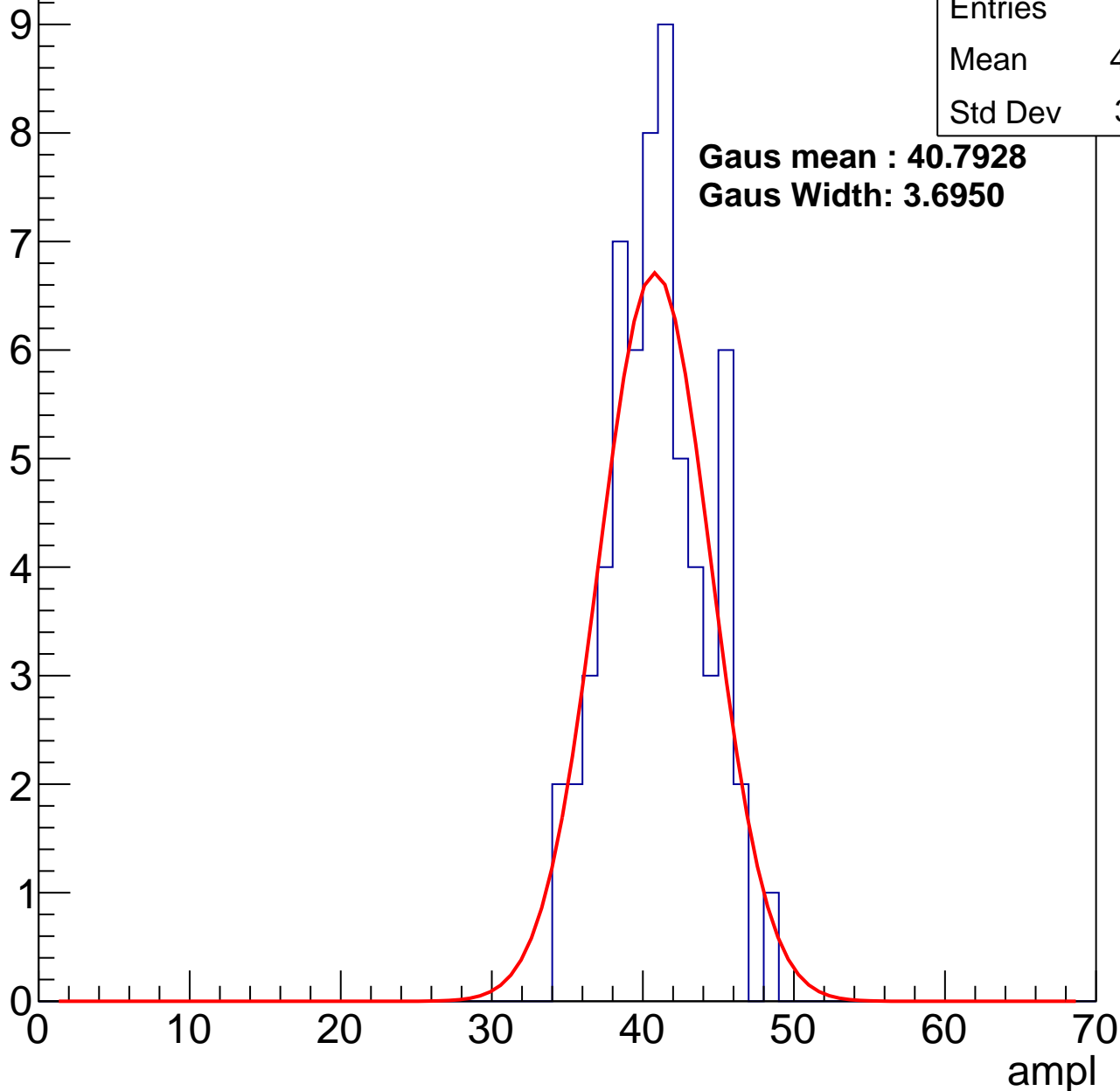
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	40.44
Std Dev	3.181

Gaus mean : 40.7928

Gaus Width: 3.6950



B1L102S, U6-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	47.77
Std Dev	3.709

Entry

10

8

6

4

2

0

0

10

20

30

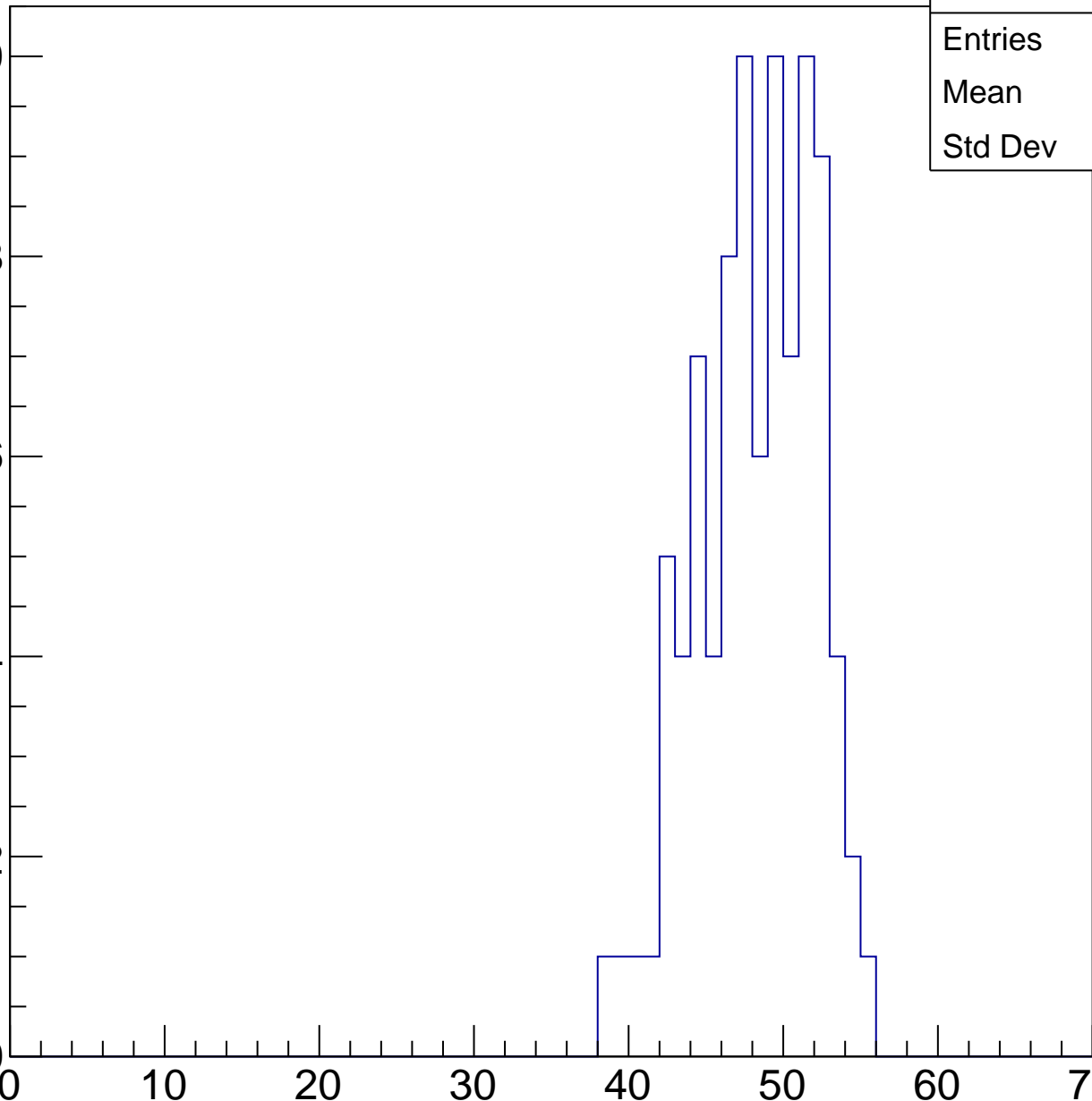
40

50

60

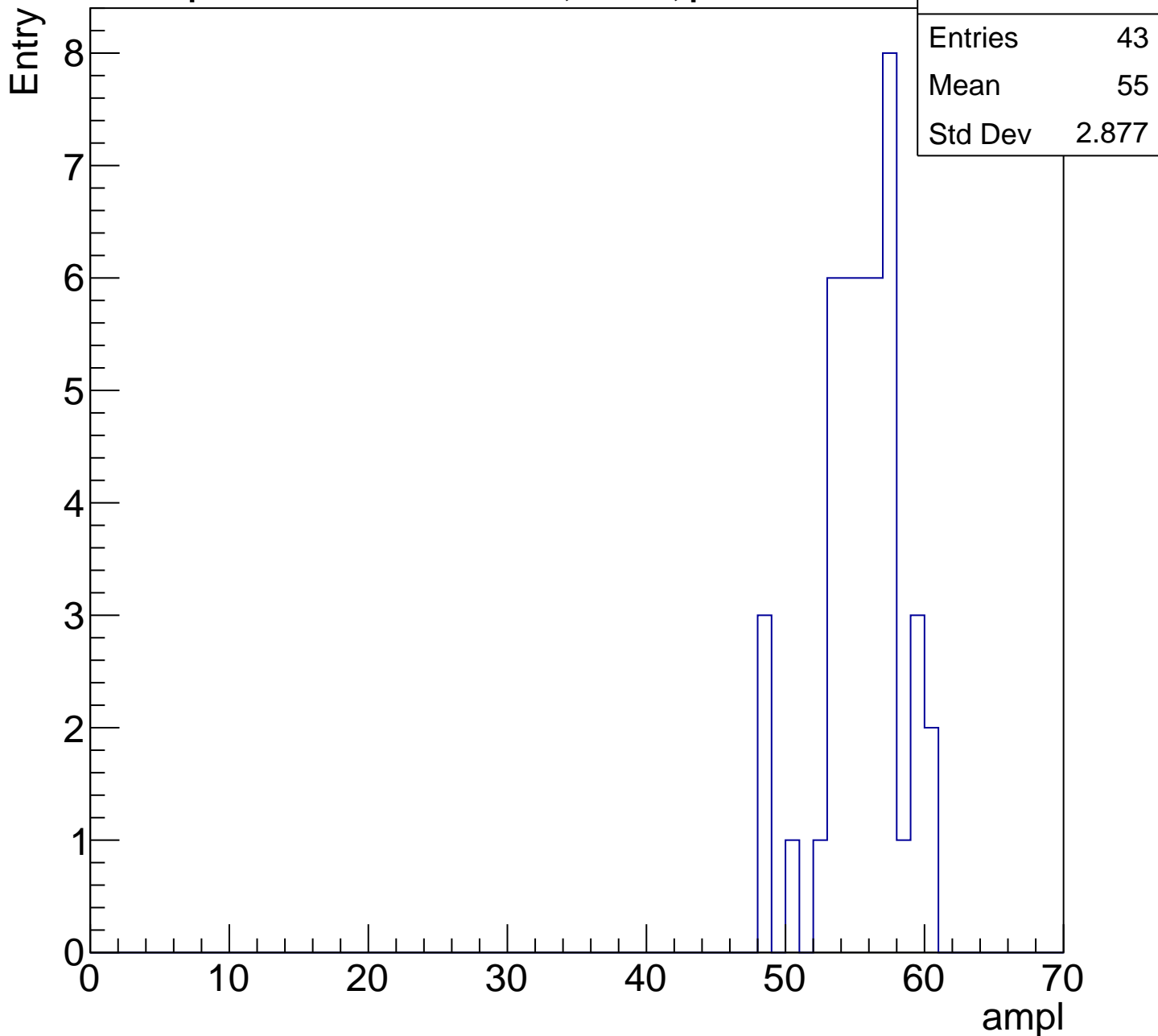
70

ampl



B1L102S, U6-ch74, adc4

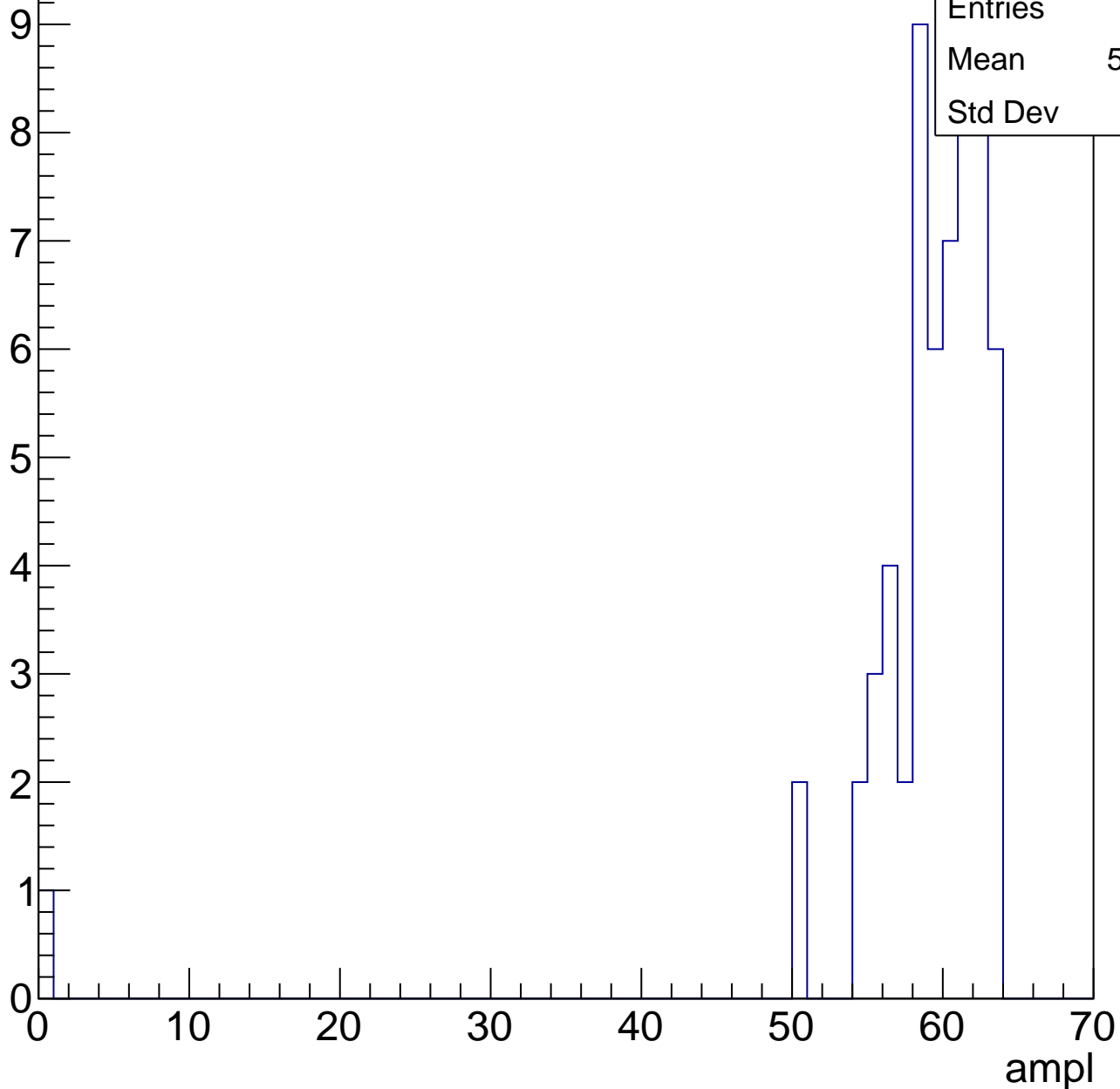
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

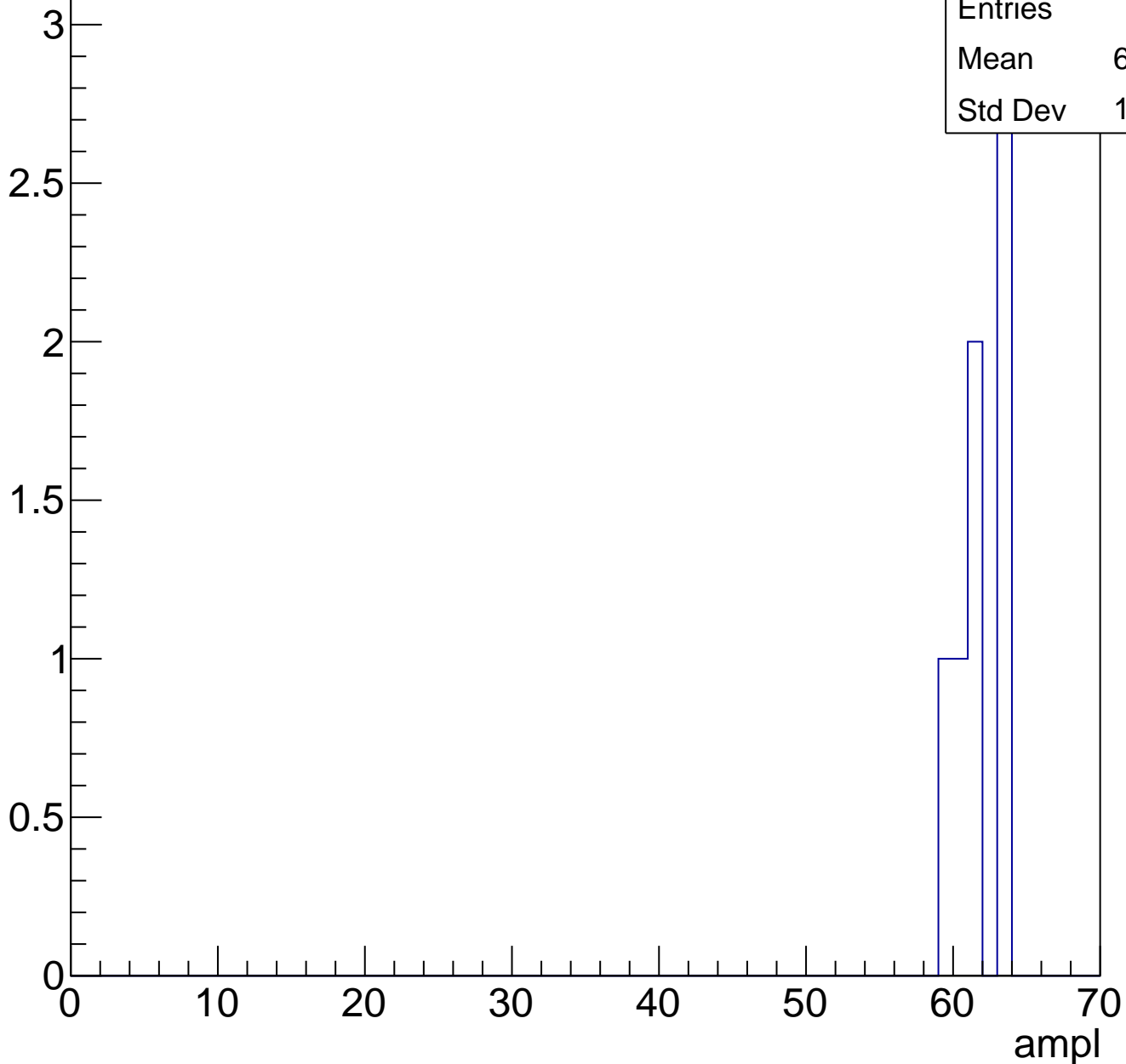
Entry



B1L102S, U6-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

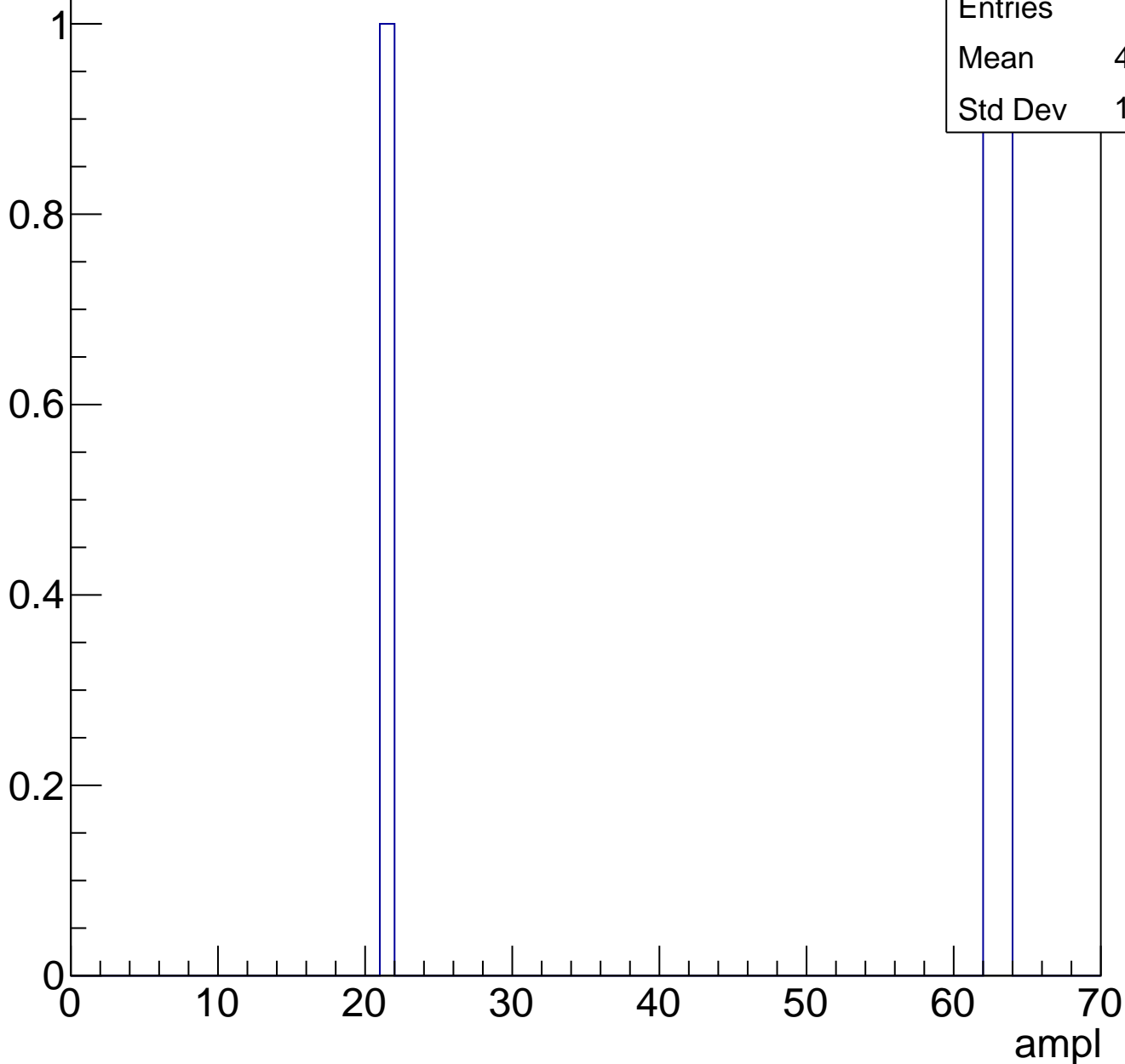
Entry



B1L102S, U6-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch75, adc0

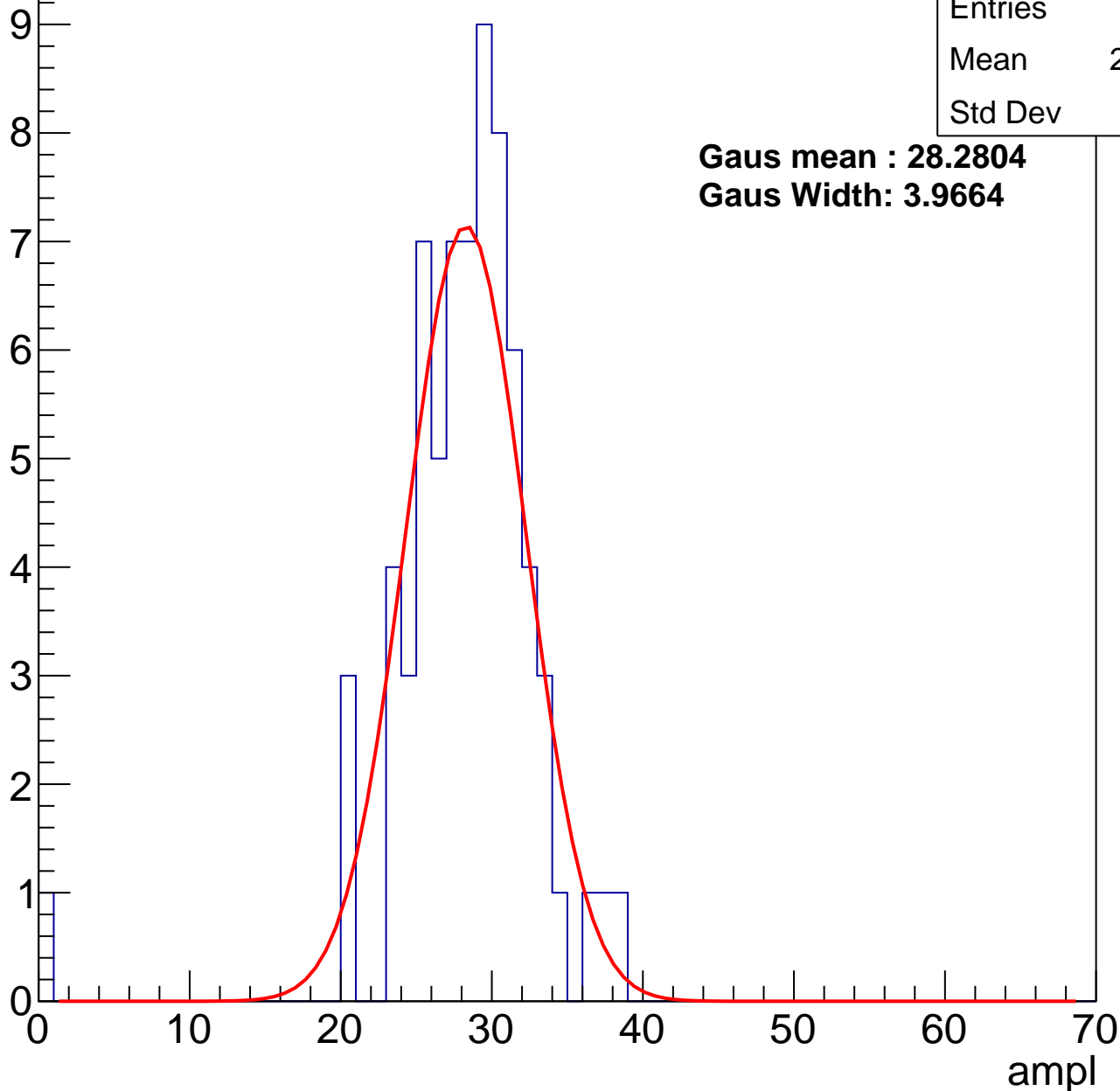
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.79
Std Dev	4.93

Gaus mean : 28.2804

Gaus Width: 3.9664



B1L102S, U6-ch75, adc1

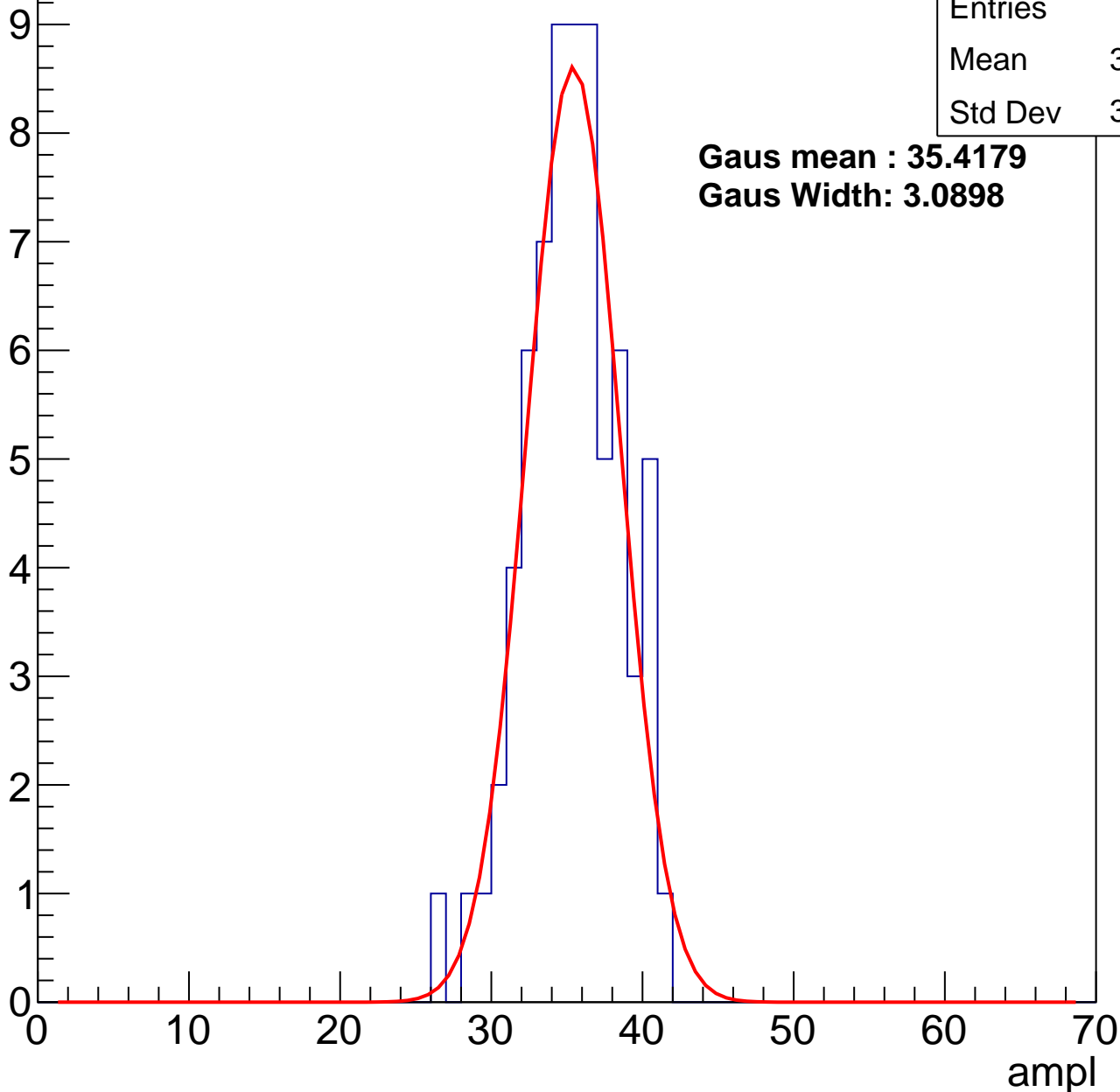
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.87
Std Dev	3.097

Gaus mean : 35.4179

Gaus Width: 3.0898



B1L102S, U6-ch75, adc2

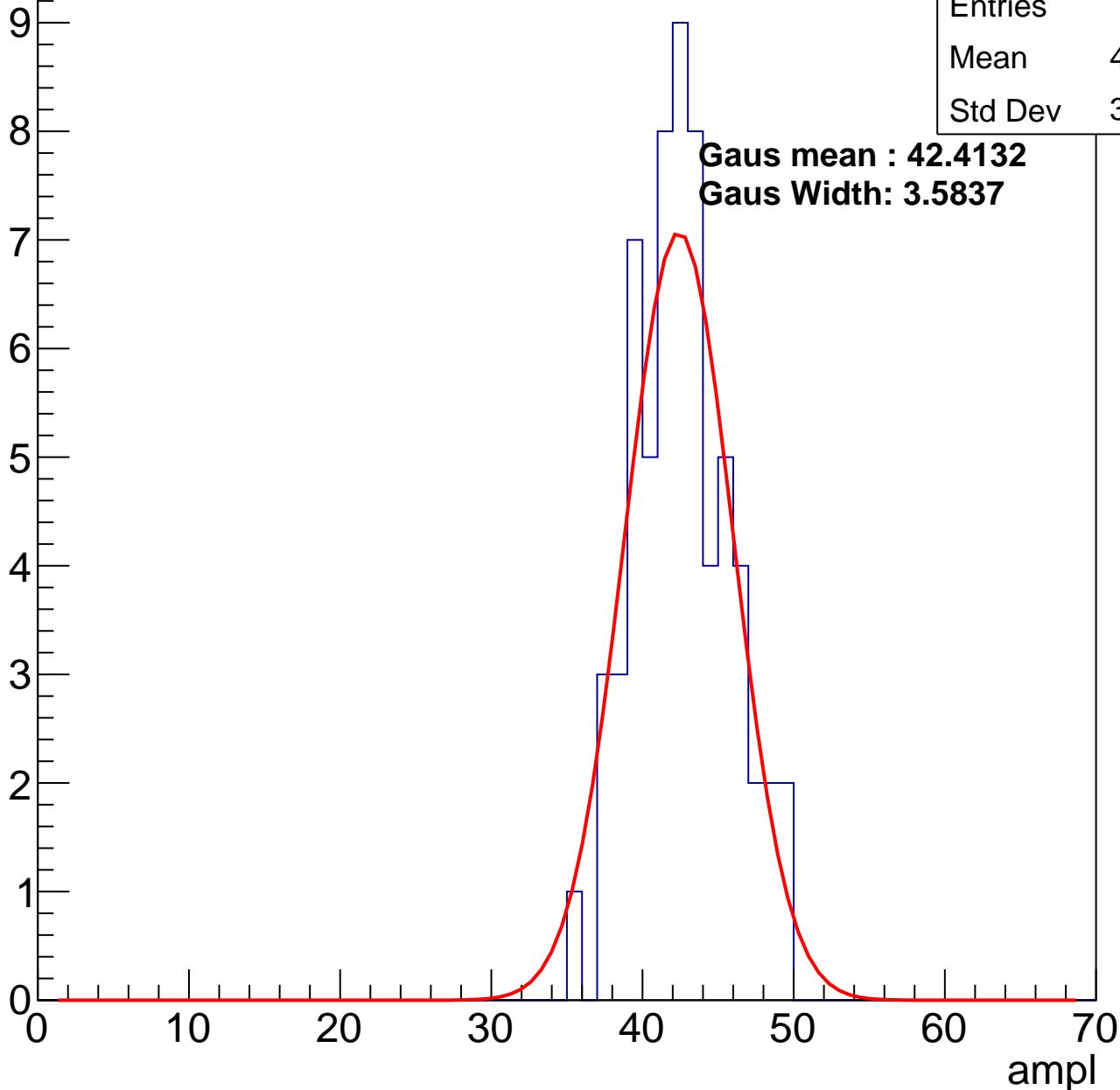
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.16
Std Dev	3.123

Gaus mean : 42.4132

Gaus Width: 3.5837

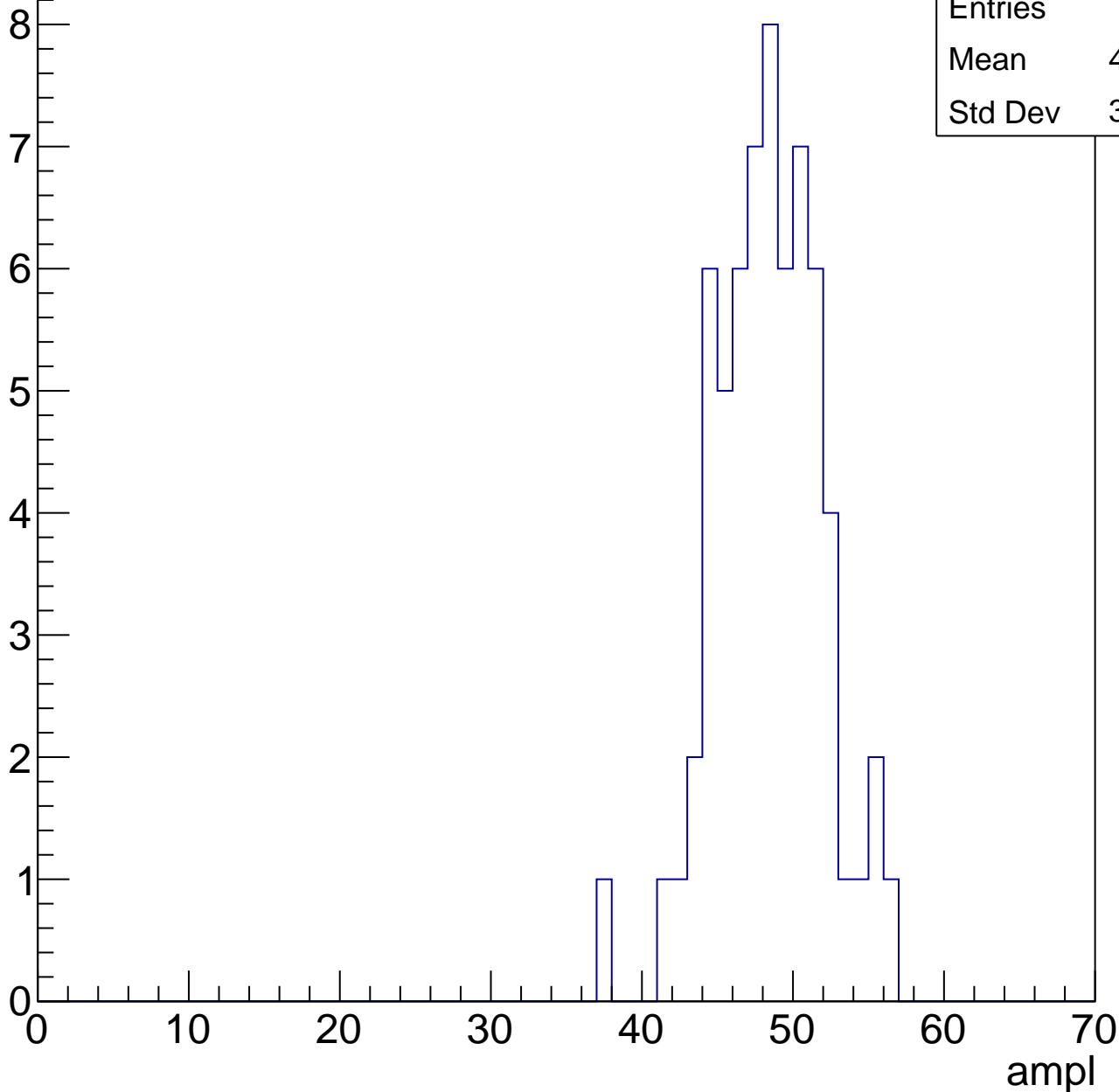


B1L102S, U6-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.92
Std Dev	3.514

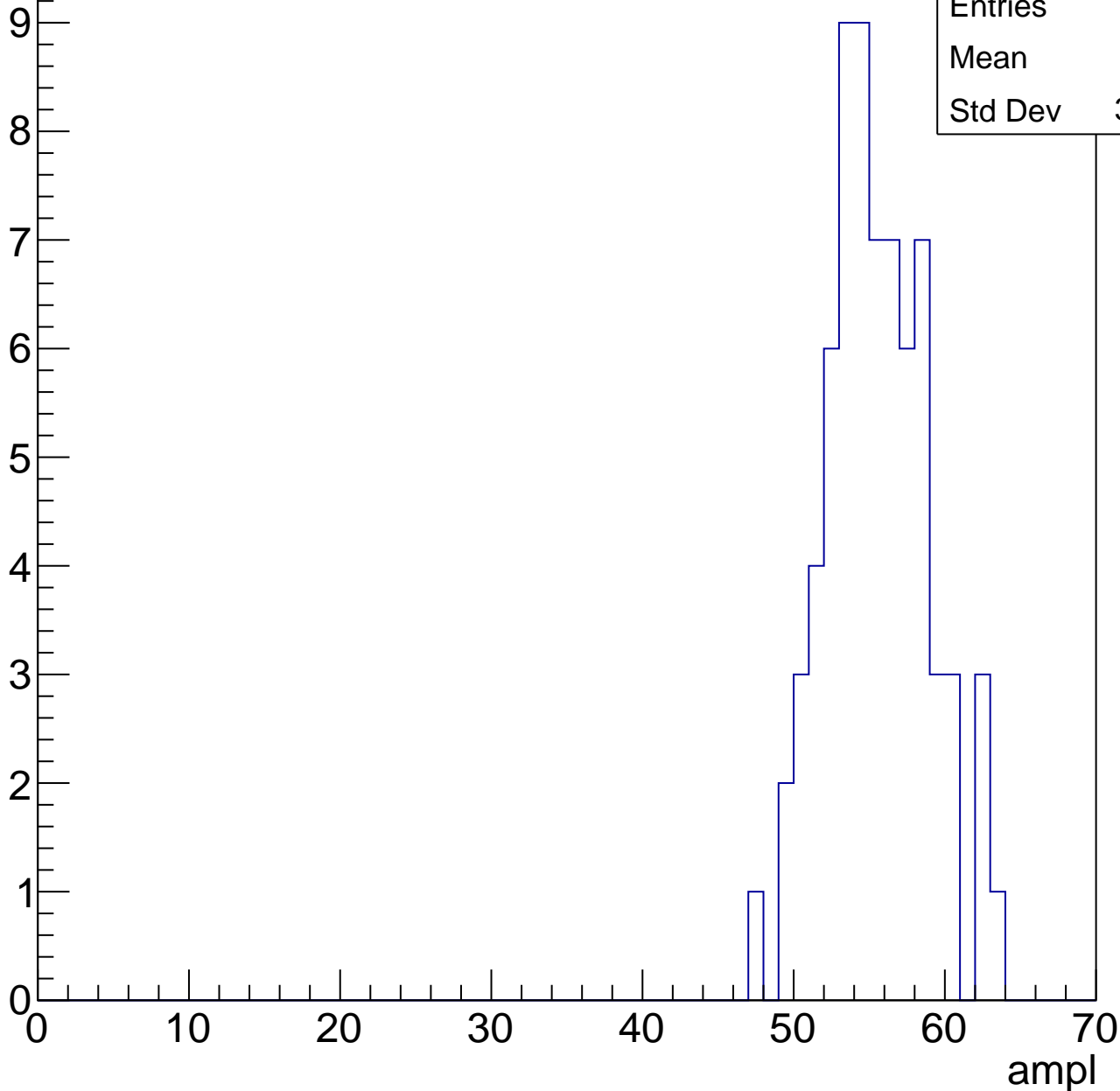


B1L102S, U6-ch75, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55
Std Dev	3.361

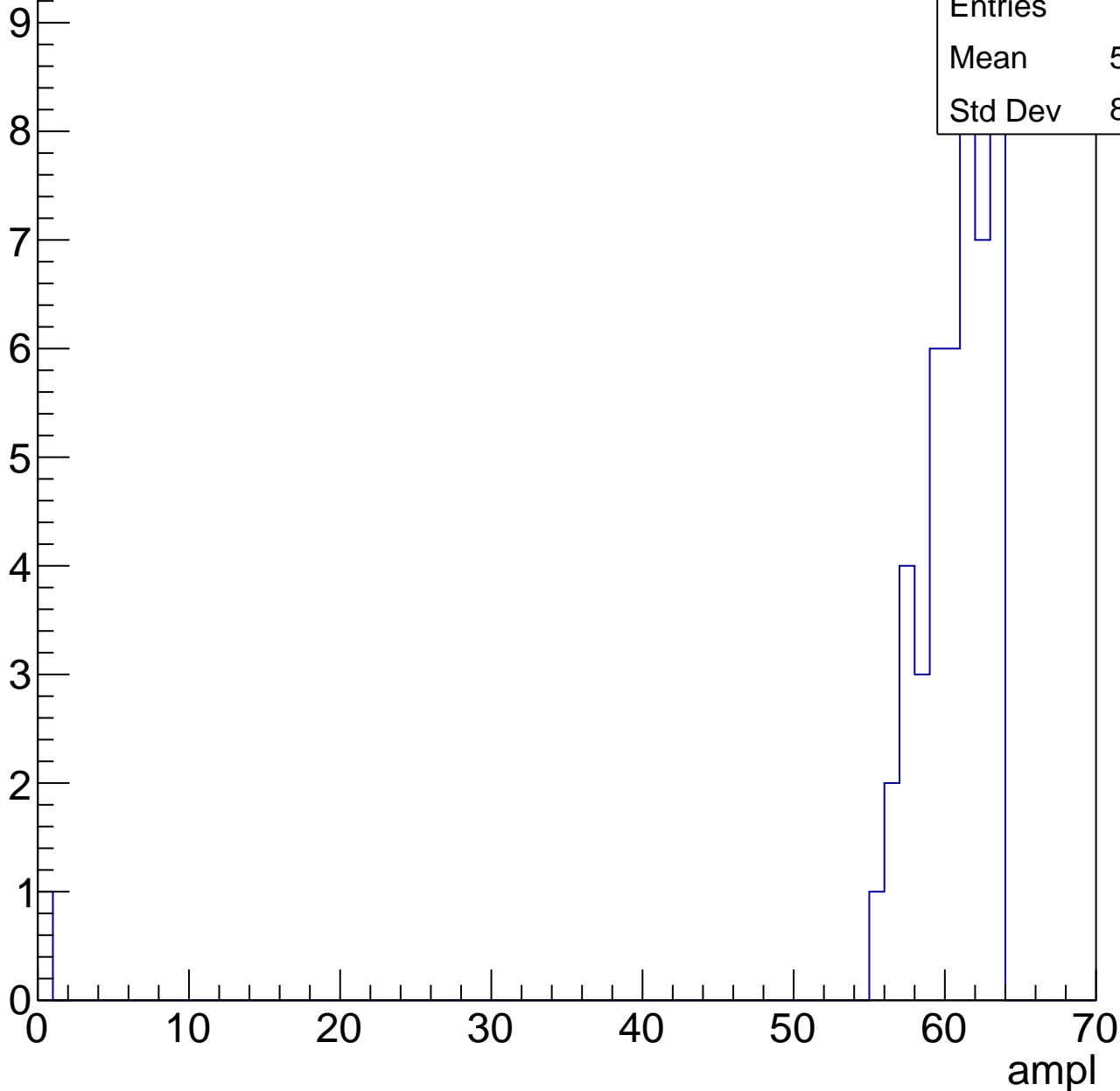


B1L102S, U6-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

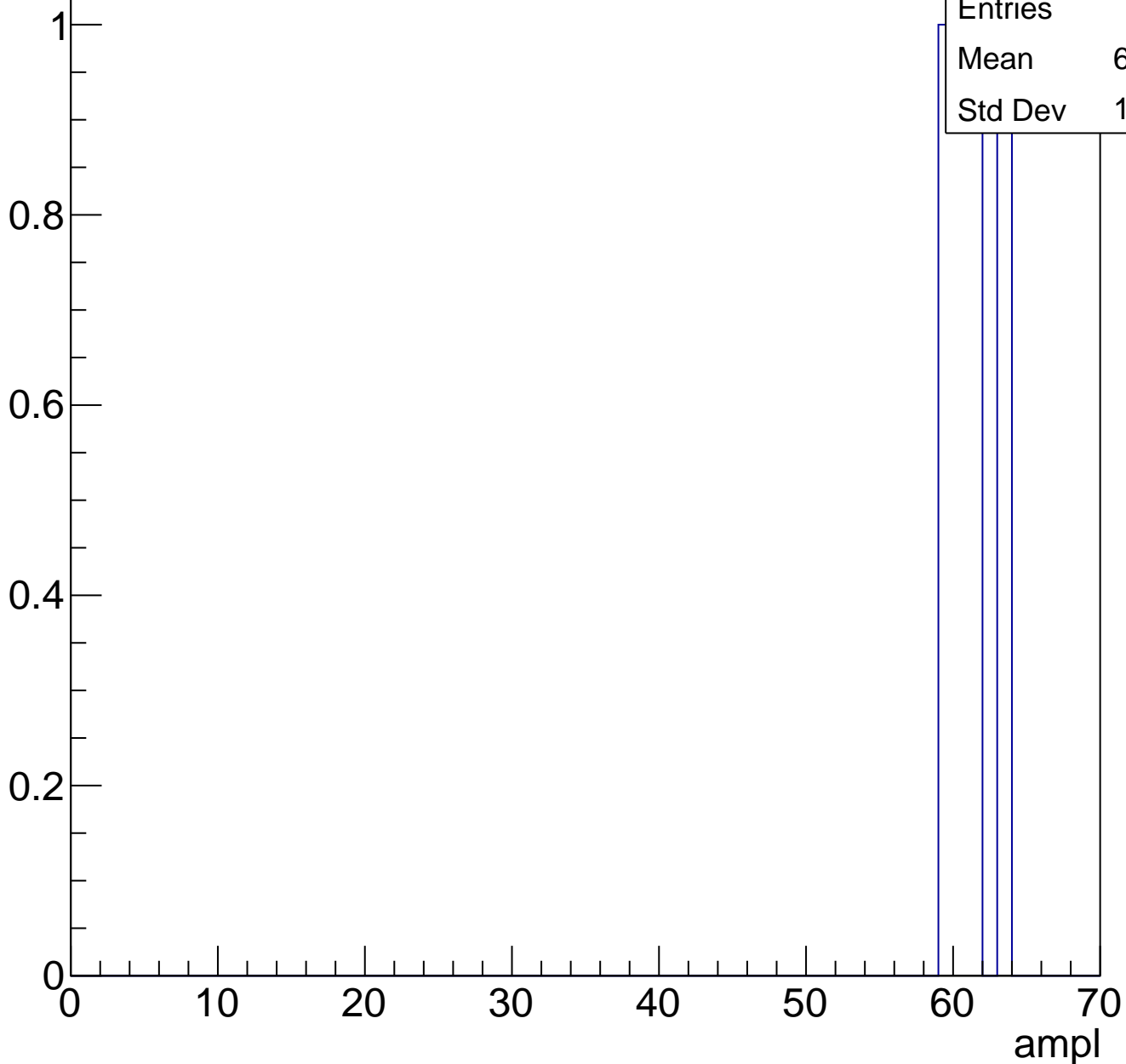
Entries	47
Mean	58.94
Std Dev	8.952



B1L102S, U6-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch76, adc0

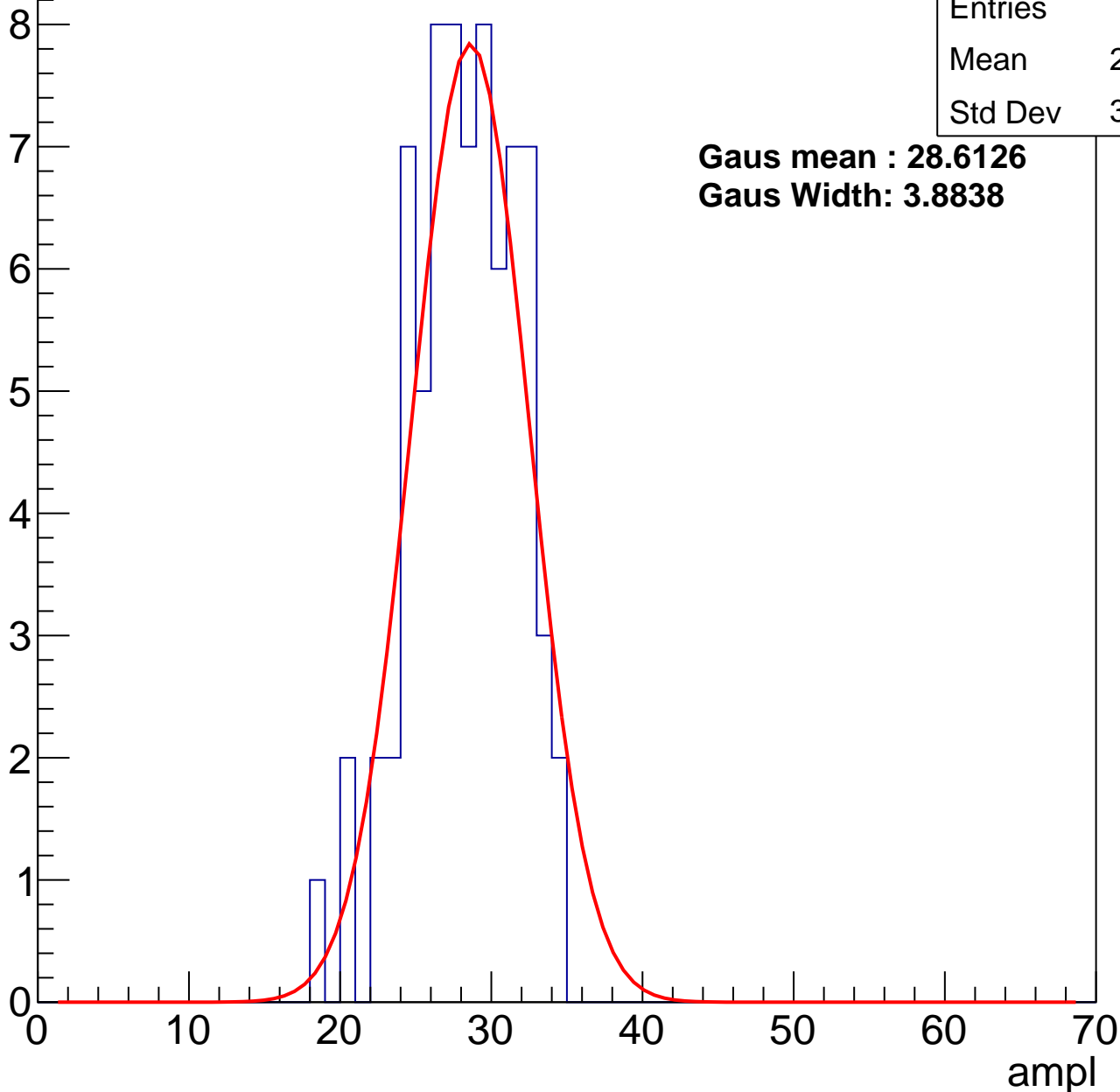
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.75
Std Dev	3.457

Gaus mean : 28.6126

Gaus Width: 3.8838



B1L102S, U6-ch76, adc1

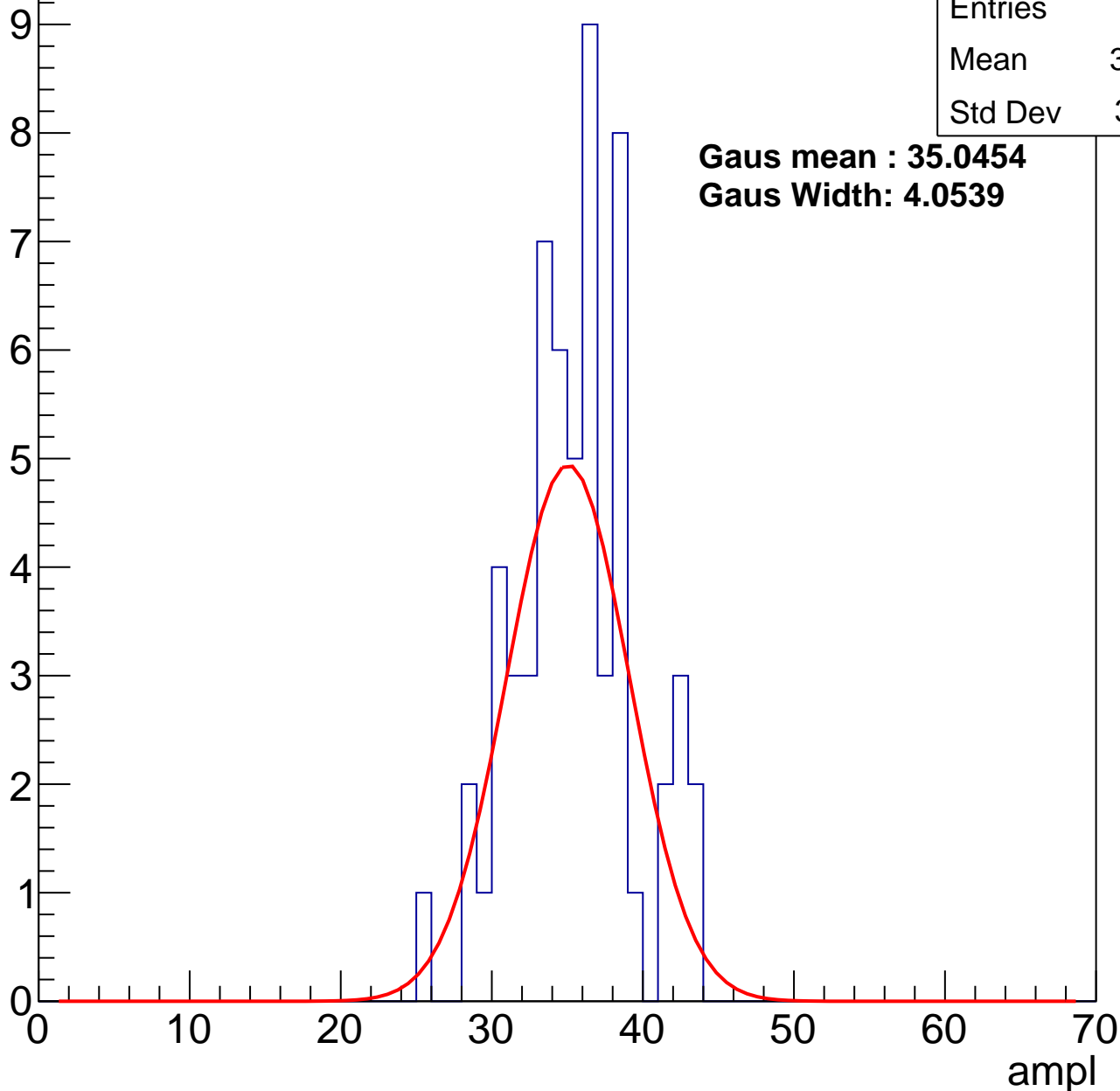
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.02
Std Dev	3.871

Gaus mean : 35.0454

Gaus Width: 4.0539



B1L102S, U6-ch76, adc2

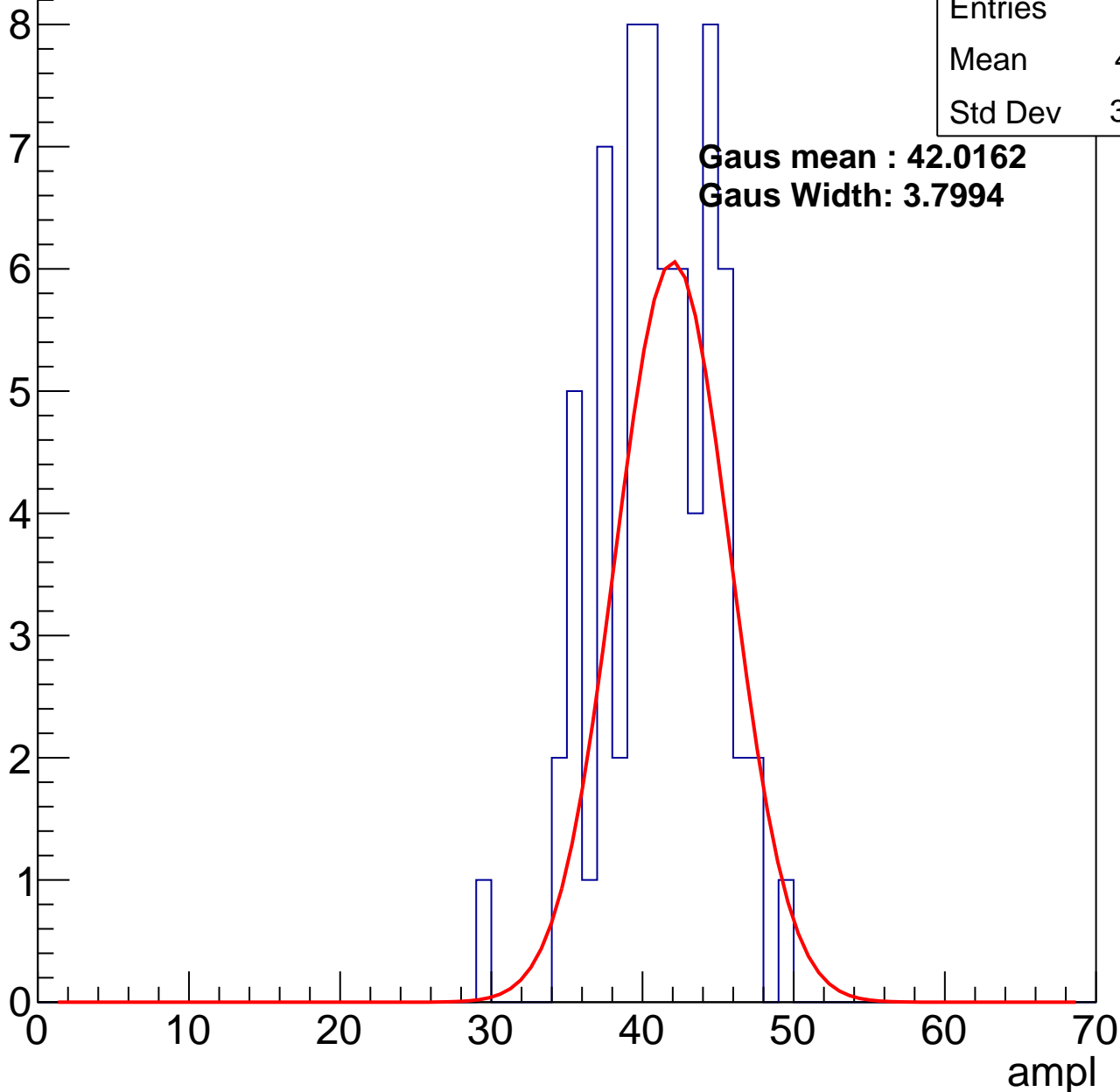
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.61
Std Dev	3.773

Gaus mean : 42.0162

Gaus Width: 3.7994

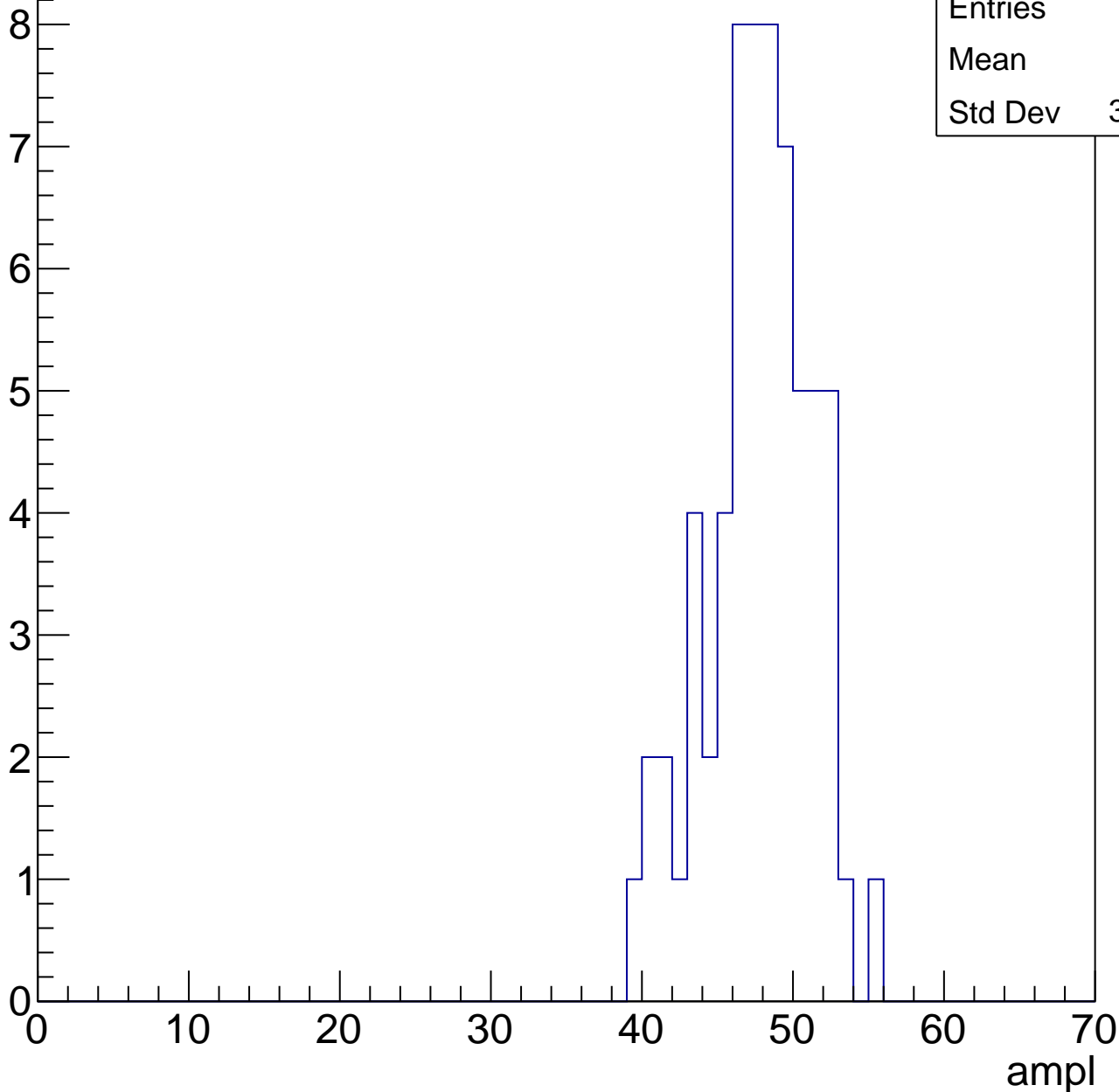


B1L102S, U6-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.3
Std Dev	3.413

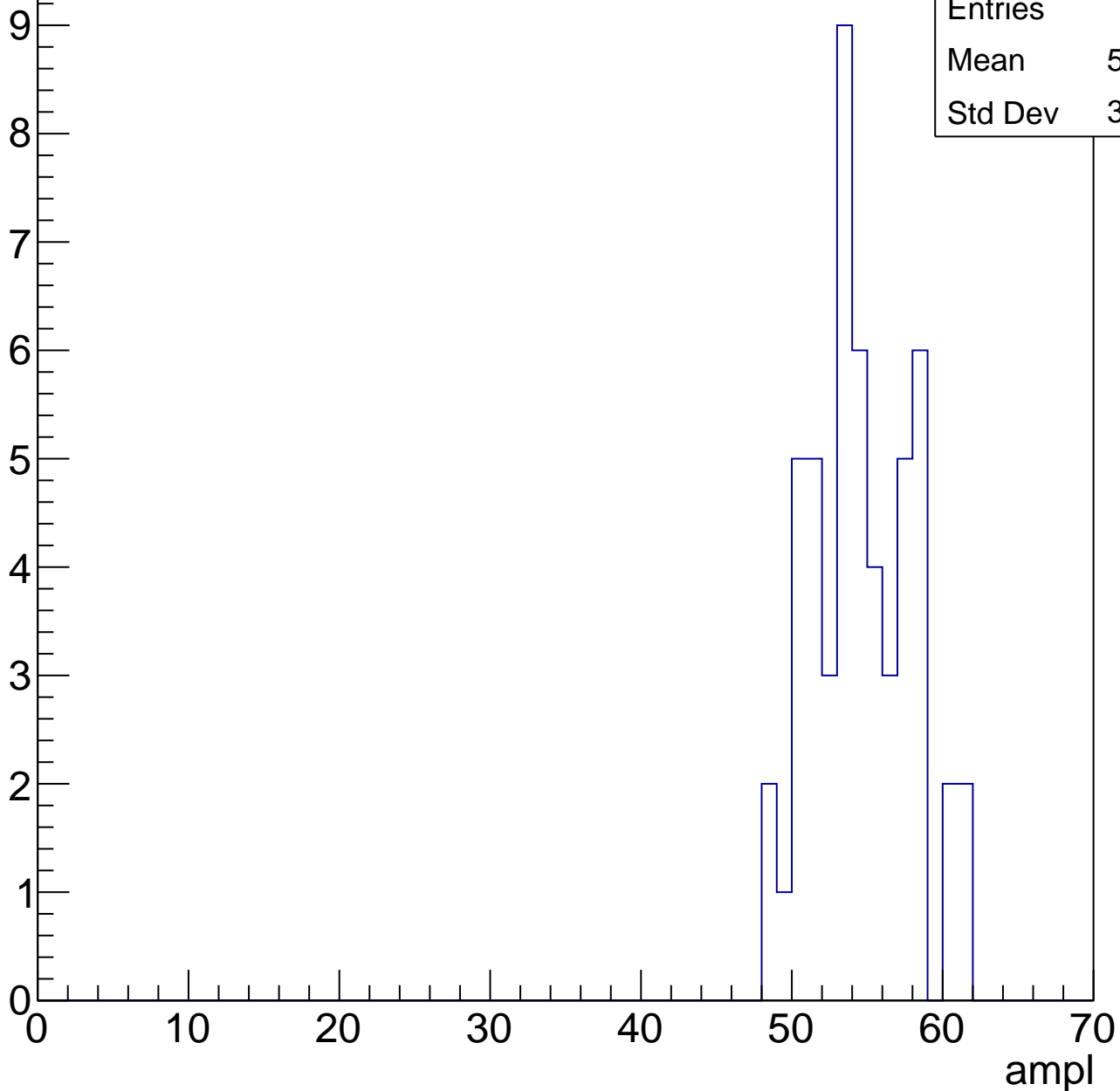


B1L102S, U6-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.15
Std Dev	3.276

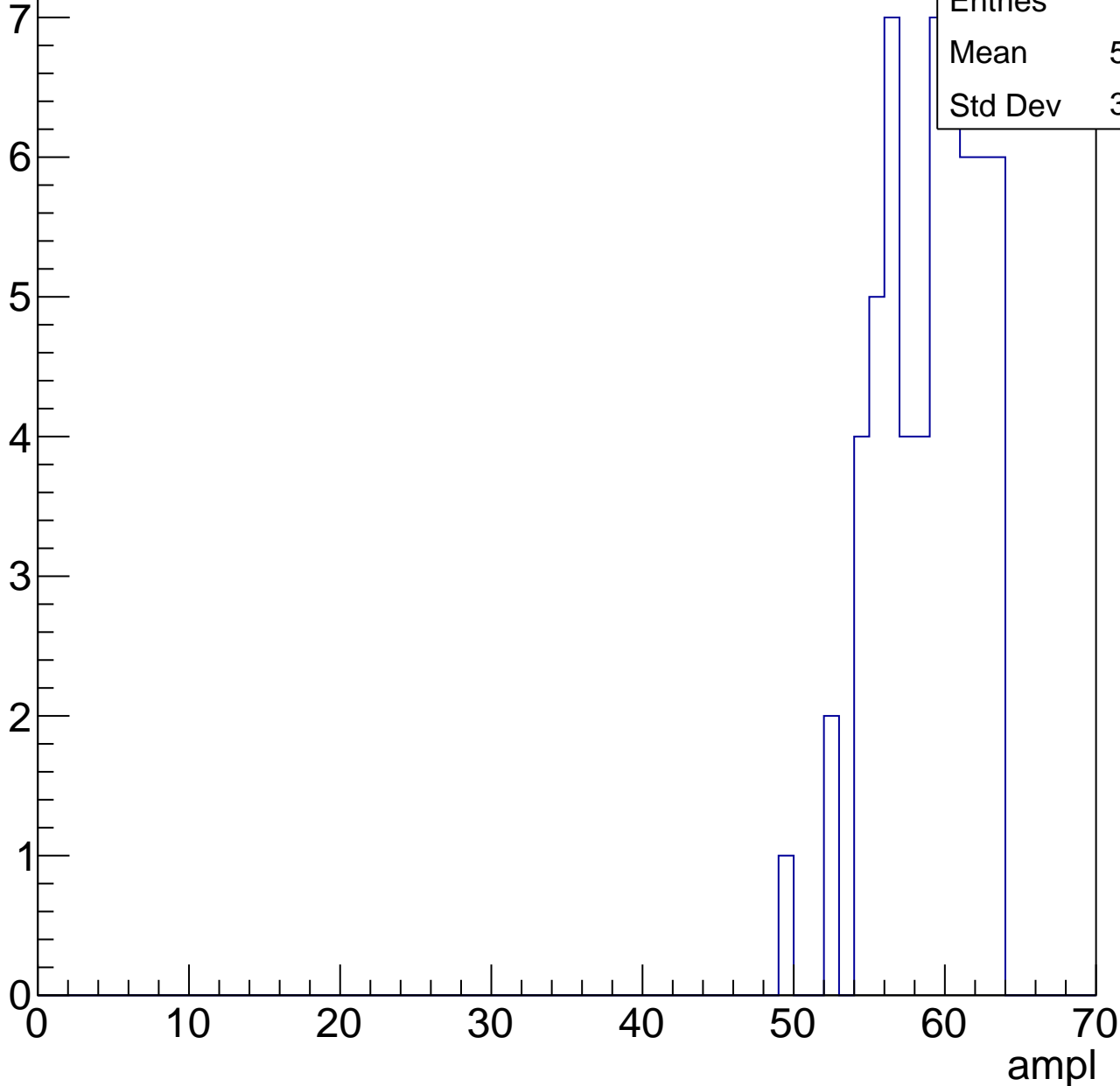


B1L102S, U6-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

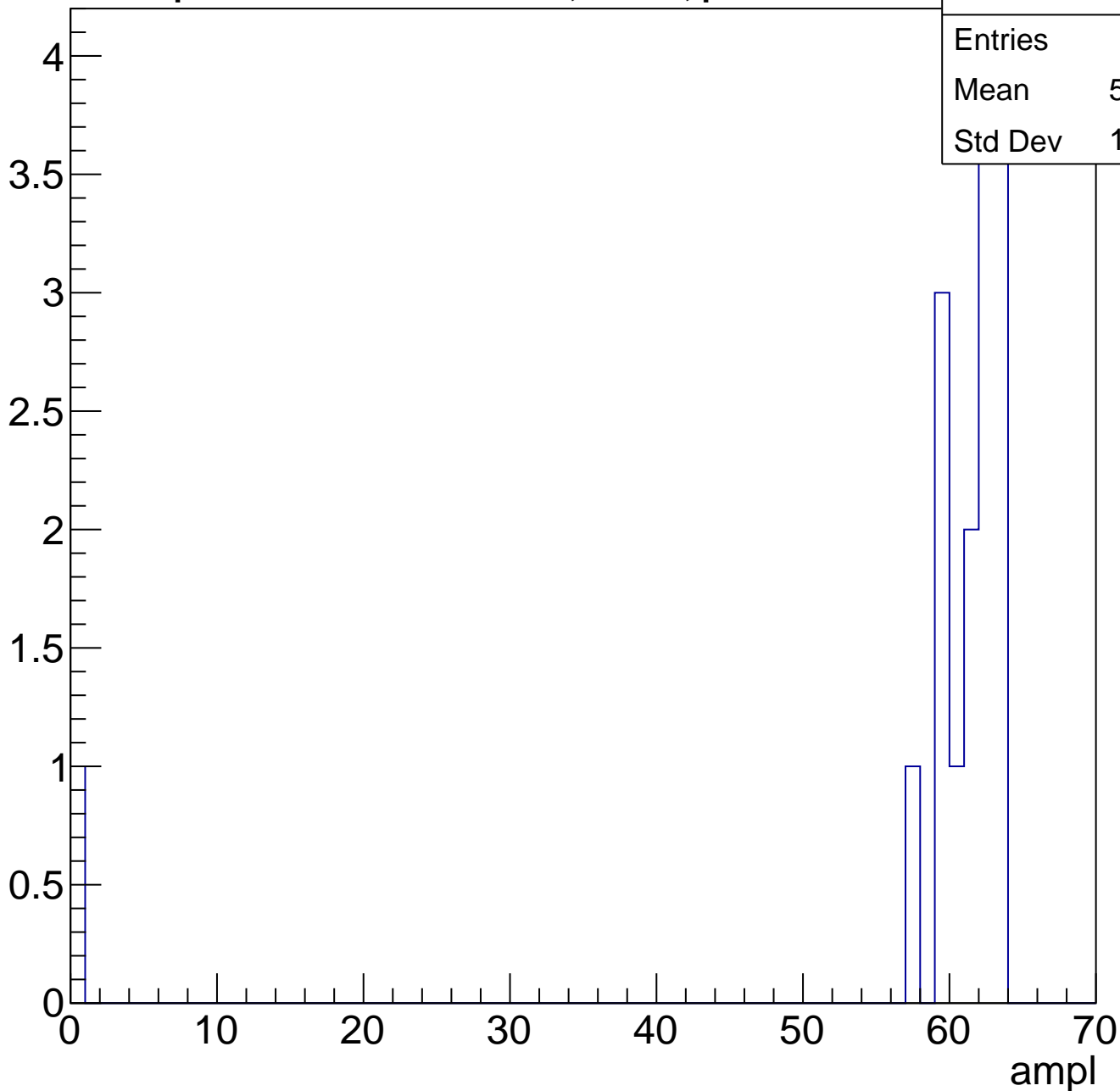
Entries	59
Mean	58.39
Std Dev	3.242



B1L102S, U6-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch77, adc0

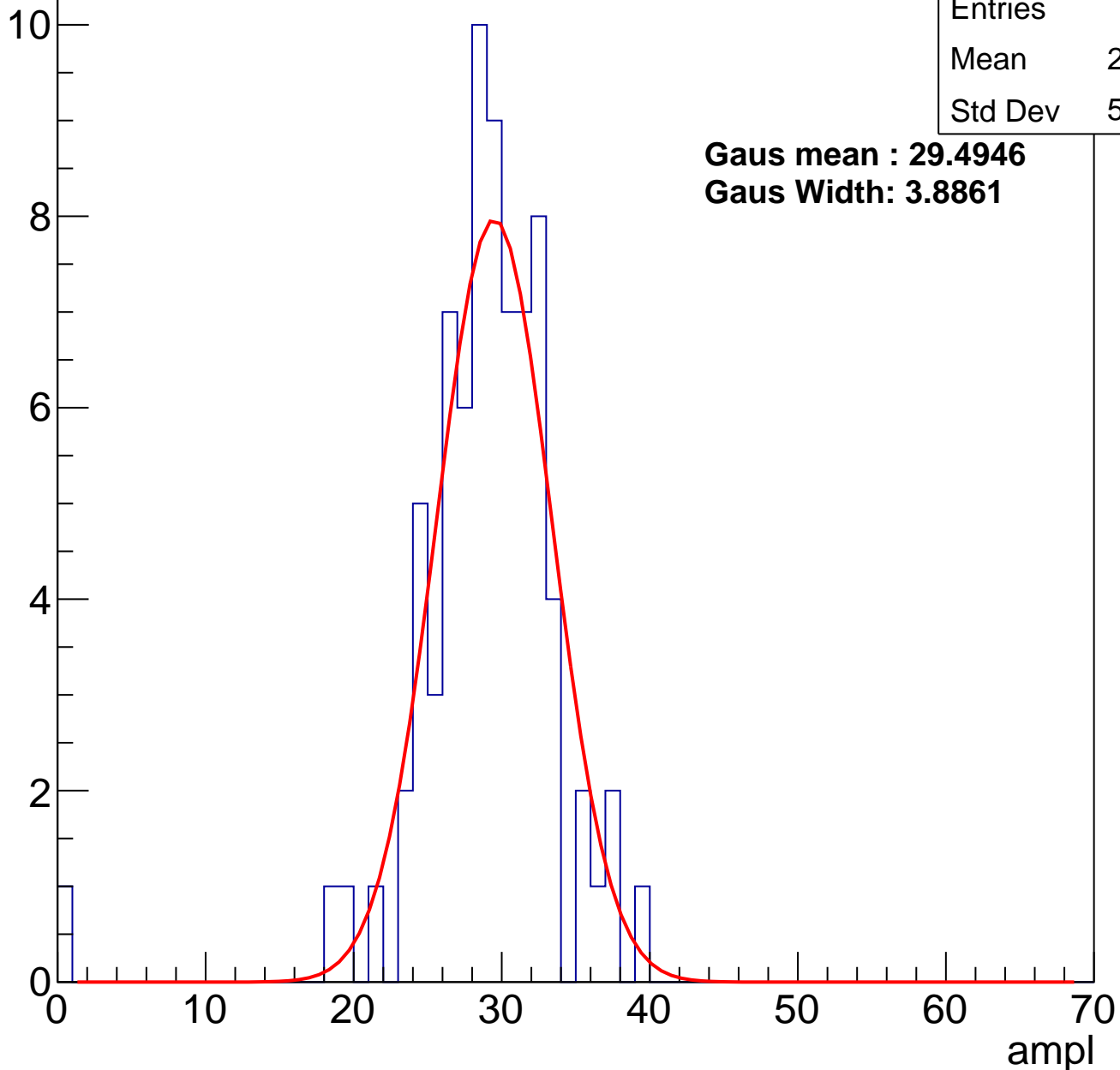
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.44
Std Dev	5.017

Gaus mean : 29.4946

Gaus Width: 3.8861

Entry



B1L102S, U6-ch77, adc1

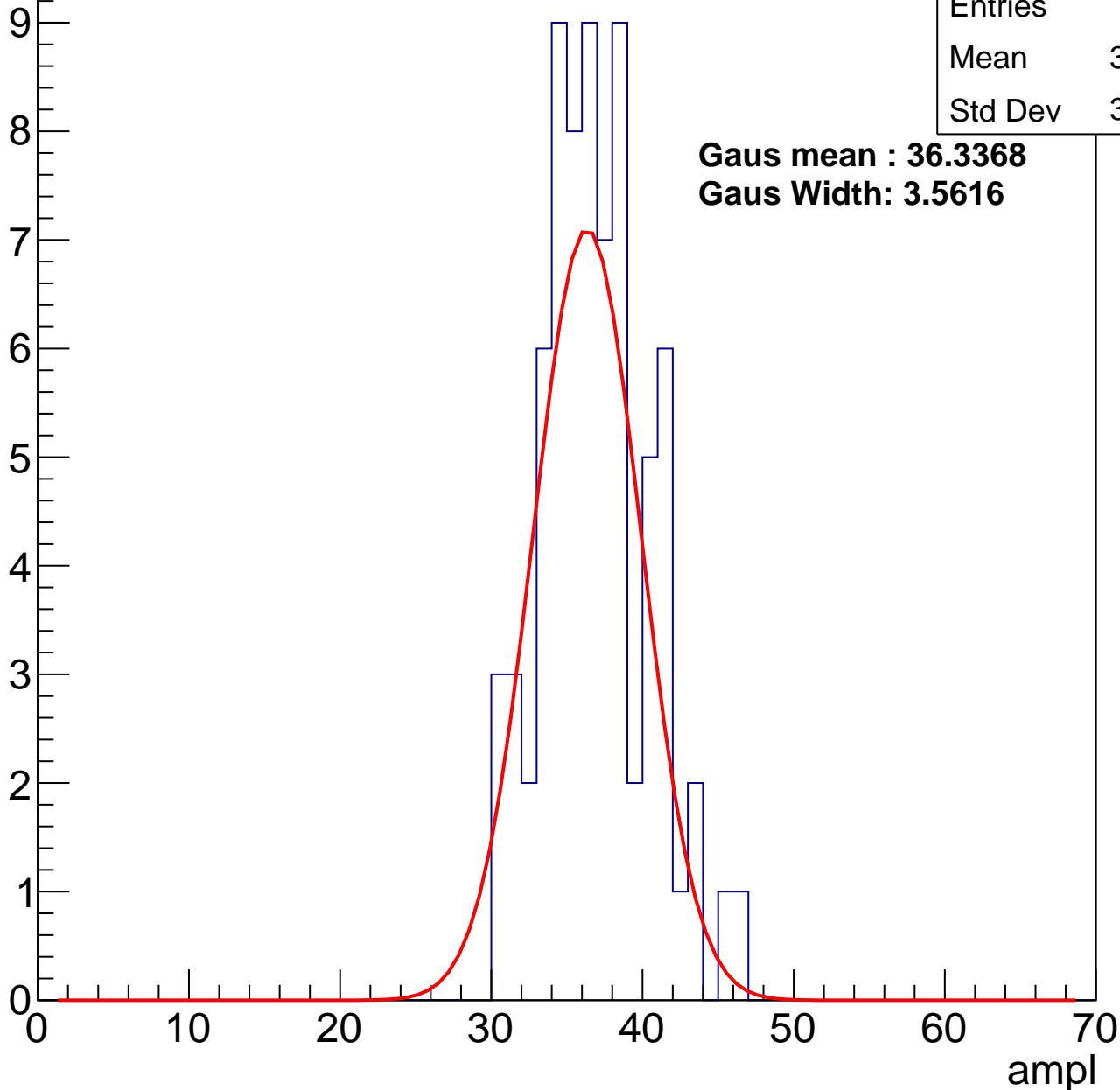
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	36.47
Std Dev	3.492

Gaus mean : 36.3368

Gaus Width: 3.5616



B1L102S, U6-ch77, adc2

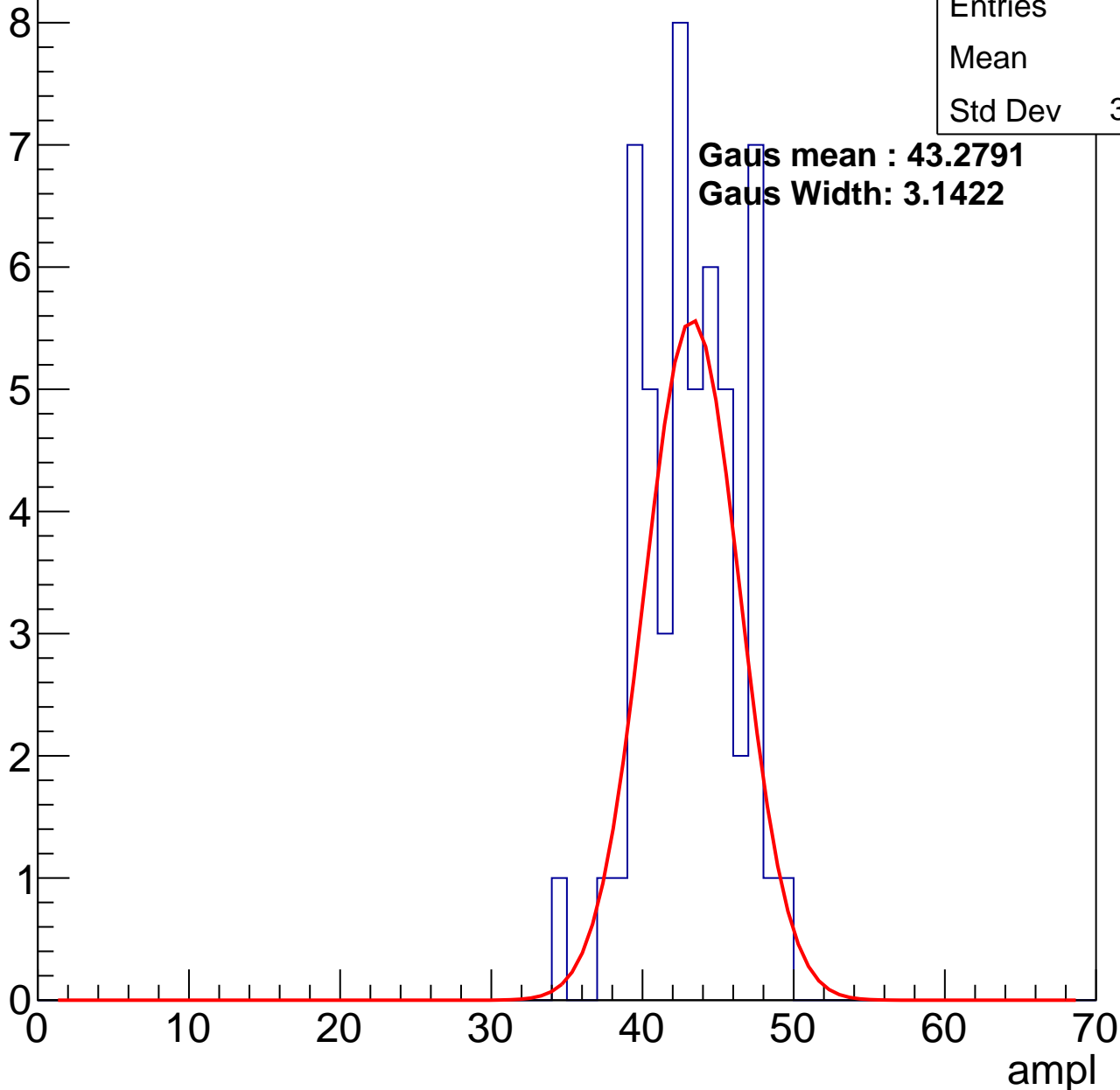
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	42.7
Std Dev	3.166

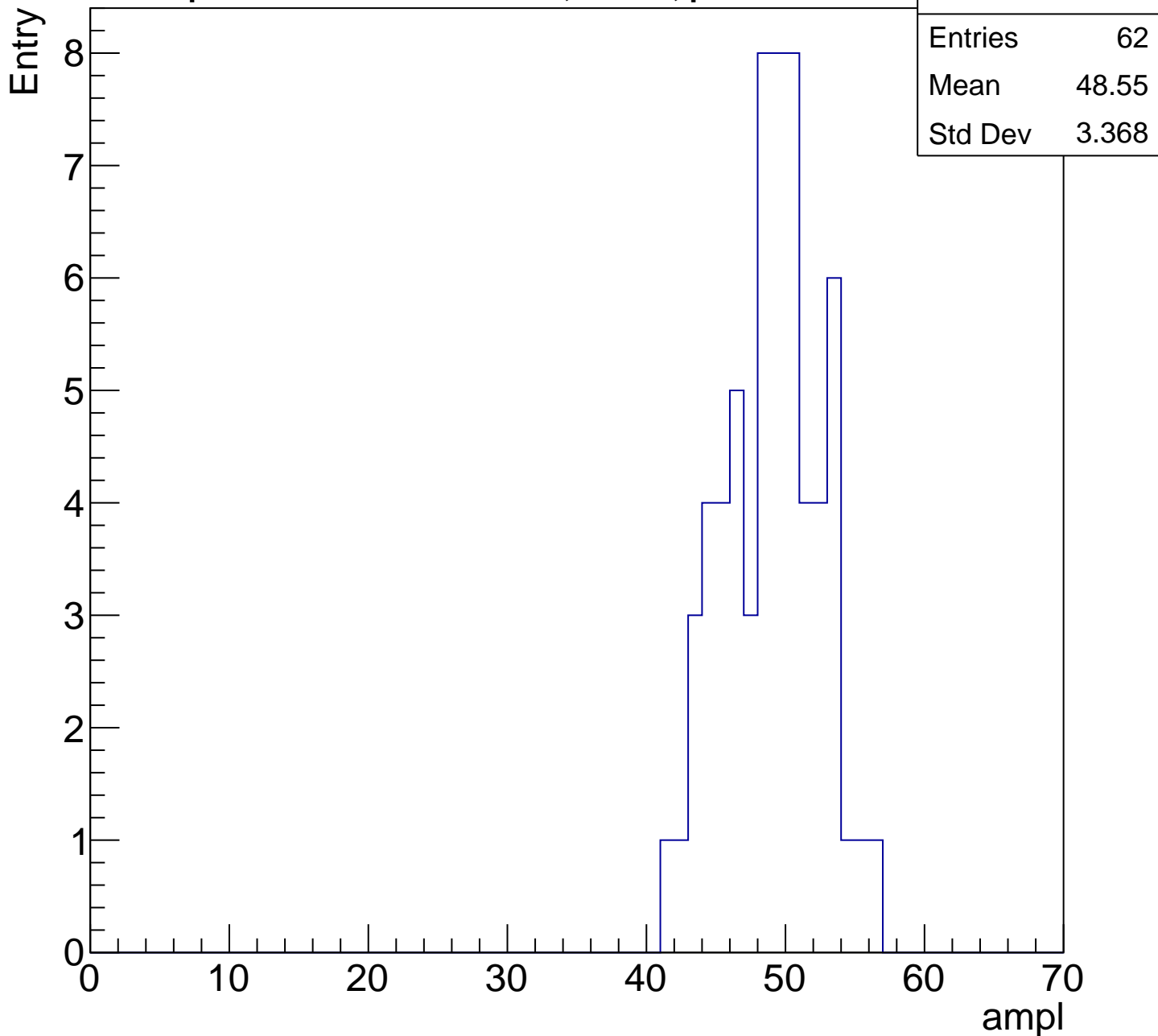
Gaus mean : 43.2791

Gaus Width: 3.1422



B1L102S, U6-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	55.29
Std Dev	3.589

Entry

10

8

6

4

2

0

0

10

20

30

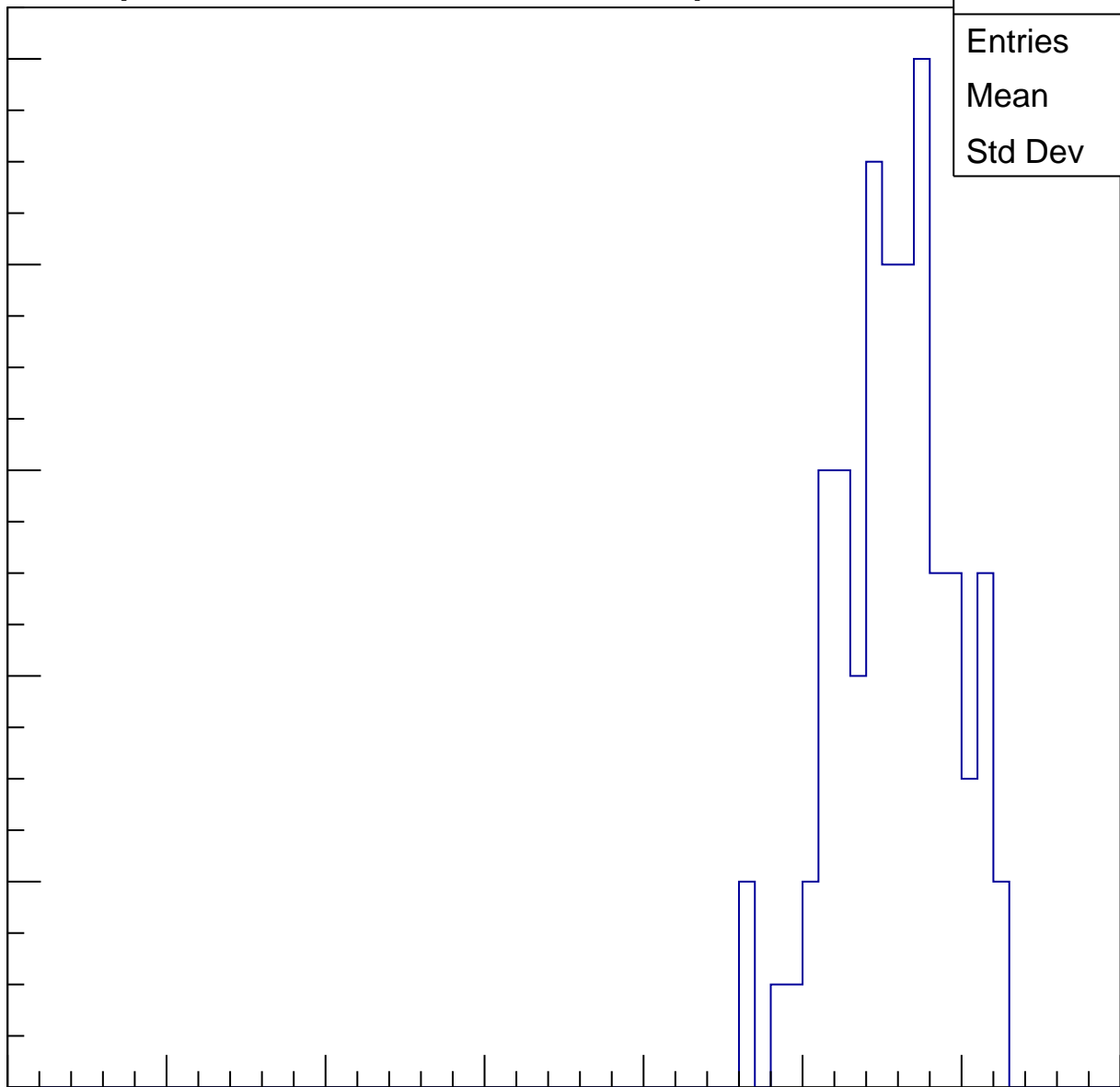
40

50

60

ampl

70

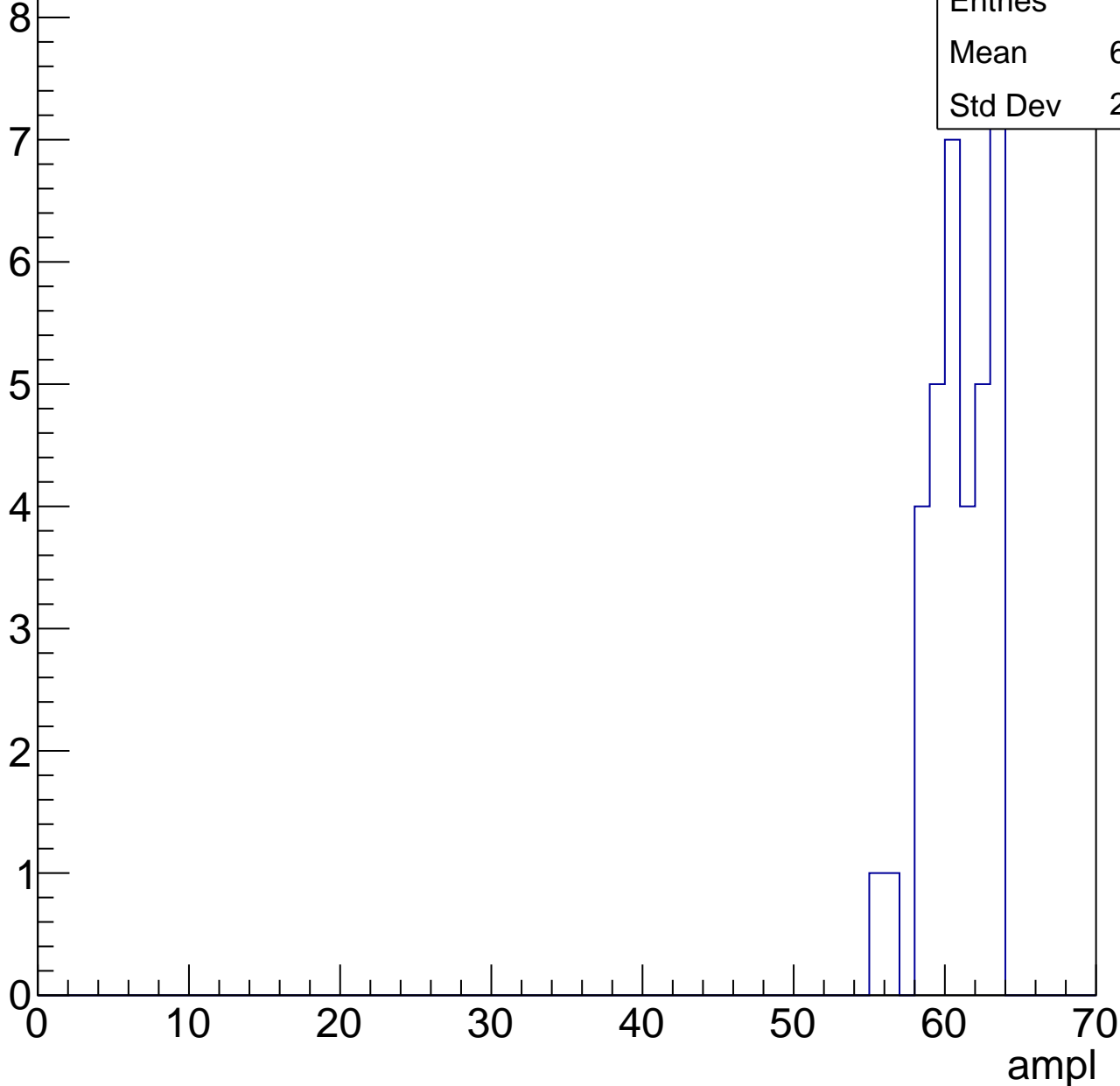


B1L102S, U6-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	60.46
Std Dev	2.075



B1L102S, U6-ch77, adc6

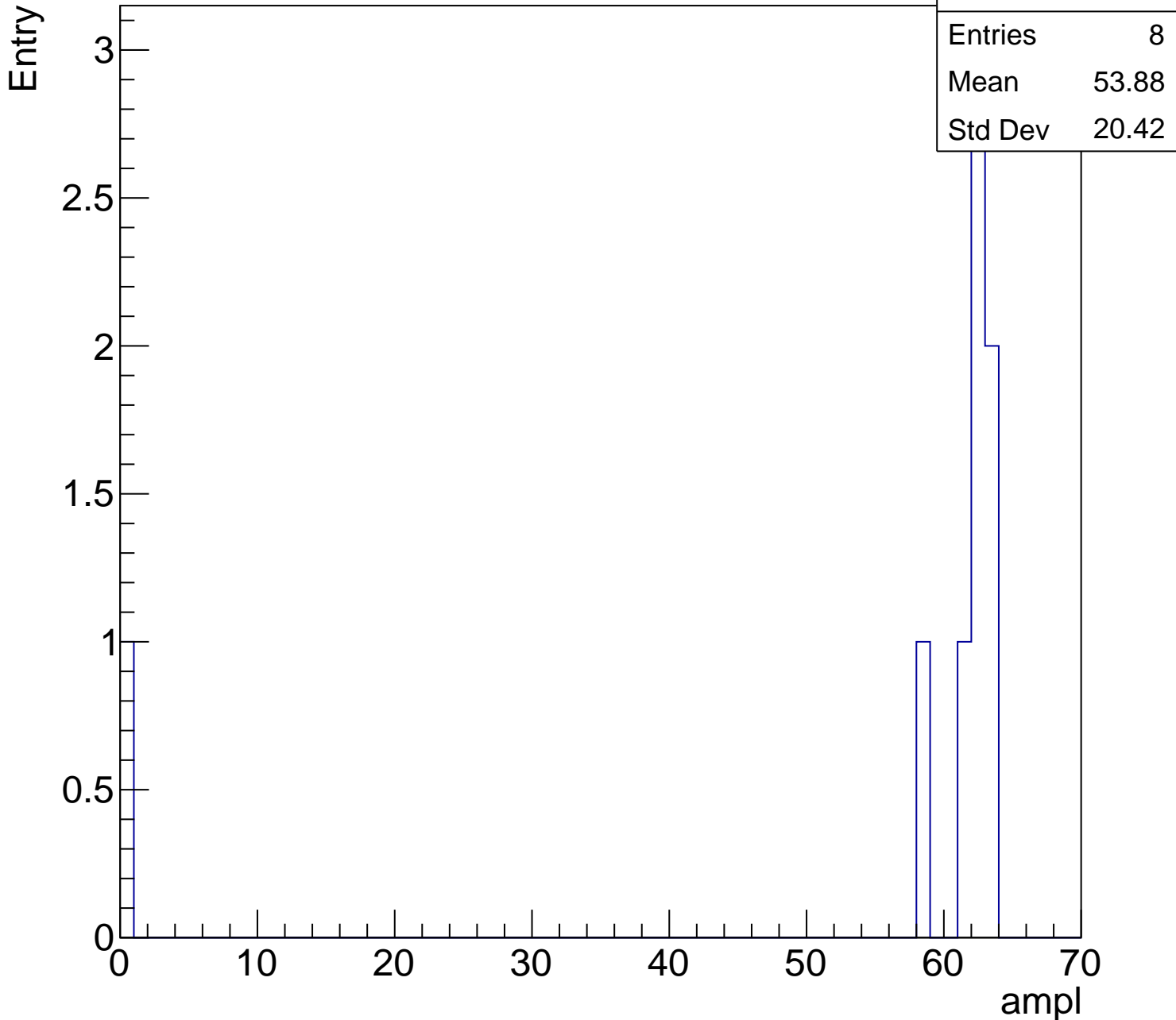
calib_packv5_042523_0143.root, FC#11, port A2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	53.88
Std Dev	20.42

ampl



B1L102S, U6-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U6-ch78, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	27.87
Std Dev	3.763

Gaus mean : 27.4492

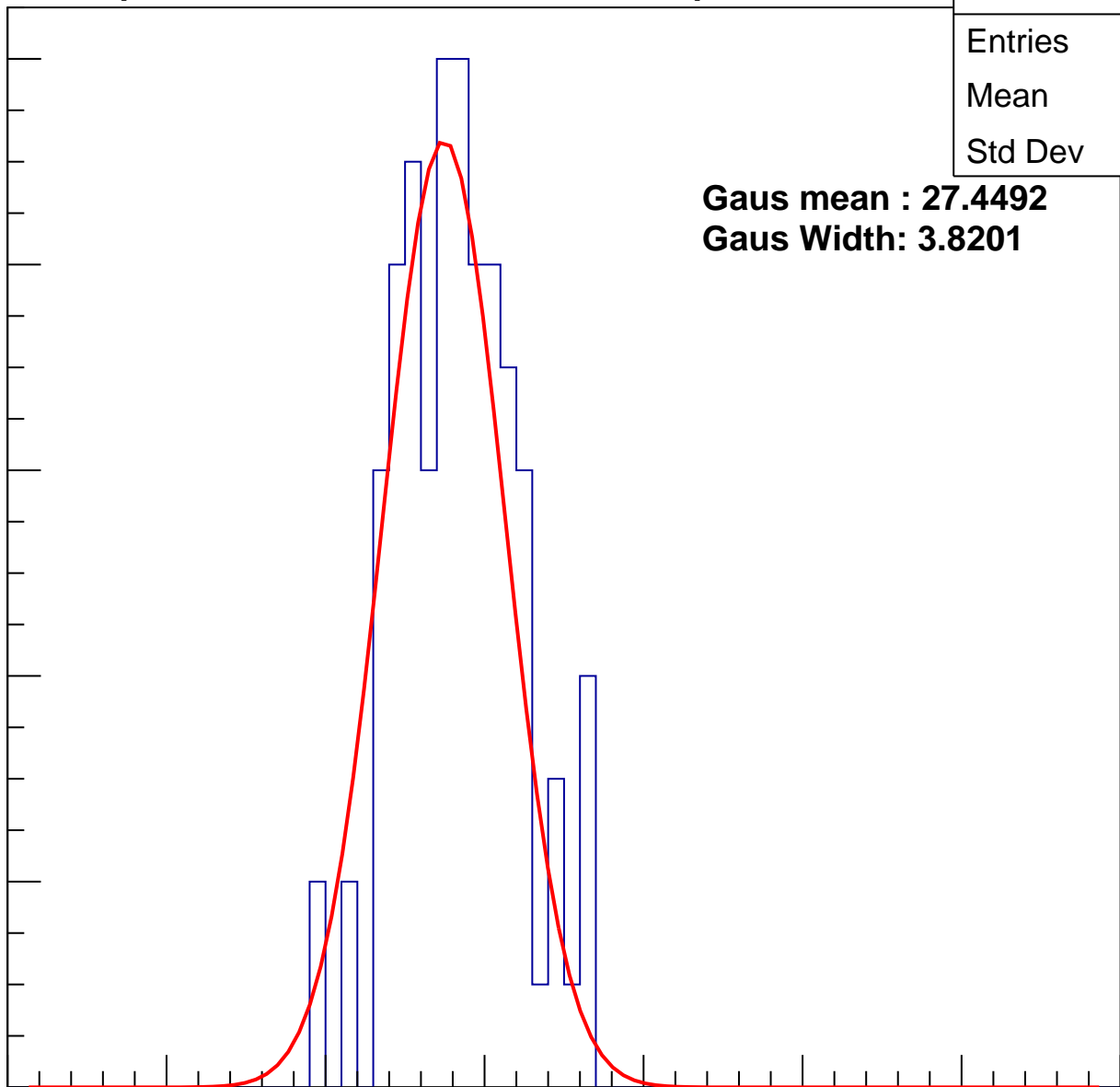
Gaus Width: 3.8201

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch78, adc1

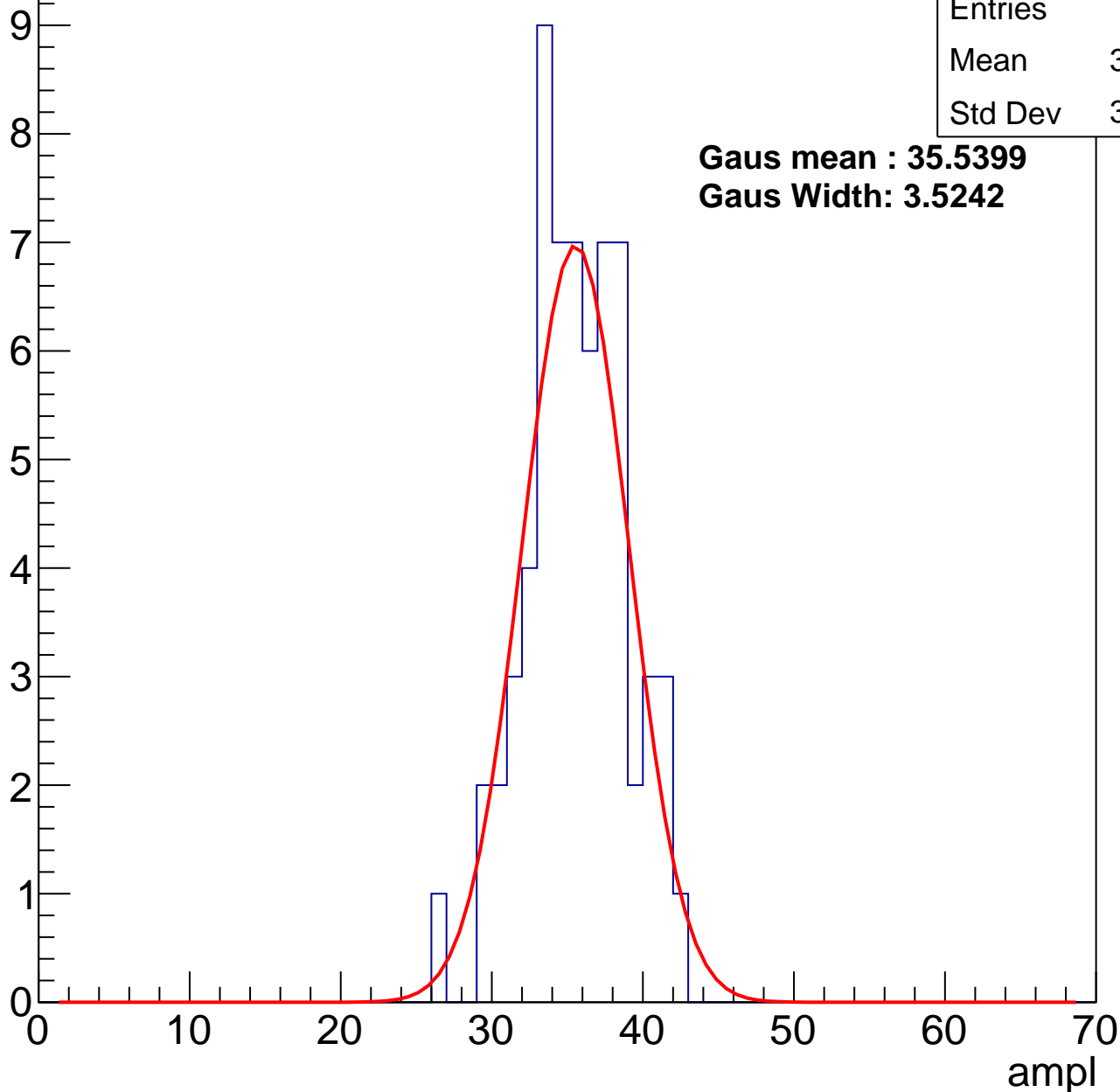
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.14
Std Dev	3.283

Gaus mean : 35.5399

Gaus Width: 3.5242



B1L102S, U6-ch78, adc2

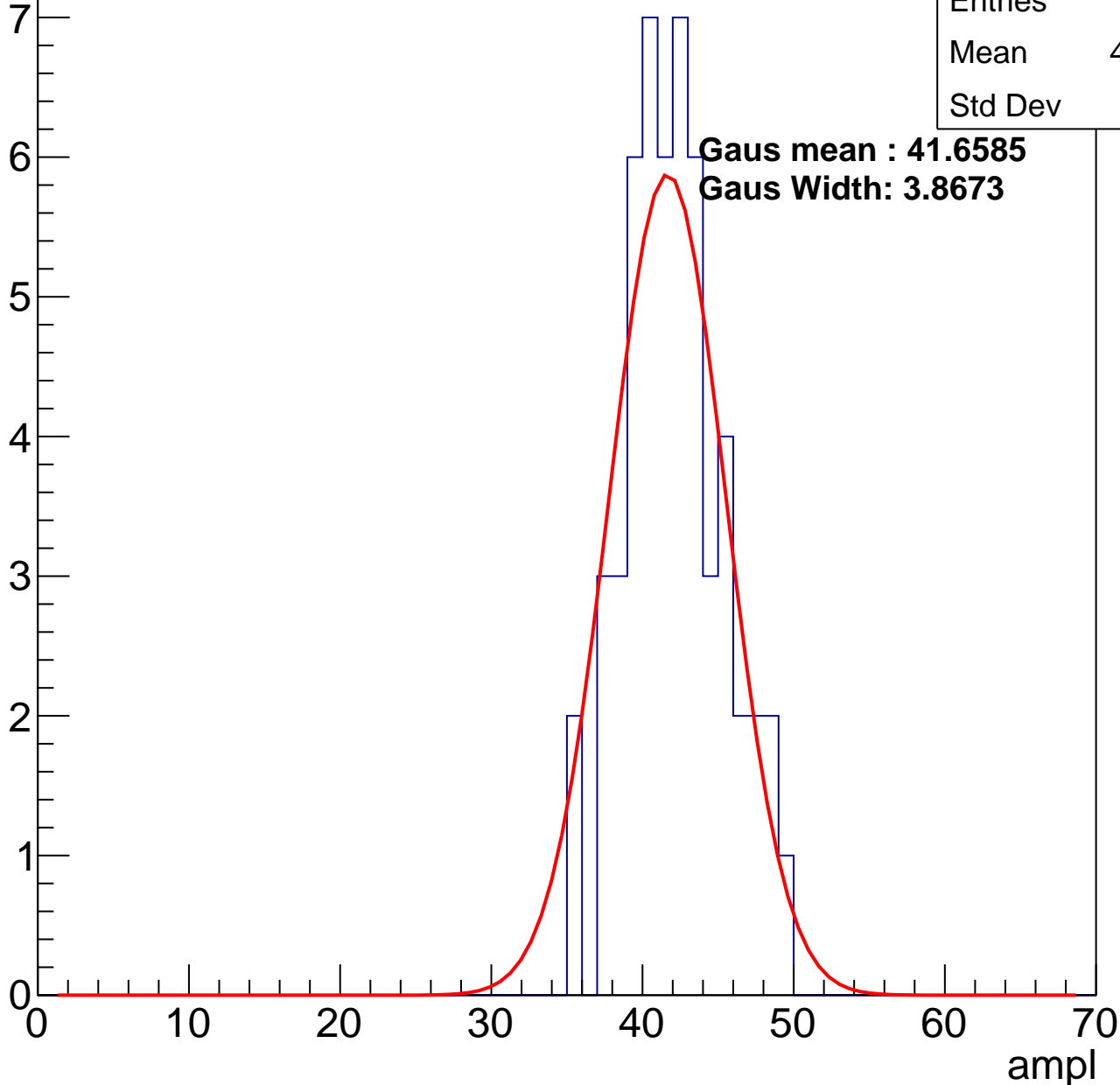
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.67
Std Dev	3.22

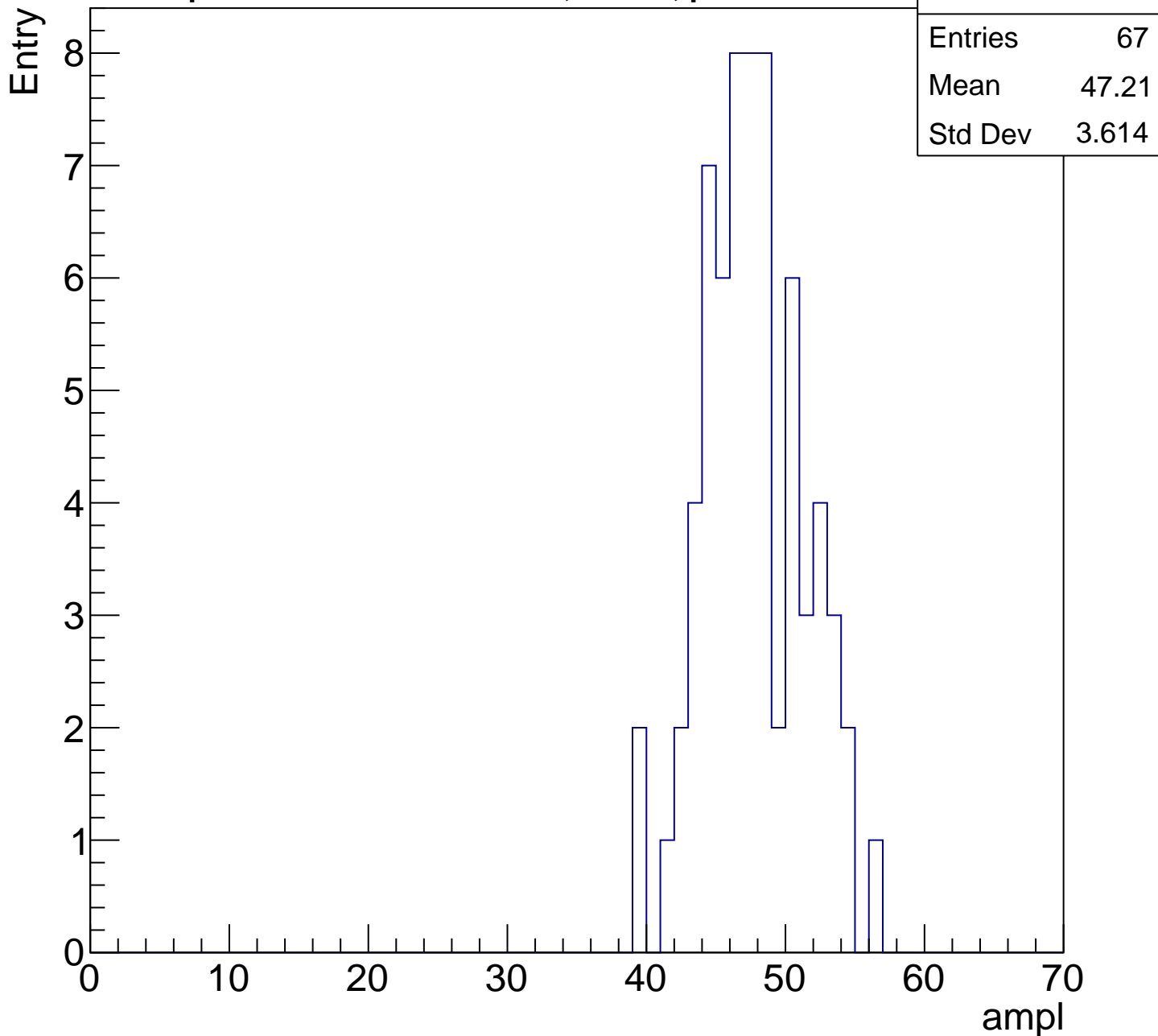
Gaus mean : 41.6585

Gaus Width: 3.8673



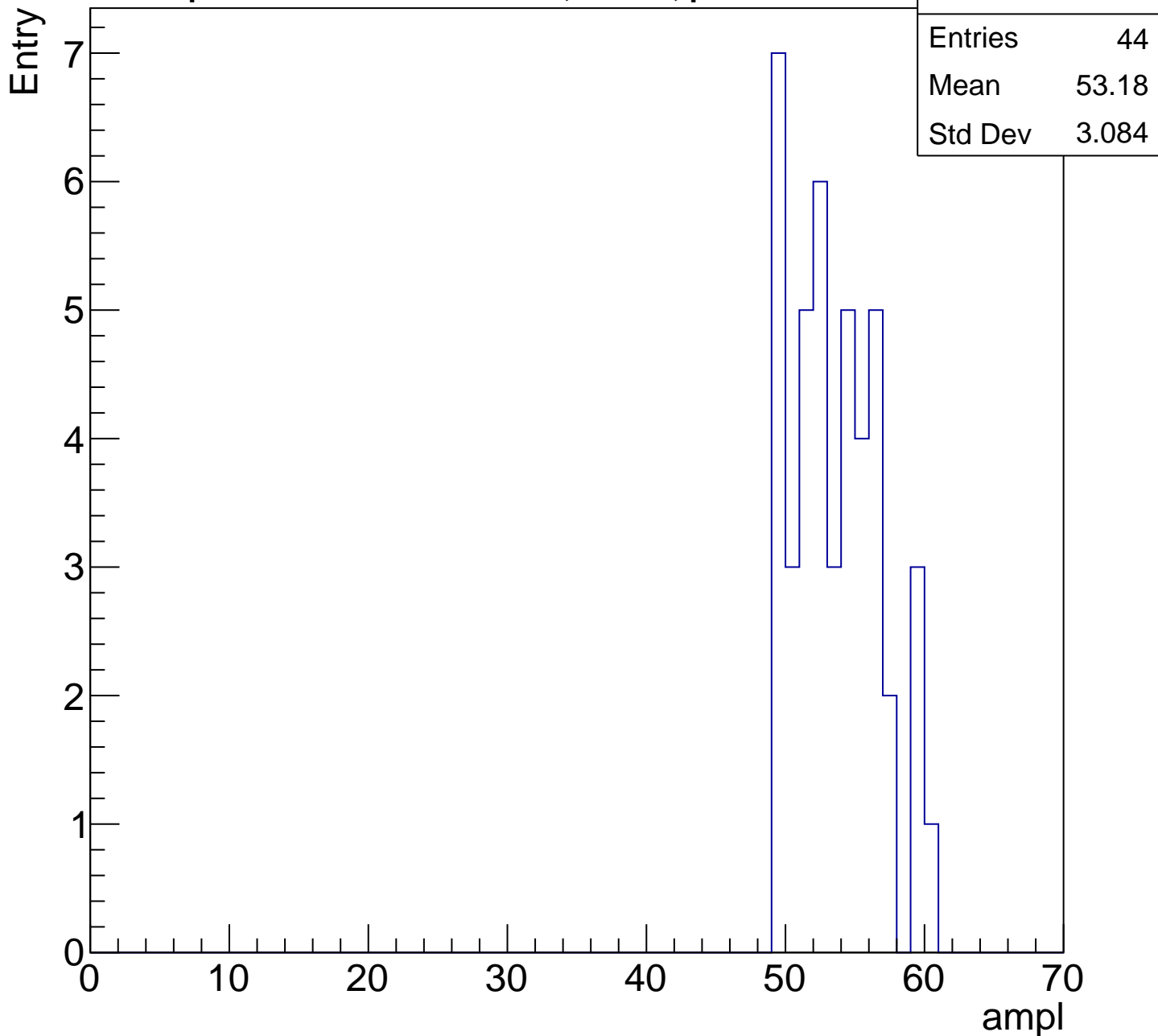
B1L102S, U6-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

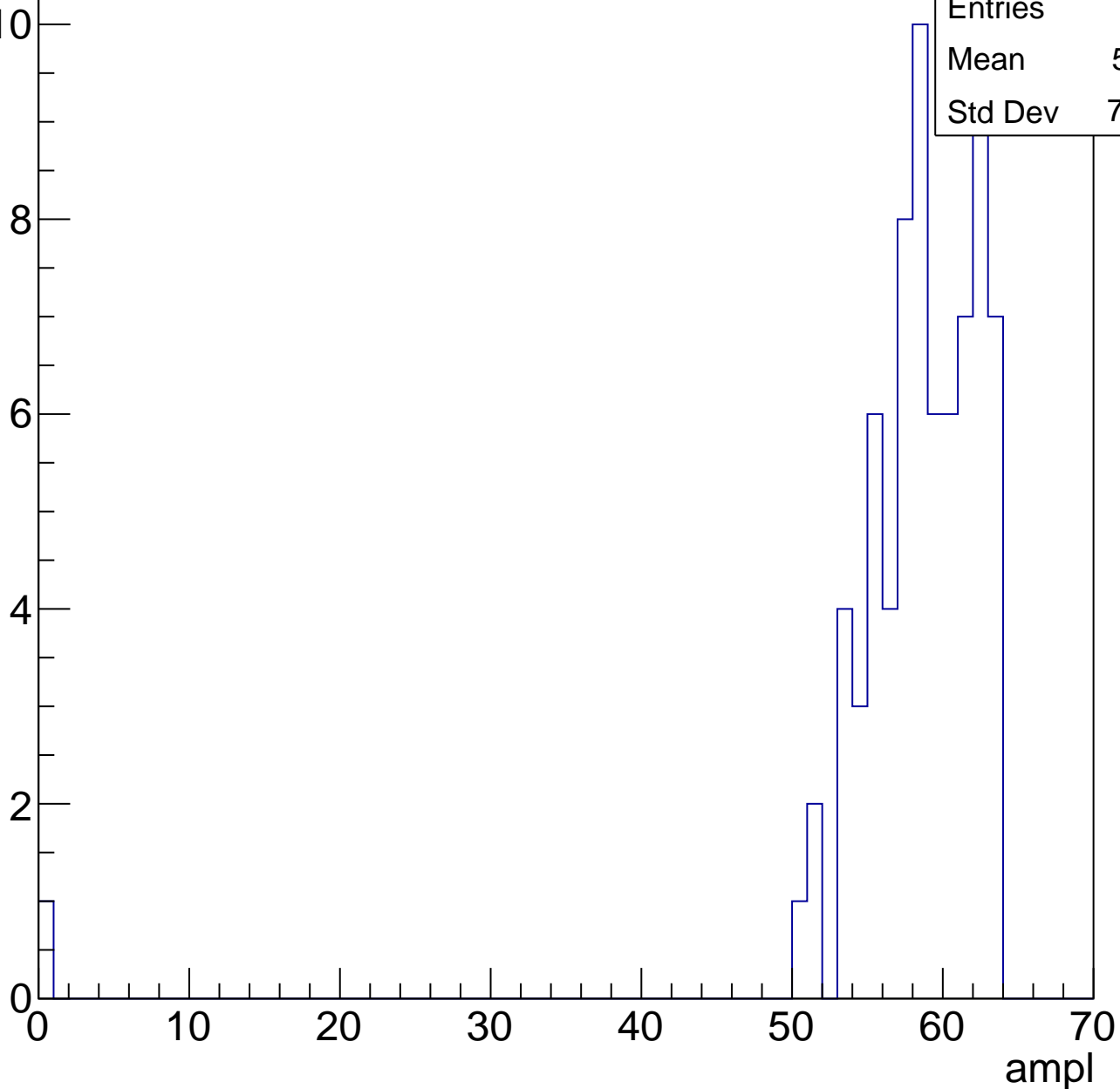


B1L102S, U6-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	57.51
Std Dev	7.486



B1L102S, U6-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	11
Mean	61.18
Std Dev	1.336

B1L102S, U6-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U6-ch79, adc0

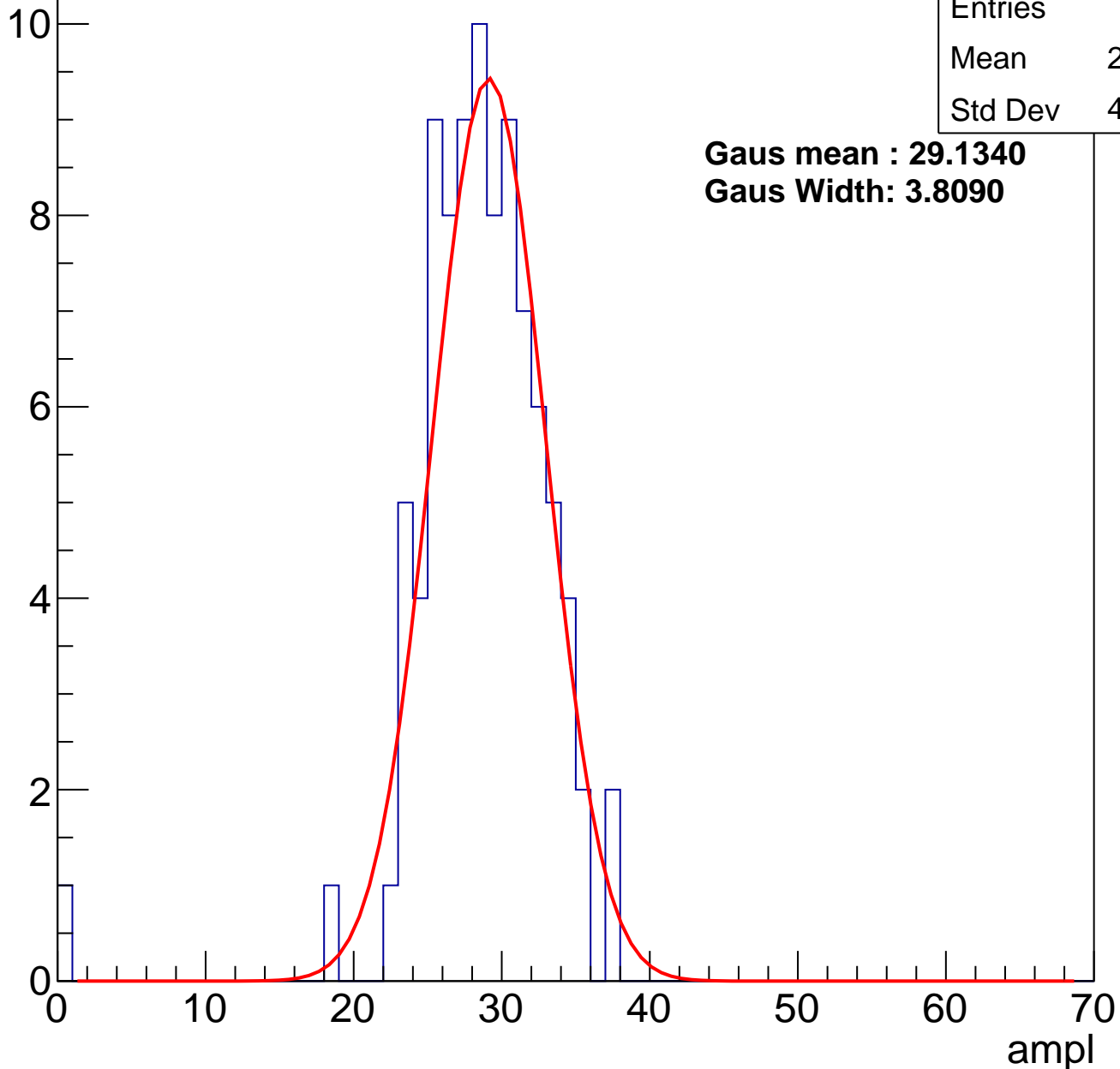
calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	28.16
Std Dev	4.642

Gaus mean : 29.1340

Gaus Width: 3.8090

Entry



B1L102S, U6-ch79, adc1

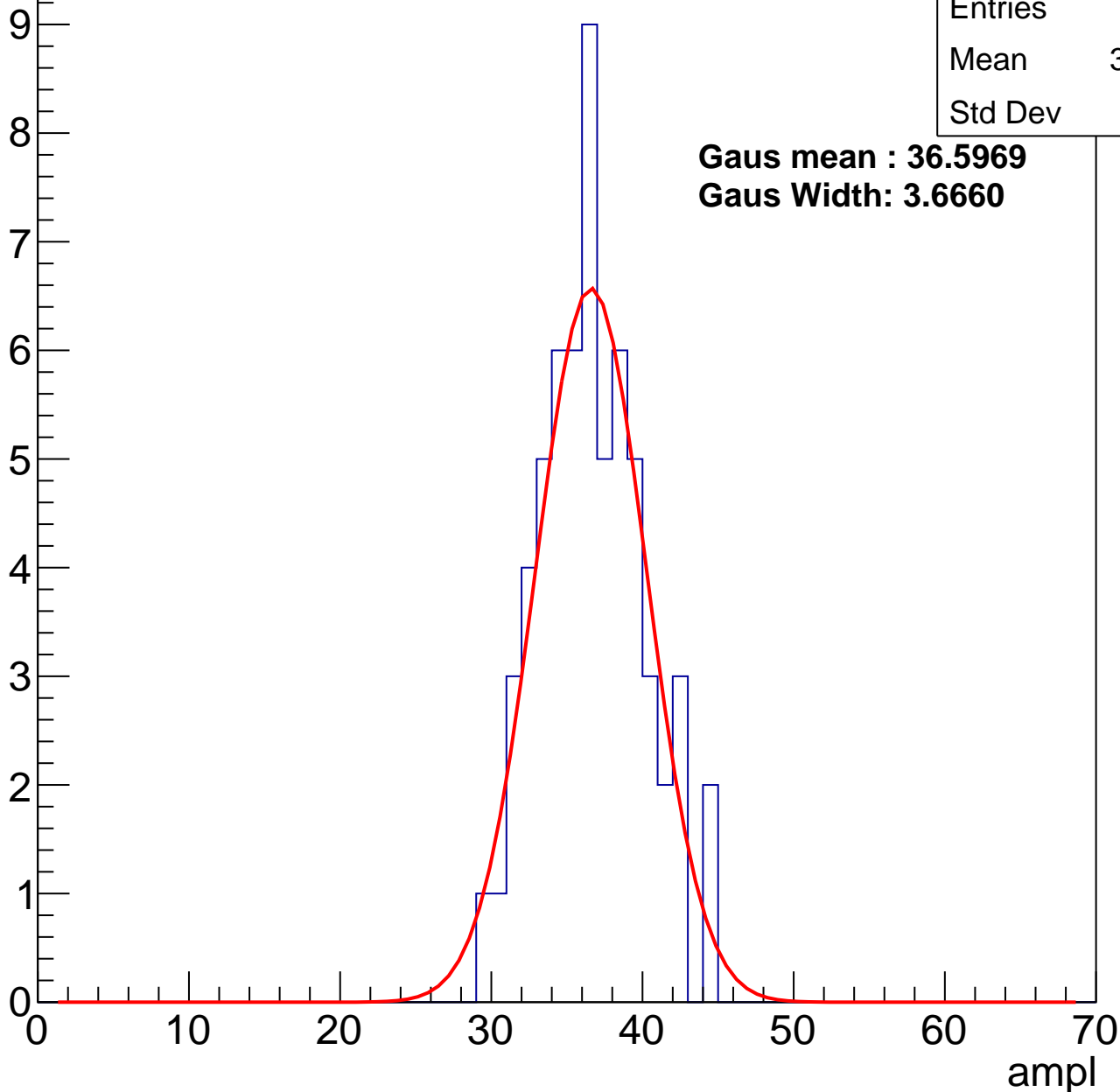
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.18
Std Dev	3.4

Gaus mean : 36.5969

Gaus Width: 3.6660



B1L102S, U6-ch79, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.33
Std Dev	3.642

Gaus mean : 42.0260

Gaus Width: 3.3406

10

8

6

4

2

0

0

10

20

30

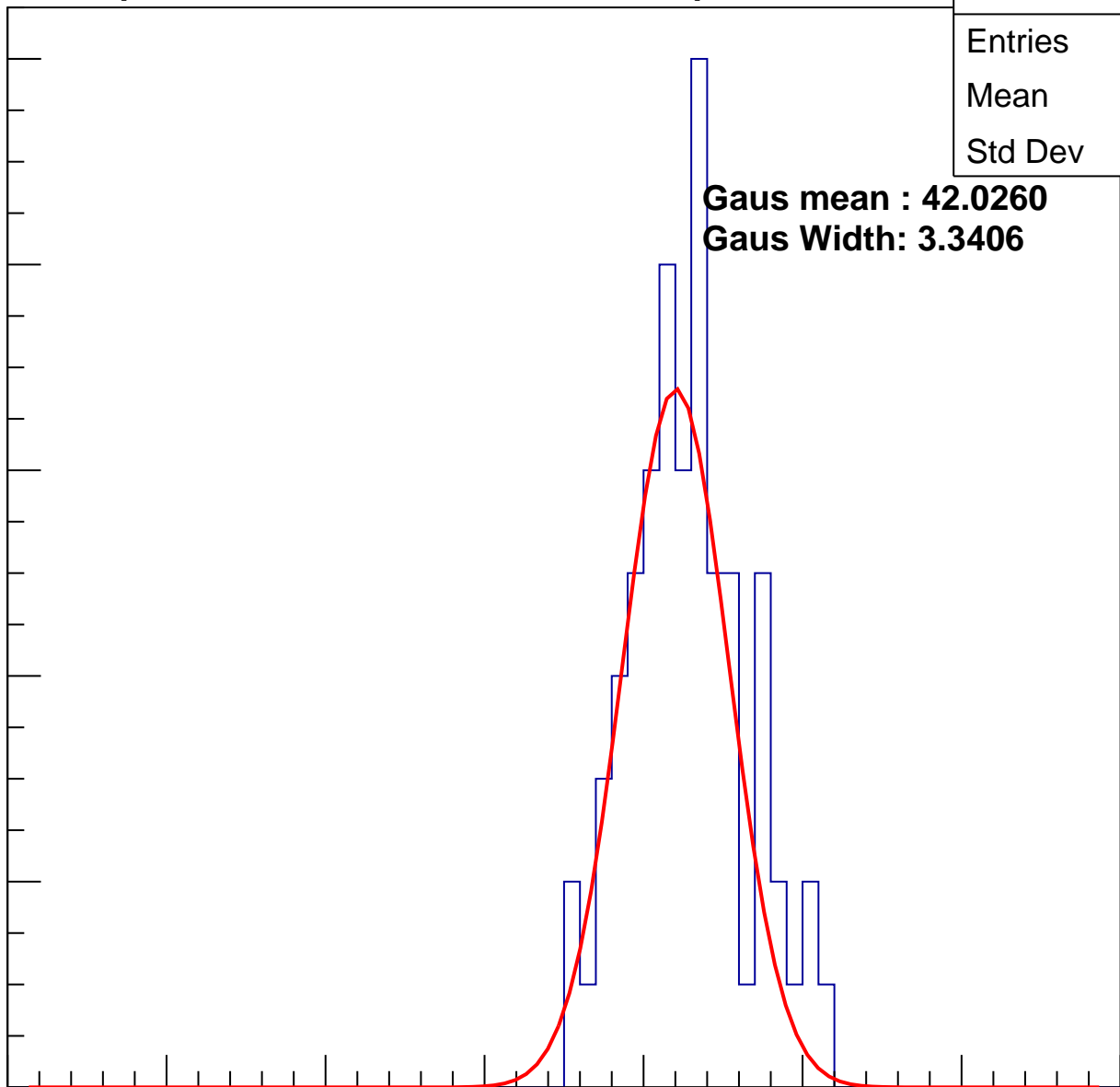
40

50

60

70

ampl

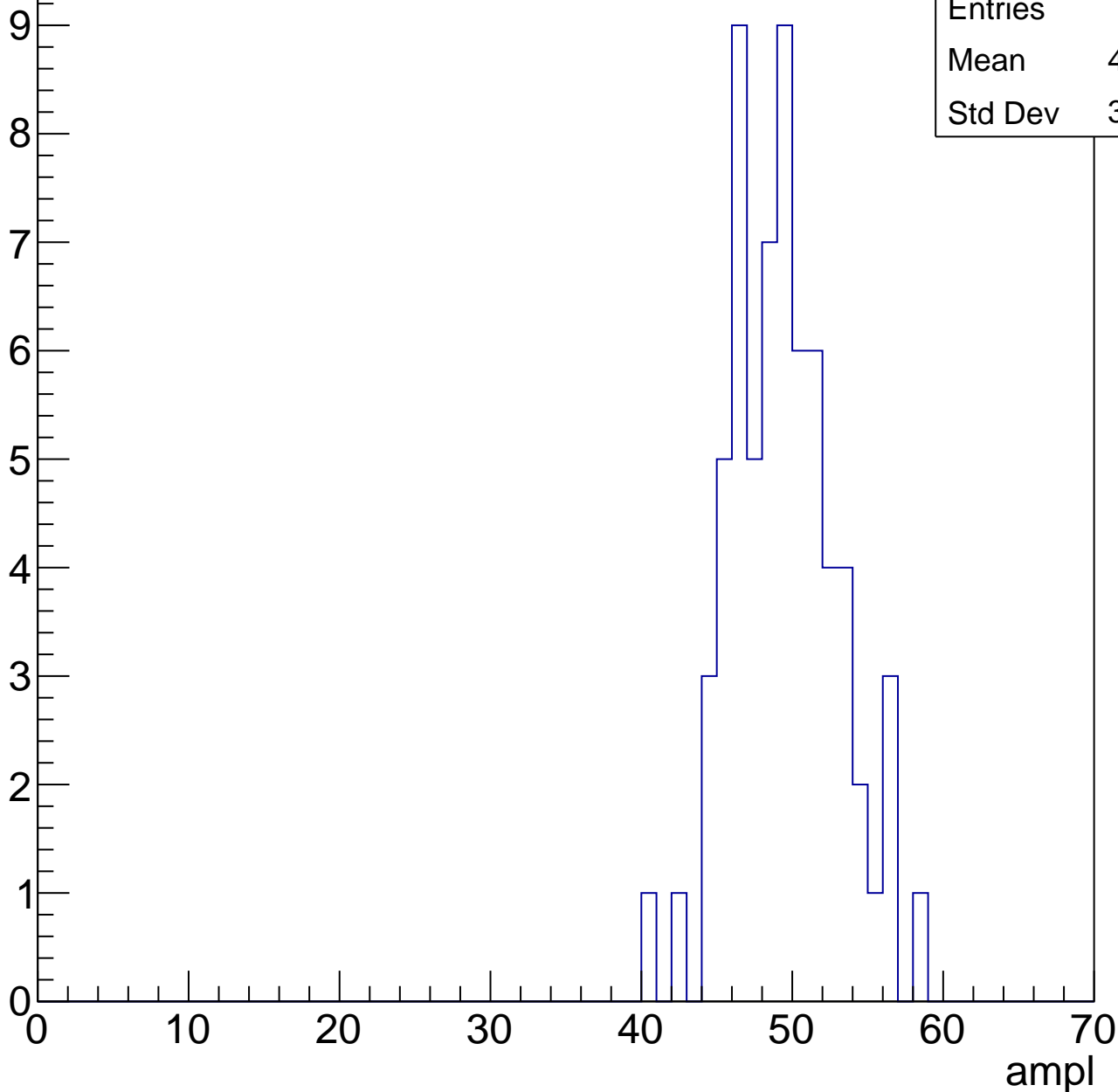


B1L102S, U6-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	48.96
Std Dev	3.534

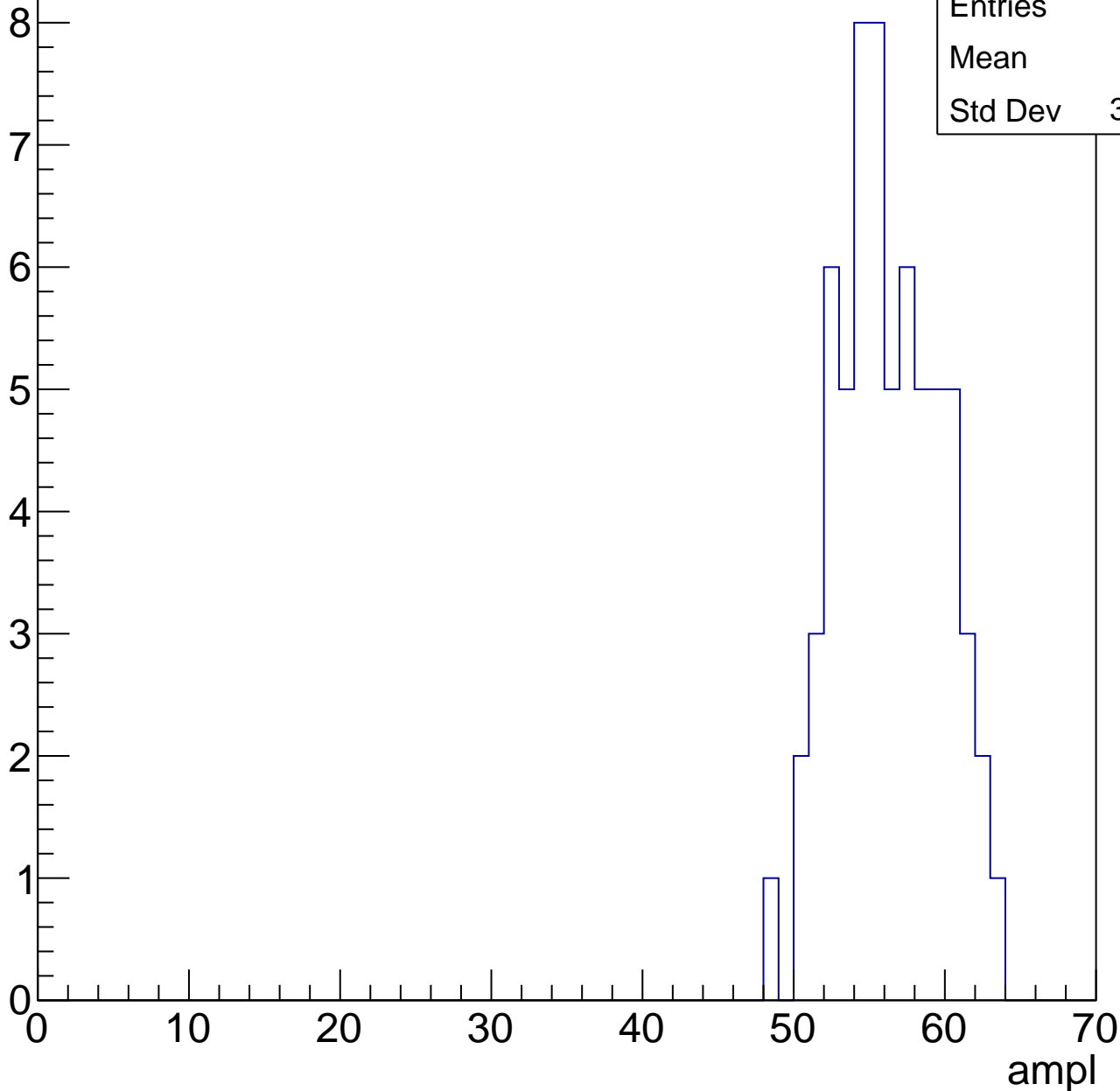


B1L102S, U6-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	55.8
Std Dev	3.366

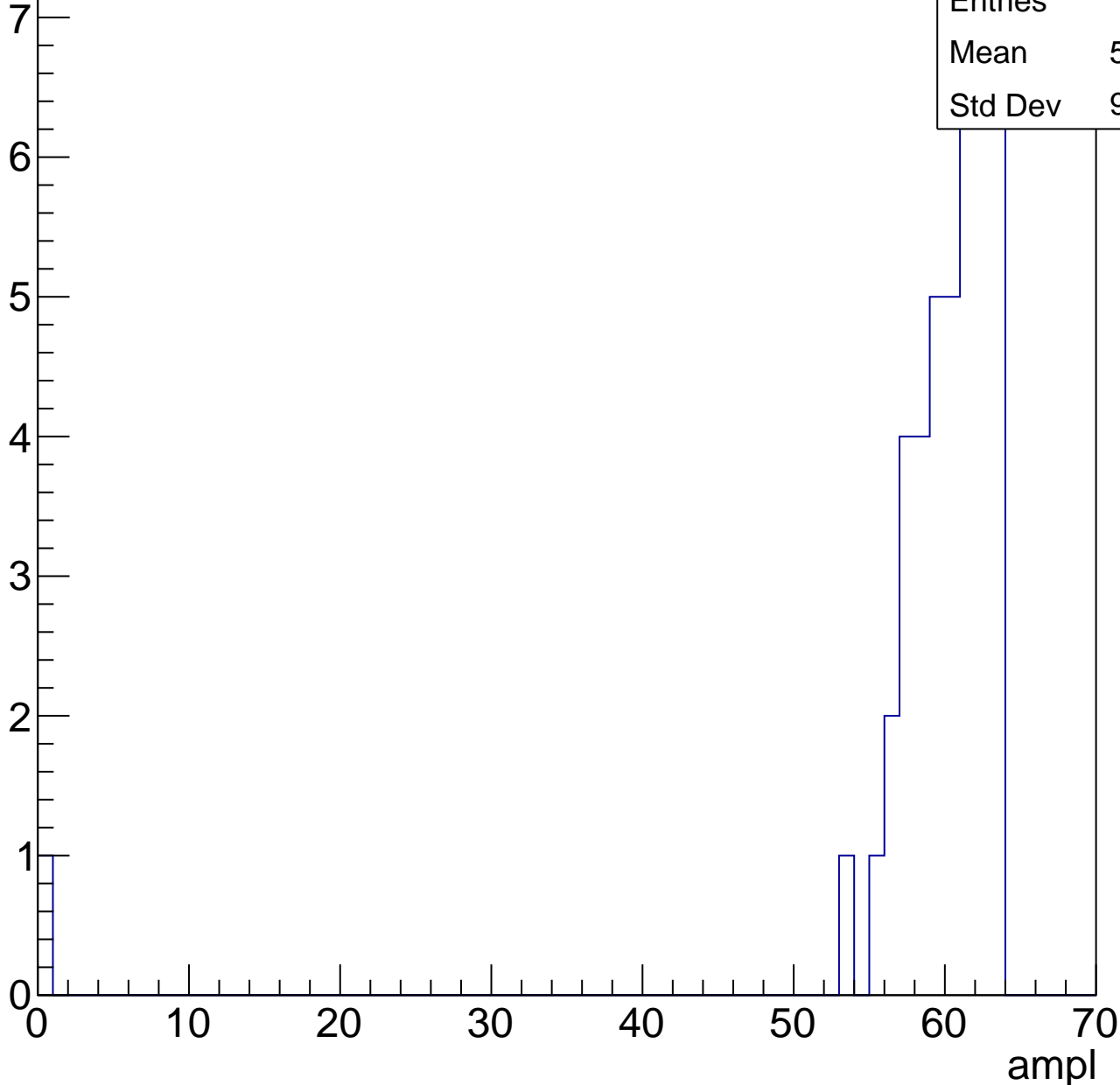


B1L102S, U6-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.57
Std Dev	9.257



B1L102S, U6-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

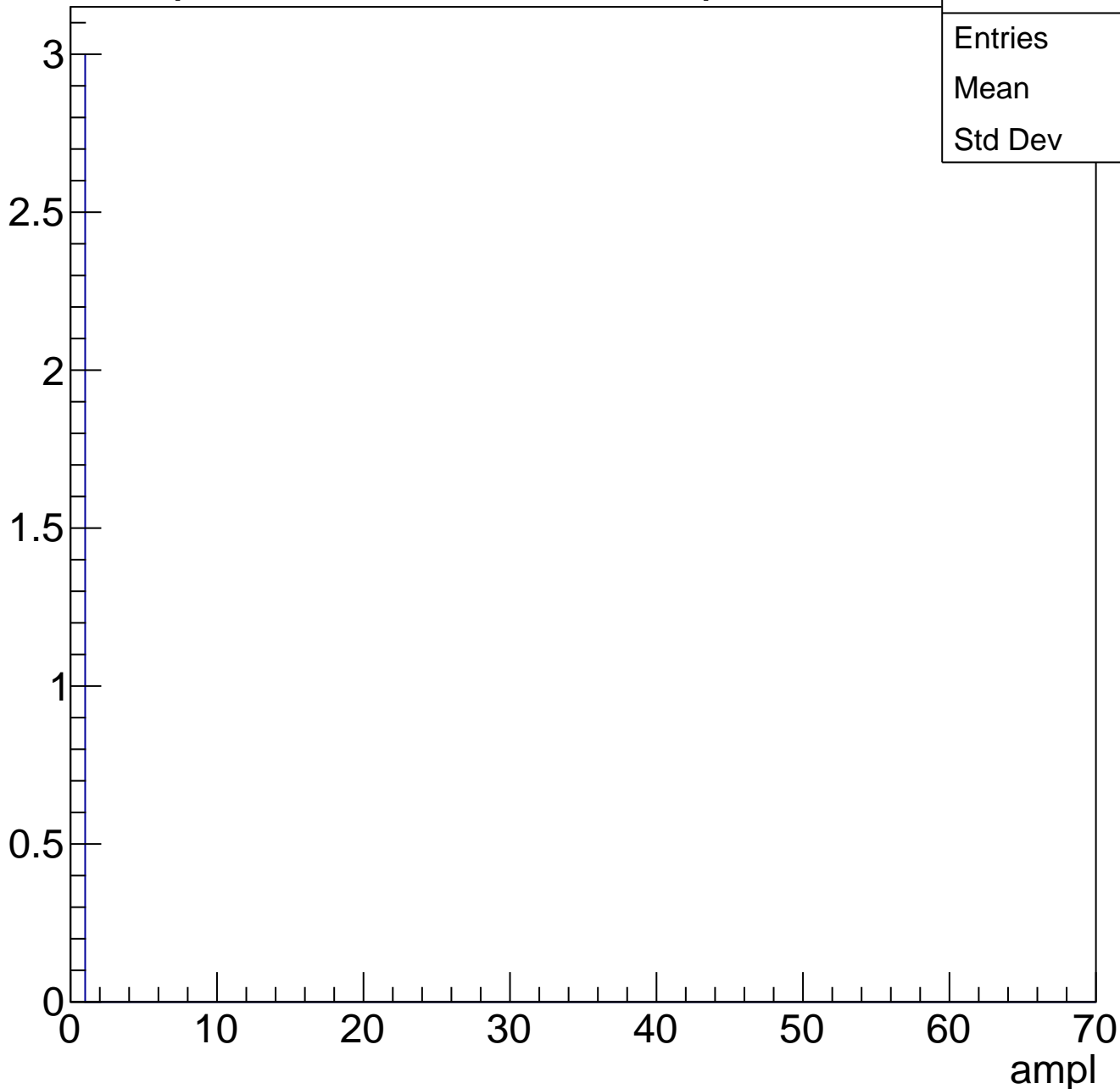
ampl

0 10 20 30 40 50 60 70

B1L102S, U6-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U6-ch80, adc0

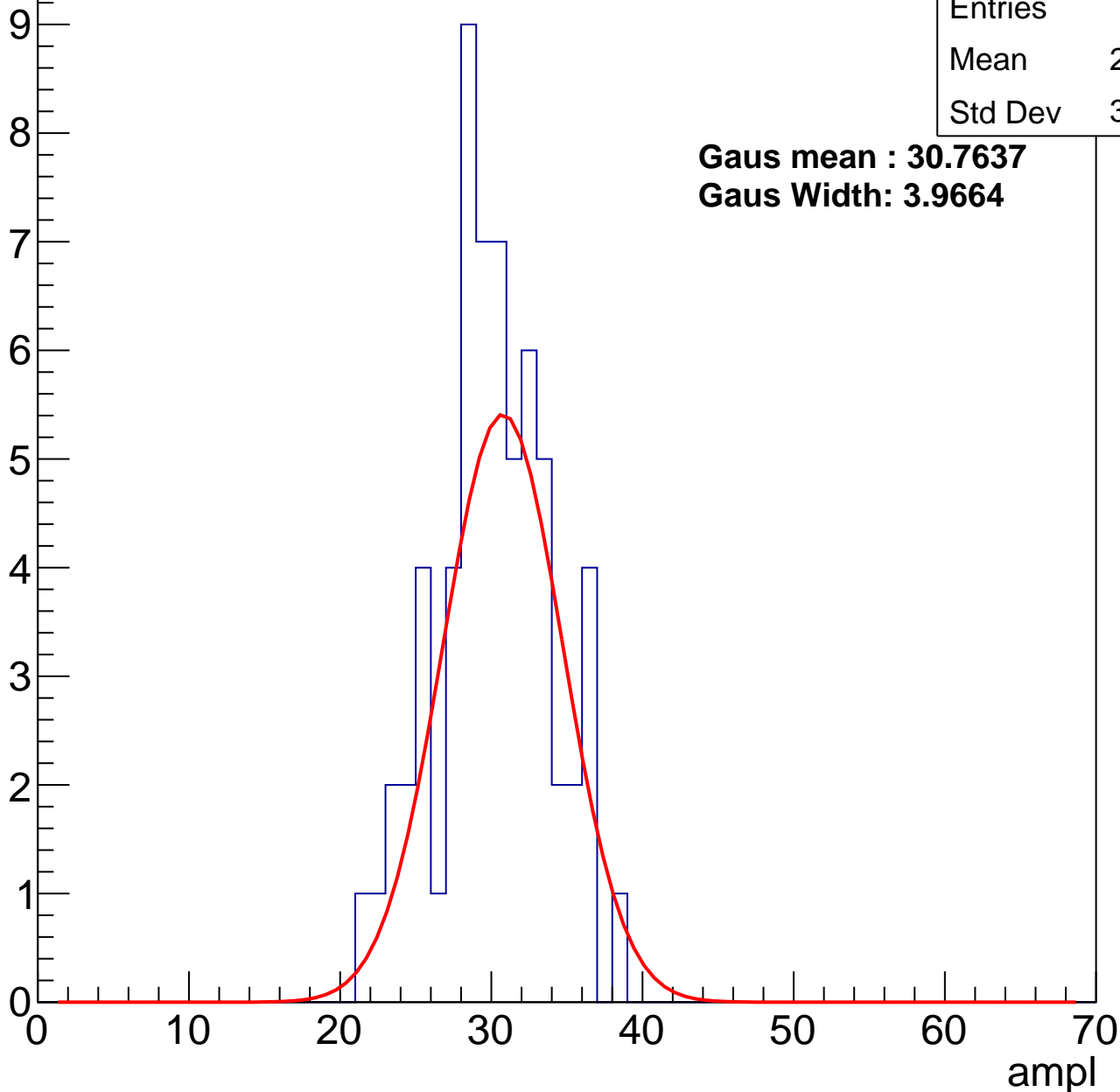
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.65
Std Dev	3.708

Gaus mean : 30.7637

Gaus Width: 3.9664



B1L102S, U6-ch80, adc1

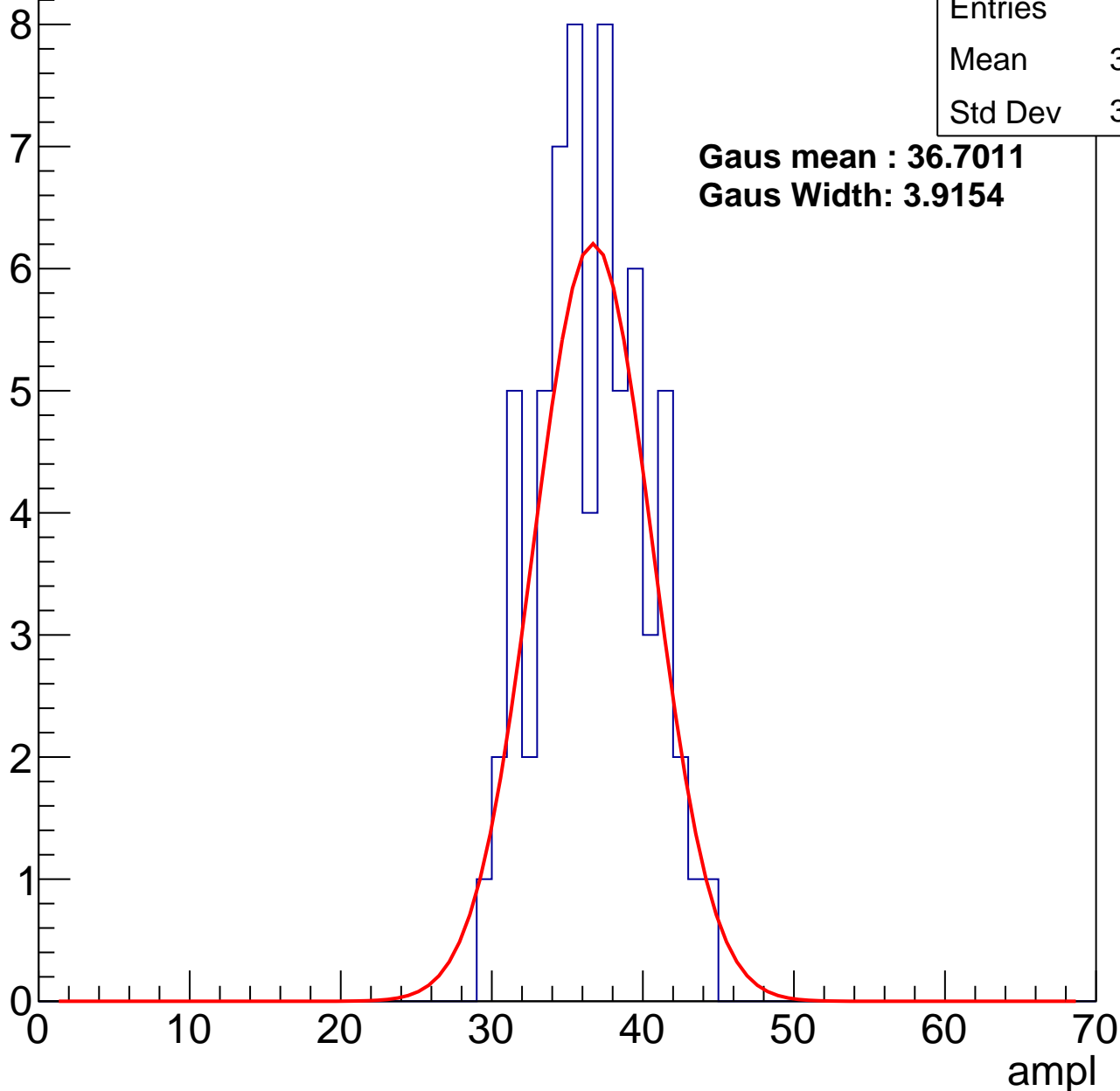
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.17
Std Dev	3.502

Gaus mean : 36.7011

Gaus Width: 3.9154



B1L102S, U6-ch80, adc2

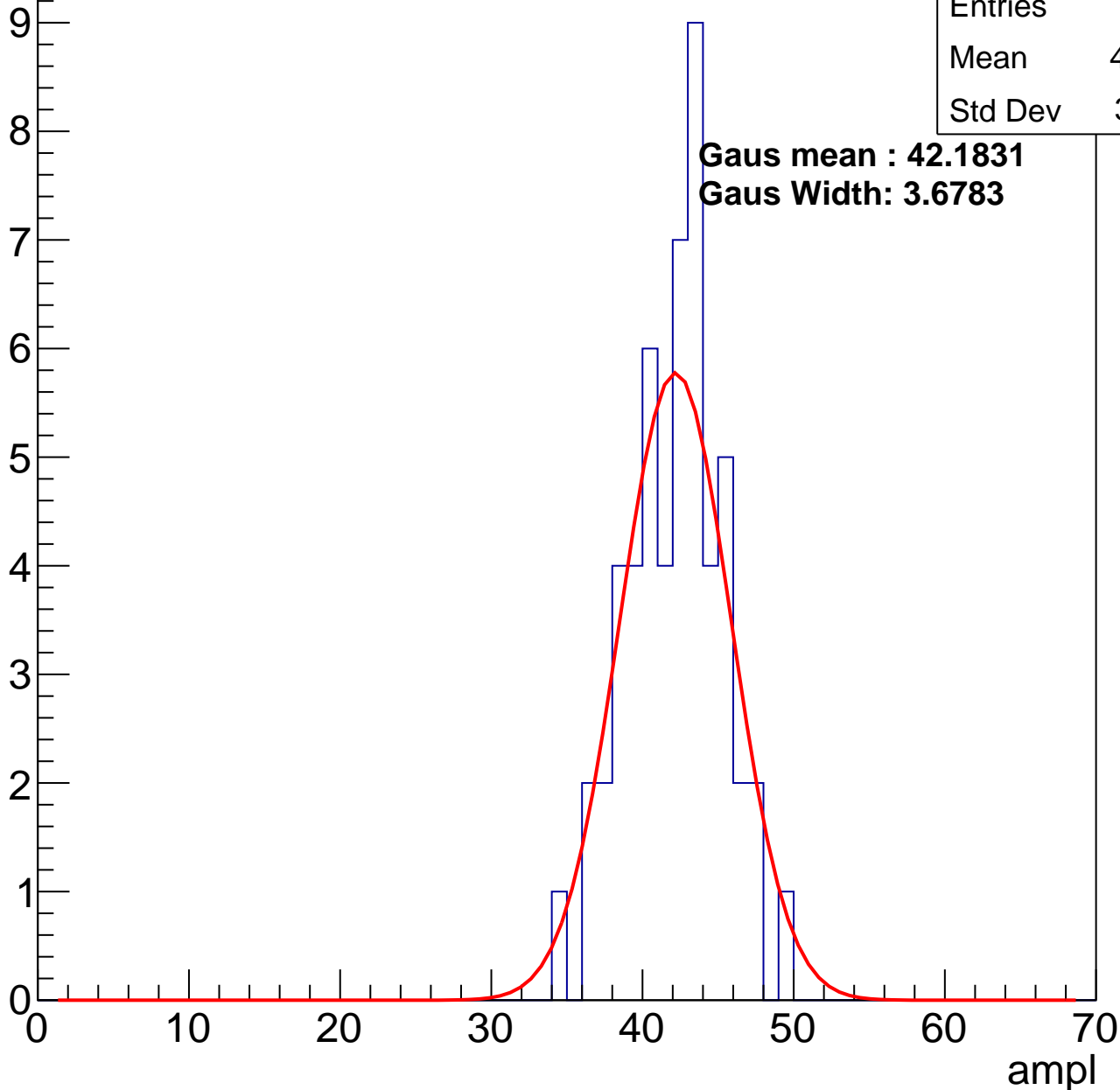
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	41.68
Std Dev	3.101

Gaus mean : 42.1831

Gaus Width: 3.6783

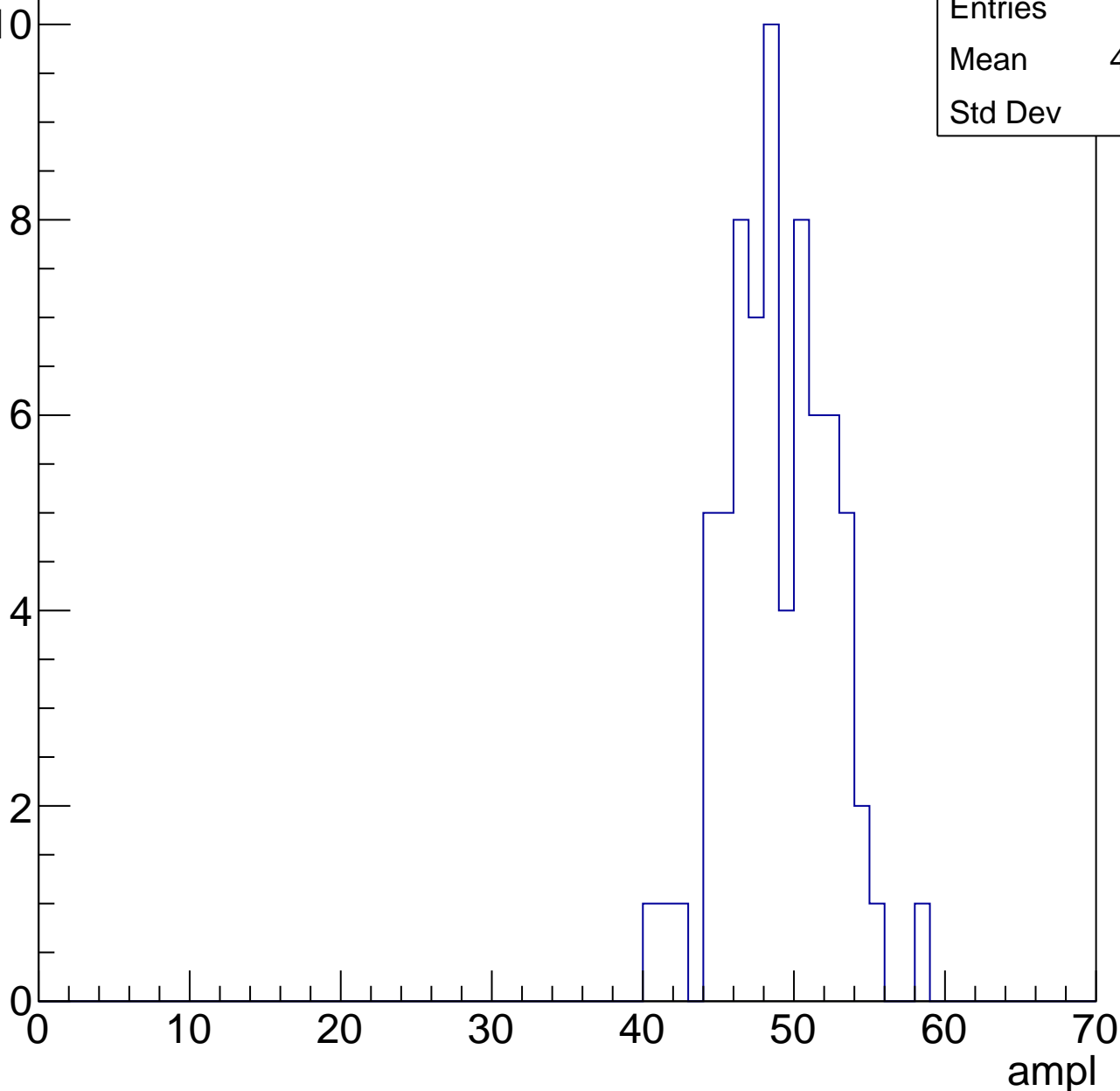


B1L102S, U6-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	48.52
Std Dev	3.41

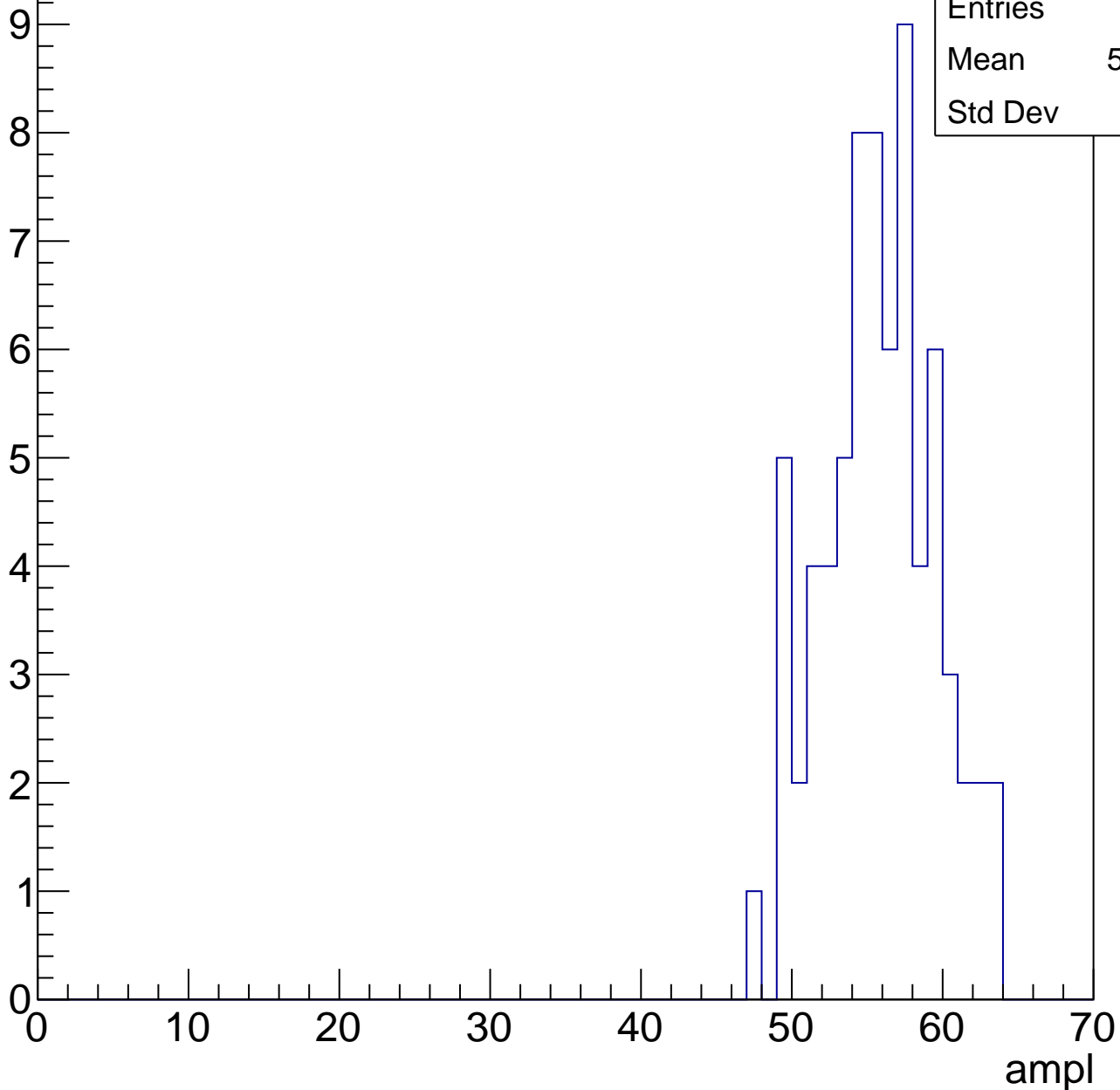


B1L102S, U6-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55.32
Std Dev	3.68

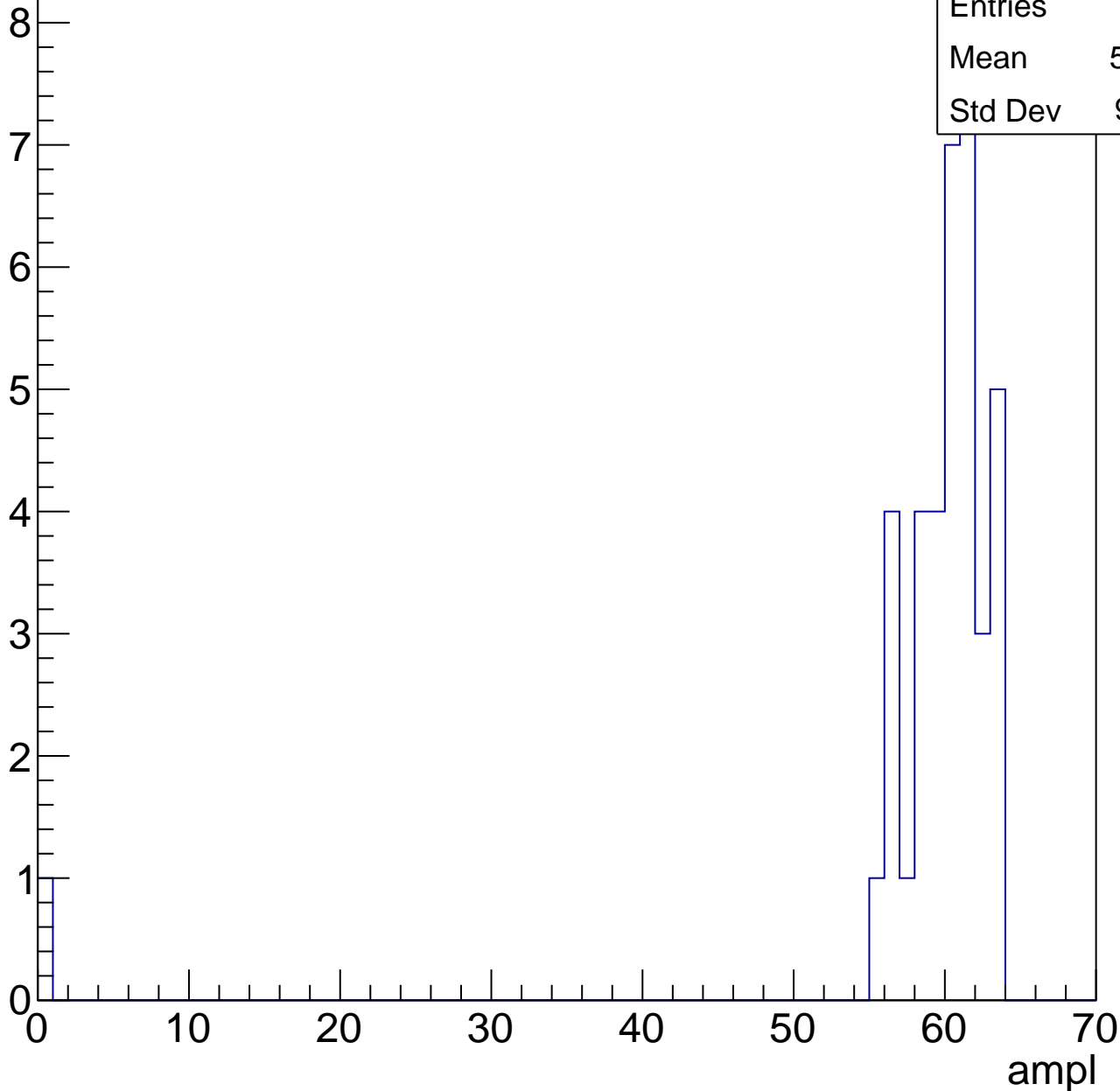


B1L102S, U6-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

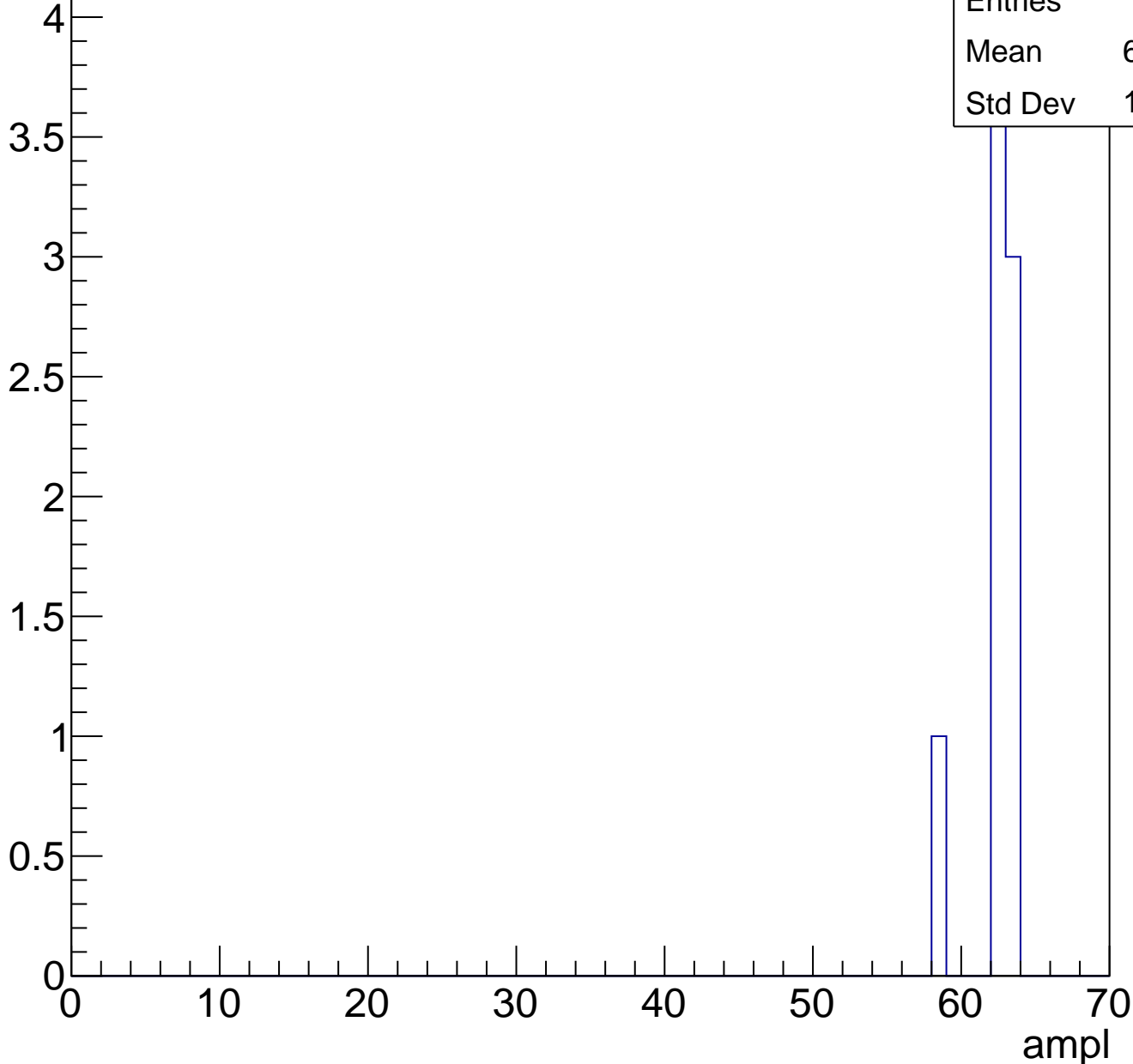
Entries	38
Mean	58.24
Std Dev	9.821



B1L102S, U6-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

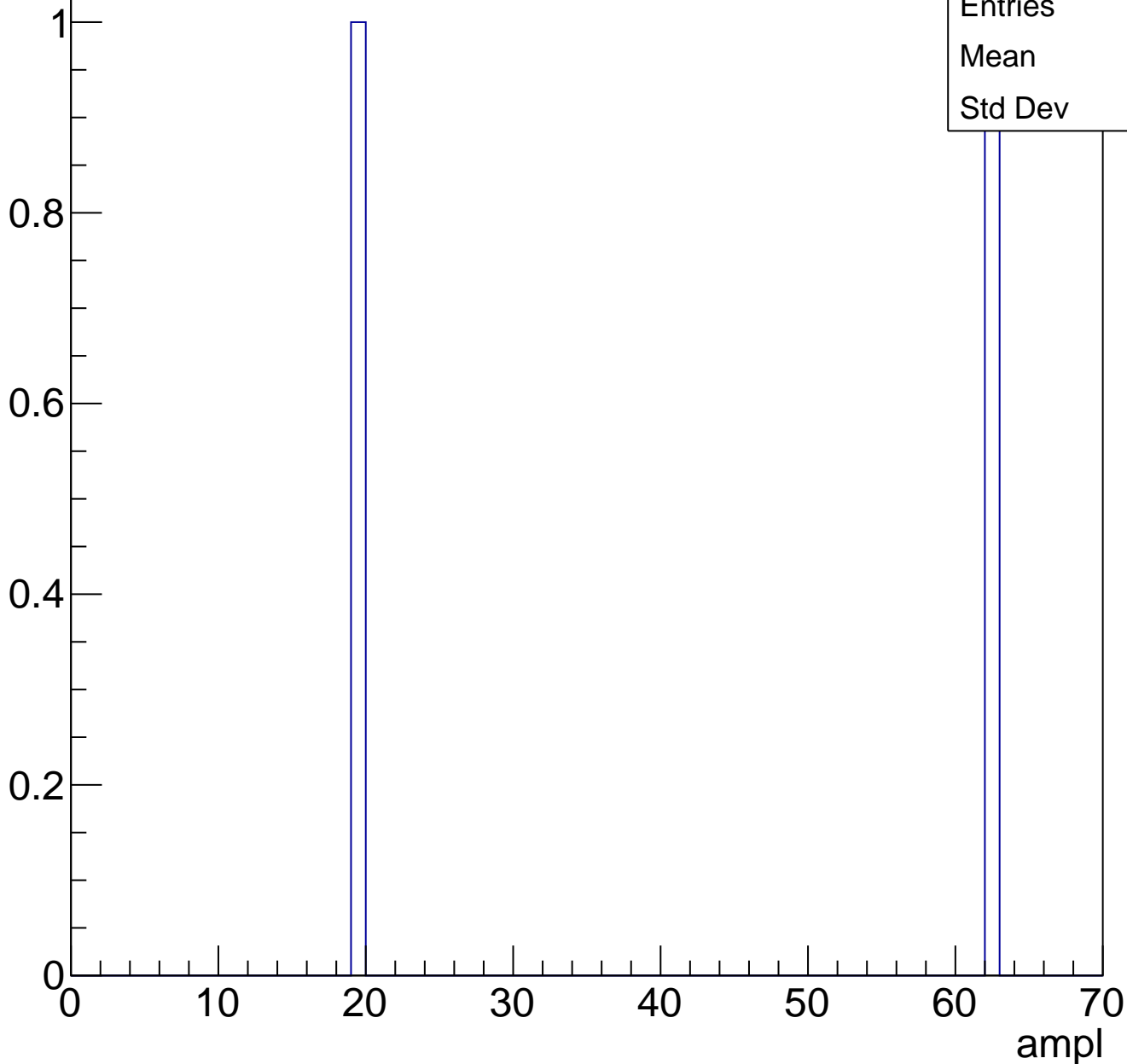
Entry



B1L102S, U6-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch81, adc0

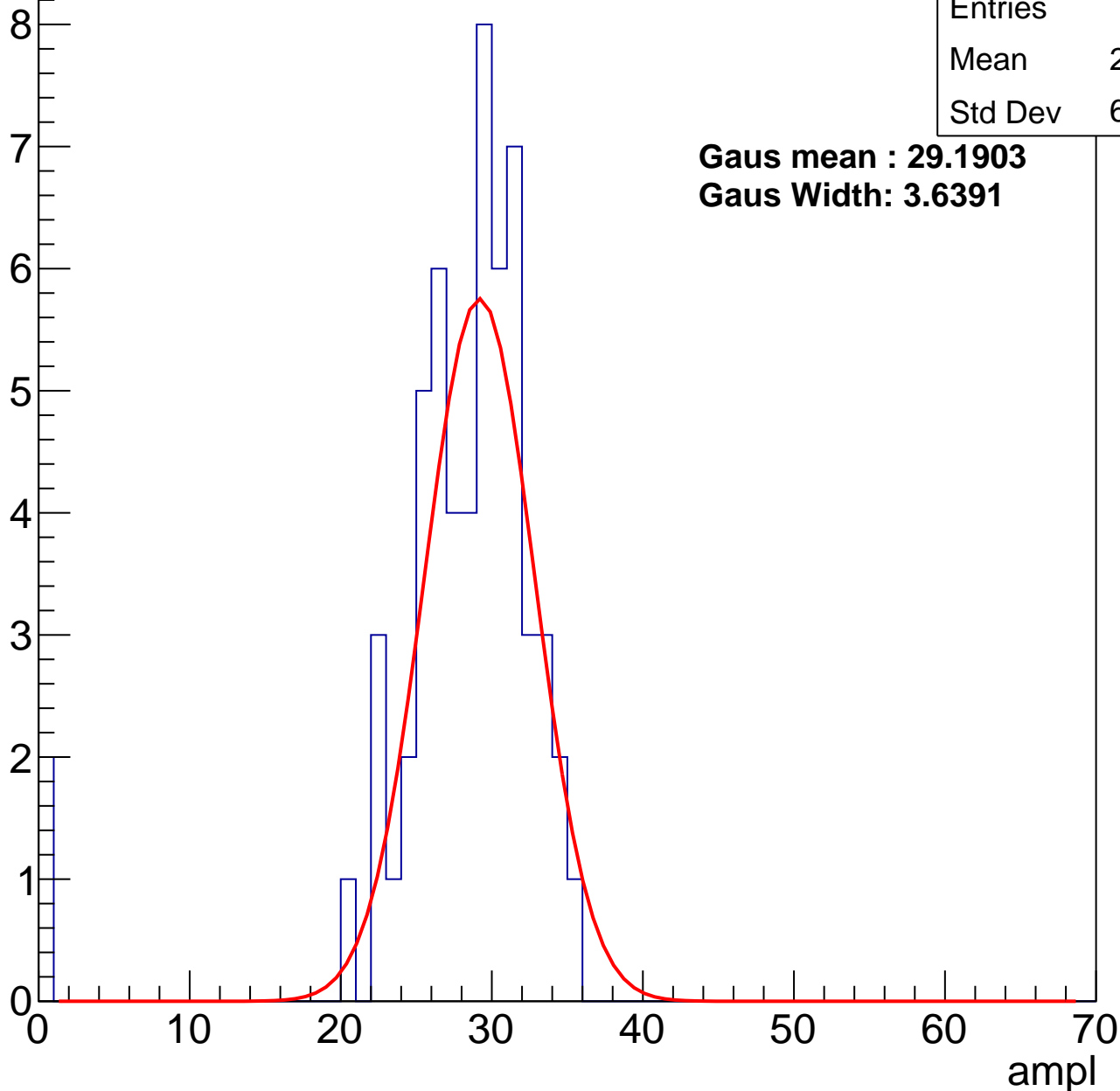
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.33
Std Dev	6.135

Gaus mean : 29.1903

Gaus Width: 3.6391



B1L102S, U6-ch81, adc1

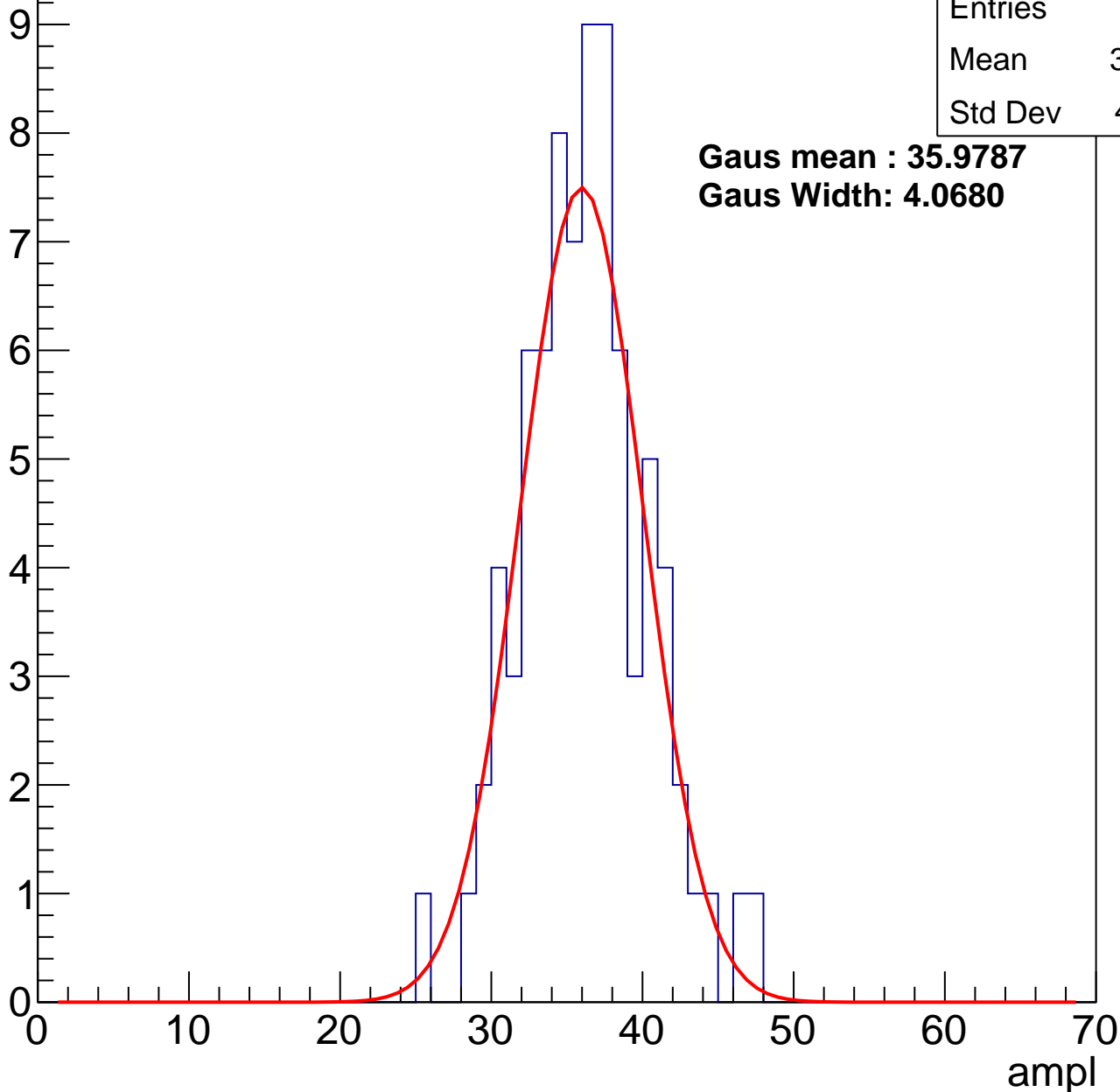
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.76
Std Dev	4.081

Gaus mean : 35.9787

Gaus Width: 4.0680



B1L102S, U6-ch81, adc2

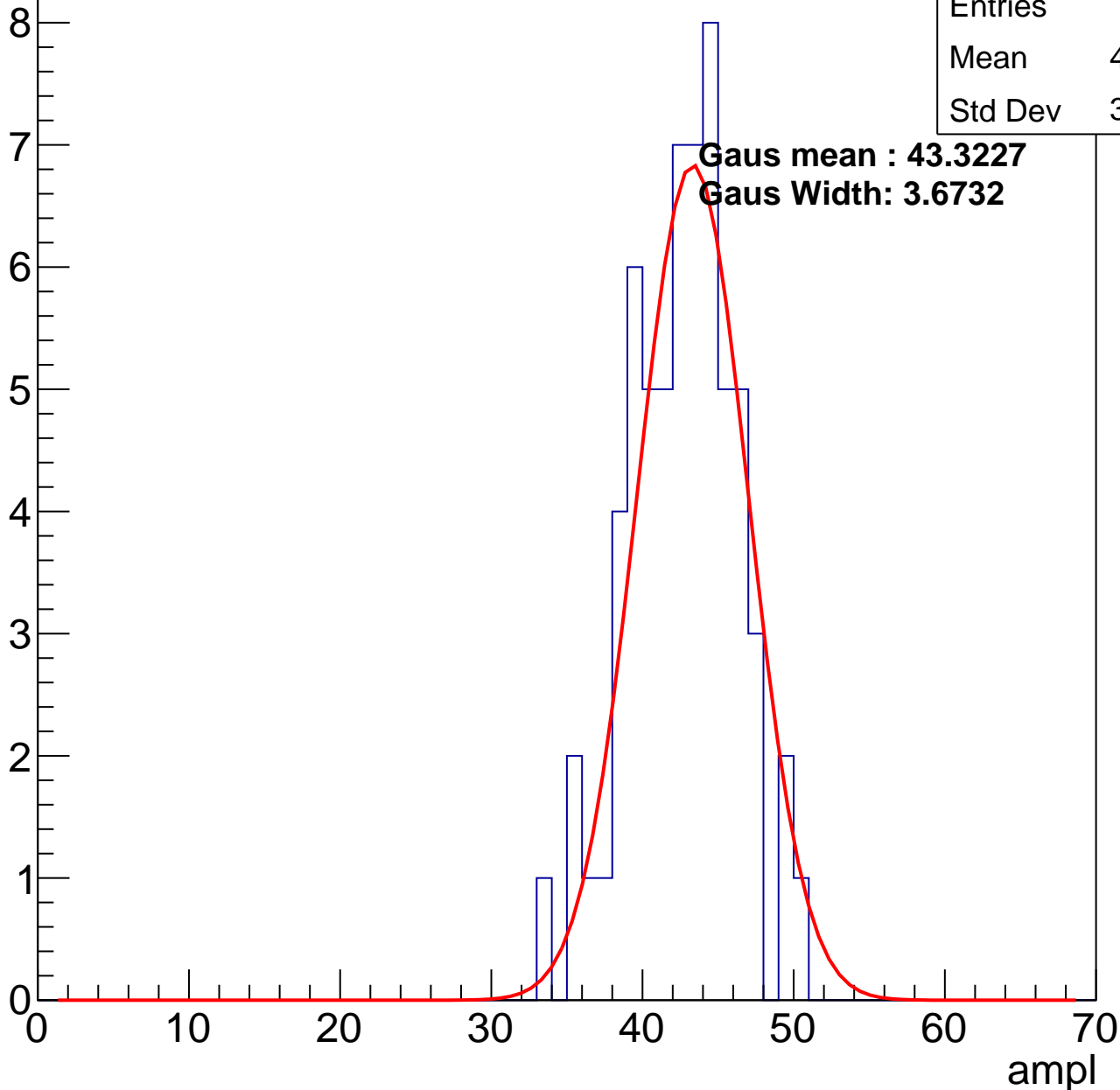
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.19
Std Dev	3.523

Gaus mean : 43.3227

Gaus Width: 3.6732

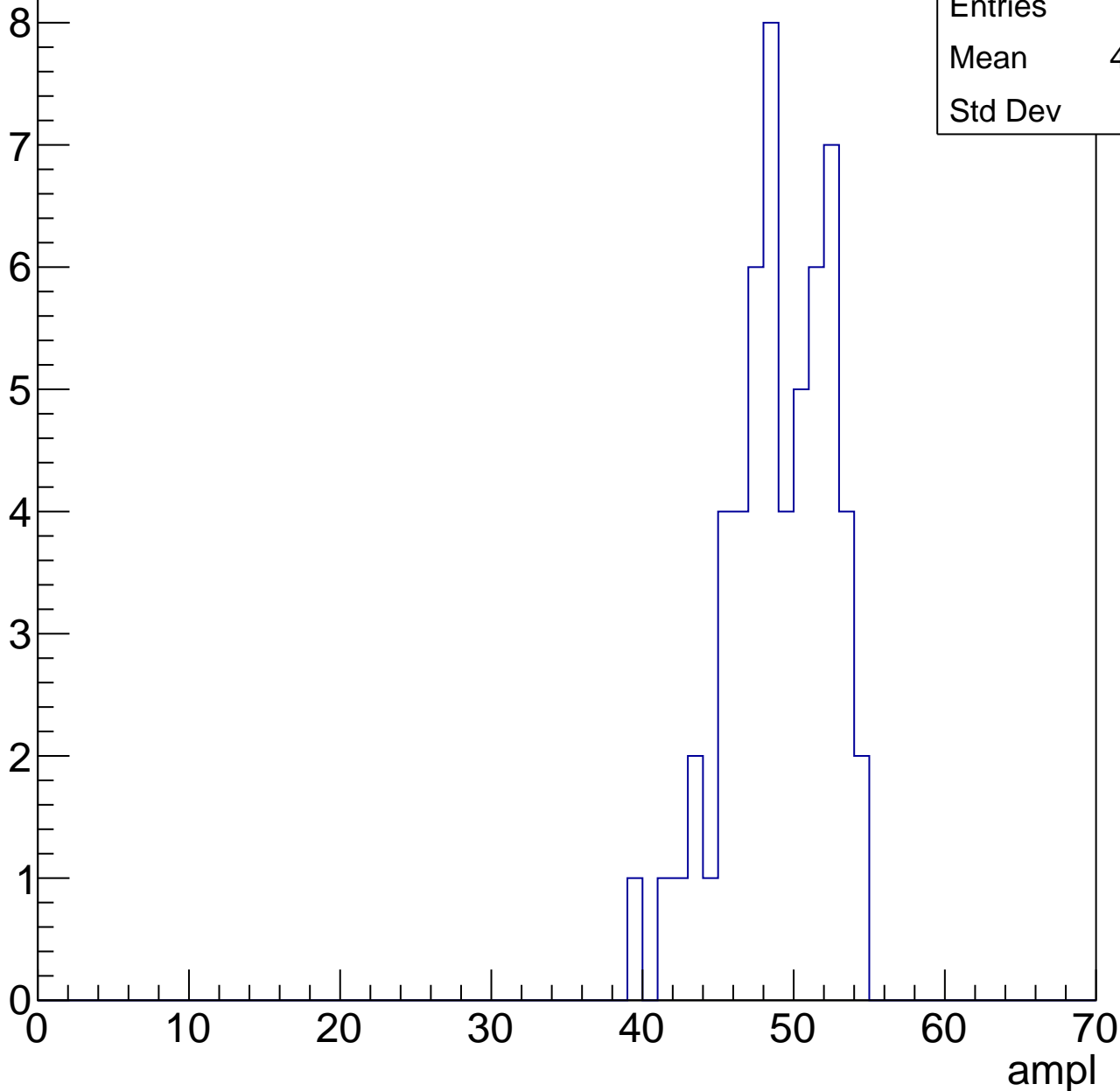


B1L102S, U6-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.54
Std Dev	3.37



B1L102S, U6-ch81, adc4

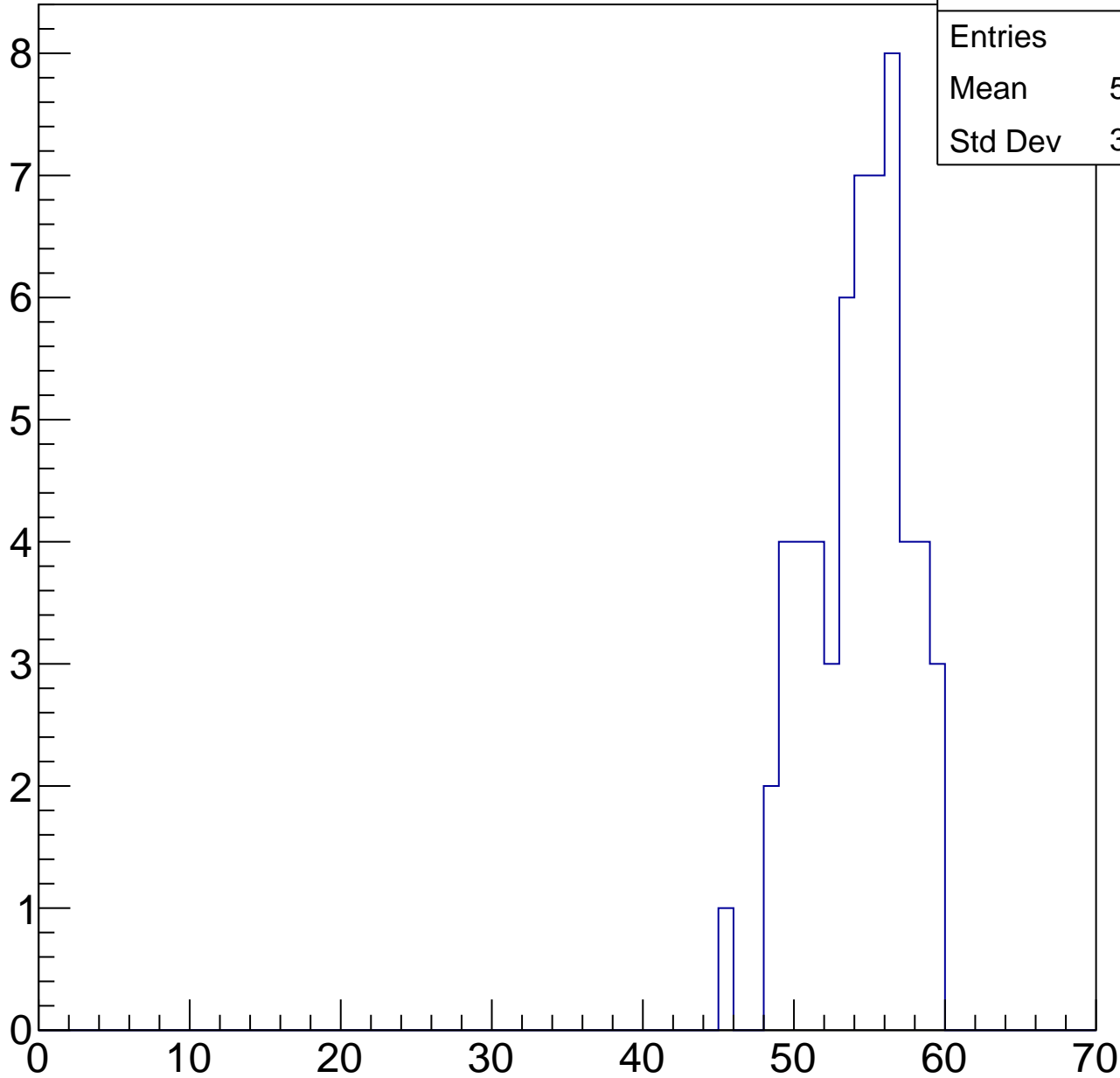
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	57
Mean	53.74
Std Dev	3.193

ampl

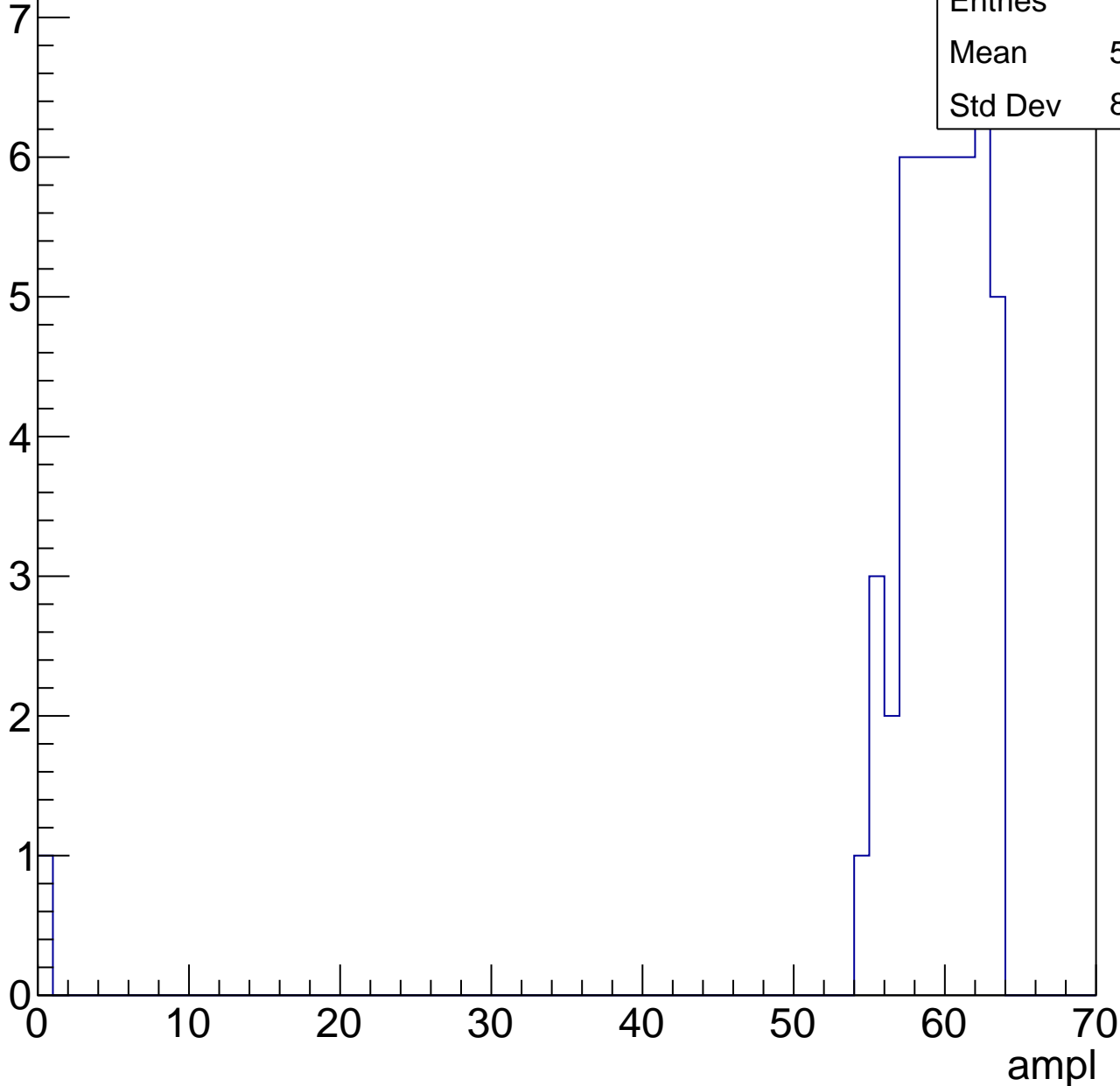


B1L102S, U6-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.16
Std Dev	8.737

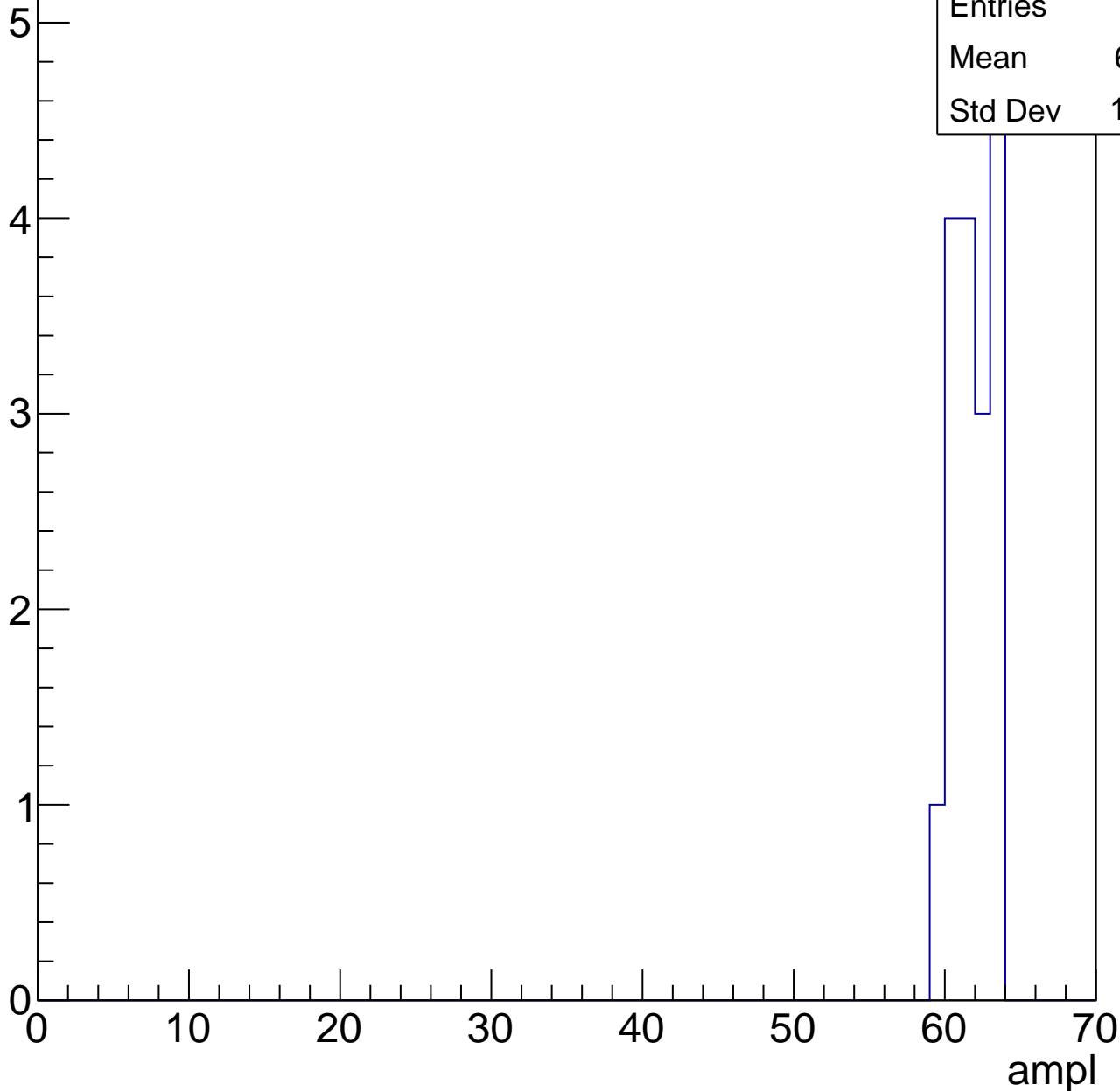


B1L102S, U6-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61.41
Std Dev	1.286



B1L102S, U6-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch82, adc0

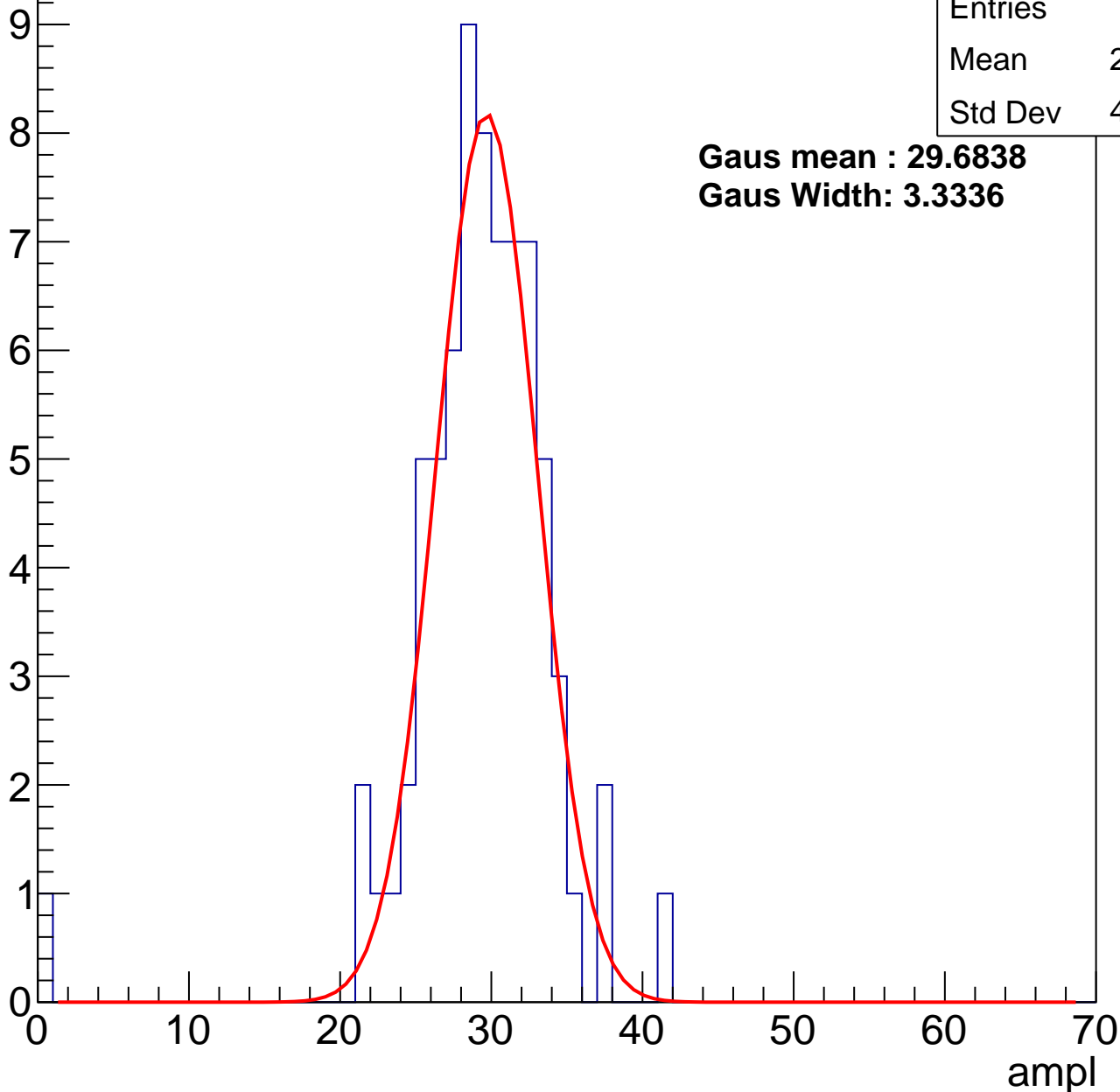
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.82
Std Dev	4.994

Gaus mean : 29.6838

Gaus Width: 3.3336



B1L102S, U6-ch82, adc1

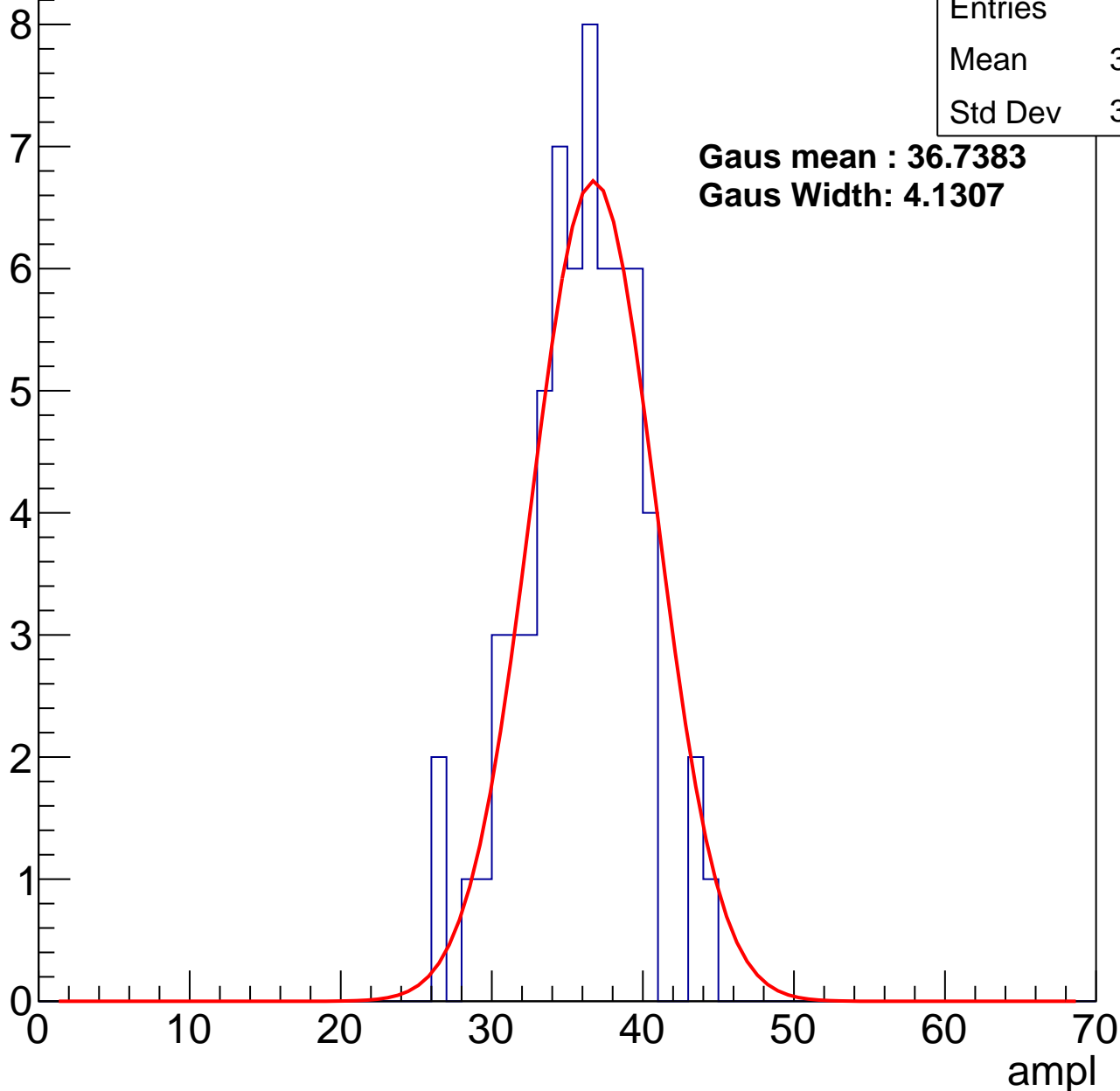
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.36
Std Dev	3.764

Gaus mean : 36.7383

Gaus Width: 4.1307



B1L102S, U6-ch82, adc2

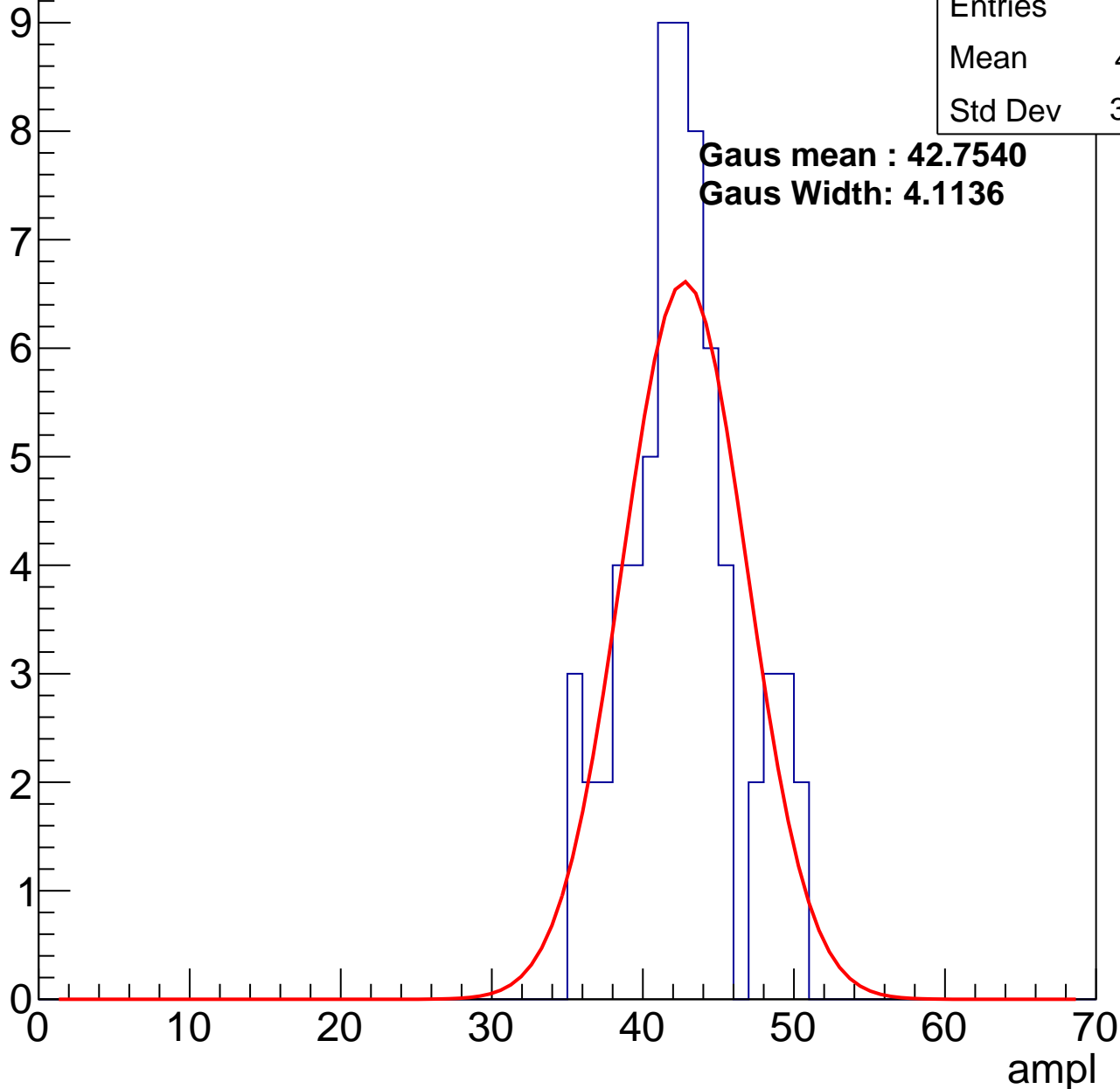
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.11
Std Dev	3.689

Gaus mean : 42.7540

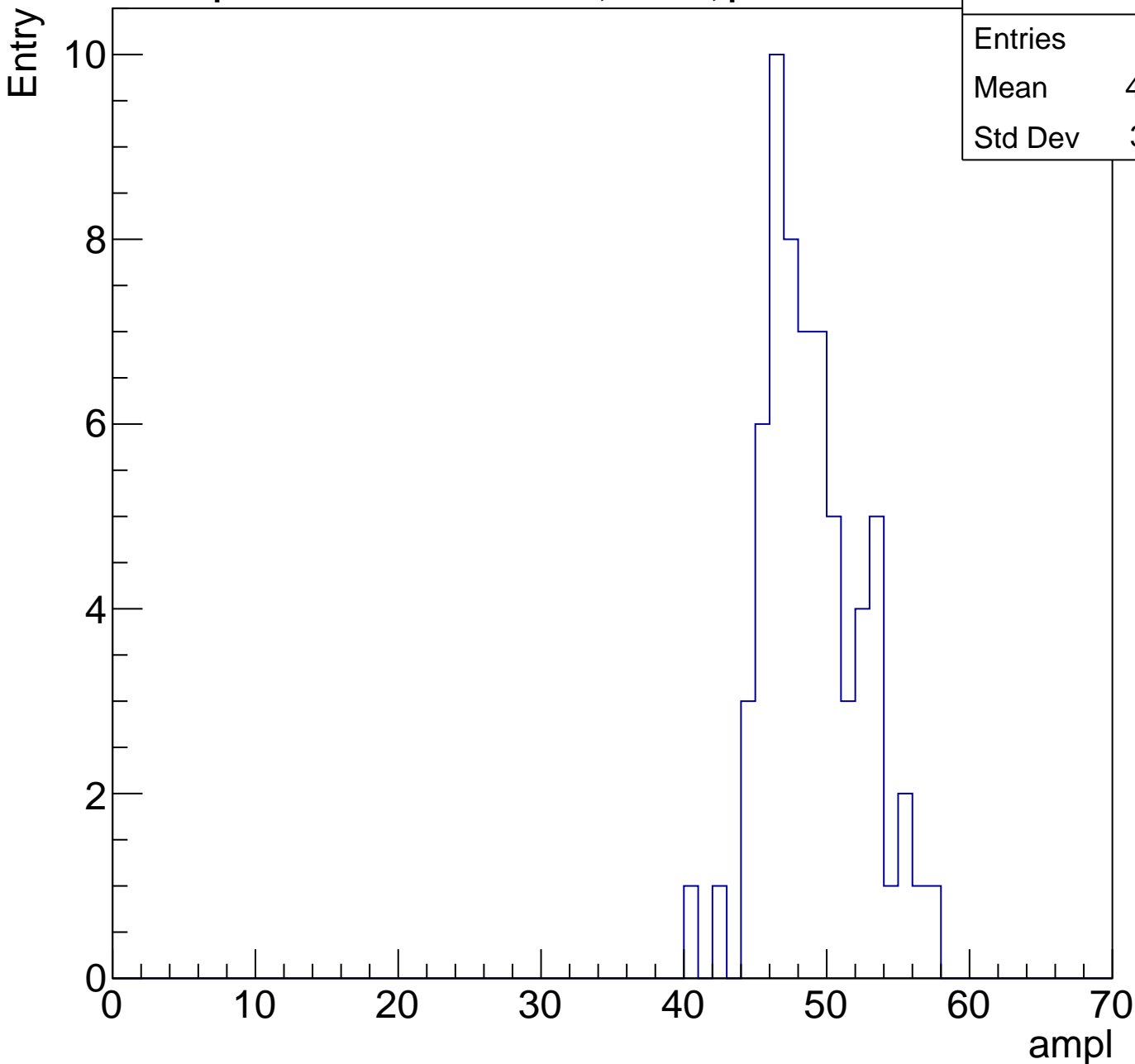
Gaus Width: 4.1136



B1L102S, U6-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	48.49
Std Dev	3.411

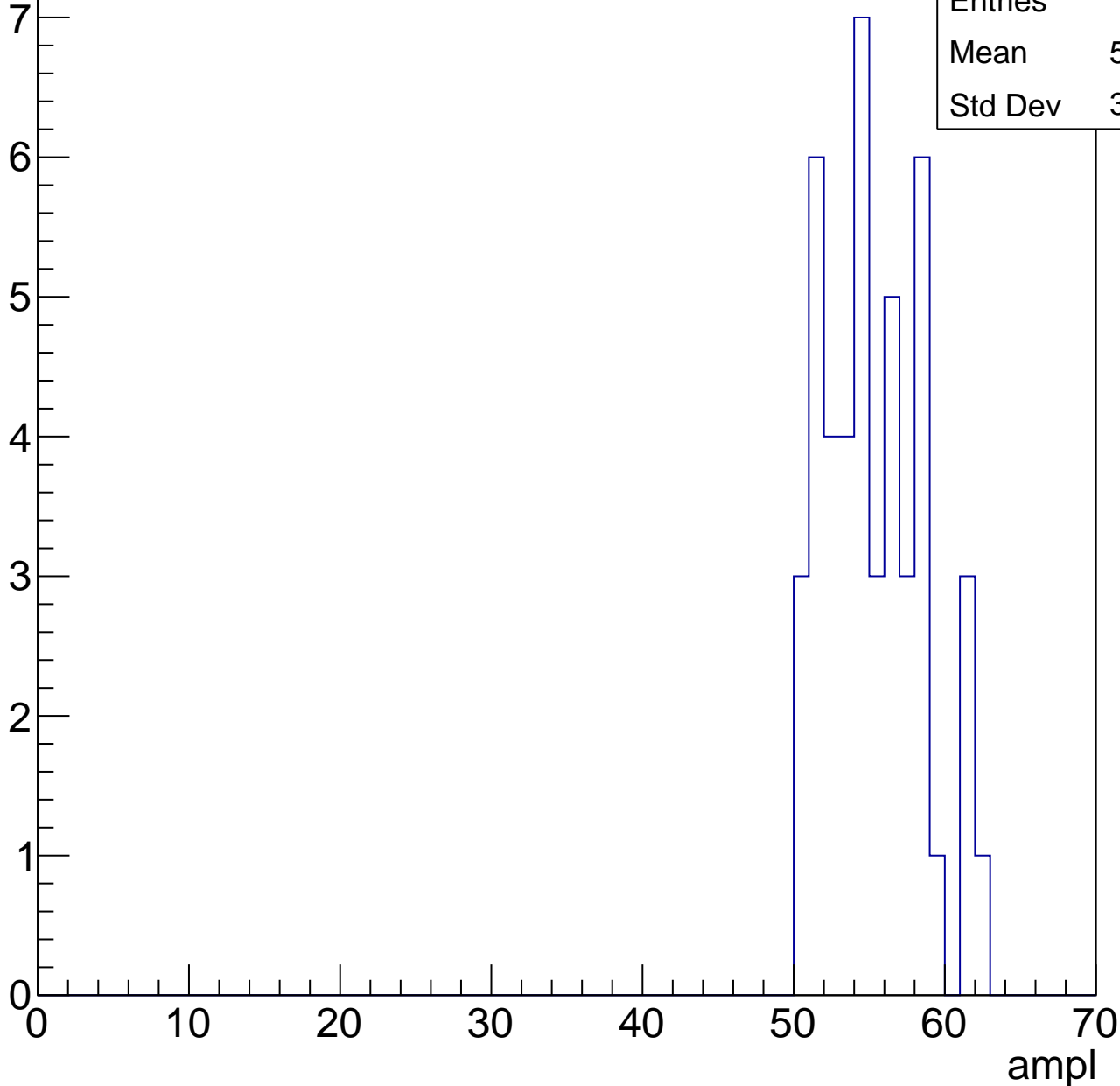


B1L102S, U6-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

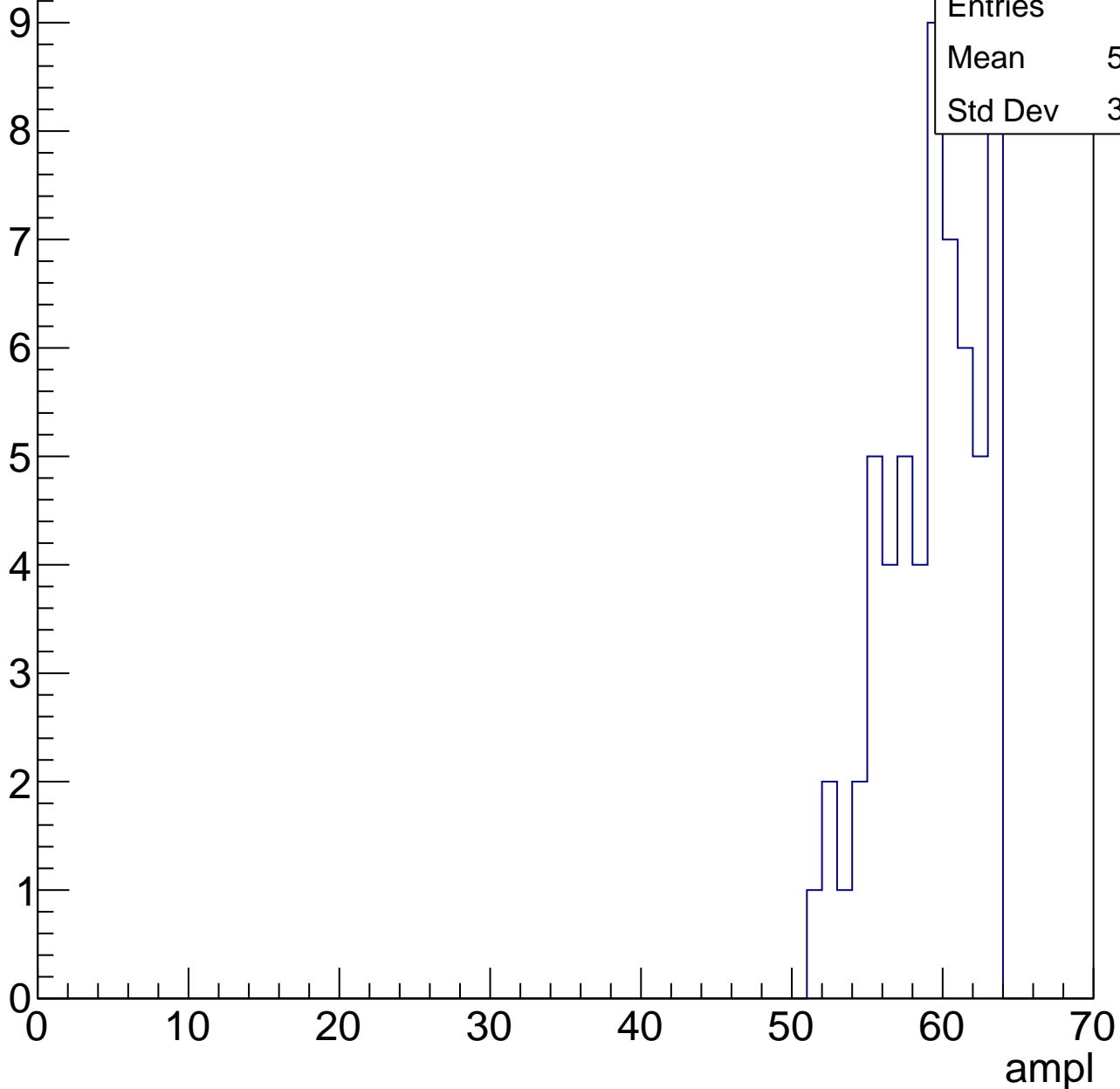
Entries	46
Mean	54.83
Std Dev	3.205



B1L102S, U6-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

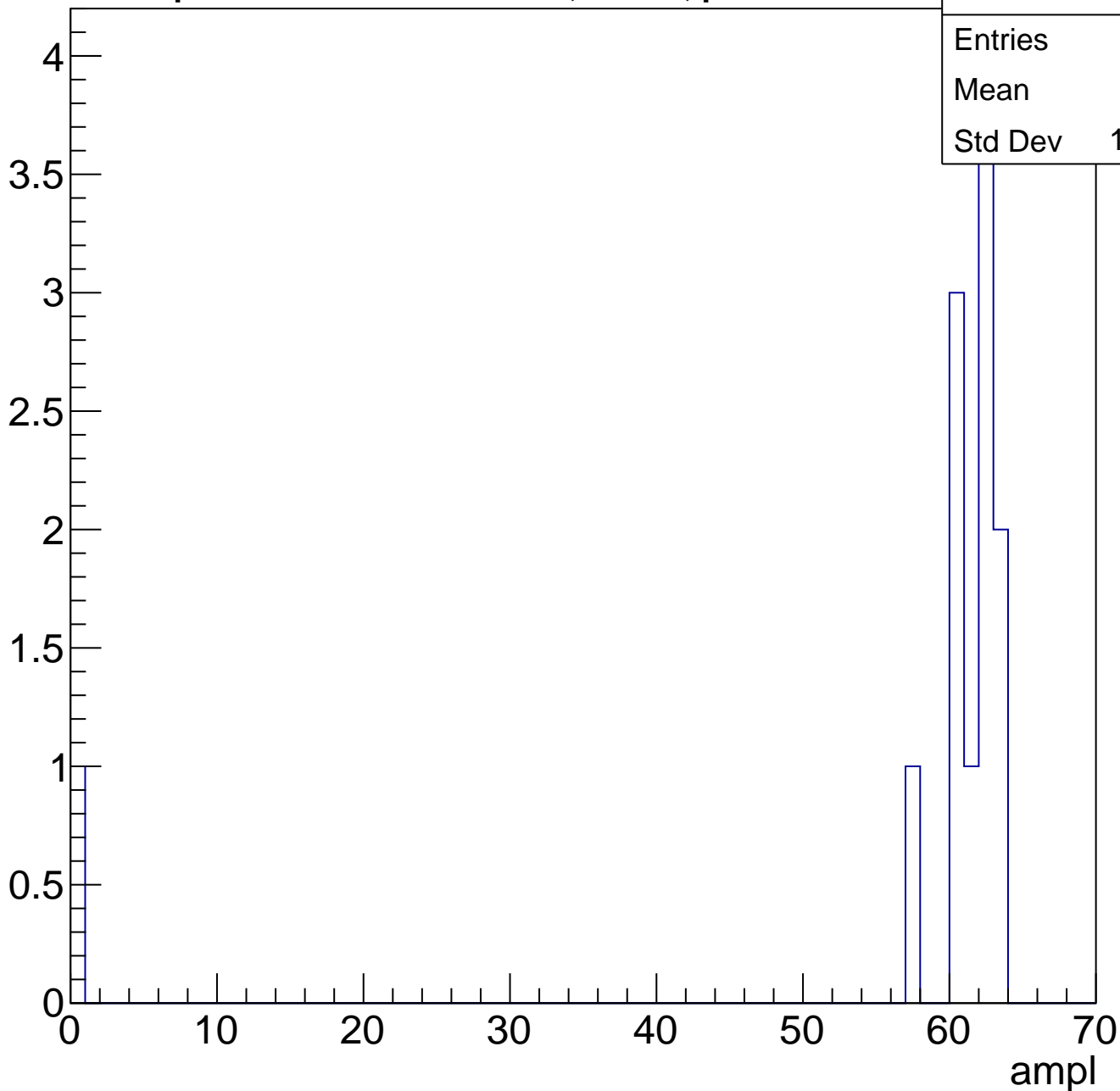
Entry



B1L102S, U6-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch83, adc0

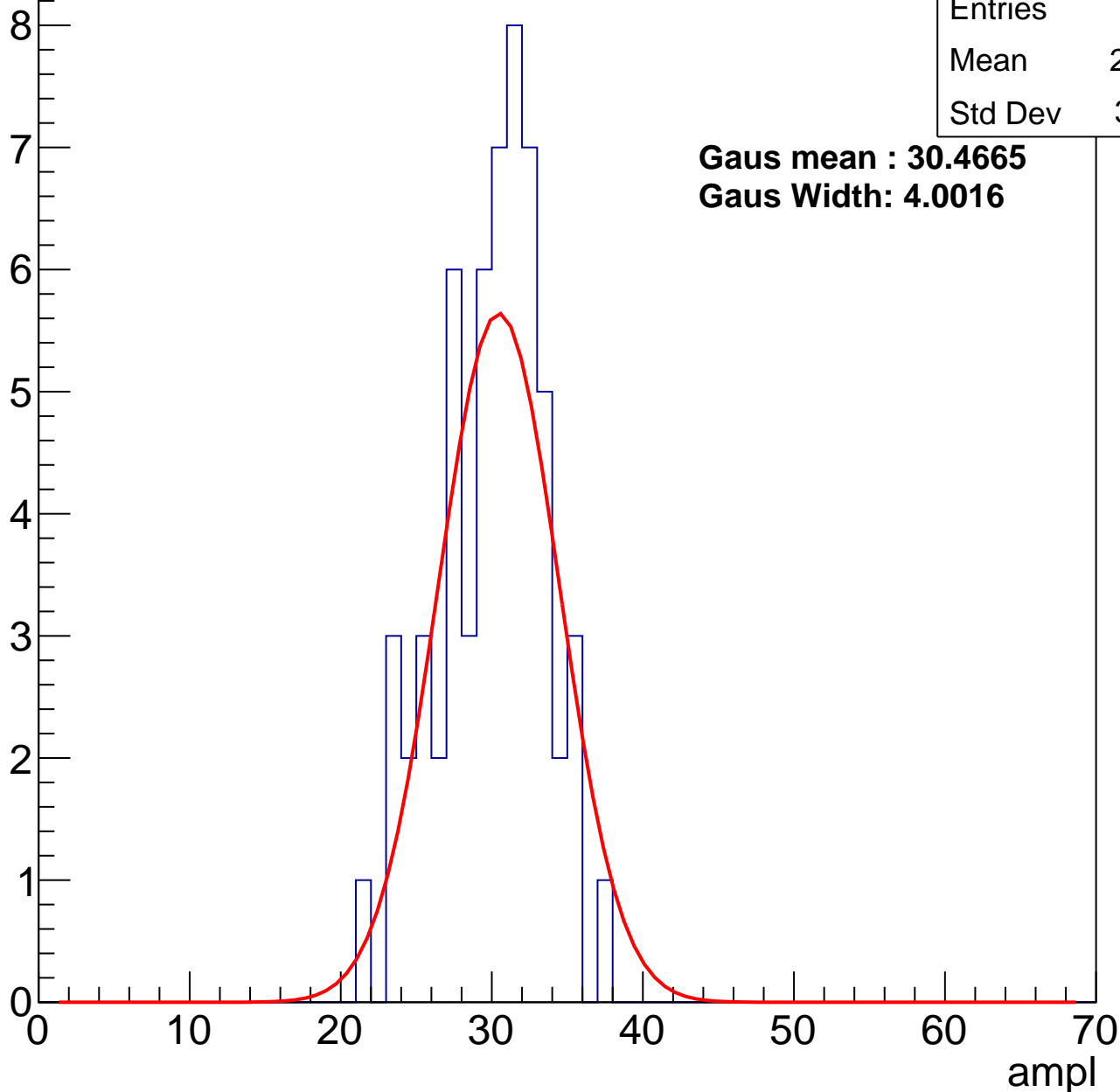
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	29.53
Std Dev	3.451

Gaus mean : 30.4665

Gaus Width: 4.0016



B1L102S, U6-ch83, adc1

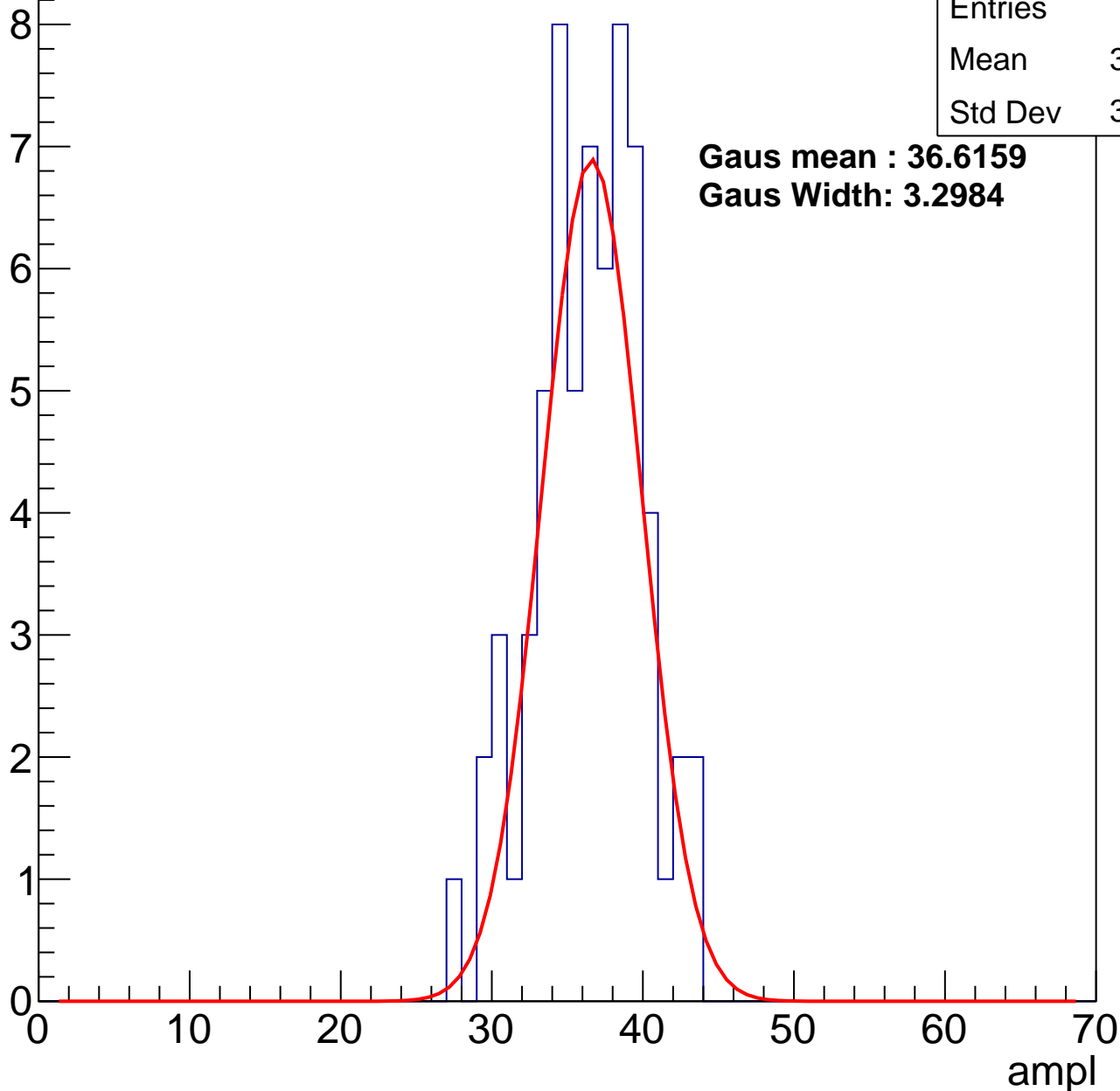
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.94
Std Dev	3.512

Gaus mean : 36.6159

Gaus Width: 3.2984



B1L102S, U6-ch83, adc2

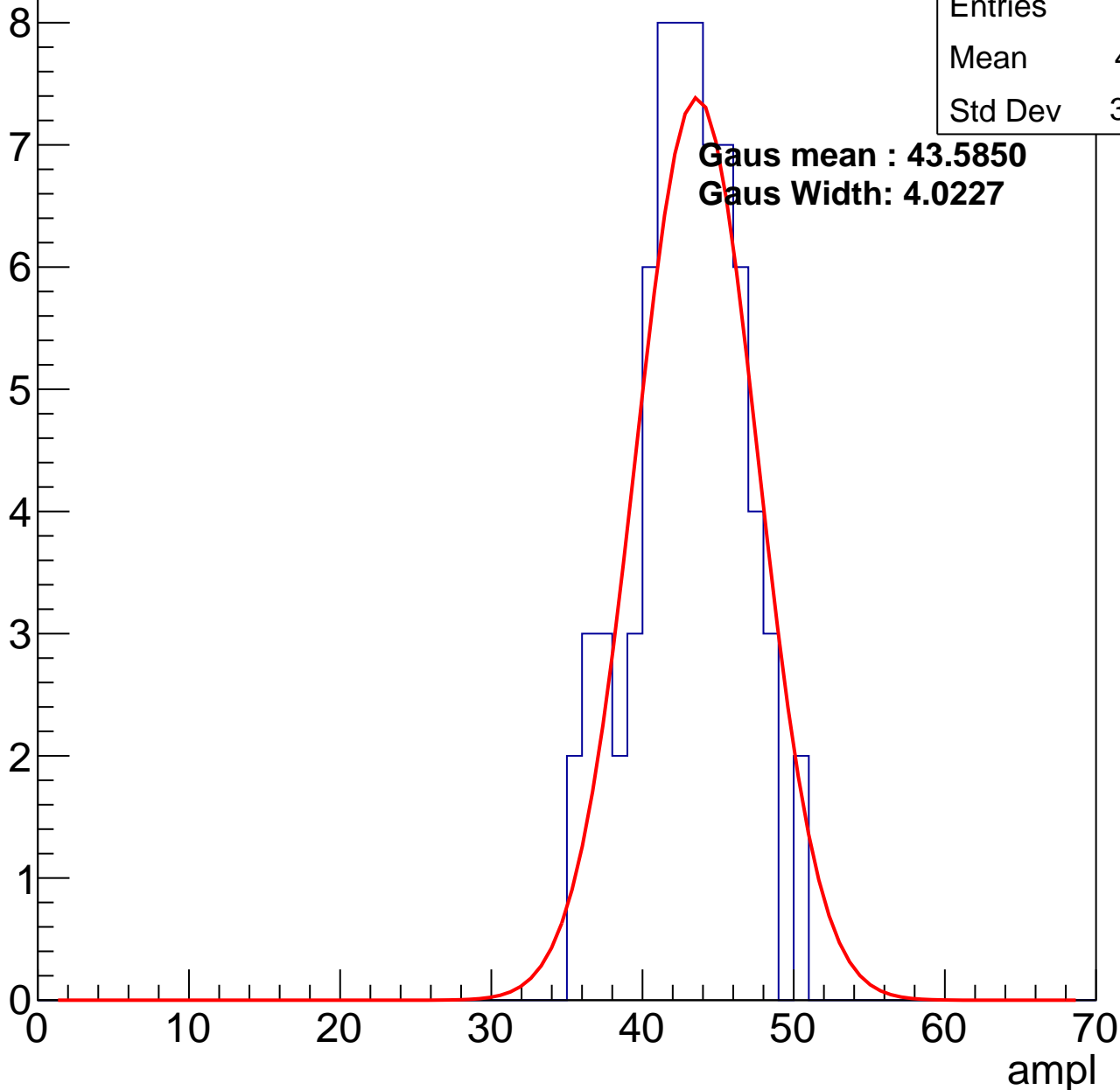
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.51
Std Dev	3.504

Gaus mean : 43.5850

Gaus Width: 4.0227

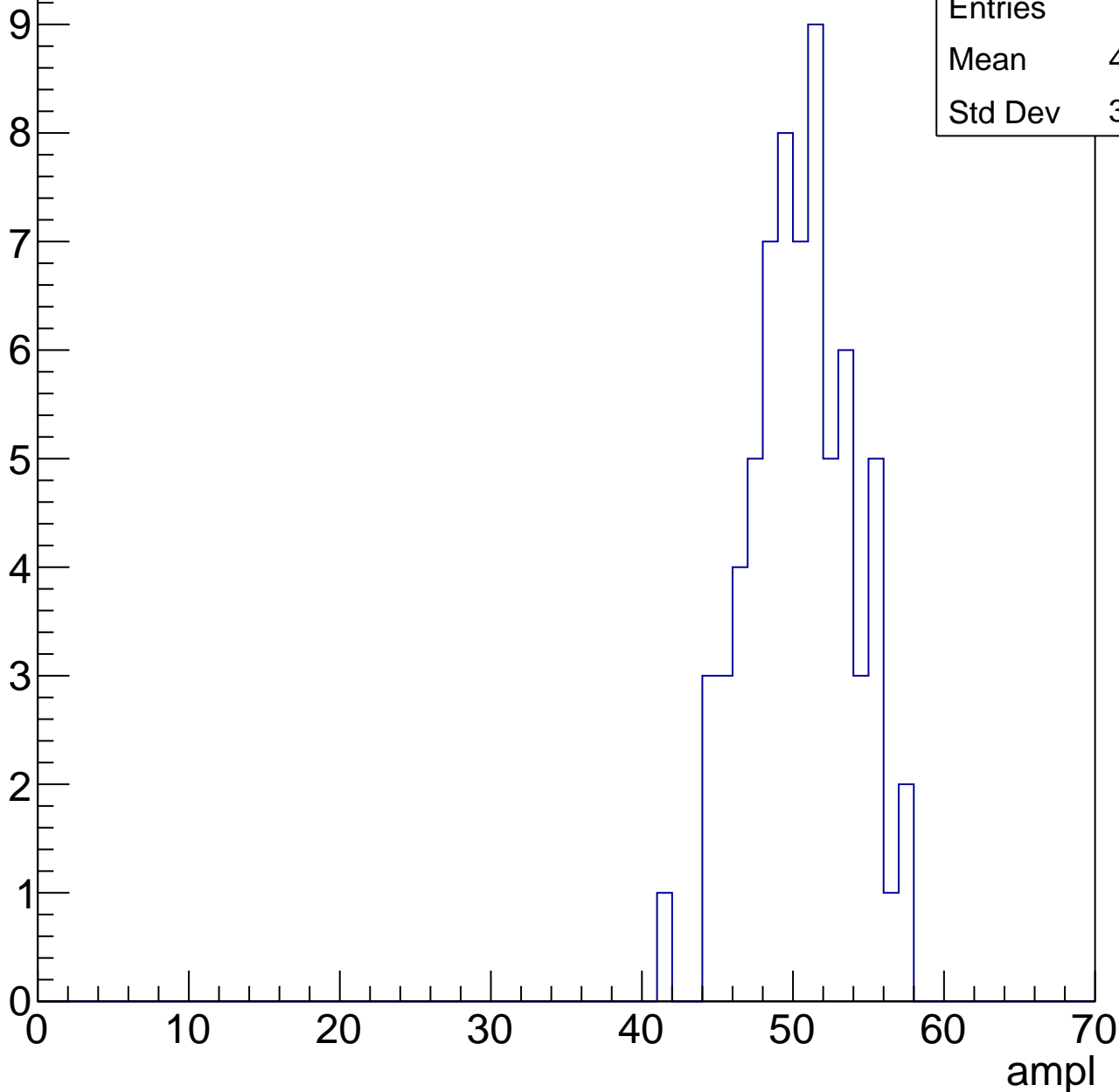


B1L102S, U6-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	49.99
Std Dev	3.403

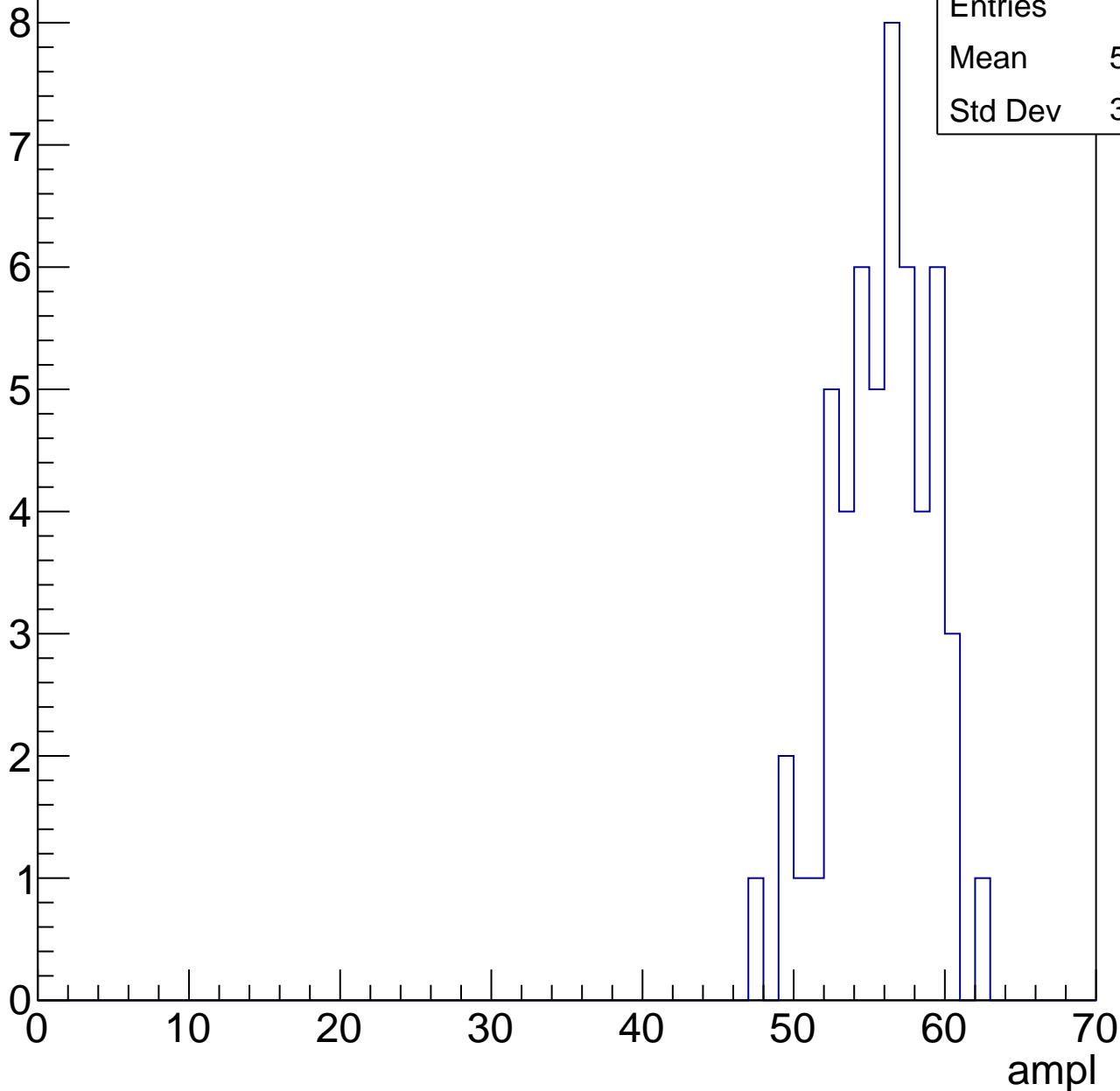


B1L102S, U6-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.38
Std Dev	3.146

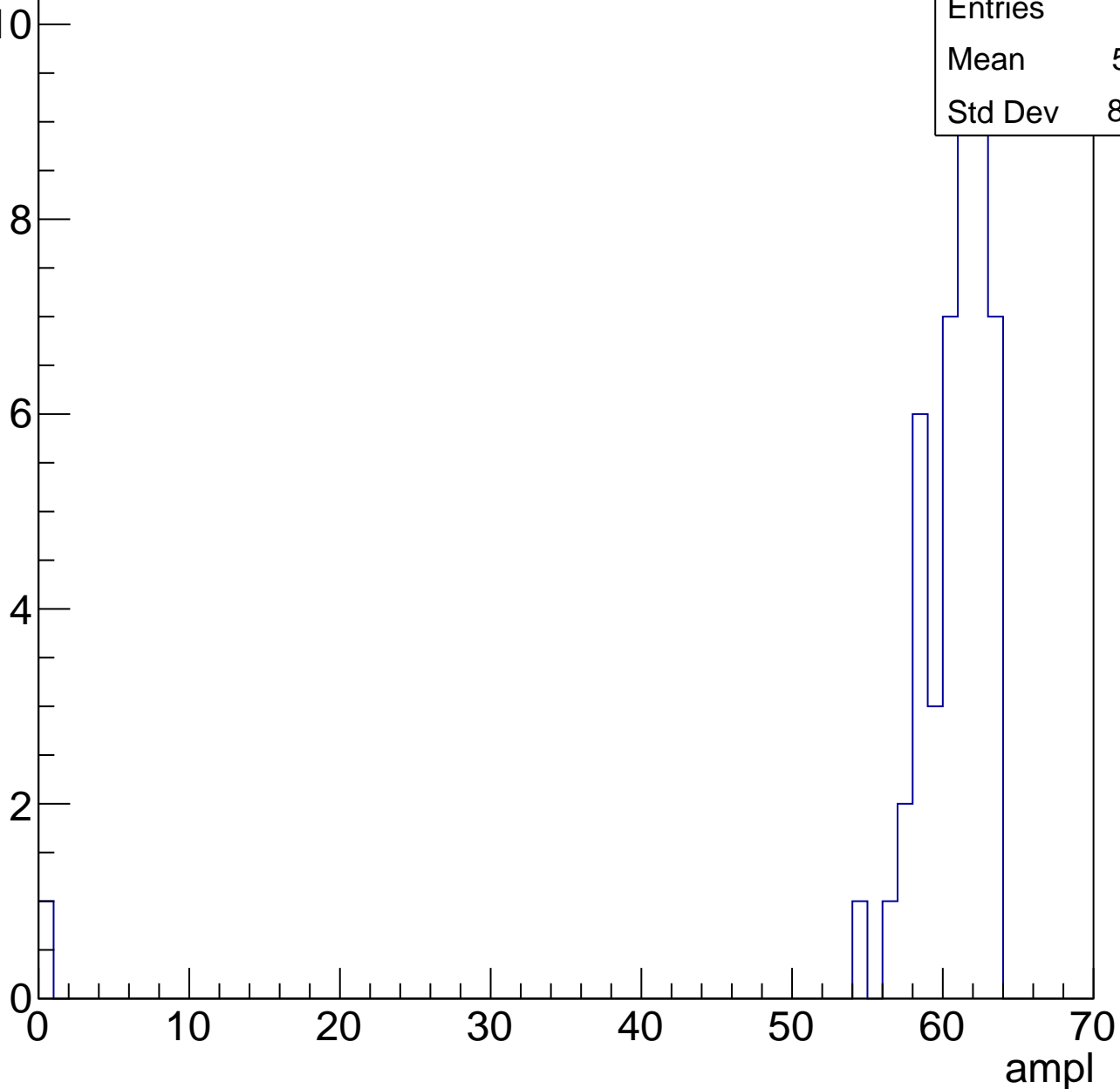


B1L102S, U6-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

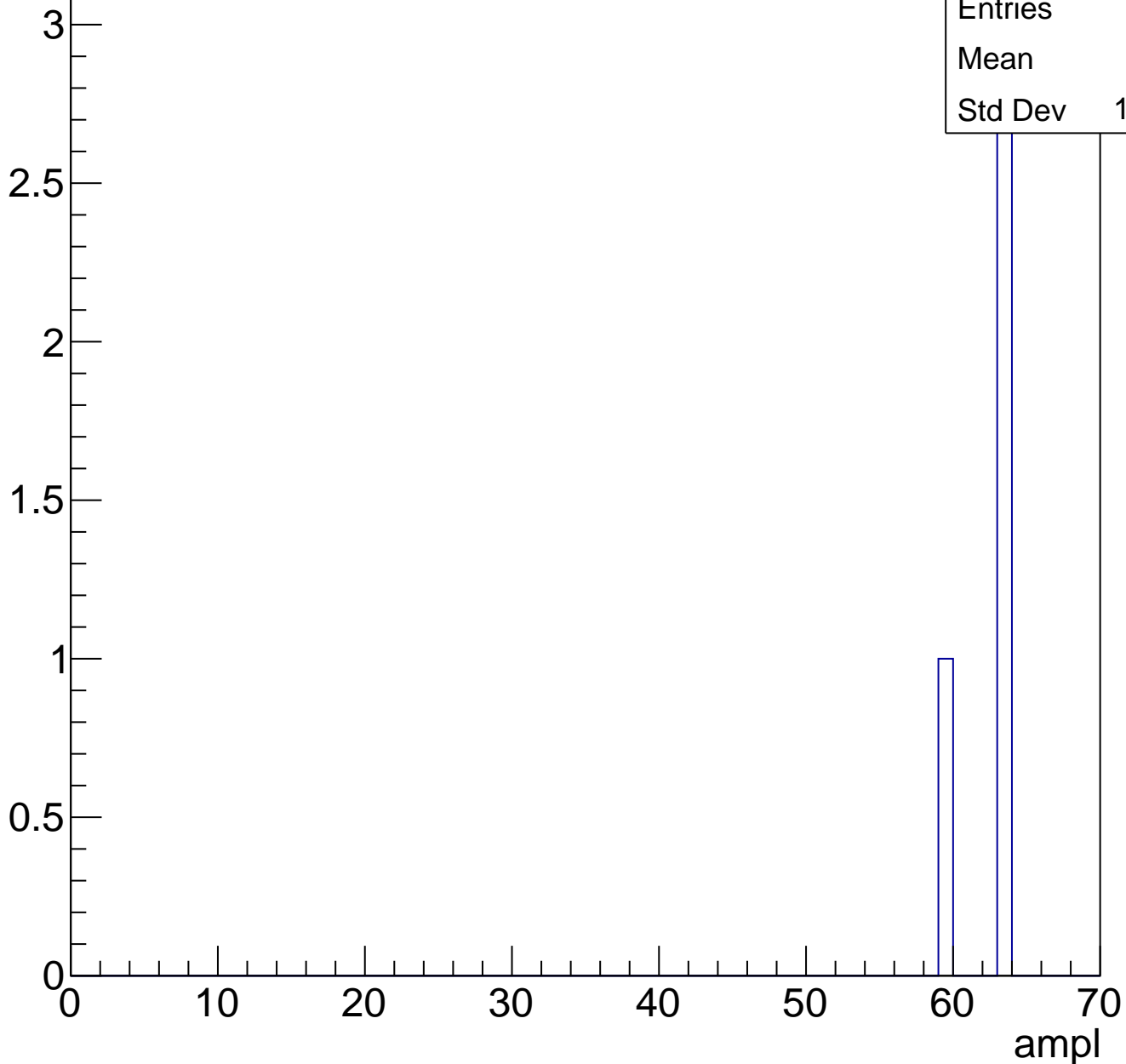
Entries	47
Mean	59.11
Std Dev	8.954



B1L102S, U6-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch84, adc0

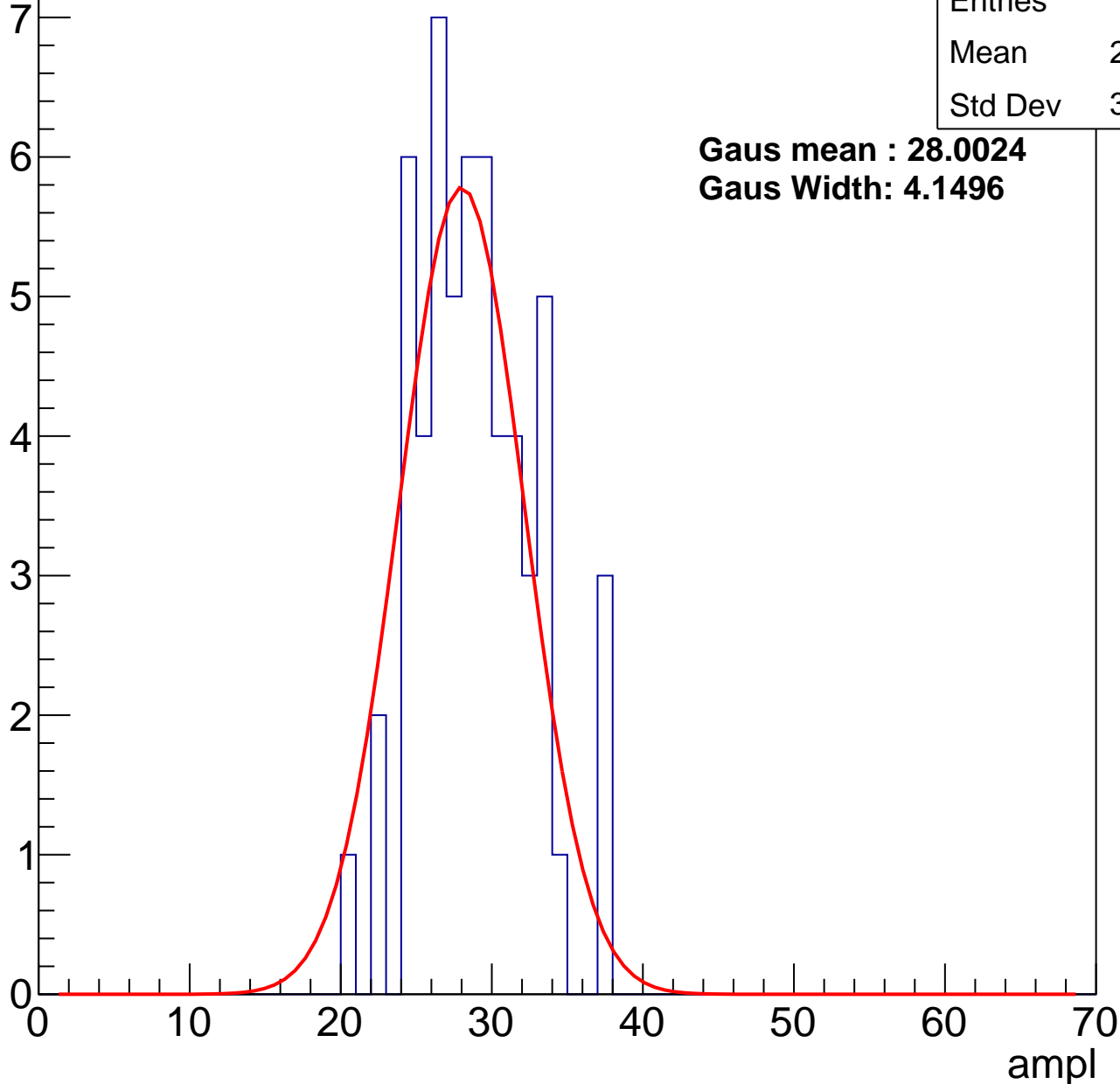
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	28.37
Std Dev	3.754

Gaus mean : 28.0024

Gaus Width: 4.1496



B1L102S, U6-ch84, adc1

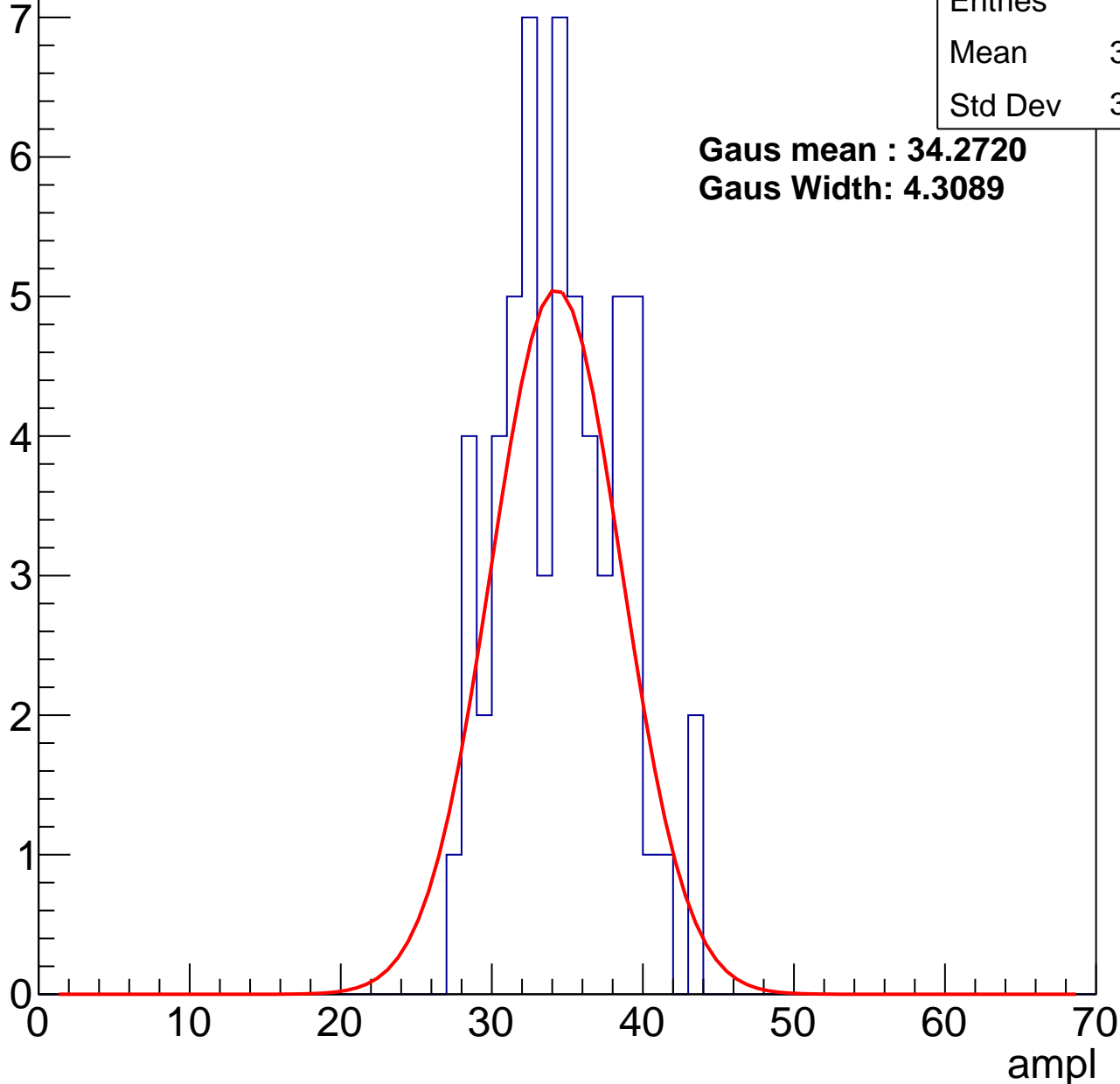
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	34.15
Std Dev	3.866

Gaus mean : 34.2720

Gaus Width: 4.3089



B1L102S, U6-ch84, adc2

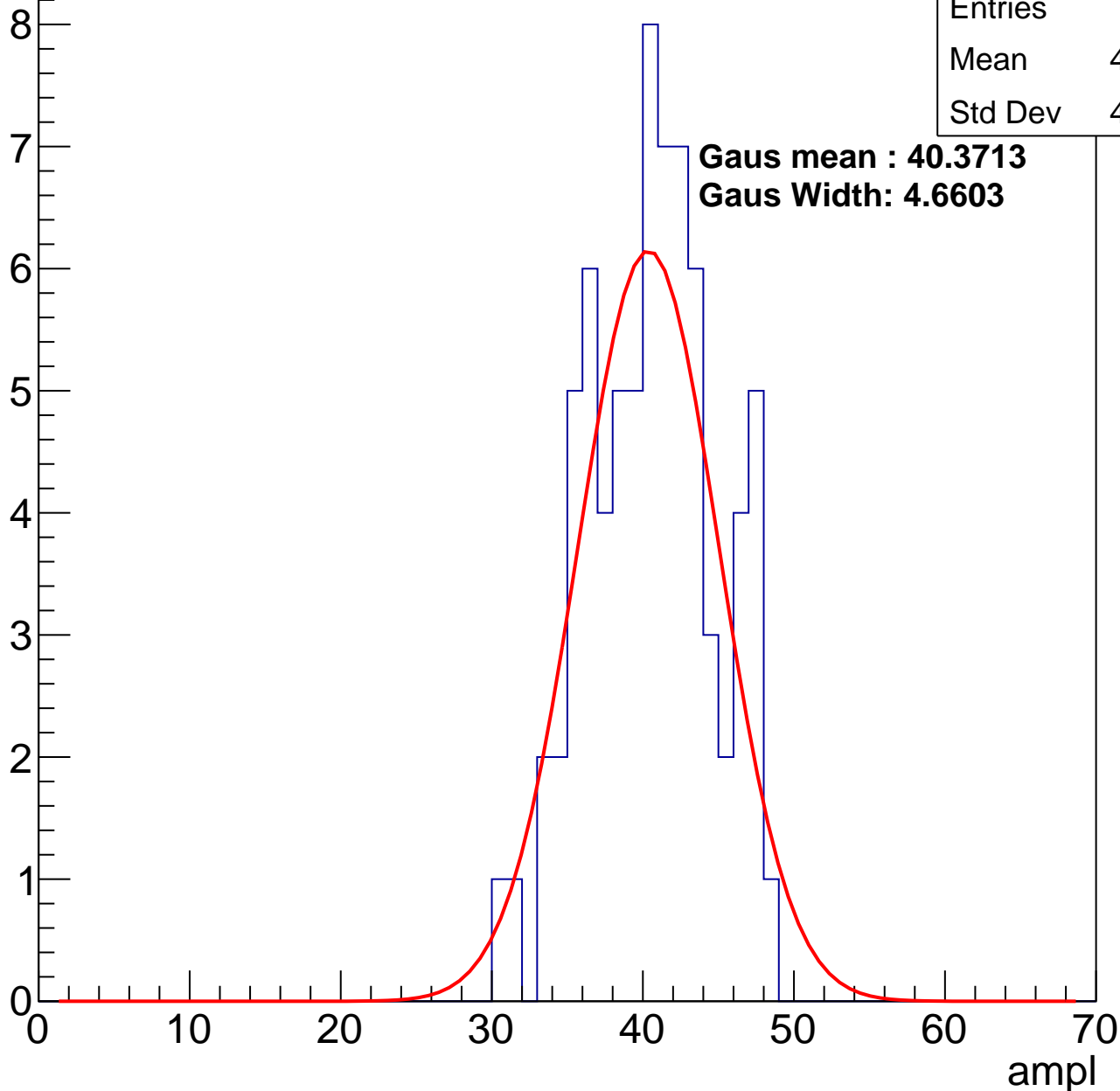
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	40.09
Std Dev	4.147

Gaus mean : 40.3713

Gaus Width: 4.6603

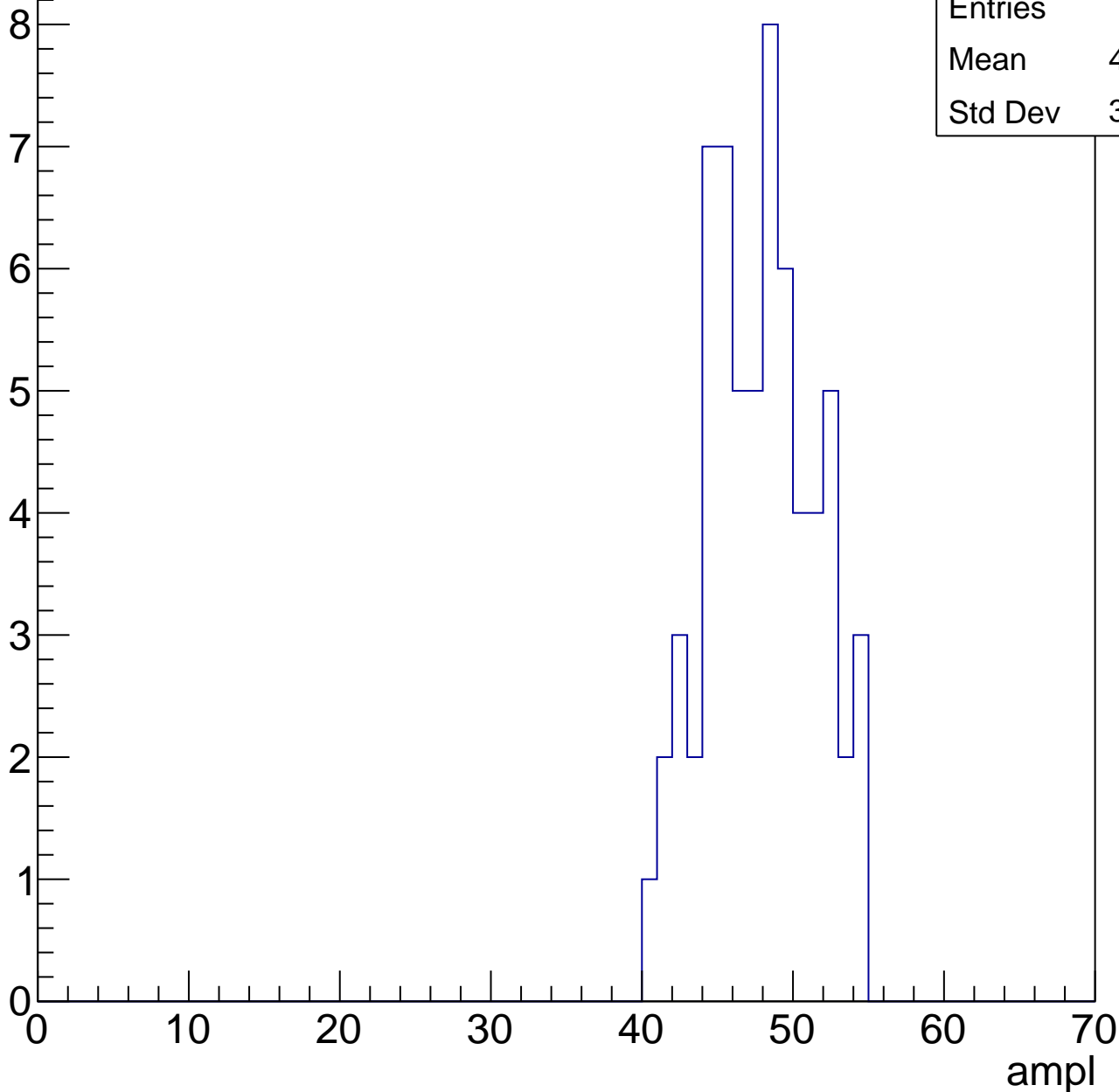


B1L102S, U6-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

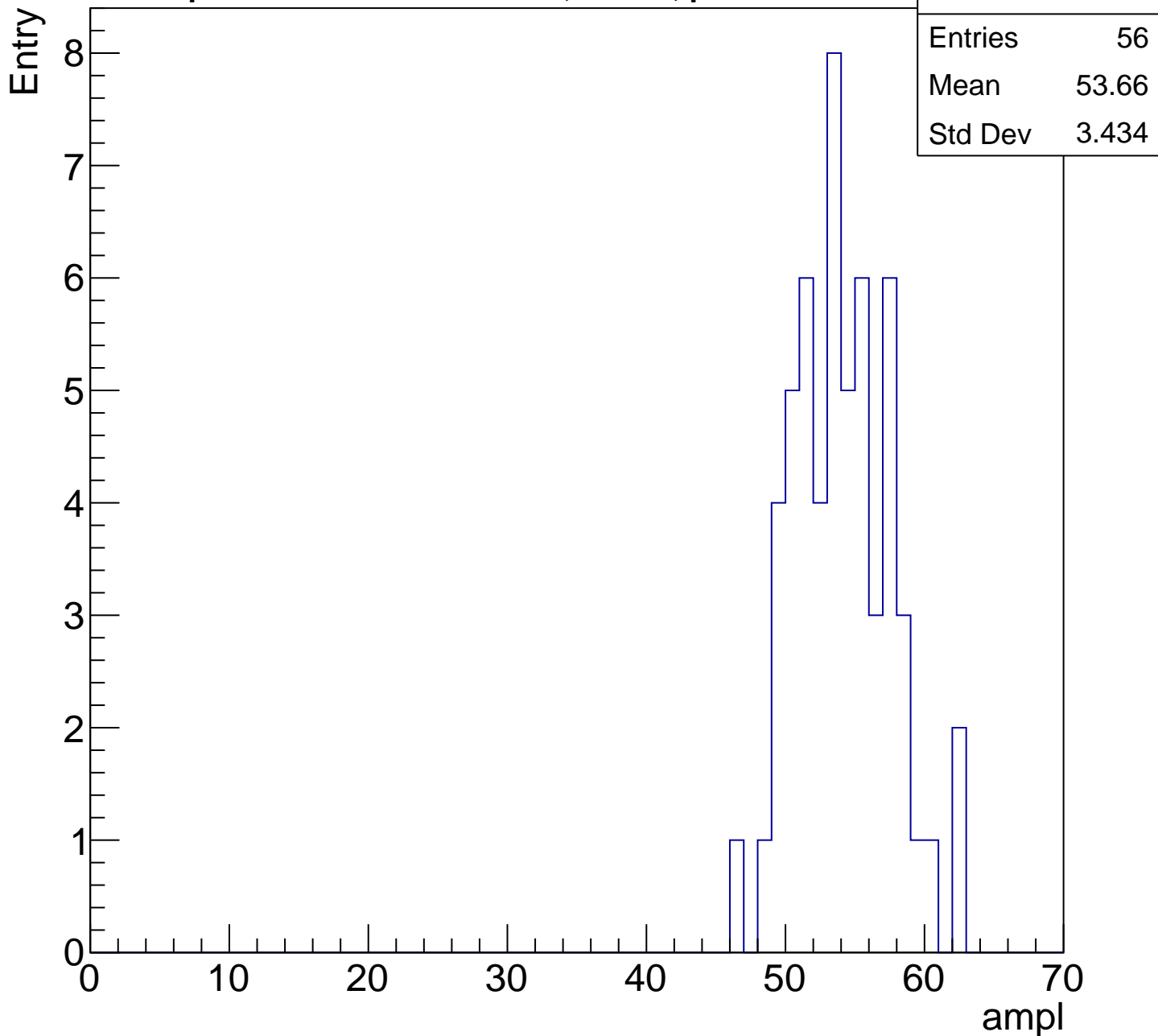
Entry

Entries	64
Mean	47.38
Std Dev	3.516



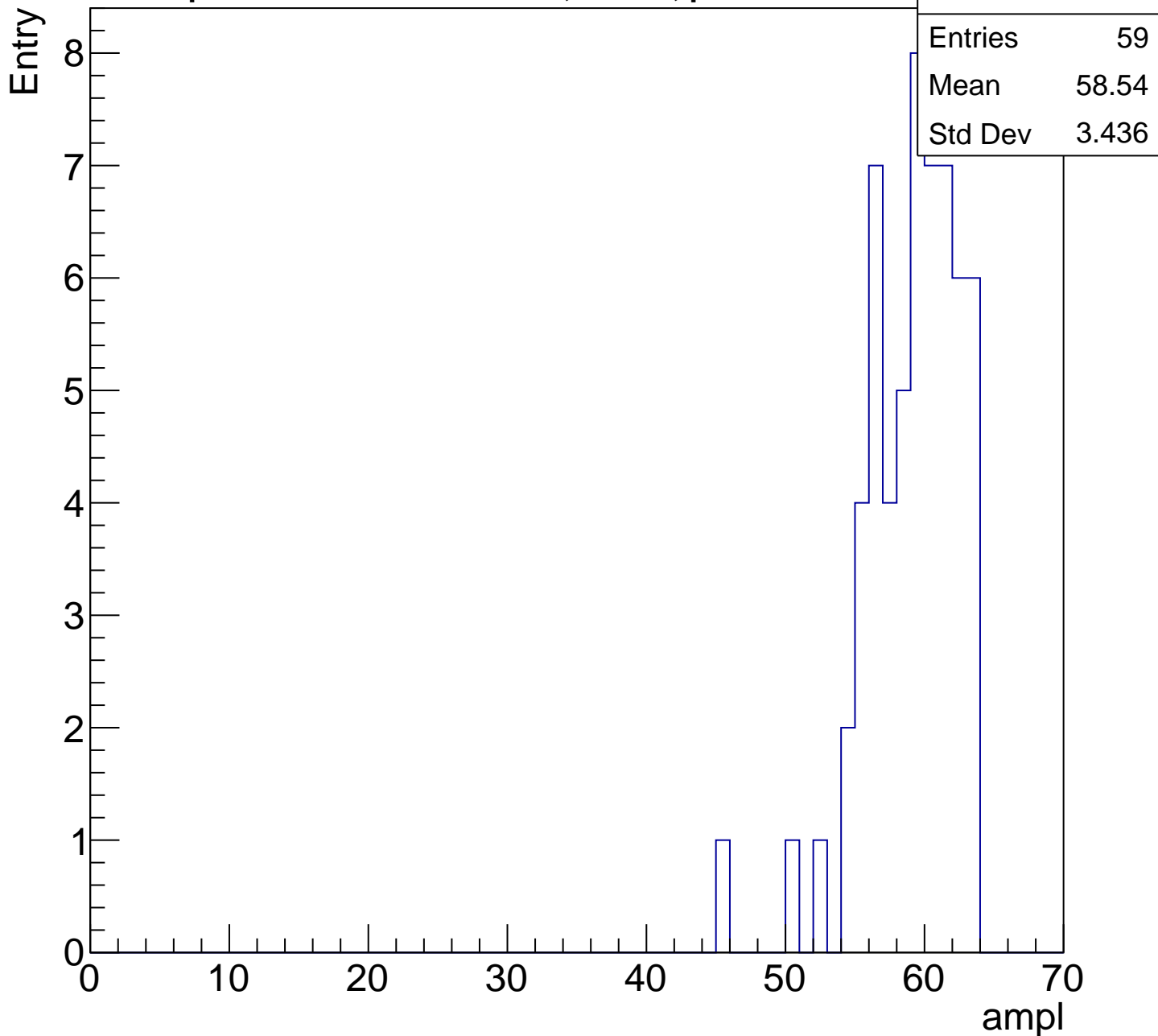
B1L102S, U6-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch84, adc5

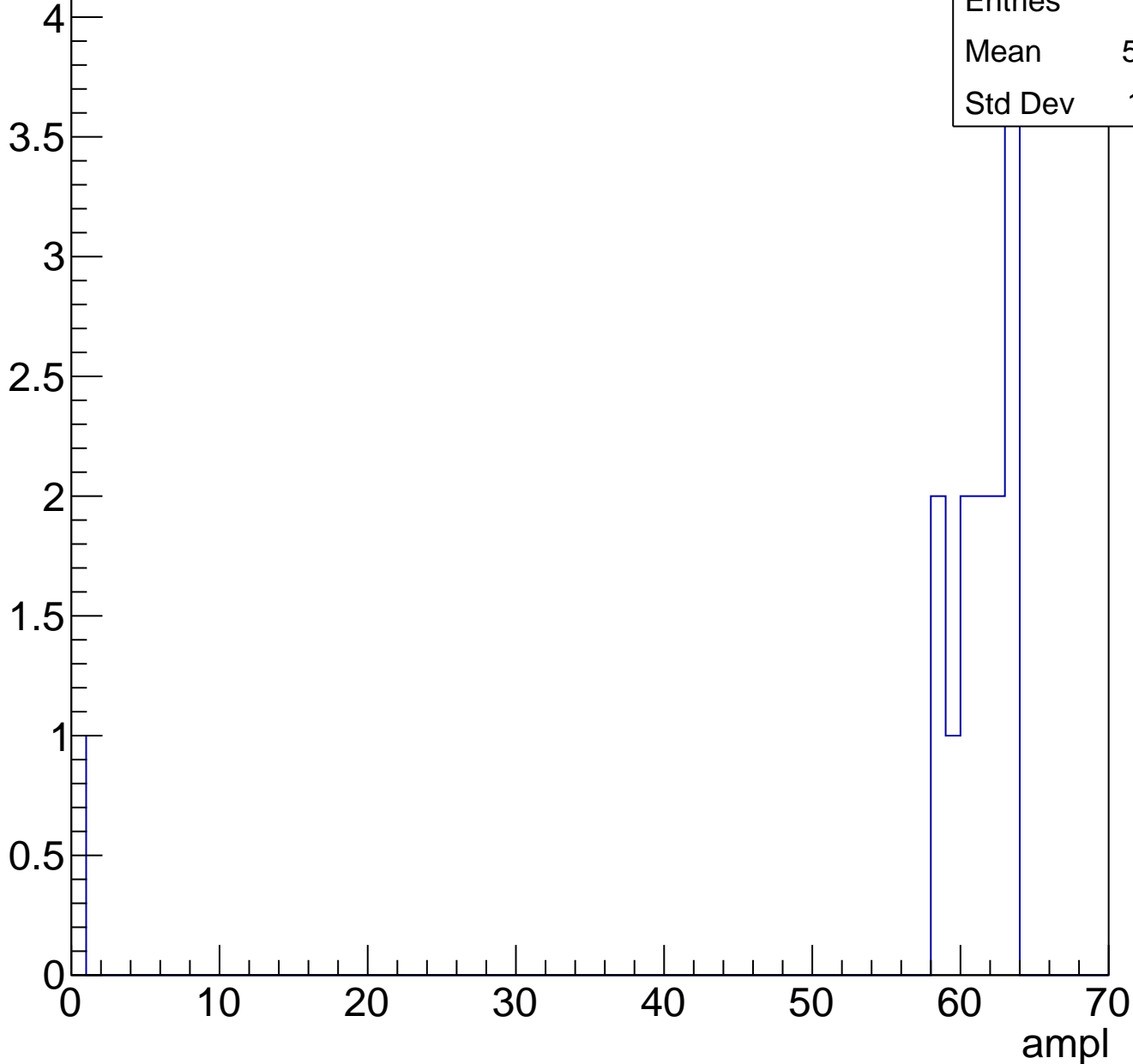
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch85, adc0

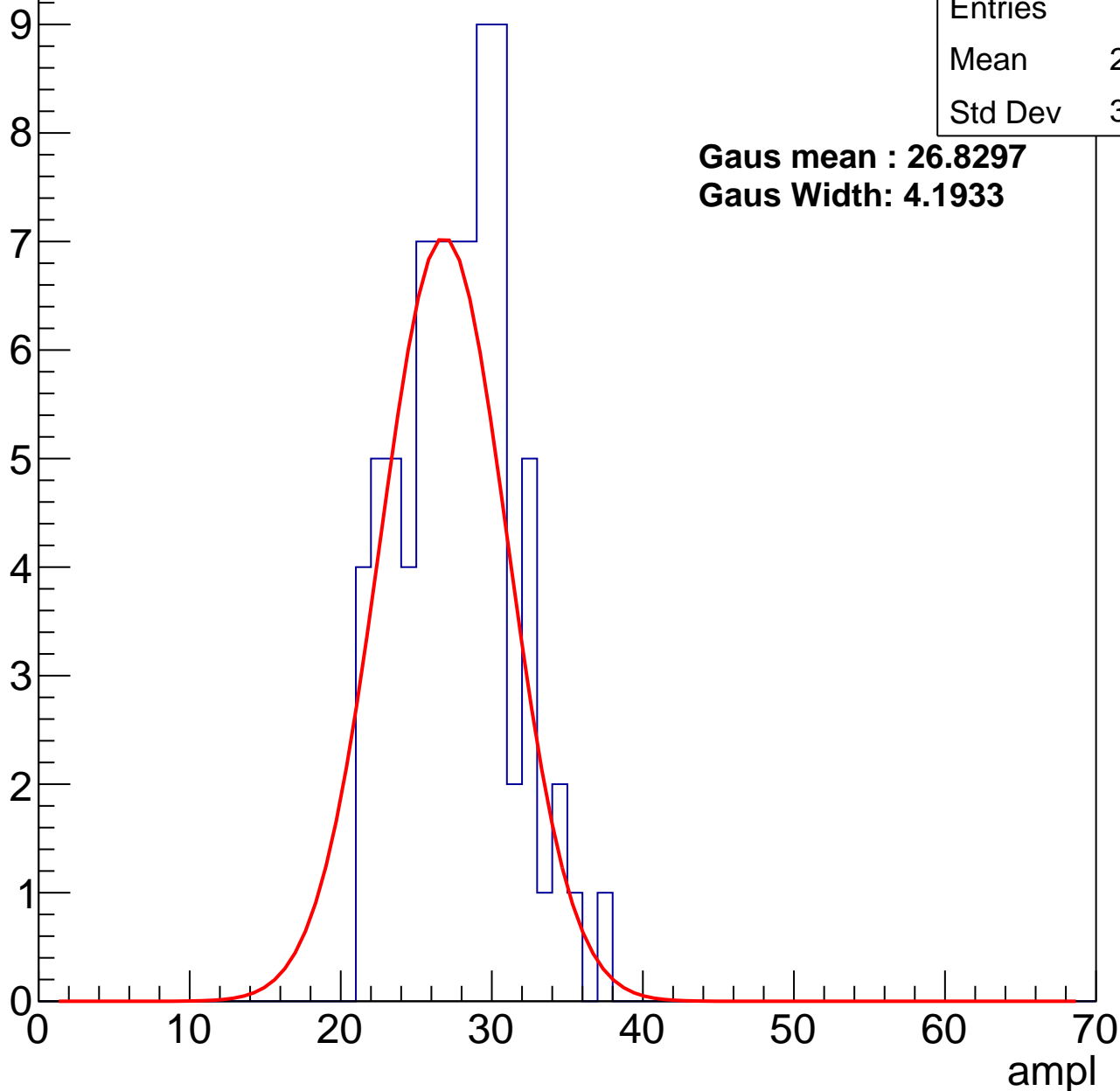
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.28
Std Dev	3.615

Gaus mean : 26.8297

Gaus Width: 4.1933



B1L102S, U6-ch85, adc1

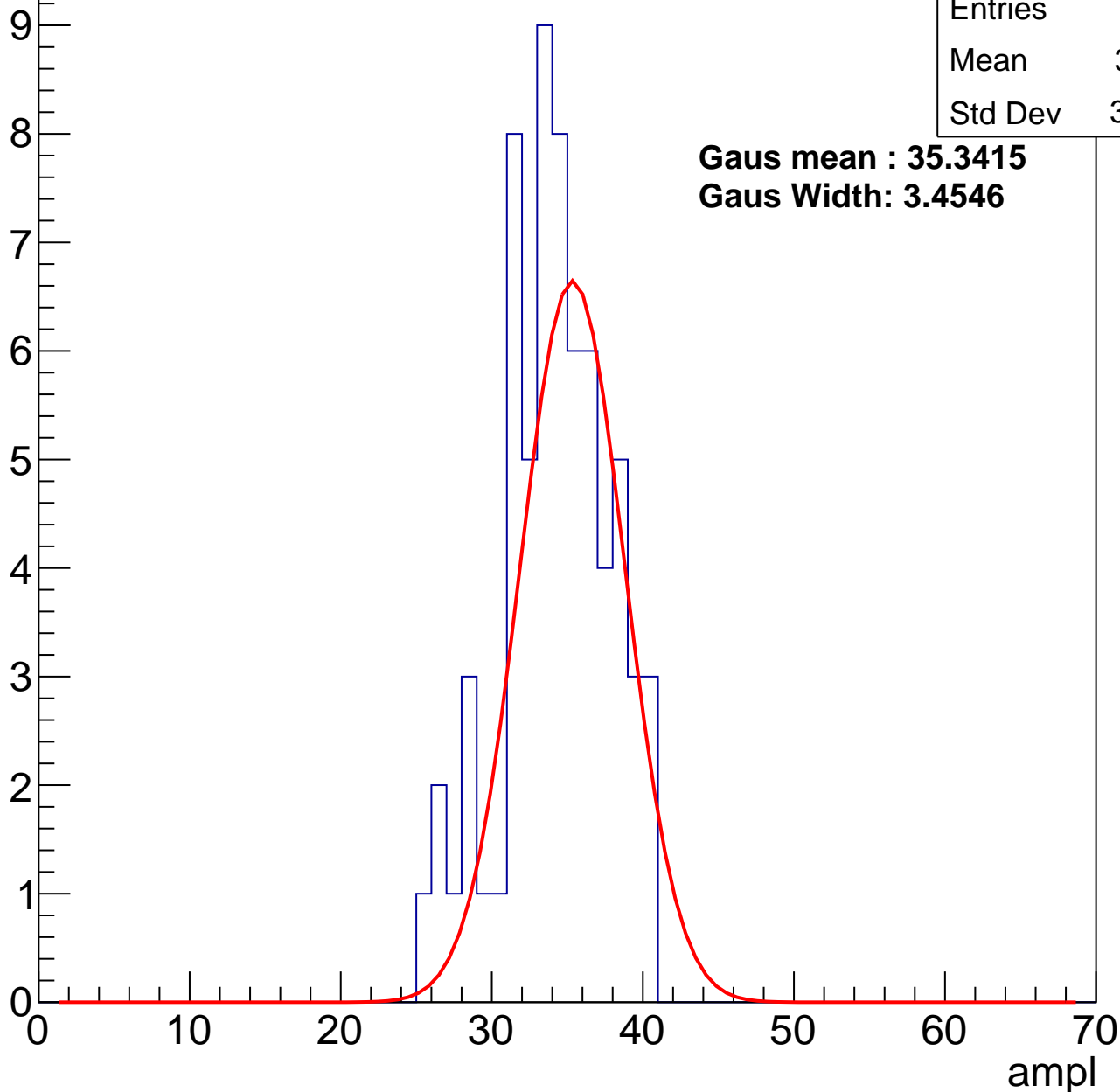
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	33.71
Std Dev	3.549

Gaus mean : 35.3415

Gaus Width: 3.4546



B1L102S, U6-ch85, adc2

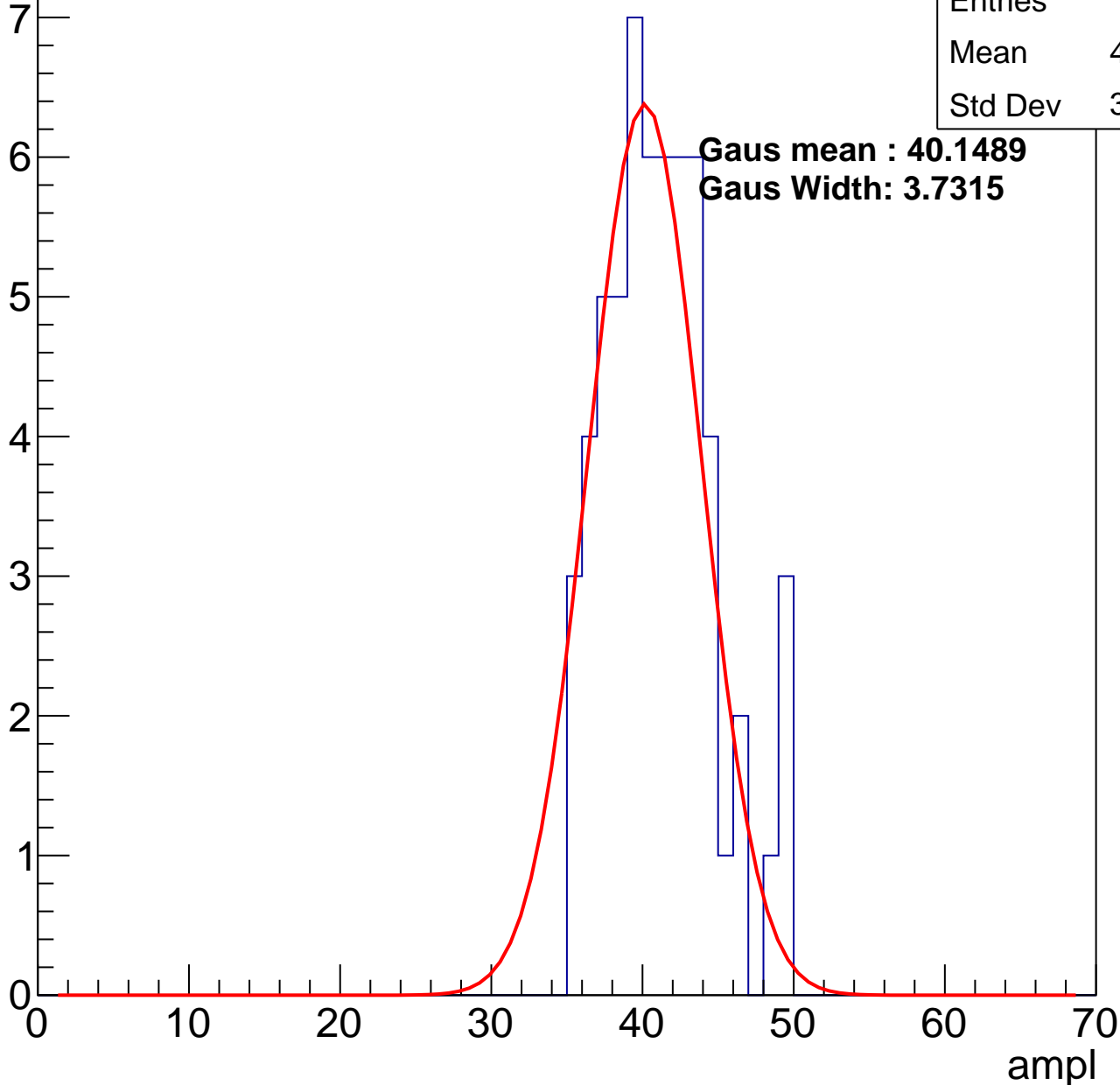
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	40.69
Std Dev	3.528

Gaus mean : 40.1489

Gaus Width: 3.7315

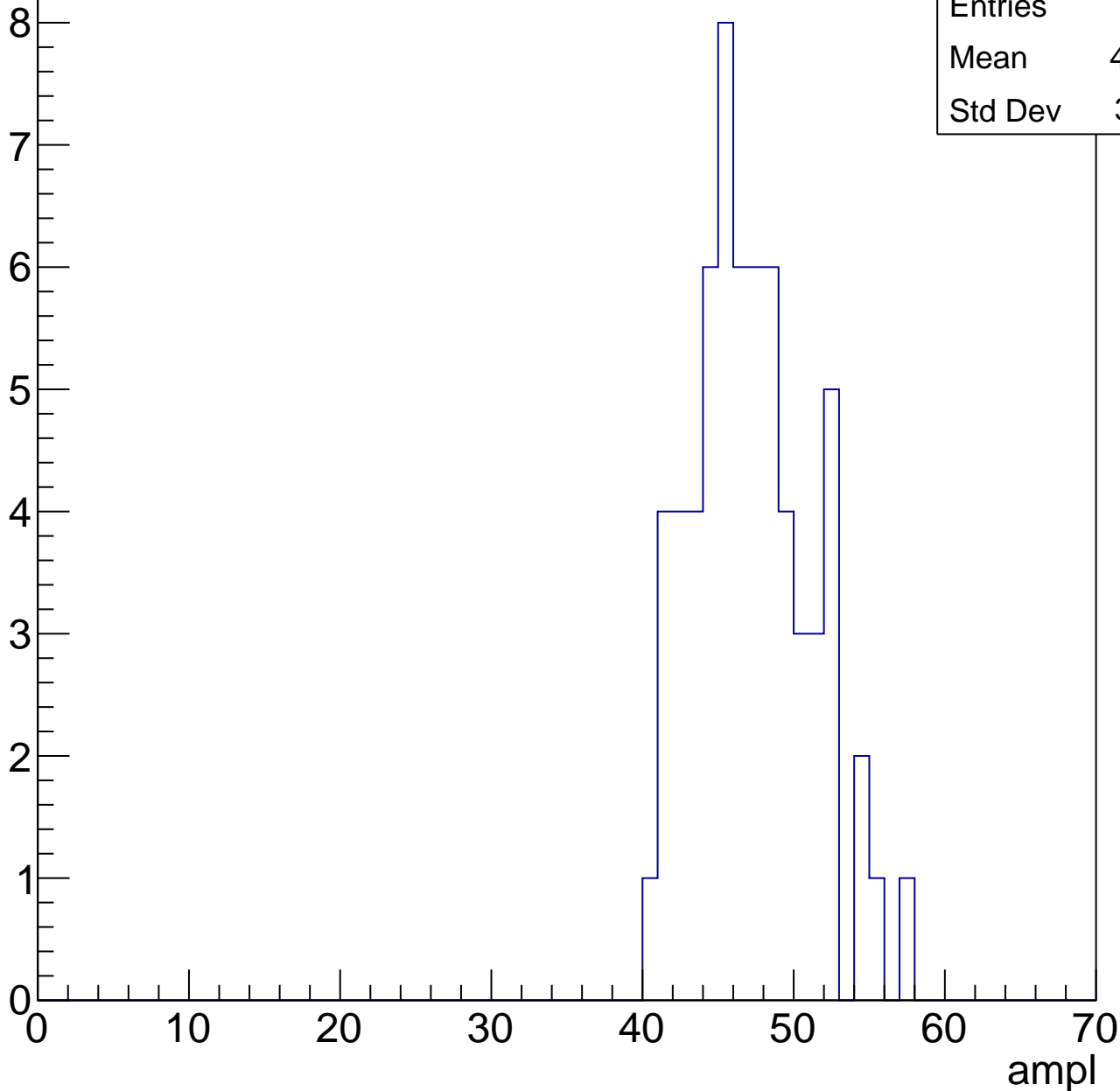


B1L102S, U6-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	46.77
Std Dev	3.811

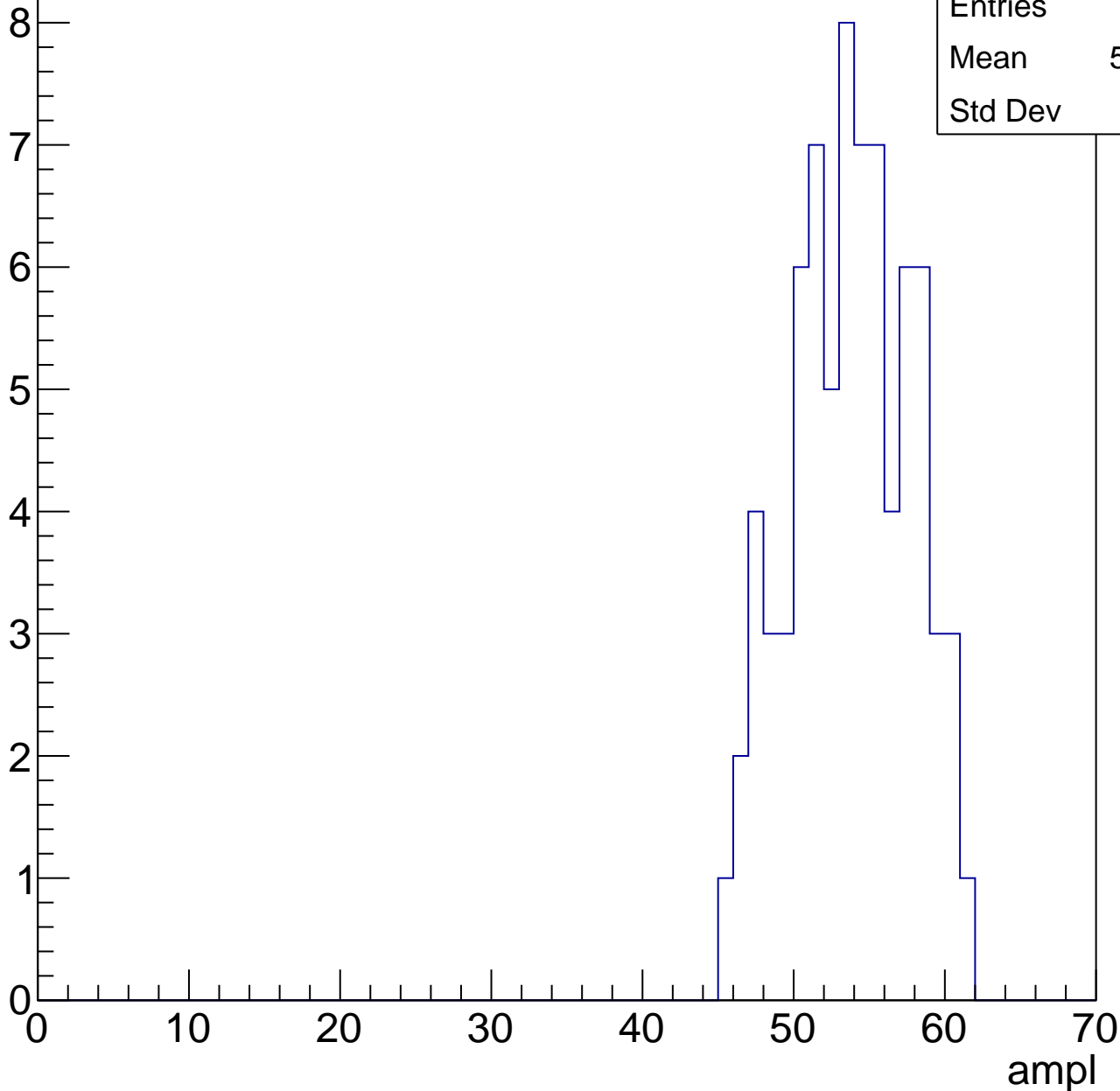


B1L102S, U6-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	53.32
Std Dev	3.88

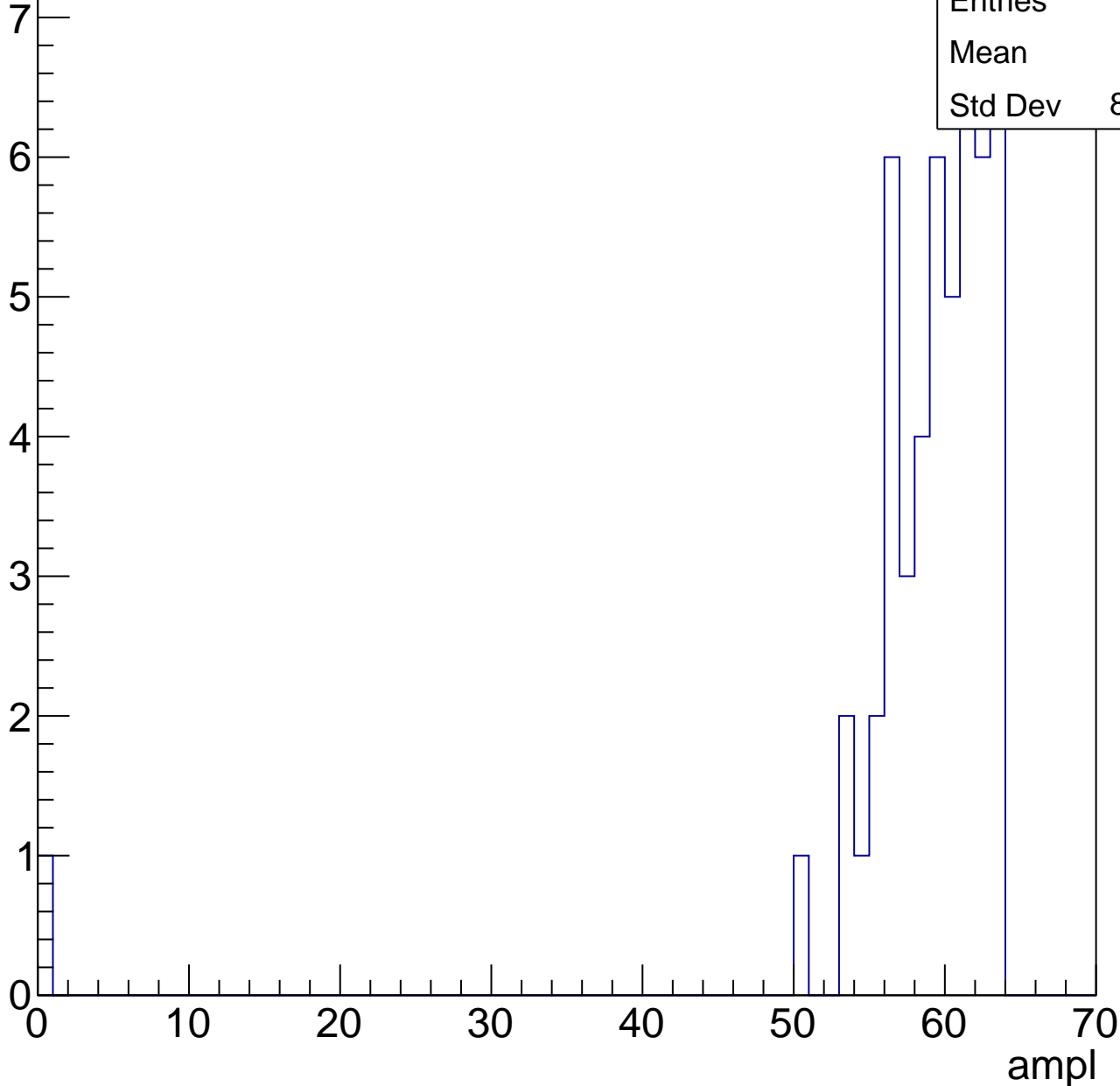


B1L102S, U6-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

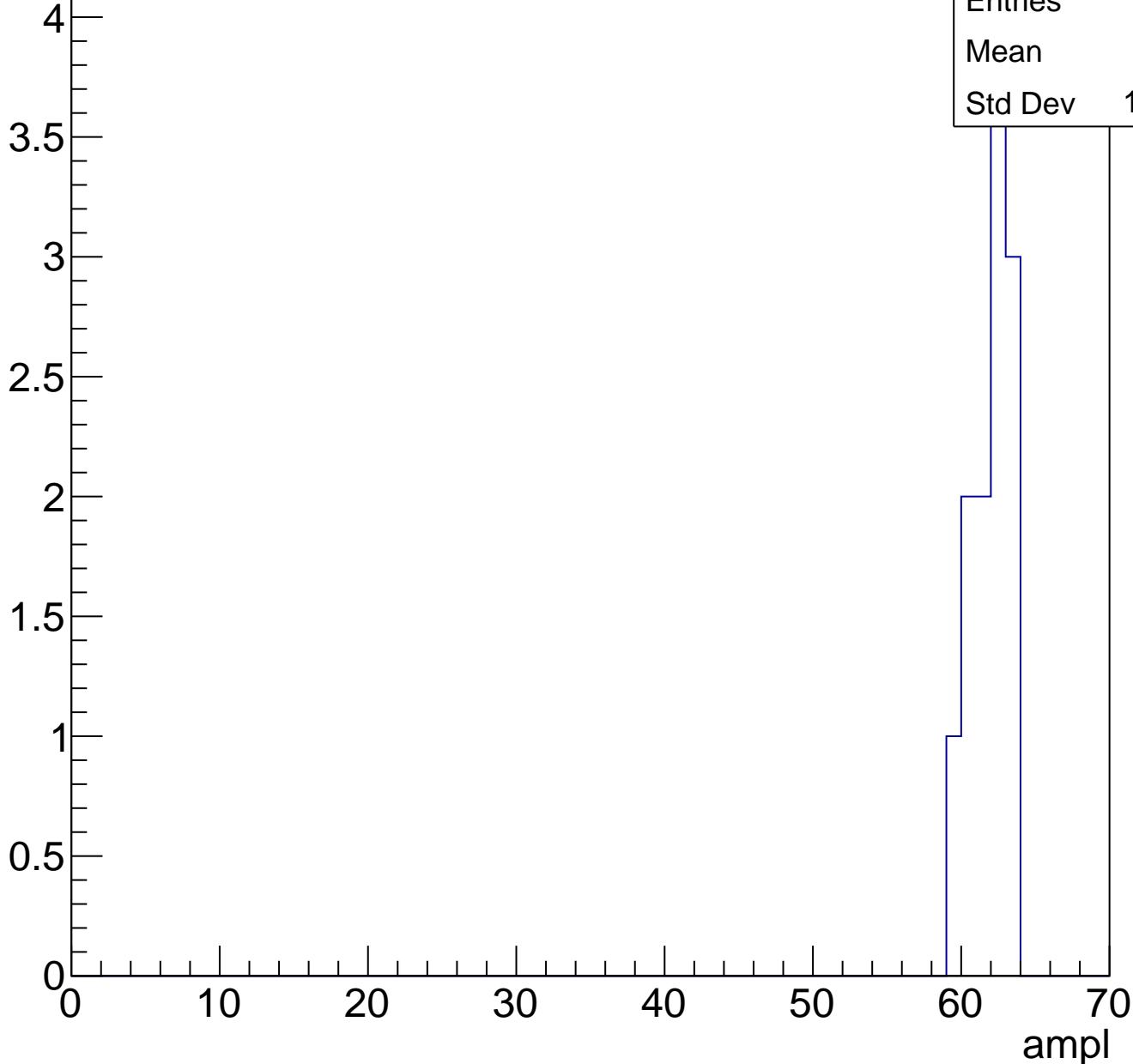
Entries	51
Mean	57.9
Std Dev	8.743



B1L102S, U6-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	12
Mean	61.5
Std Dev	1.258

B1L102S, U6-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U6-ch86, adc0

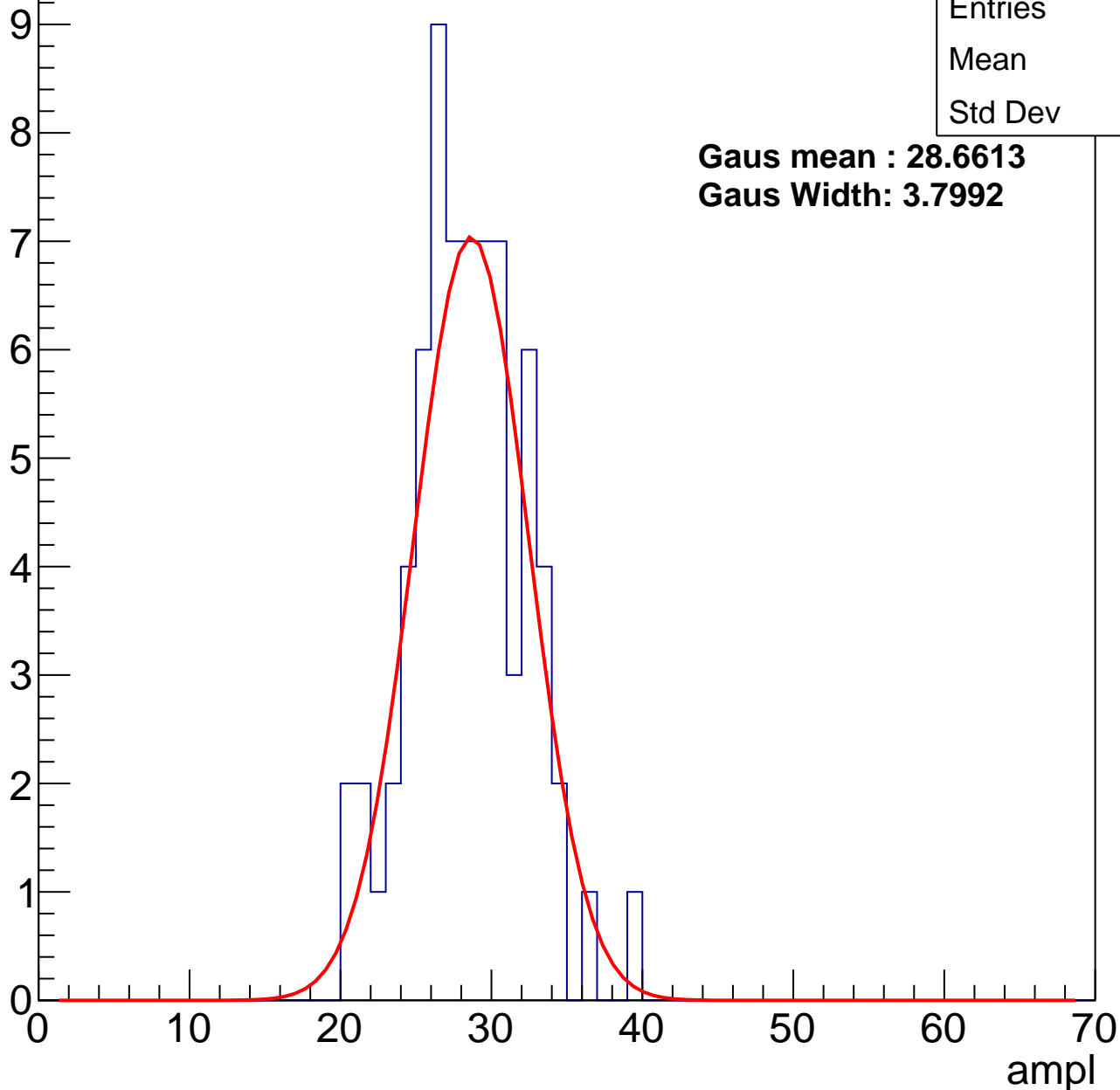
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28
Std Dev	3.73

Gaus mean : 28.6613

Gaus Width: 3.7992



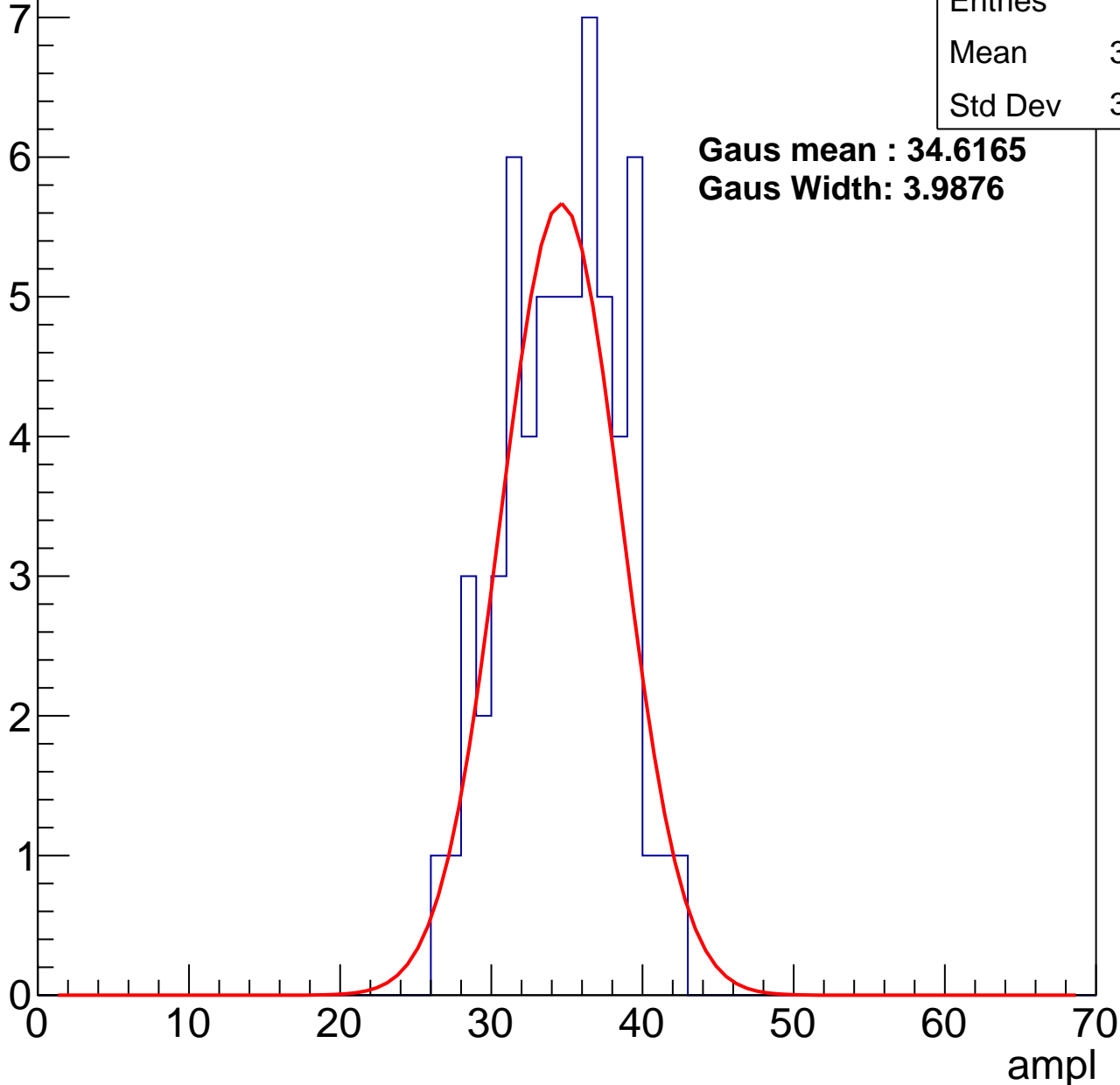
B1L102S, U6-ch86, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.25
Std Dev	3.722

Gaus mean : 34.6165
Gaus Width: 3.9876



B1L102S, U6-ch86, adc2

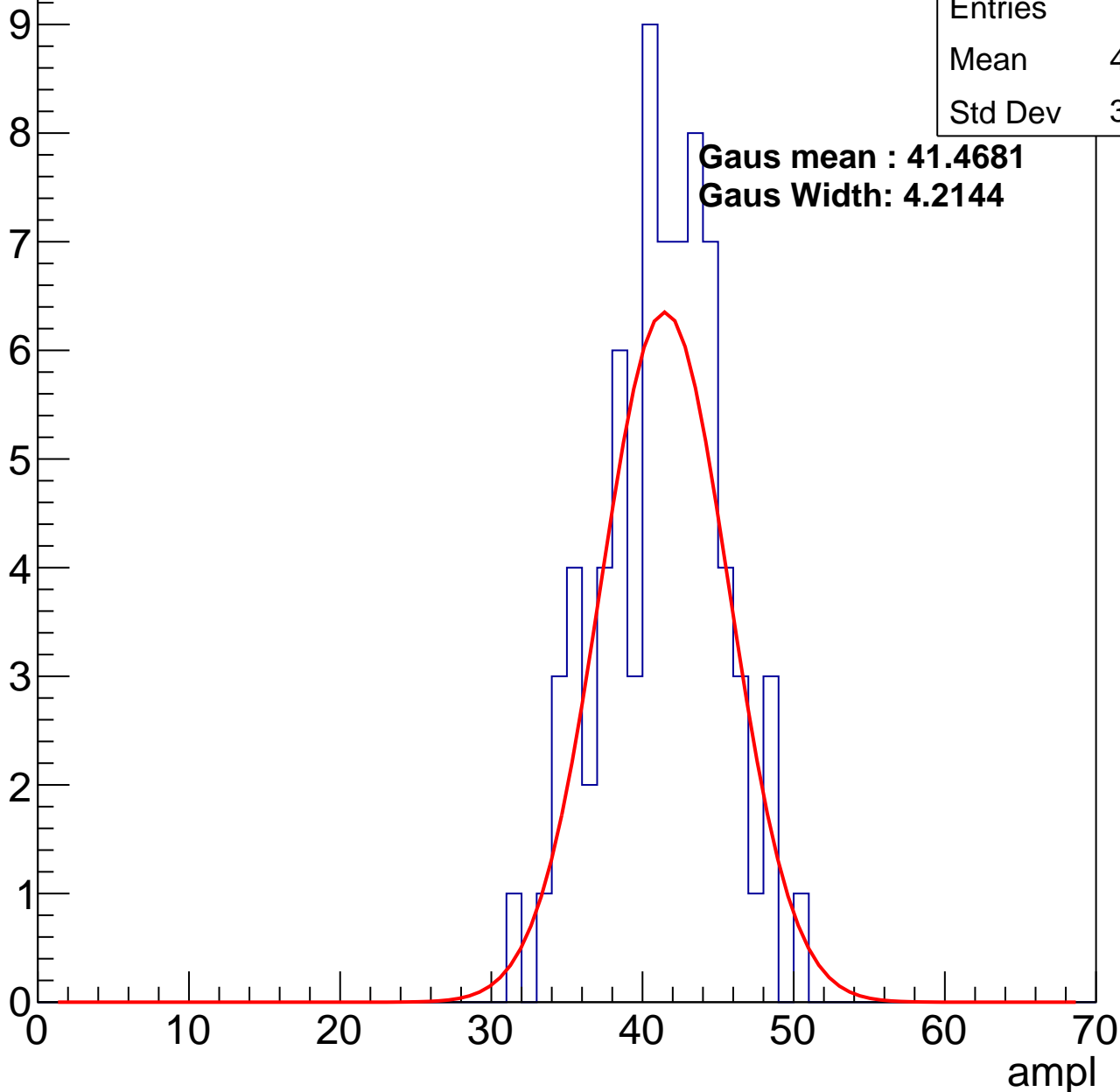
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	40.85
Std Dev	3.938

Gaus mean : 41.4681

Gaus Width: 4.2144

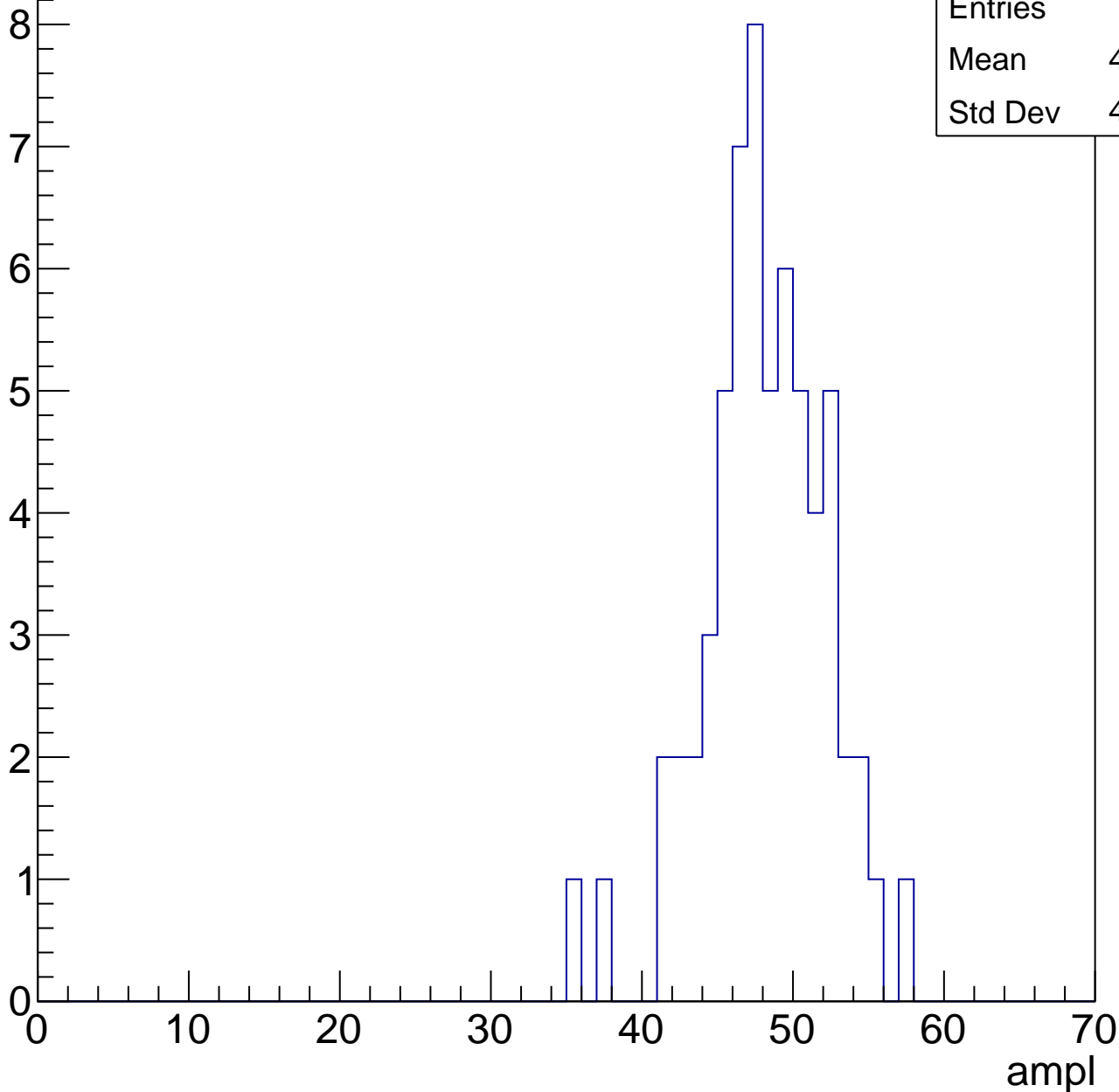


B1L102S, U6-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

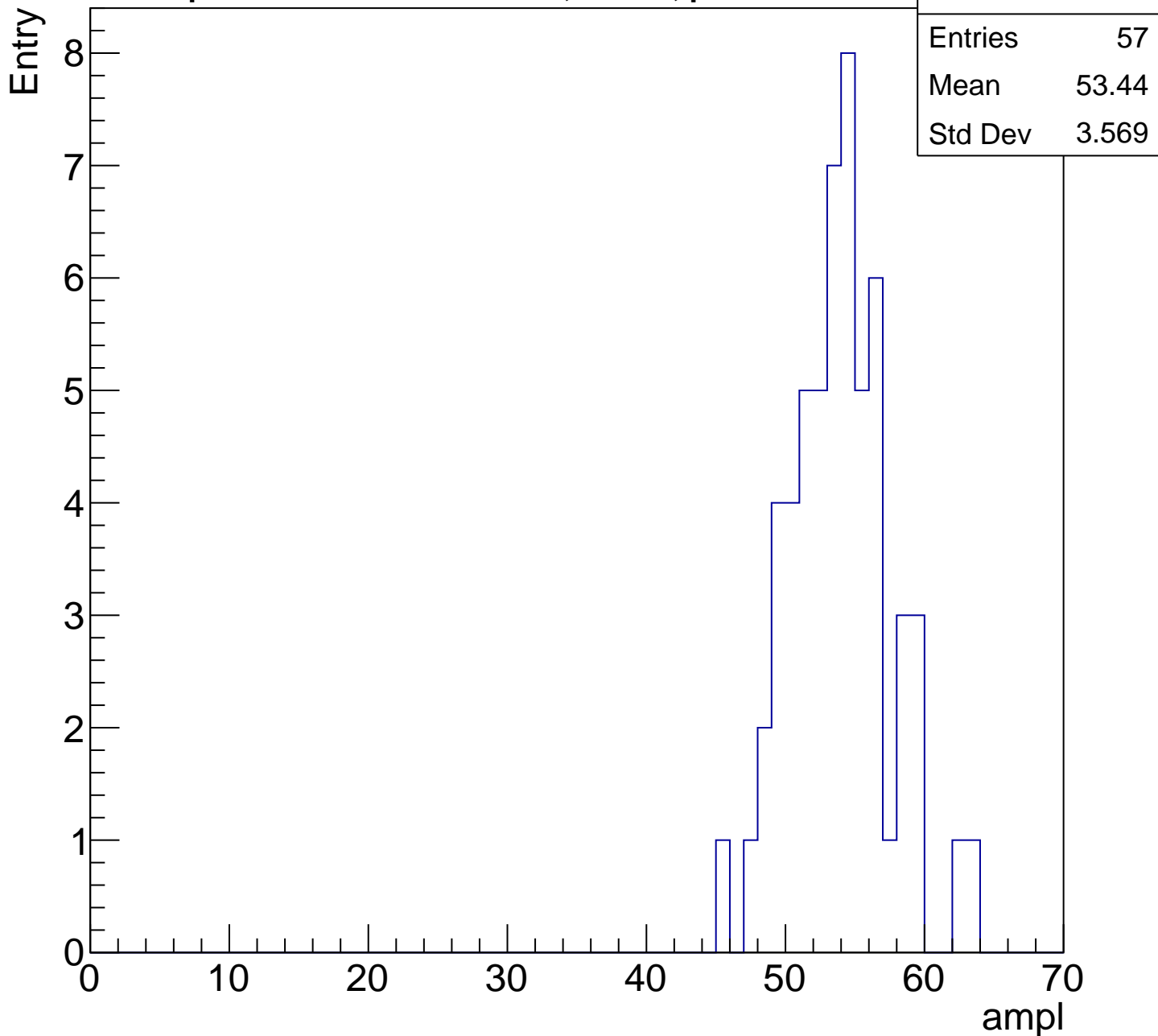
Entry

Entries	62
Mean	47.63
Std Dev	4.065



B1L102S, U6-ch86, adc4

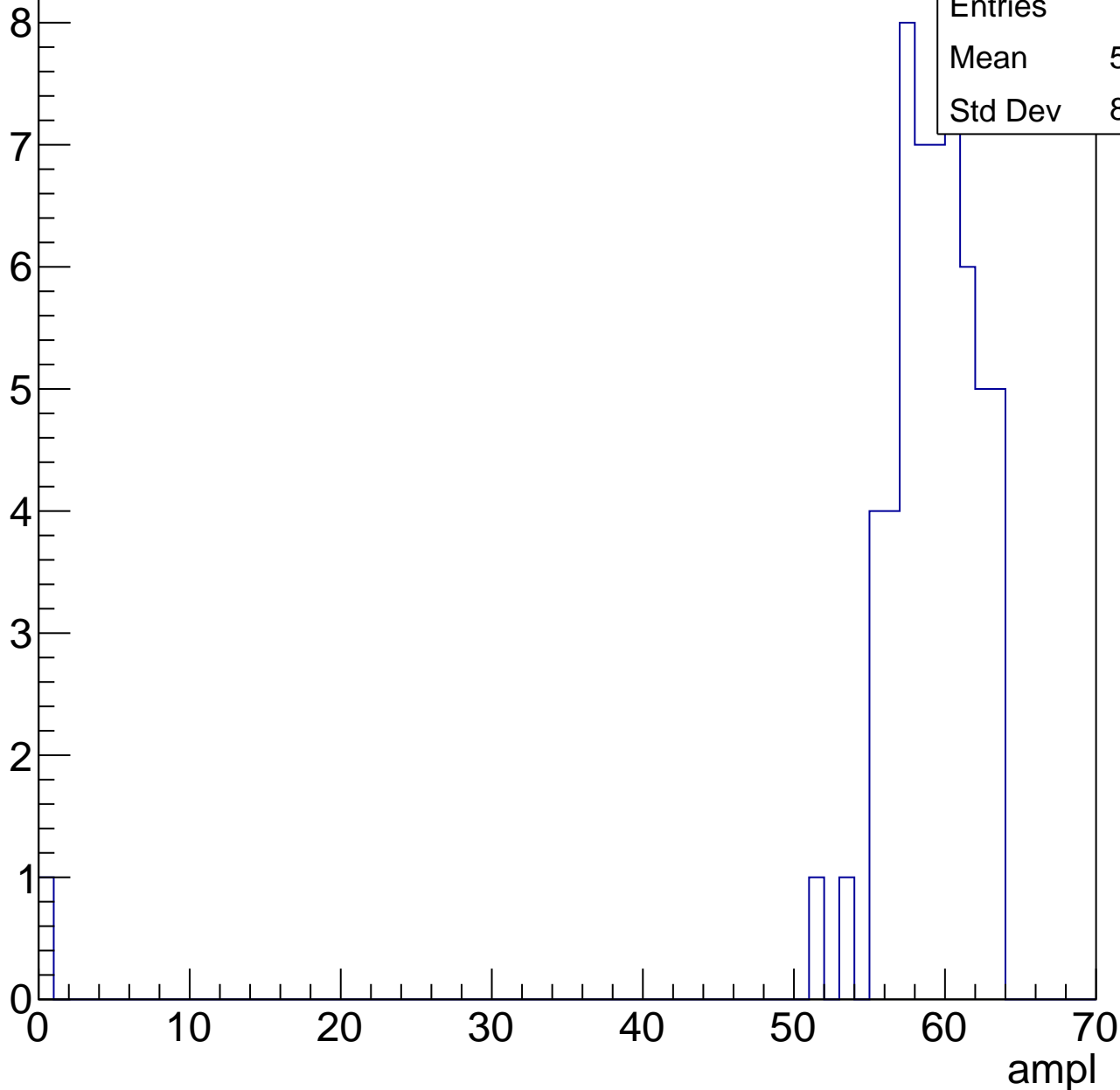
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

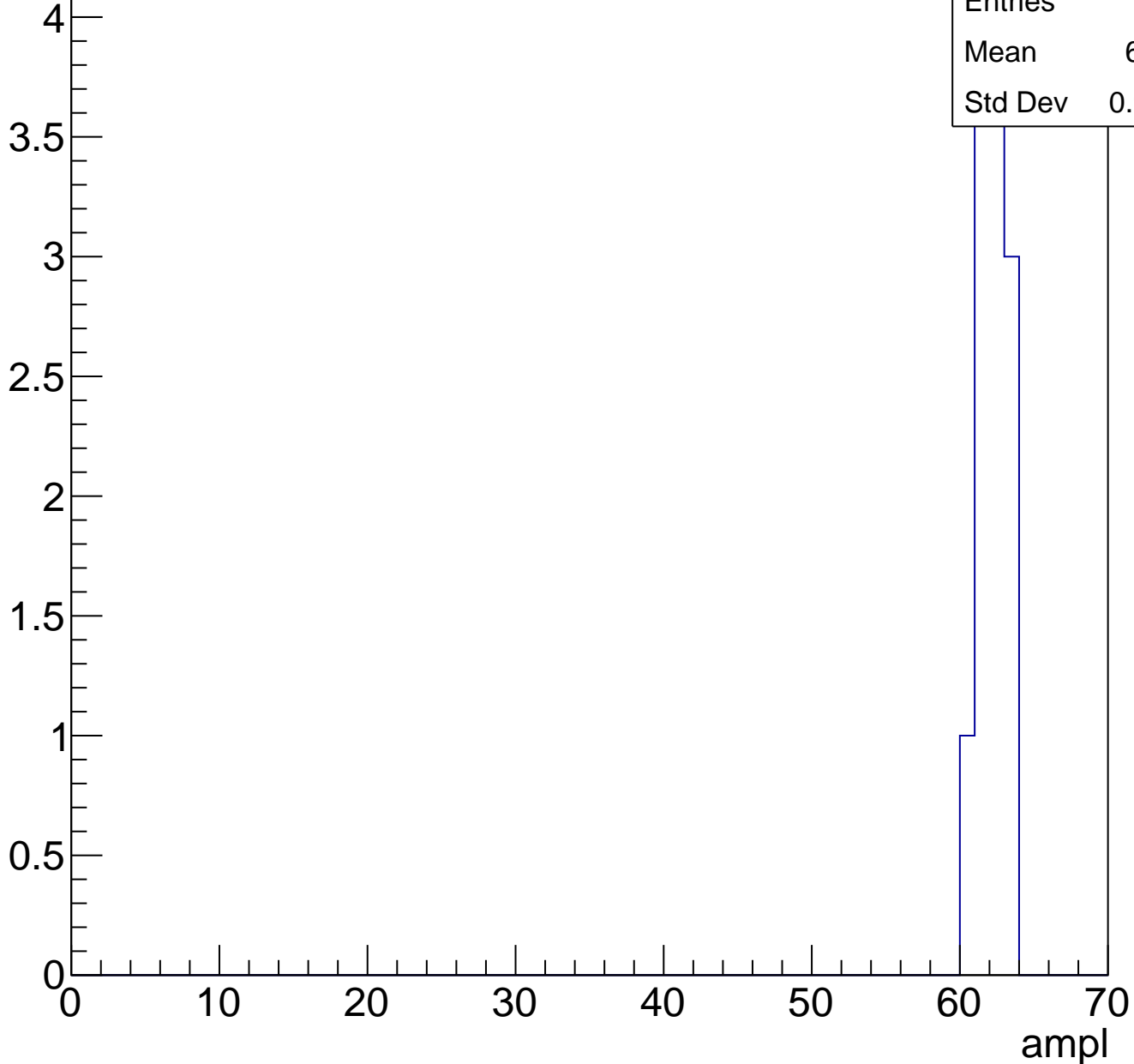
Entry



B1L102S, U6-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

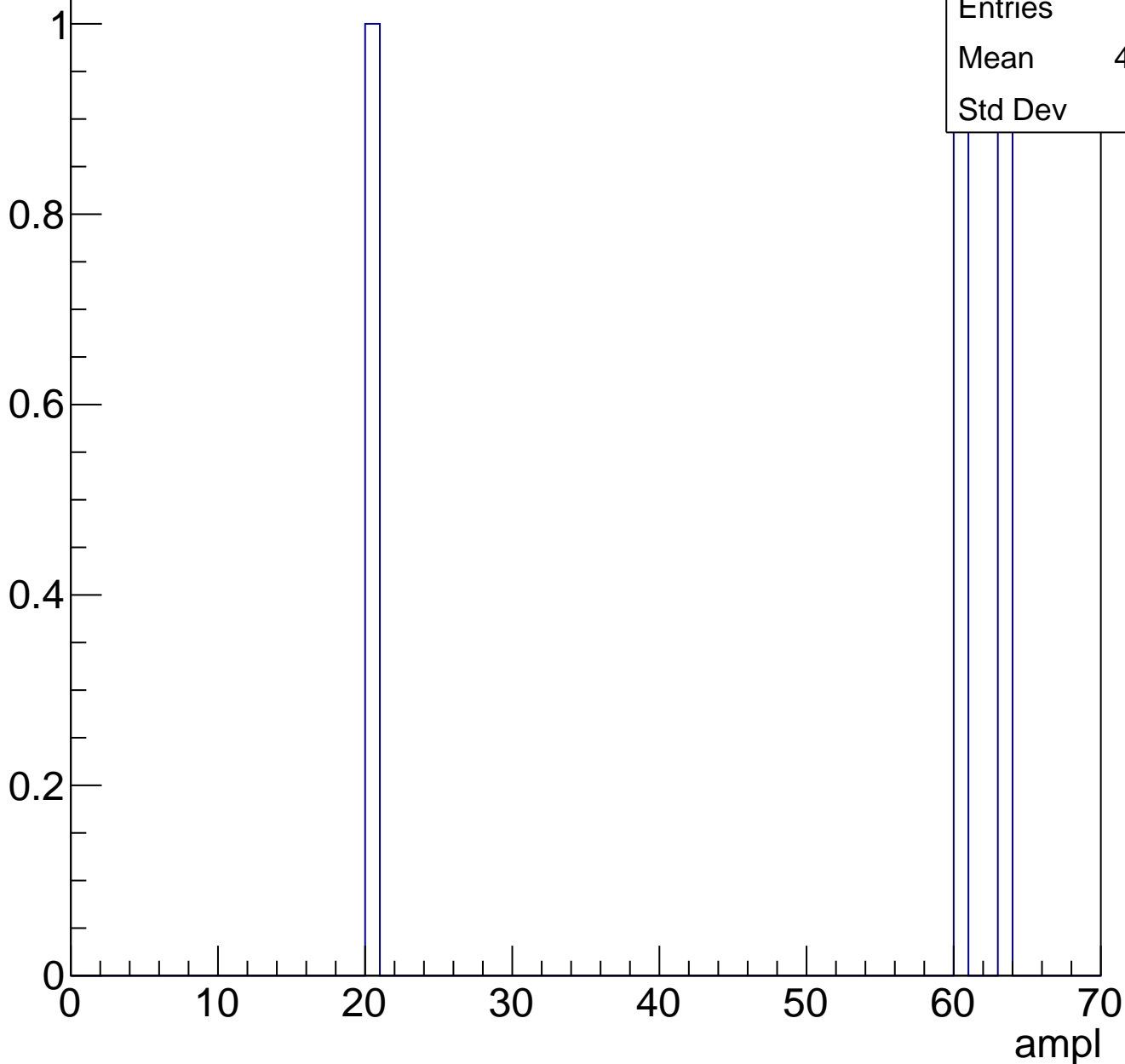
Entry



B1L102S, U6-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	47.67
Std Dev	19.6

B1L102S, U6-ch87, adc0

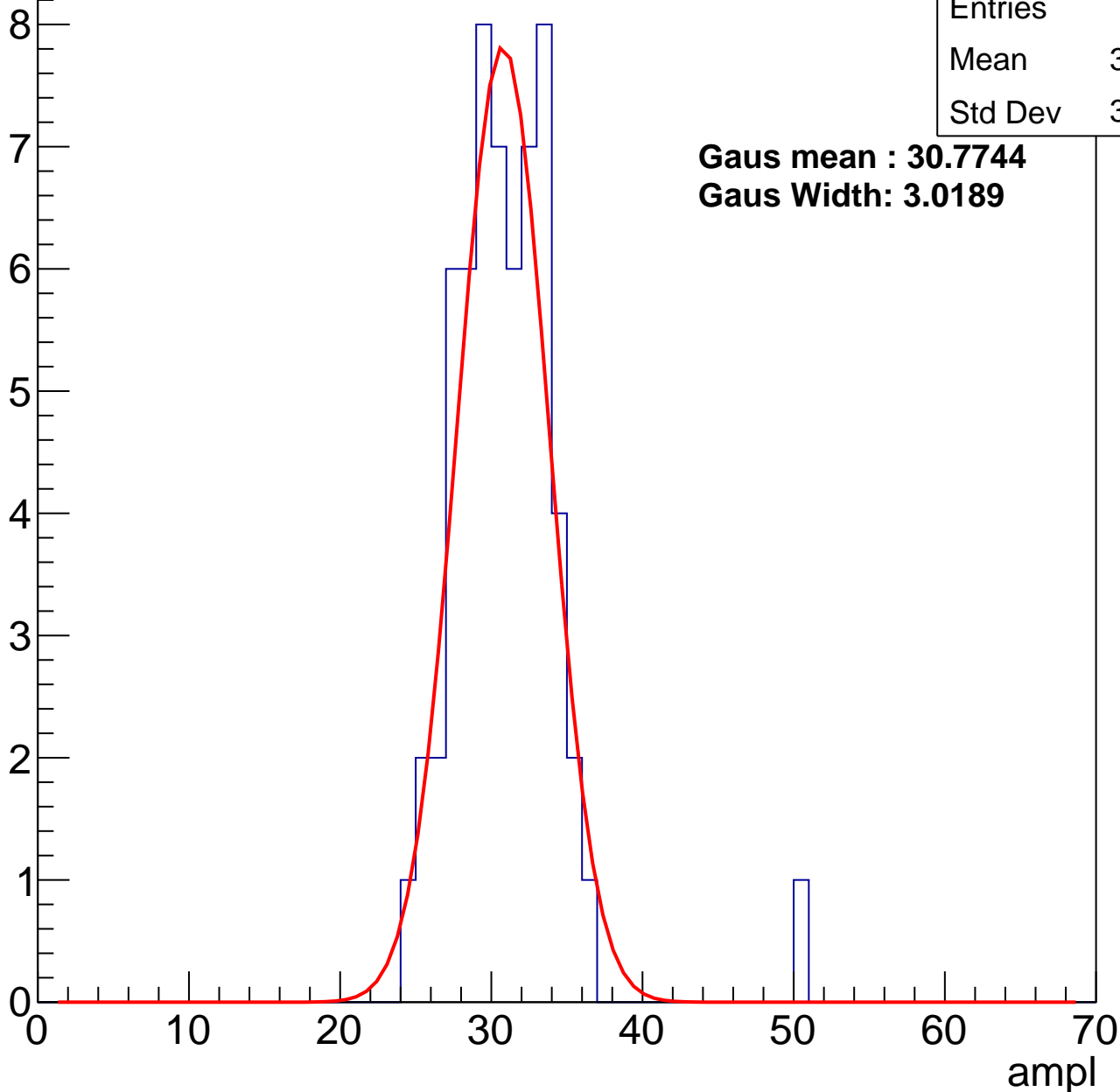
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.56
Std Dev	3.713

Gaus mean : 30.7744

Gaus Width: 3.0189



B1L102S, U6-ch87, adc1

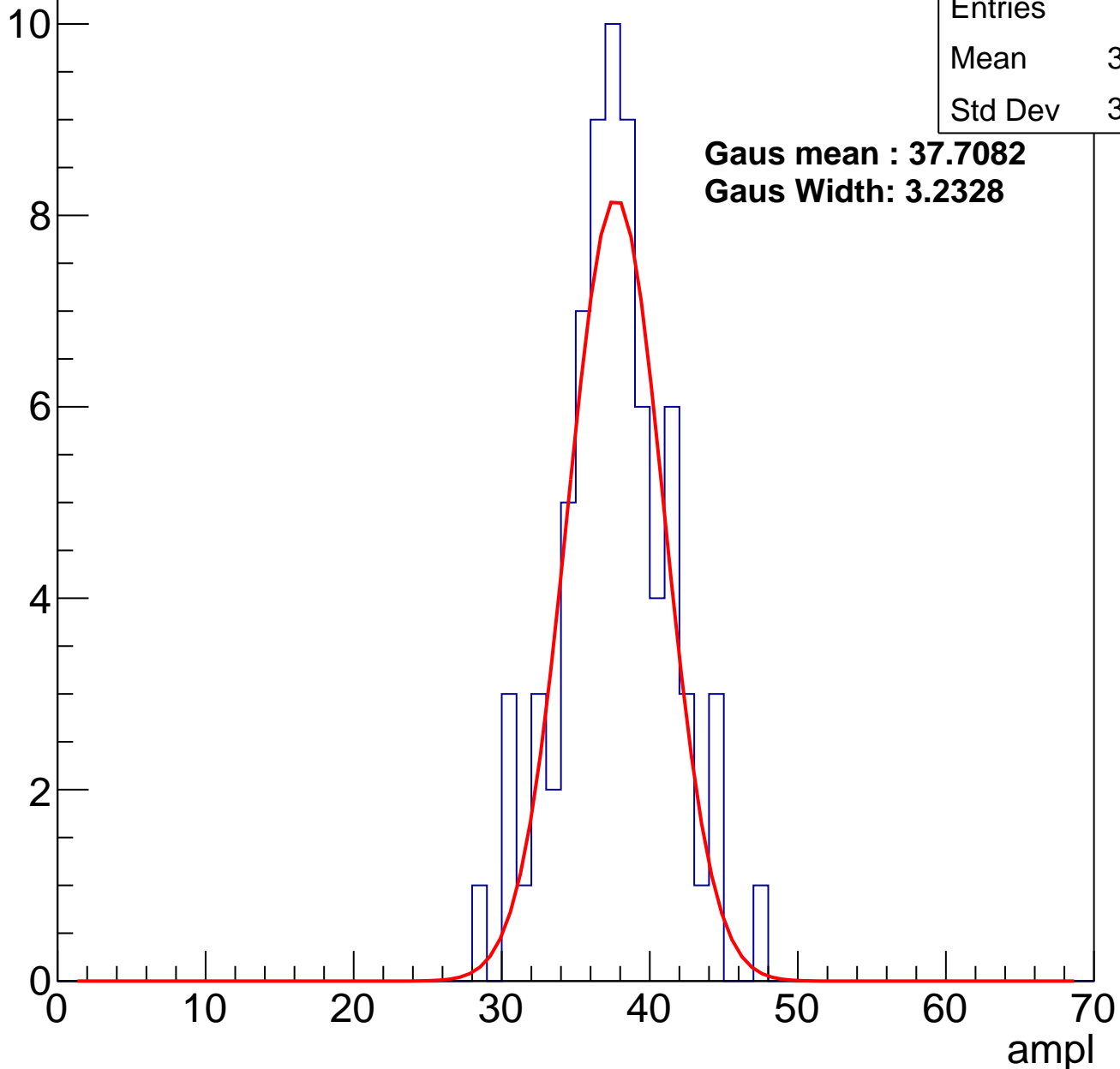
calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	37.16
Std Dev	3.628

Gaus mean : 37.7082

Gaus Width: 3.2328

Entry



B1L102S, U6-ch87, adc2

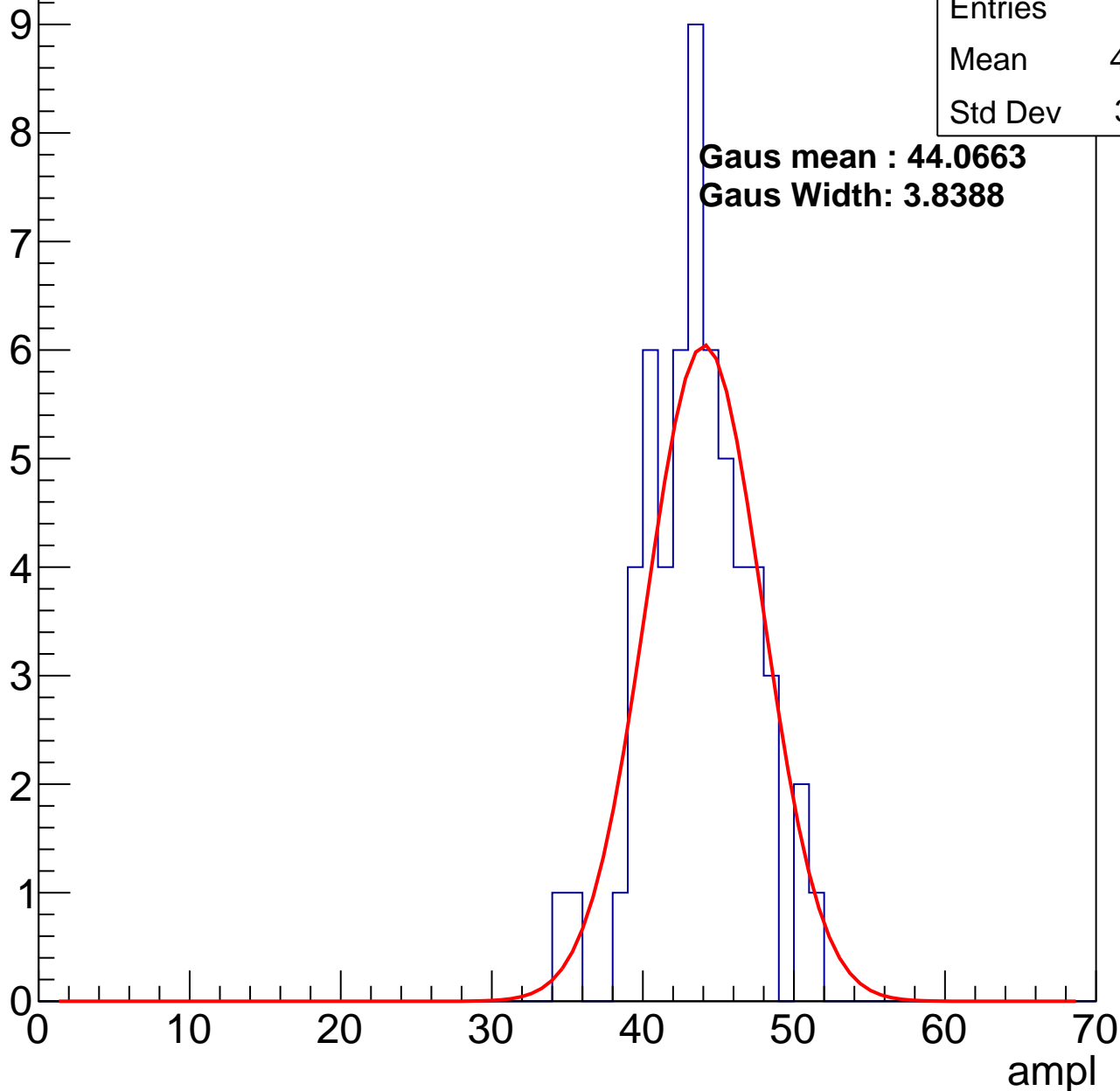
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	43.19
Std Dev	3.431

Gaus mean : 44.0663

Gaus Width: 3.8388

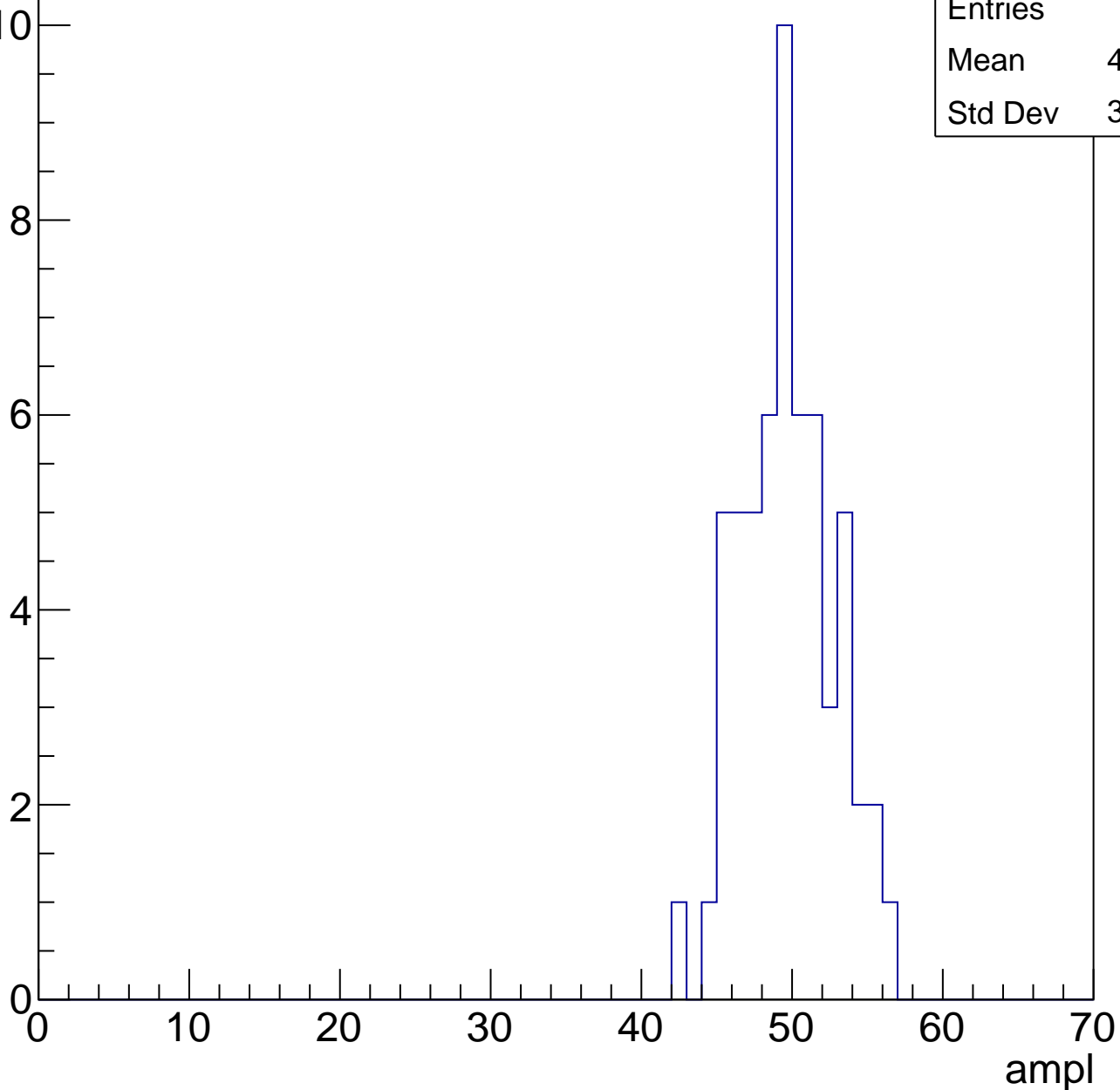


B1L102S, U6-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	49.22
Std Dev	3.023

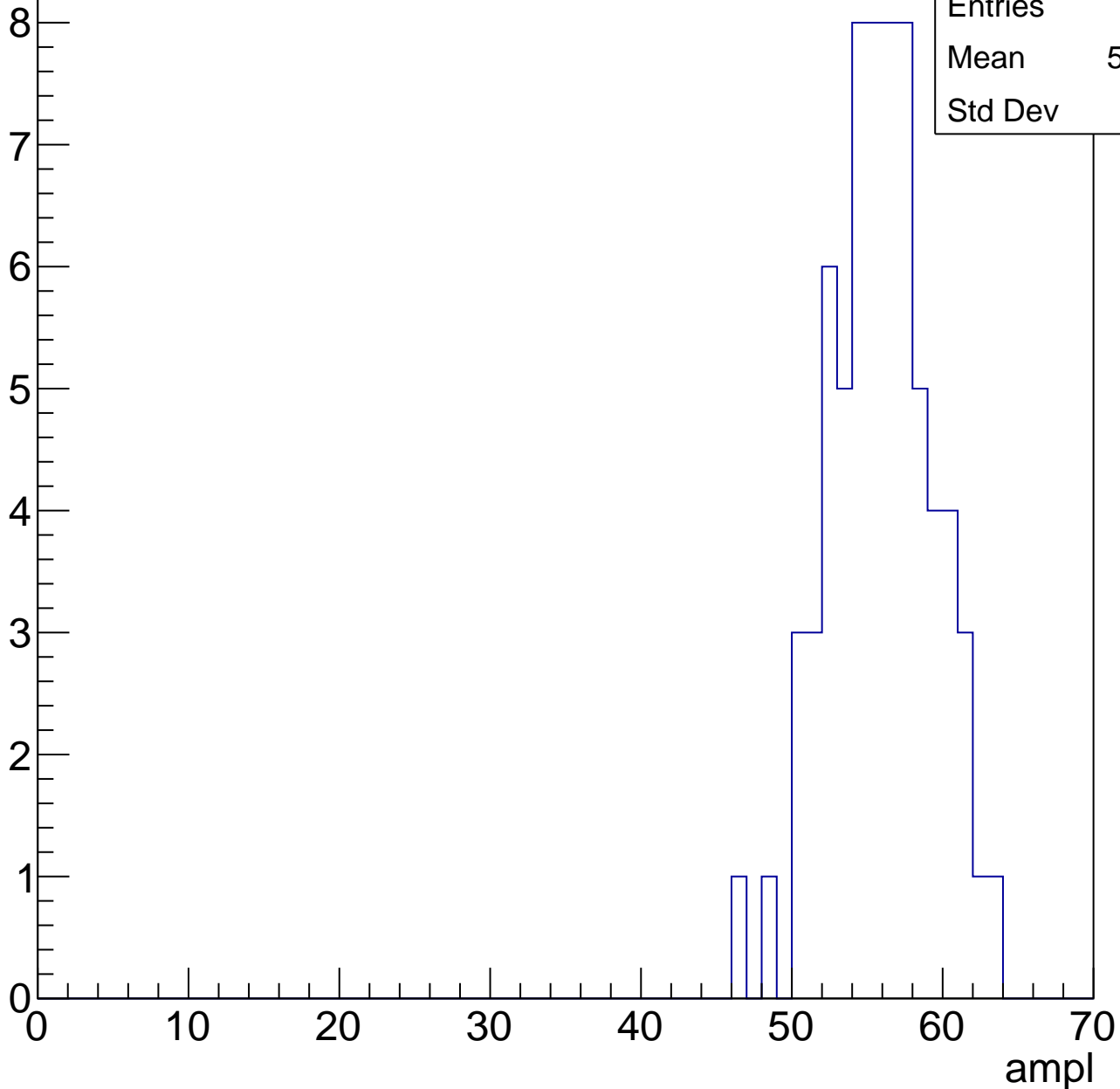


B1L102S, U6-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

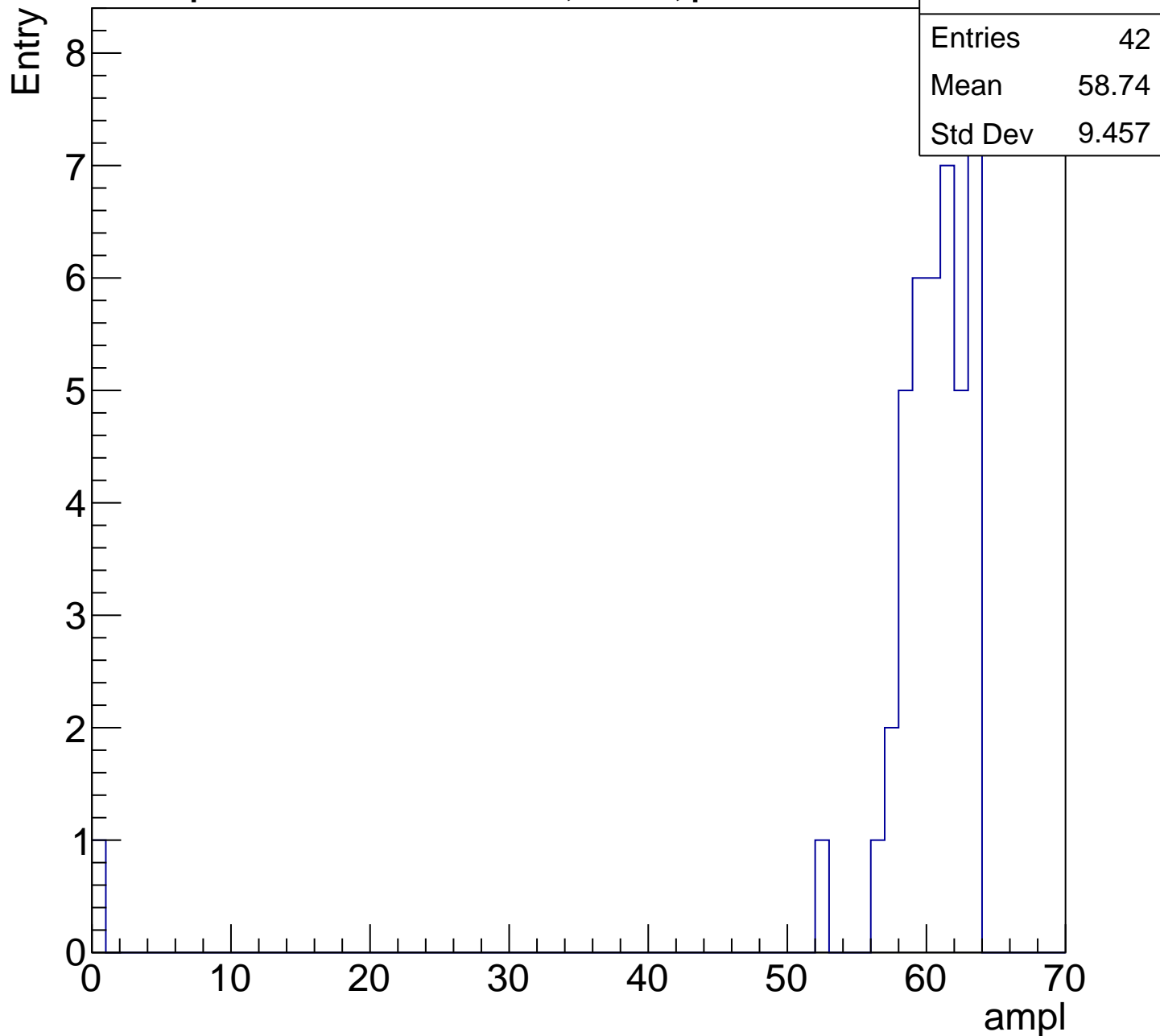
Entry

Entries	69
Mean	55.42
Std Dev	3.39



B1L102S, U6-ch87, adc5

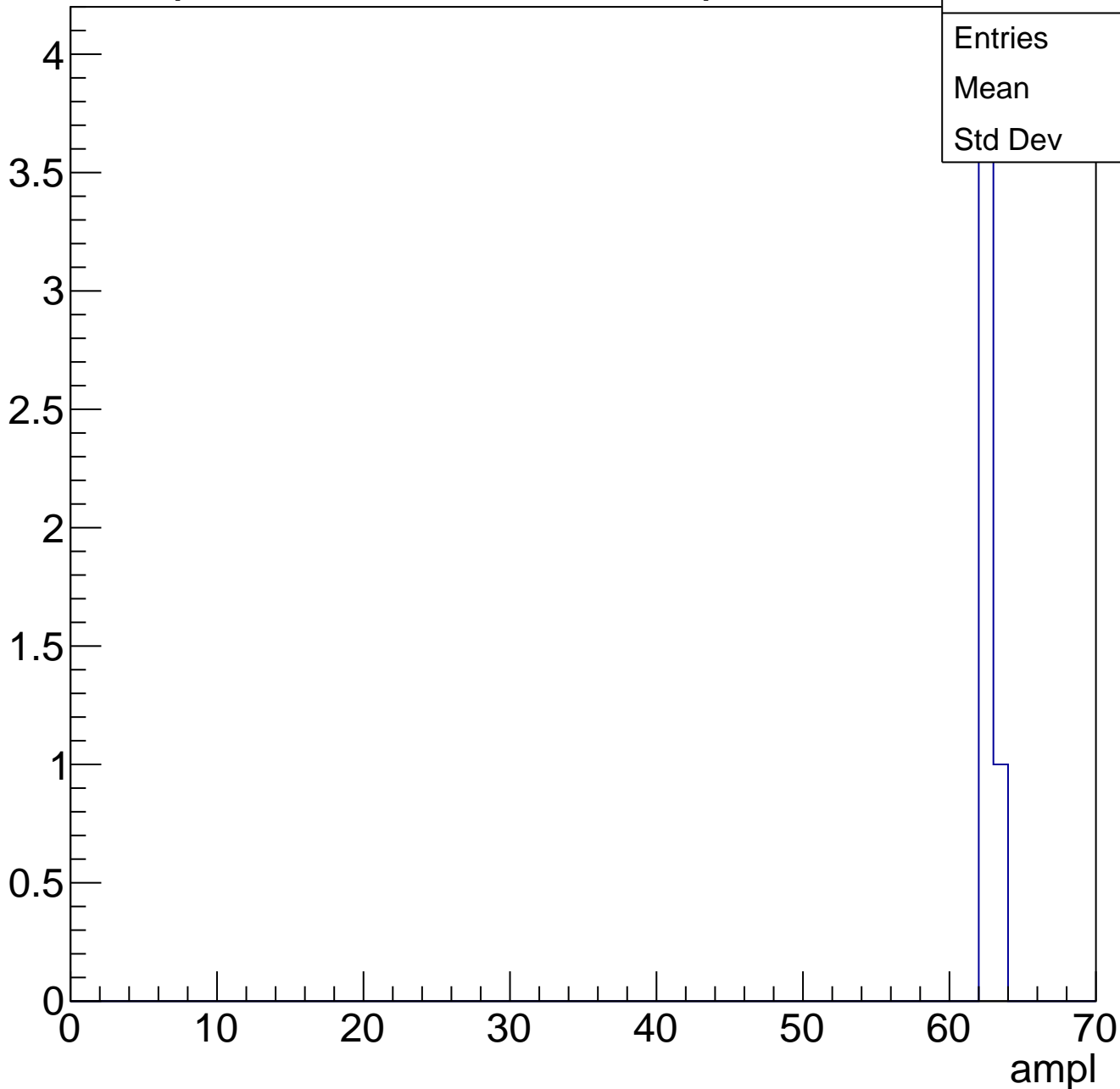
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch88, adc0

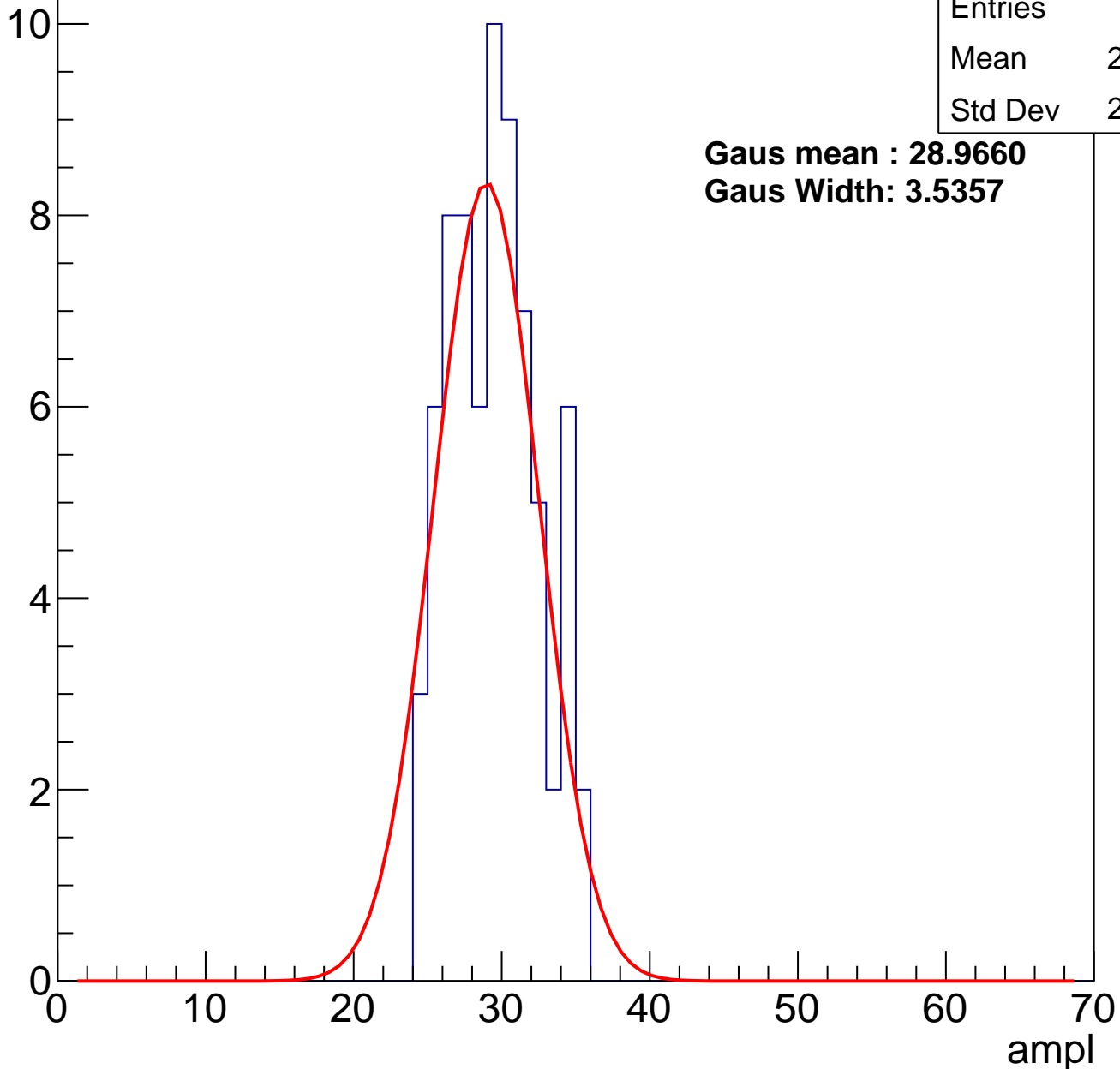
calib_packv5_042523_0143.root, FC#11, port A2

Entries	72
Mean	29.04
Std Dev	2.927

Gaus mean : 28.9660

Gaus Width: 3.5357

Entry



B1L102S, U6-ch88, adc1

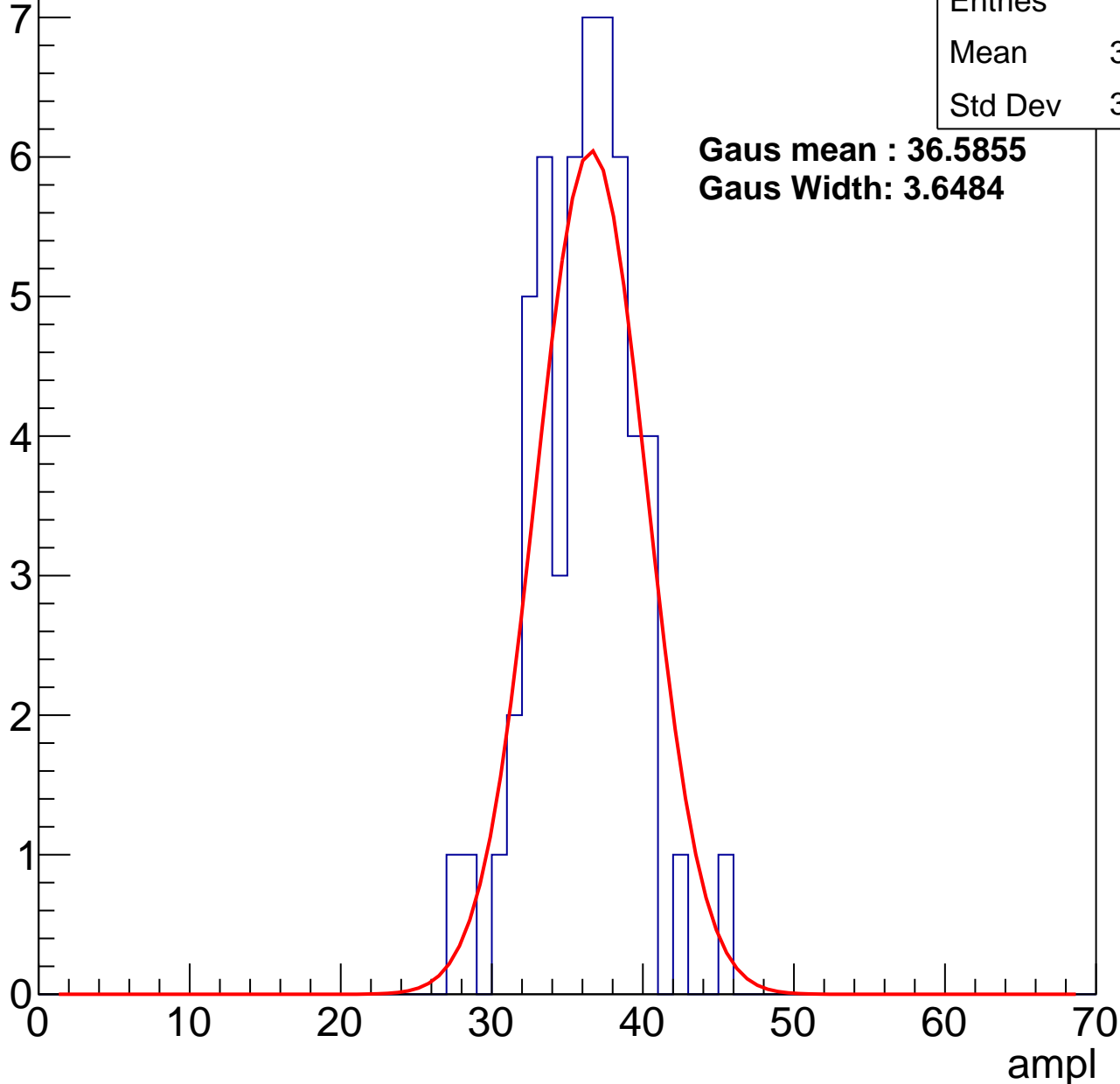
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	35.62
Std Dev	3.366

Gaus mean : 36.5855

Gaus Width: 3.6484



B1L102S, U6-ch88, adc2

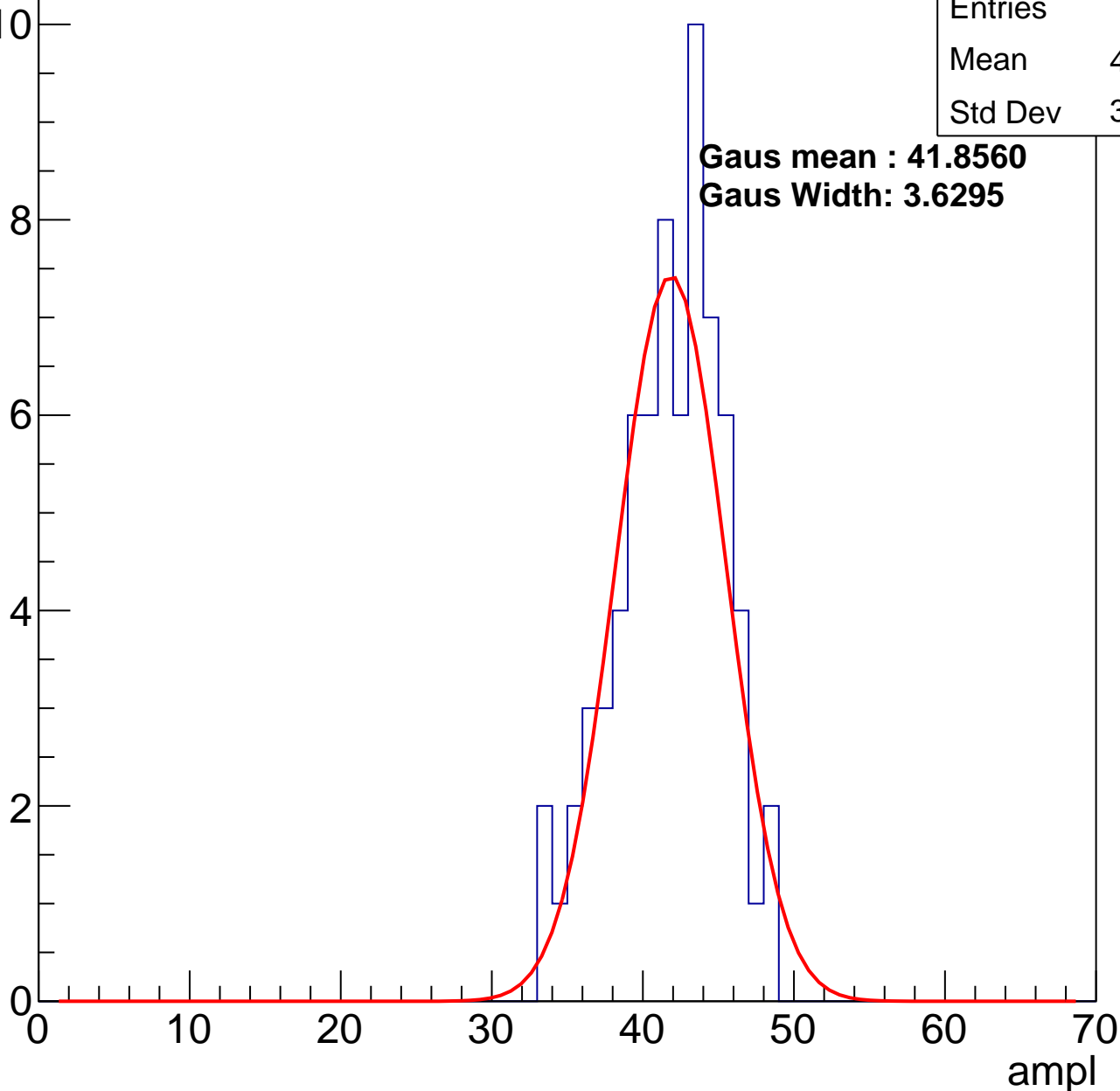
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.27
Std Dev	3.504

Gaus mean : 41.8560

Gaus Width: 3.6295

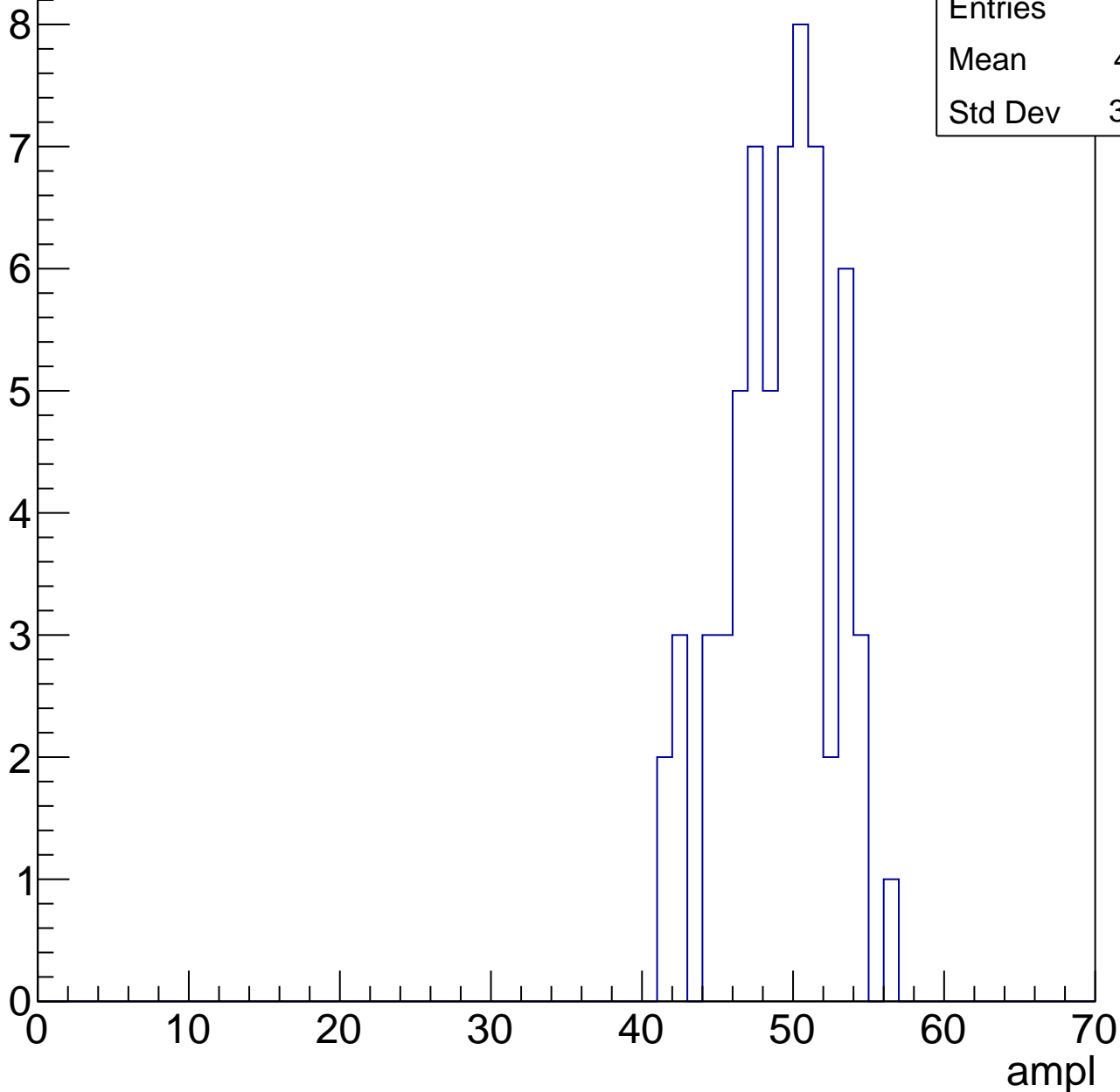


B1L102S, U6-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.61
Std Dev	3.456

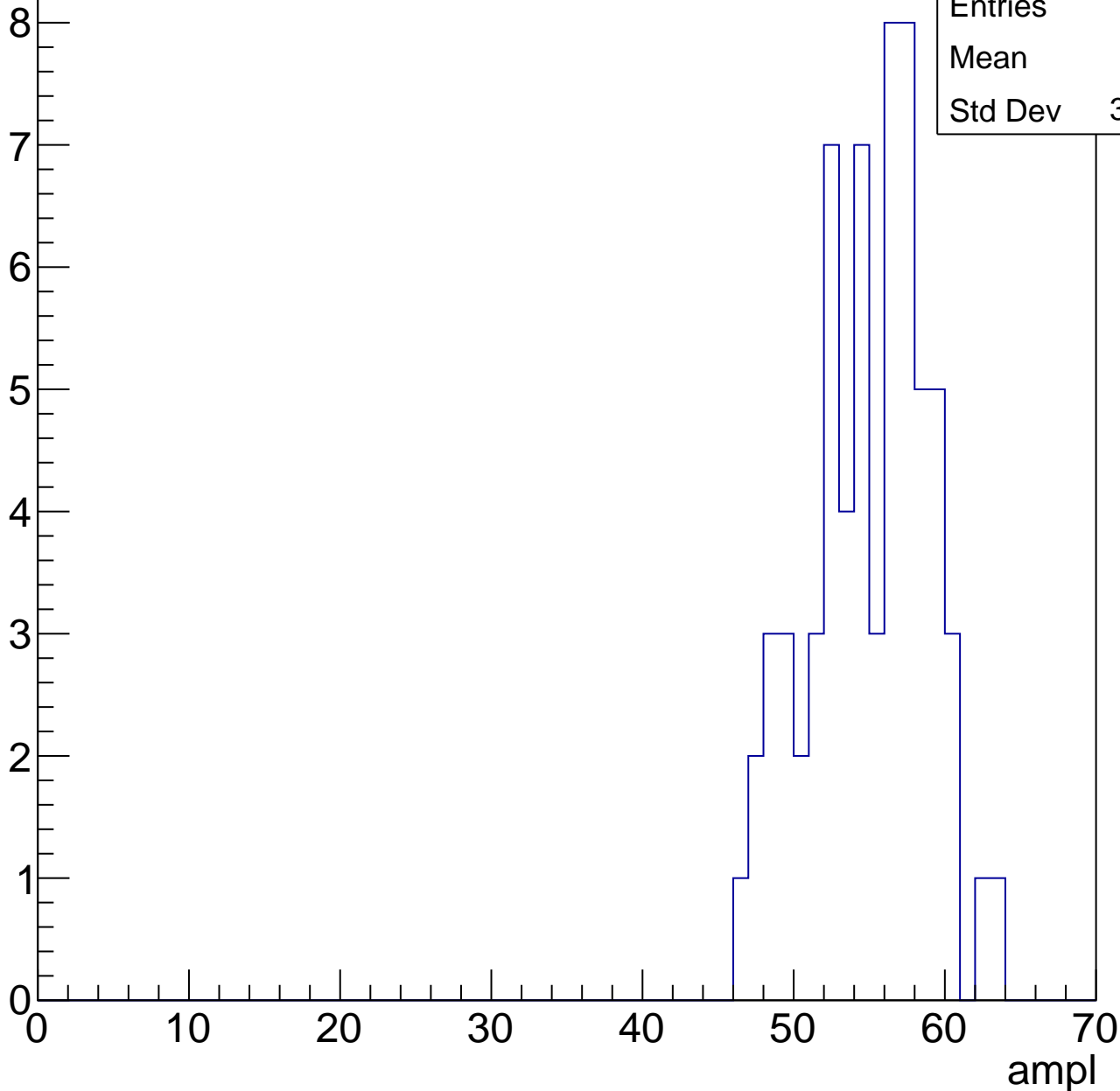


B1L102S, U6-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

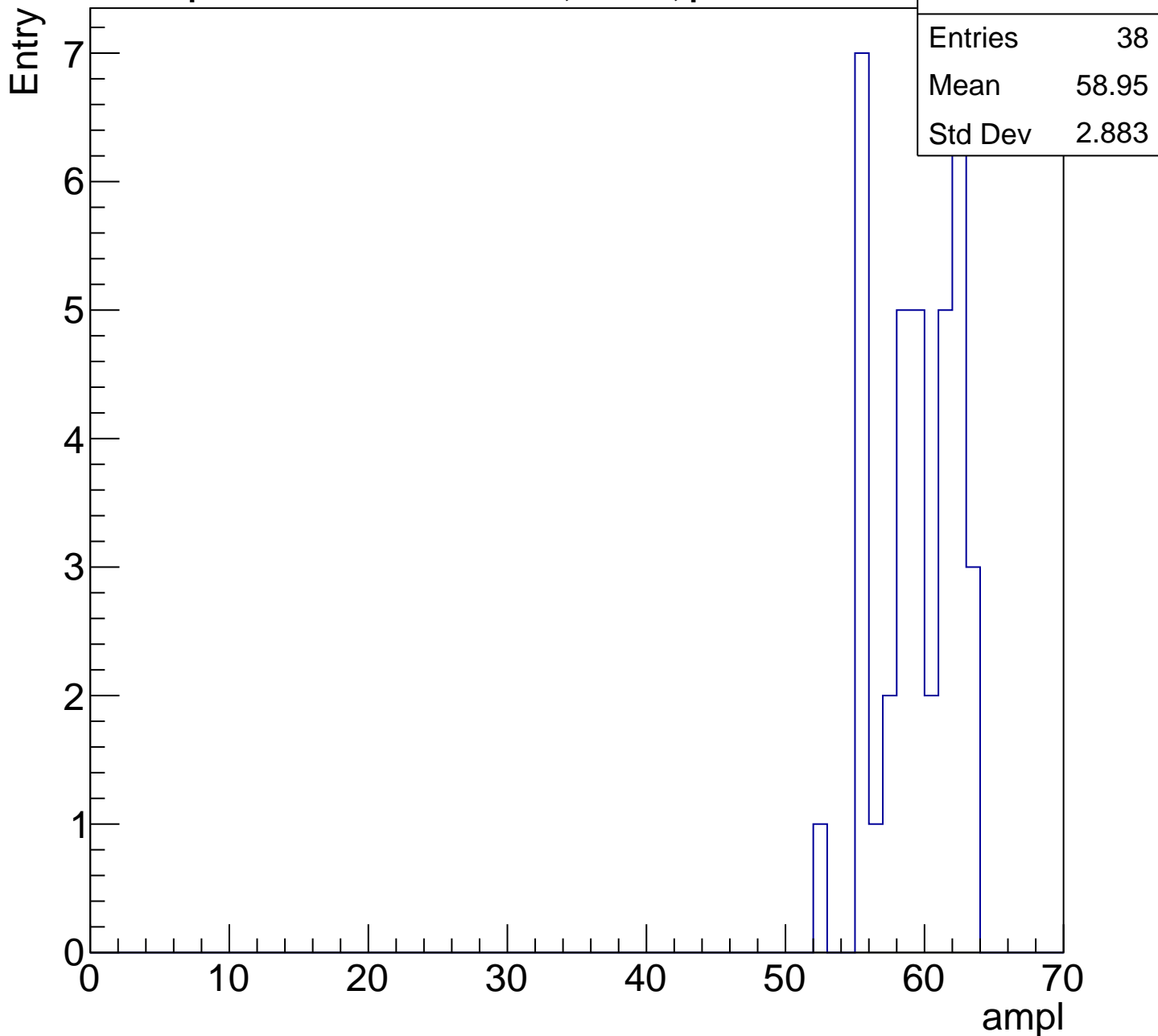
Entry

Entries	66
Mean	54.5
Std Dev	3.862



B1L102S, U6-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

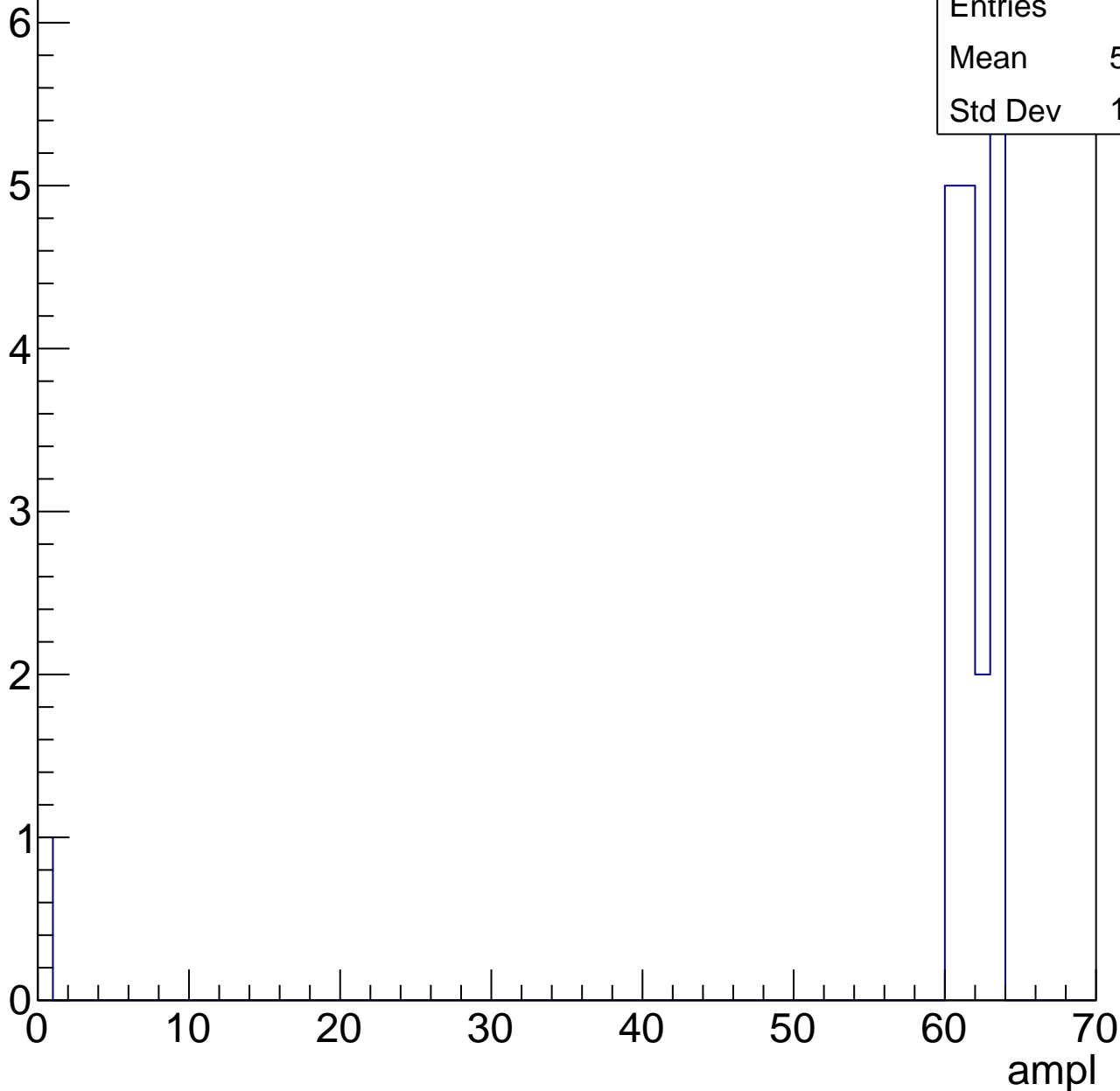


B1L102S, U6-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.26
Std Dev	13.78



B1L102S, U6-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch89, adc0

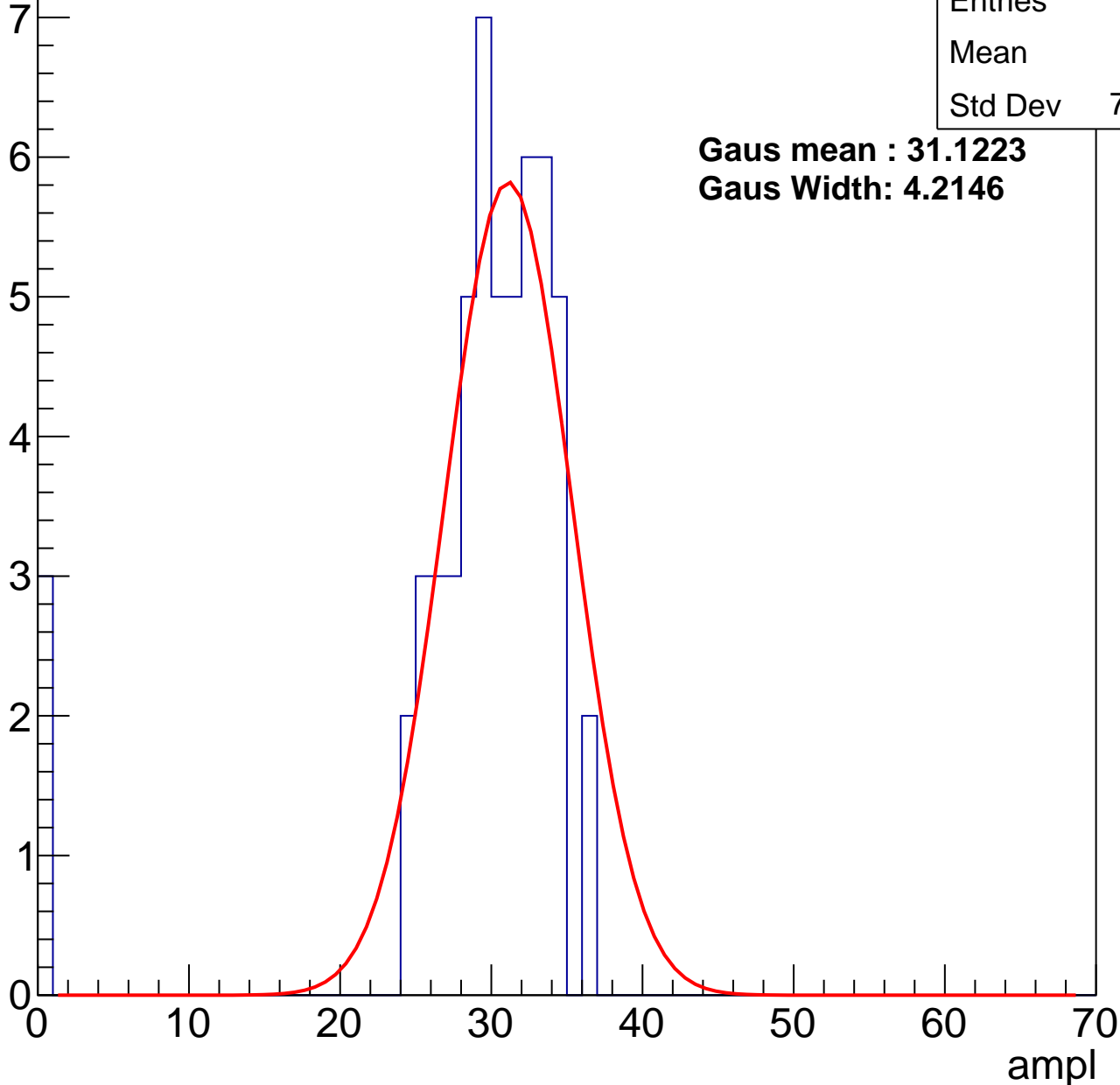
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	28.4
Std Dev	7.438

Gaus mean : 31.1223

Gaus Width: 4.2146



B1L102S, U6-ch89, adc1

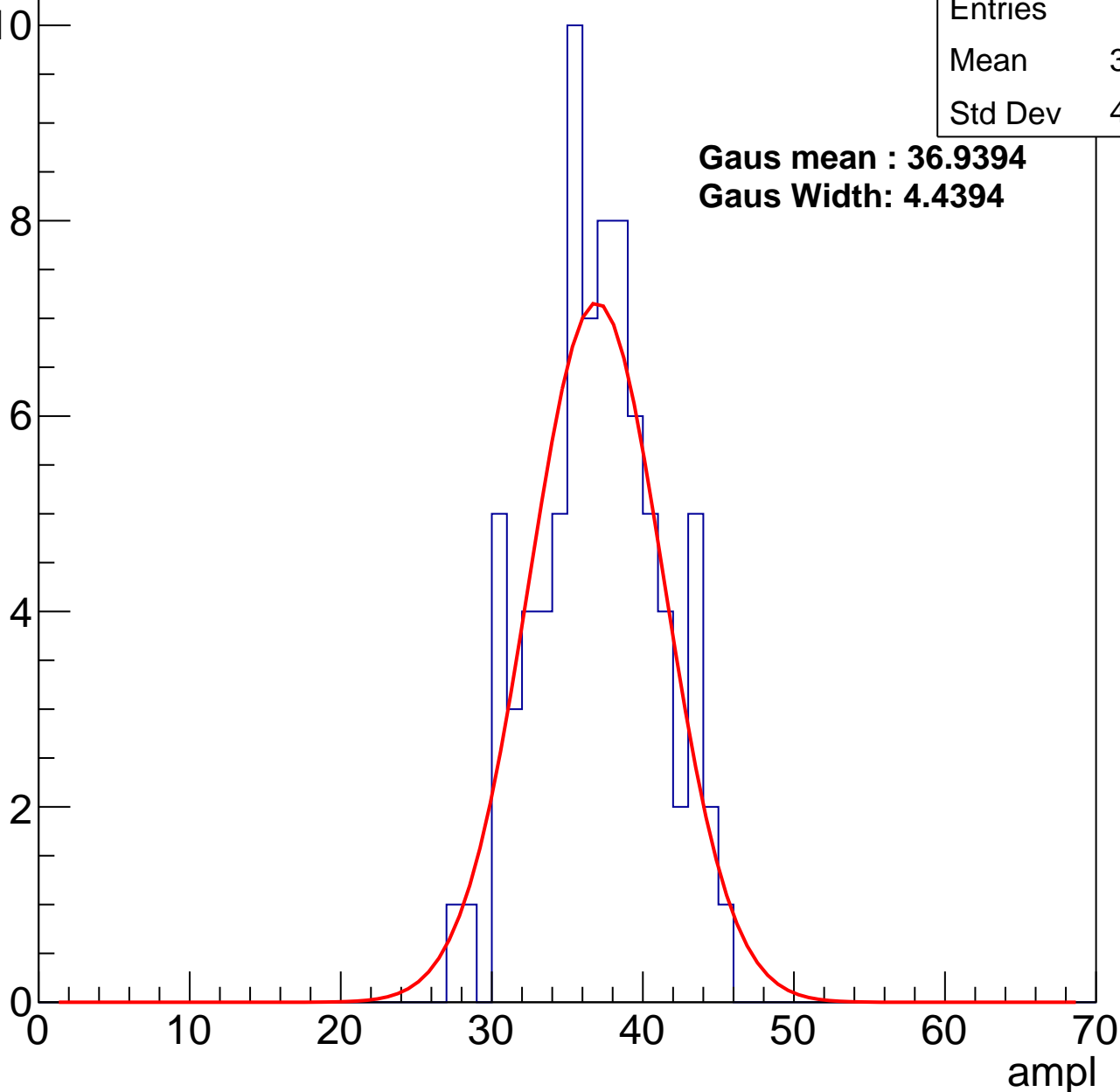
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	36.54
Std Dev	4.016

Gaus mean : 36.9394

Gaus Width: 4.4394



B1L102S, U6-ch89, adc2

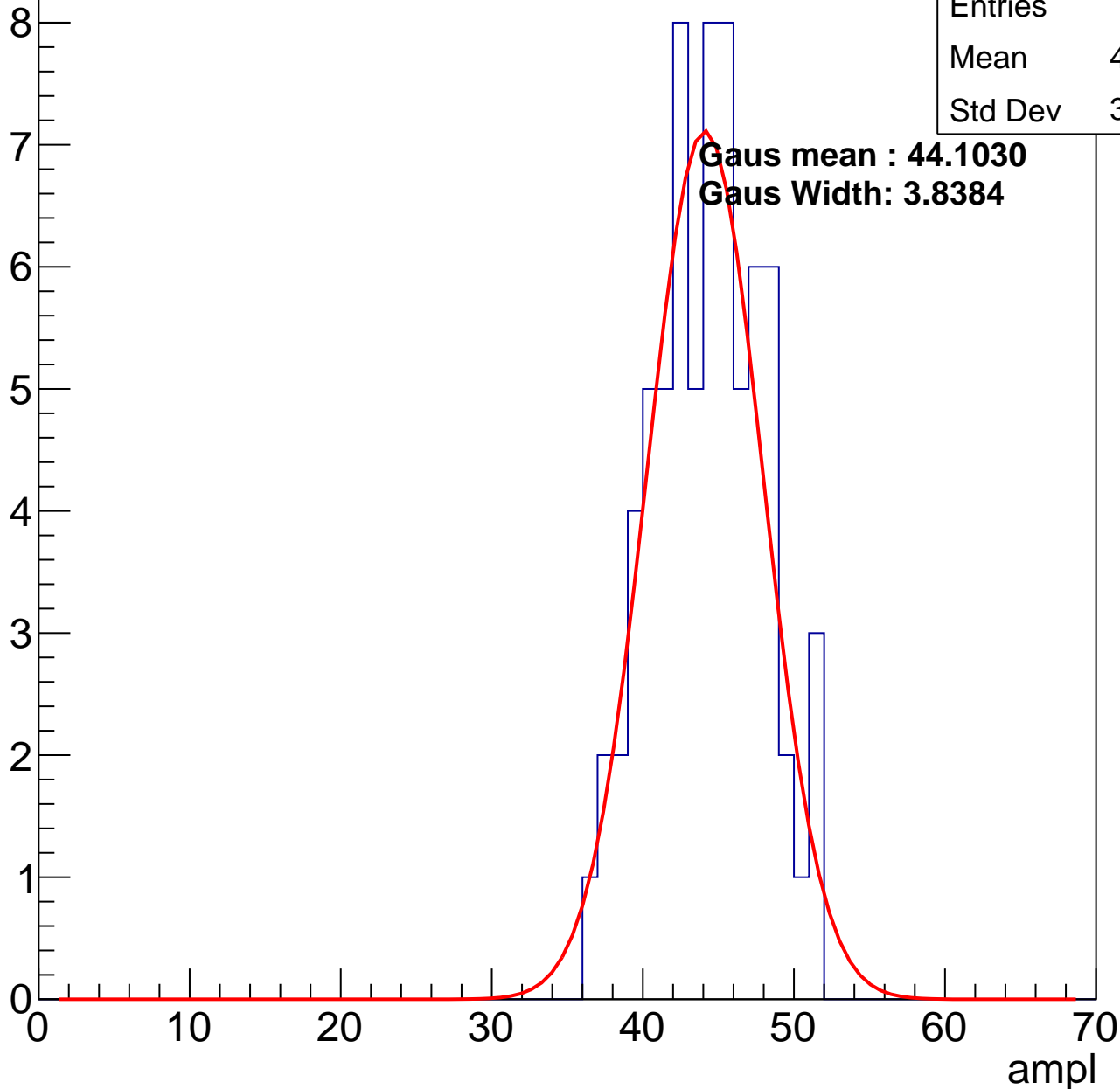
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	43.82
Std Dev	3.569

Gaus mean : 44.1030

Gaus Width: 3.8384

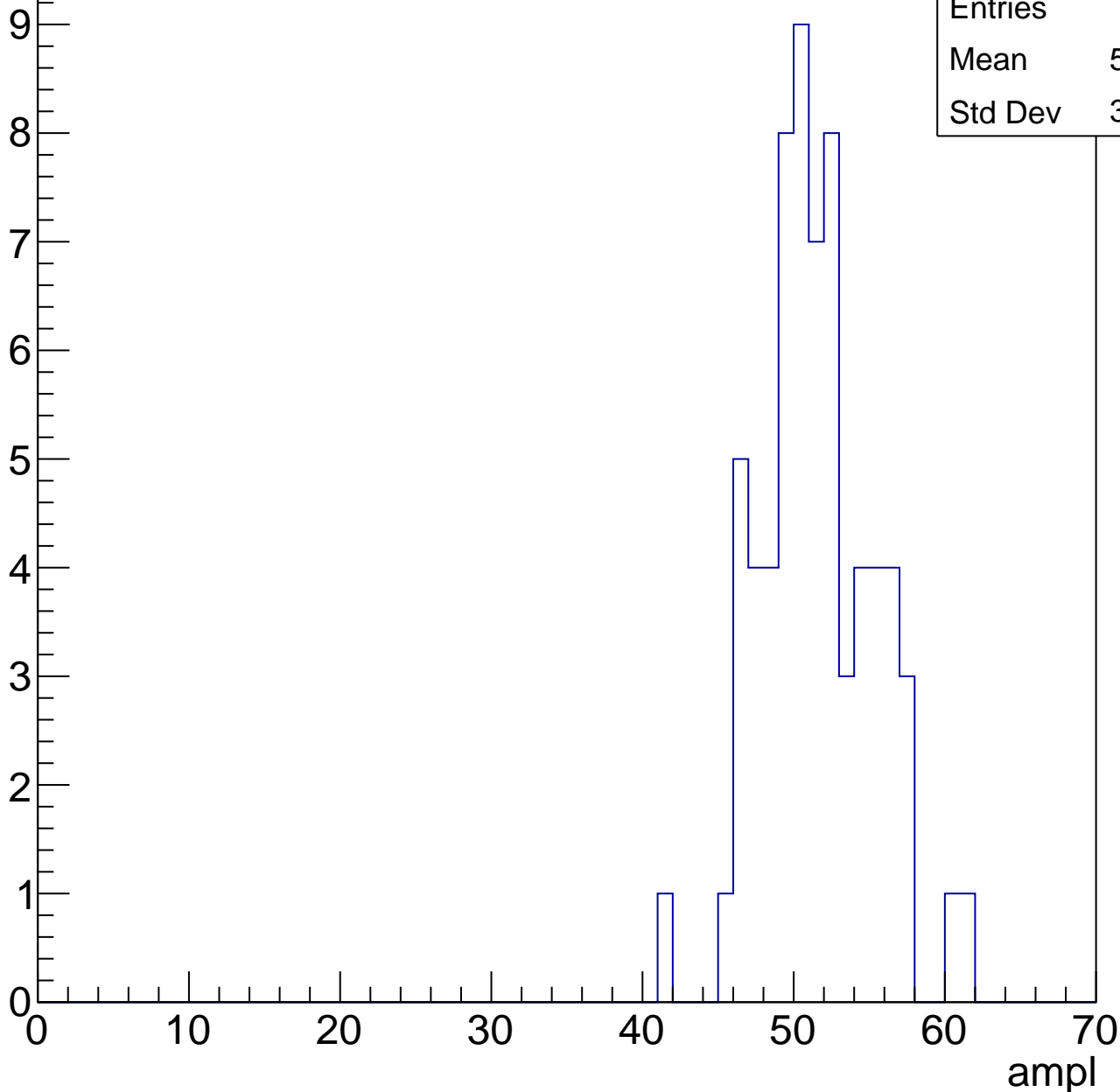


B1L102S, U6-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	51.07
Std Dev	3.687

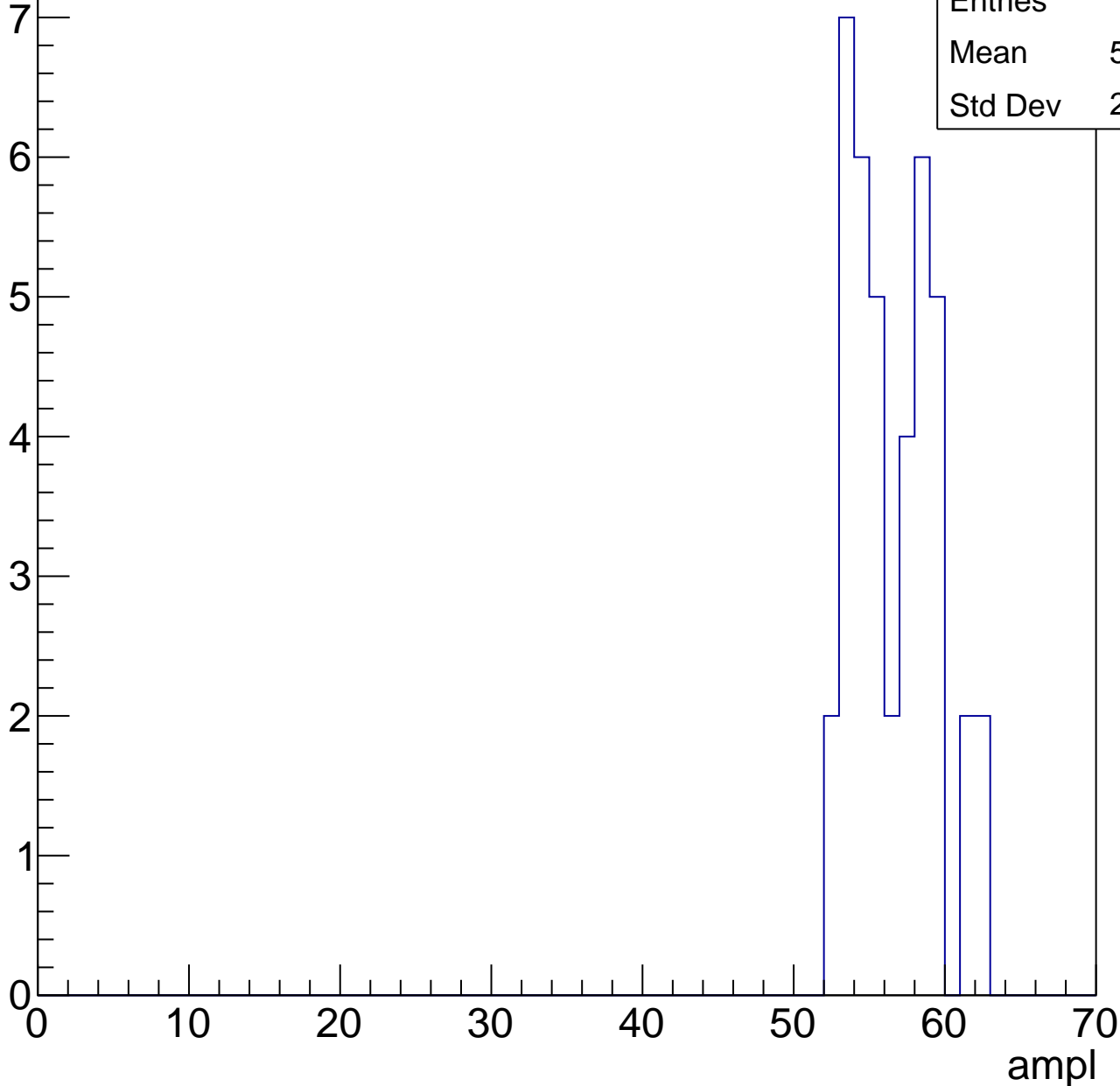


B1L102S, U6-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	56.17
Std Dev	2.784

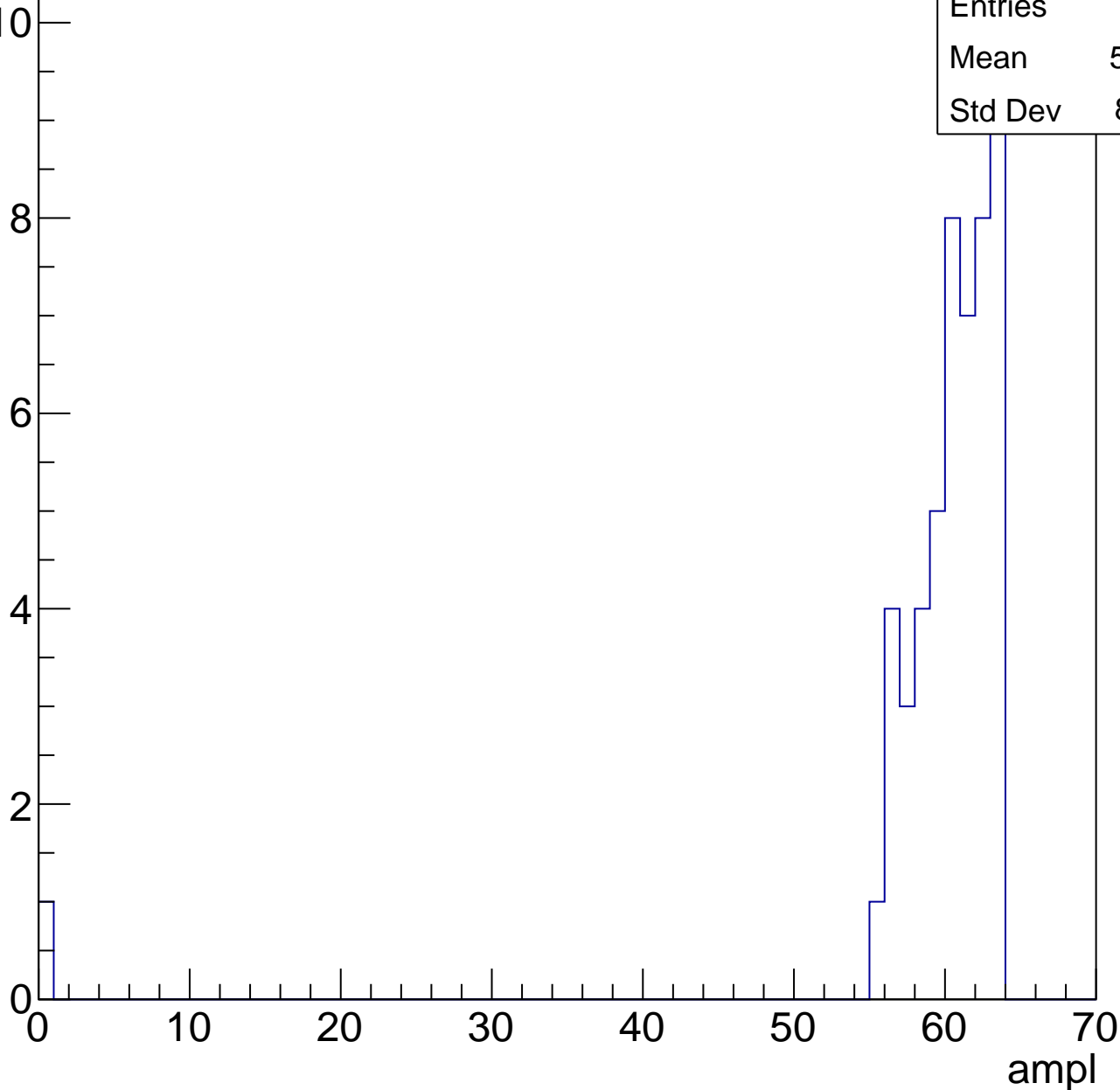


B1L102S, U6-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	59.02
Std Dev	8.651



B1L102S, U6-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	11
Std Dev	11

B1L102S, U6-ch90, adc0

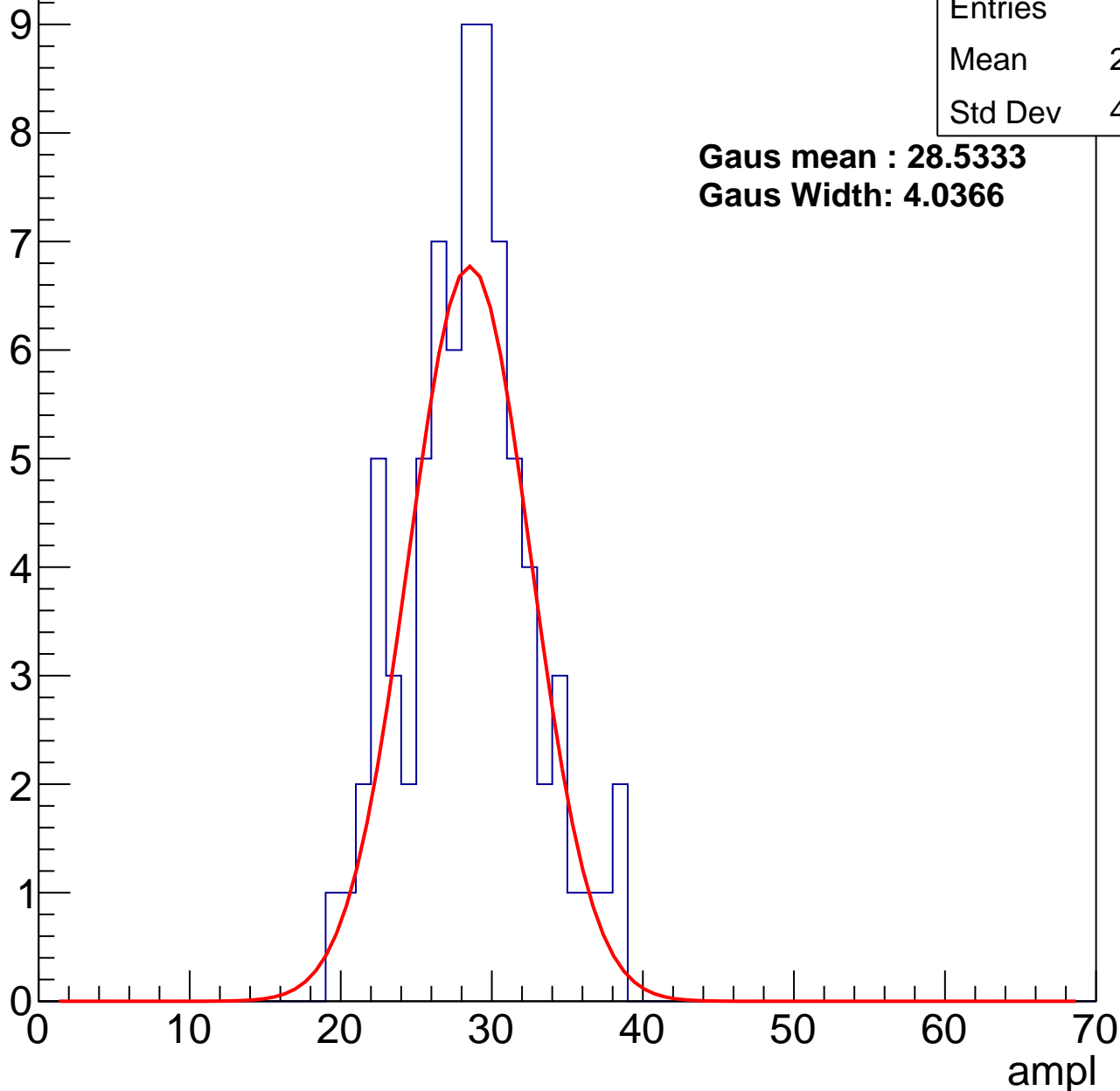
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	28.09
Std Dev	4.146

Gaus mean : 28.5333

Gaus Width: 4.0366



B1L102S, U6-ch90, adc1

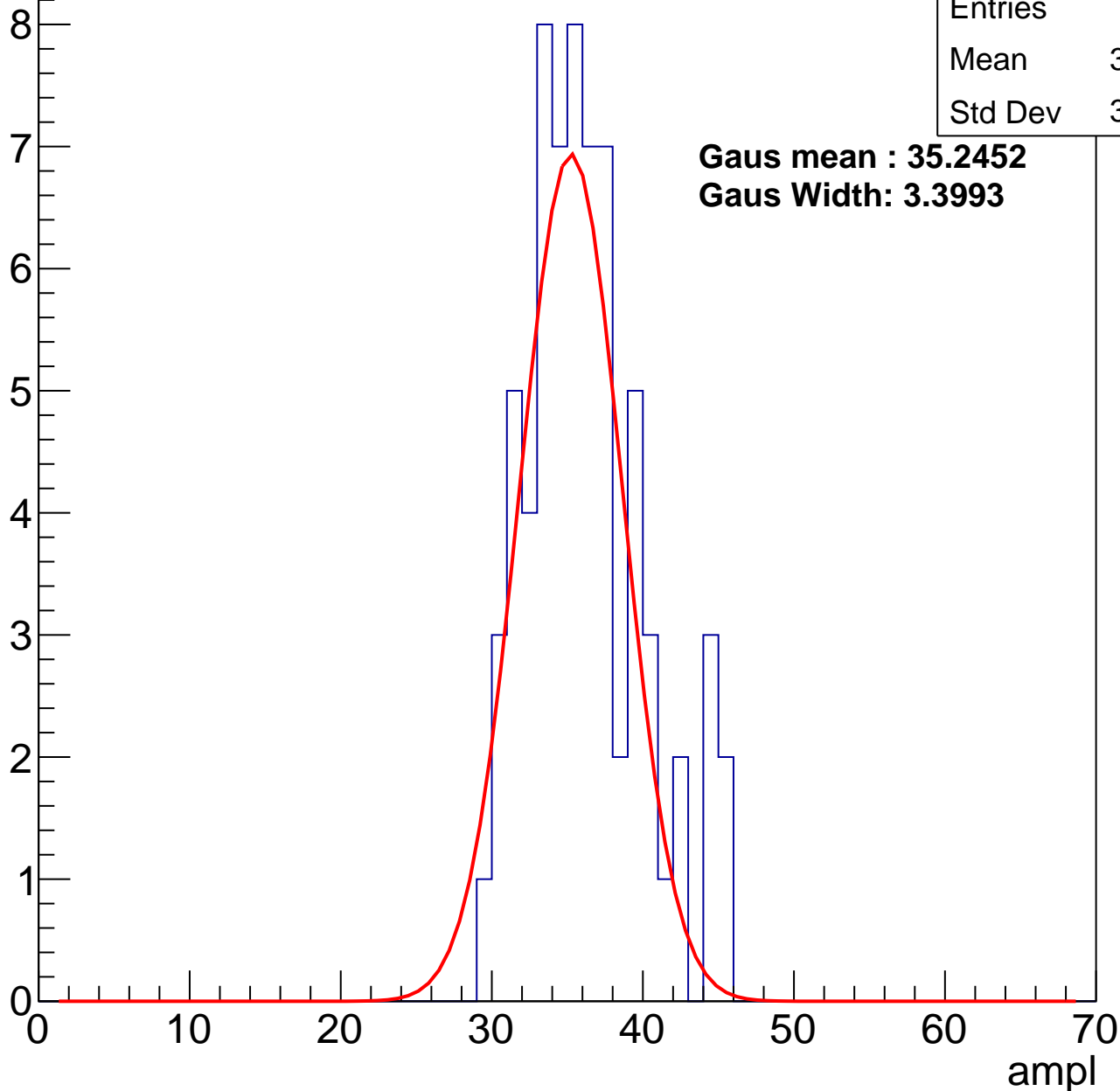
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.78
Std Dev	3.853

Gaus mean : 35.2452

Gaus Width: 3.3993



B1L102S, U6-ch90, adc2

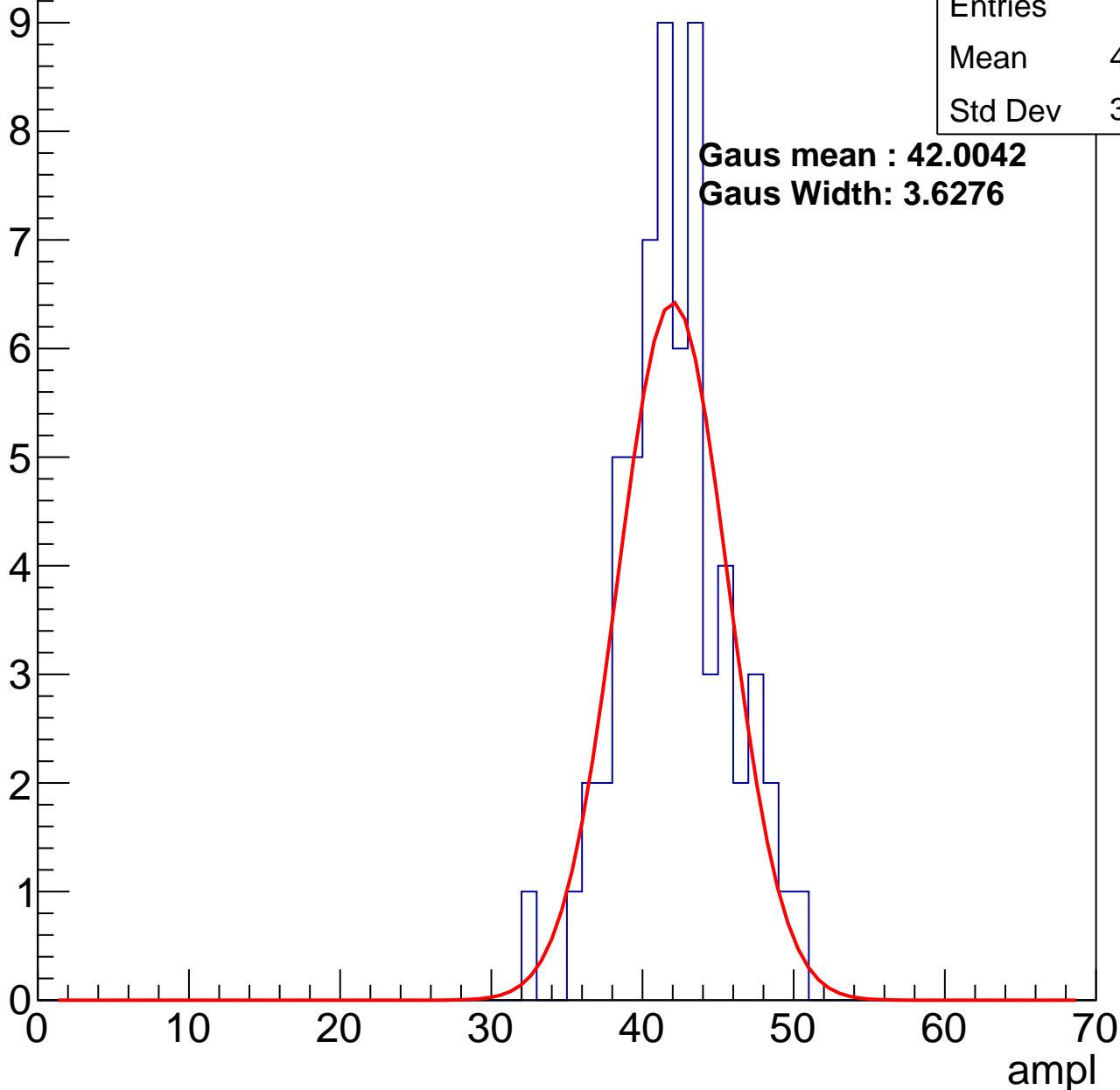
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.68
Std Dev	3.509

Gaus mean : 42.0042

Gaus Width: 3.6276

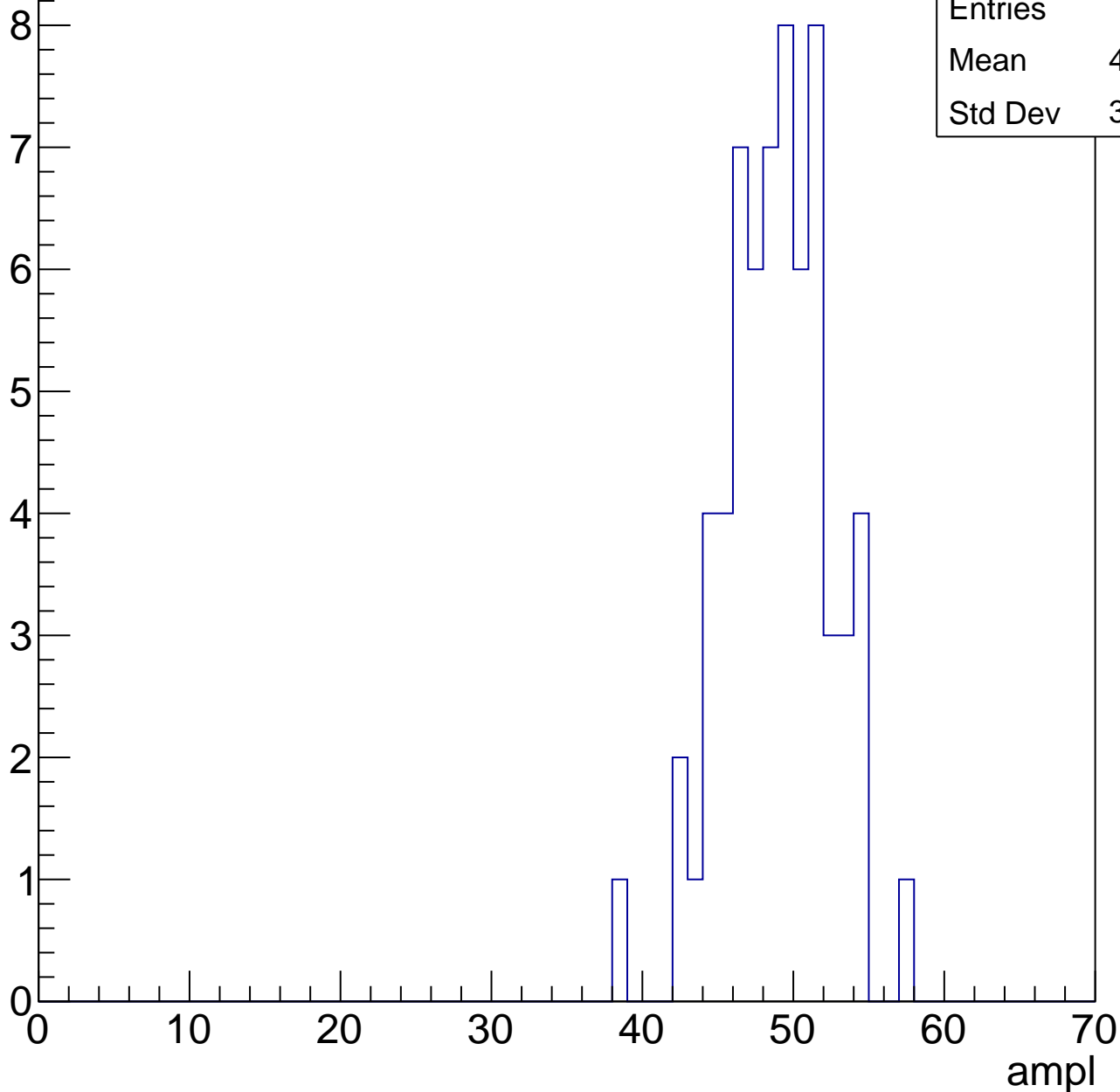


B1L102S, U6-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.45
Std Dev	3.442

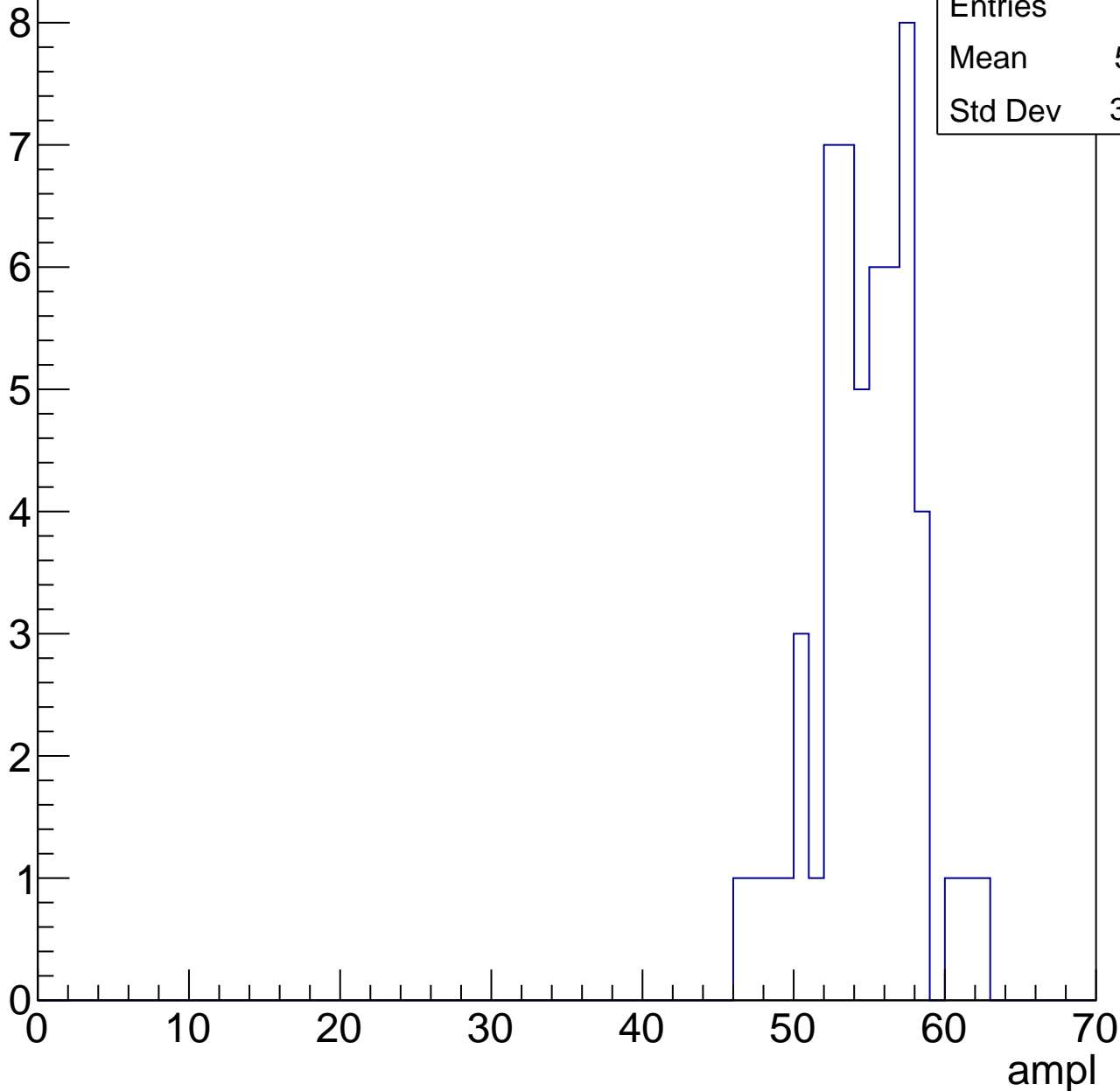


B1L102S, U6-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	54.31
Std Dev	3.265

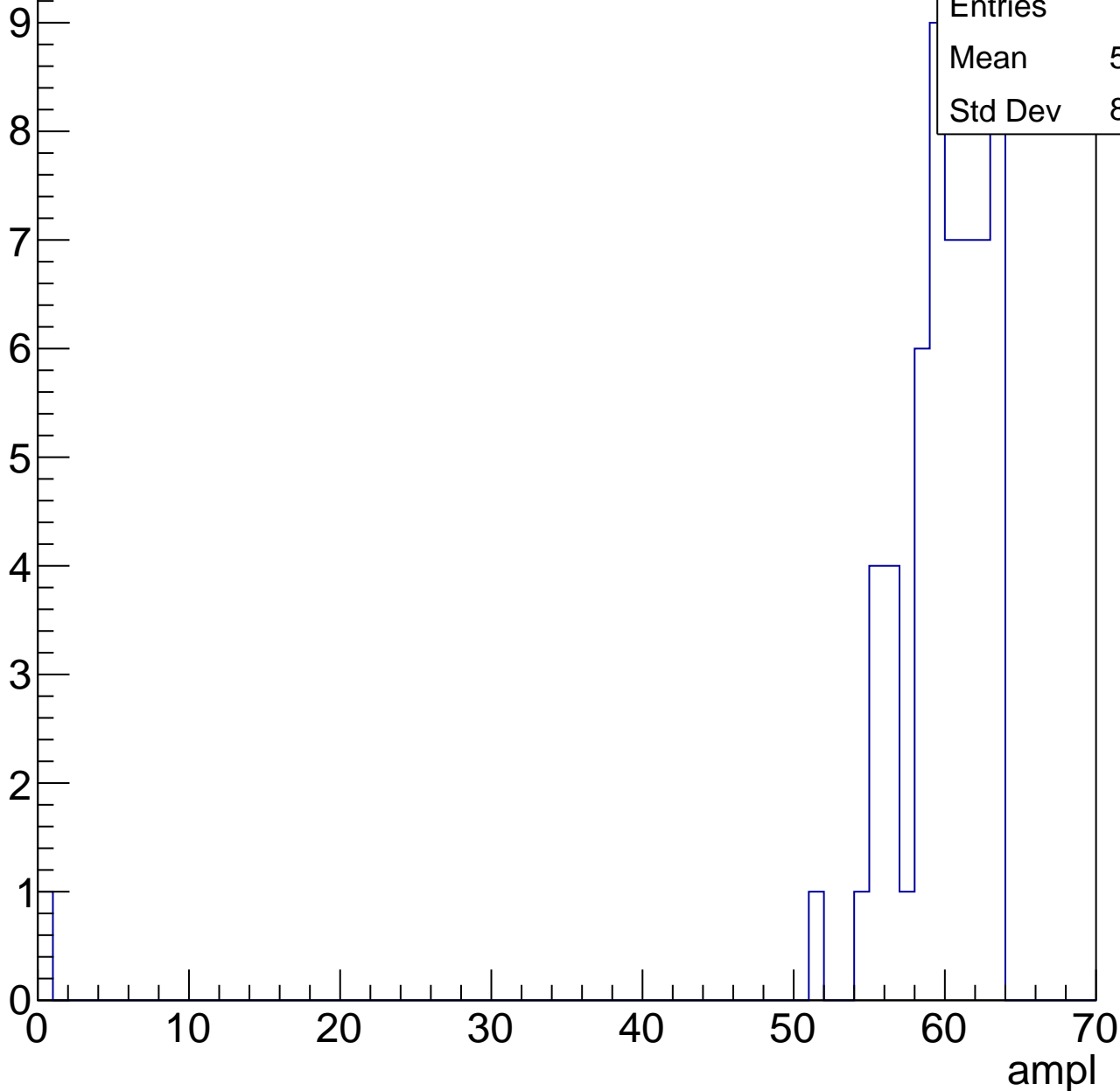


B1L102S, U6-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	58.47
Std Dev	8.278



B1L102S, U6-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61
Std Dev	1.225

ampl

0 10 20 30 40 50 60 70

B1L102S, U6-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch91, adc0

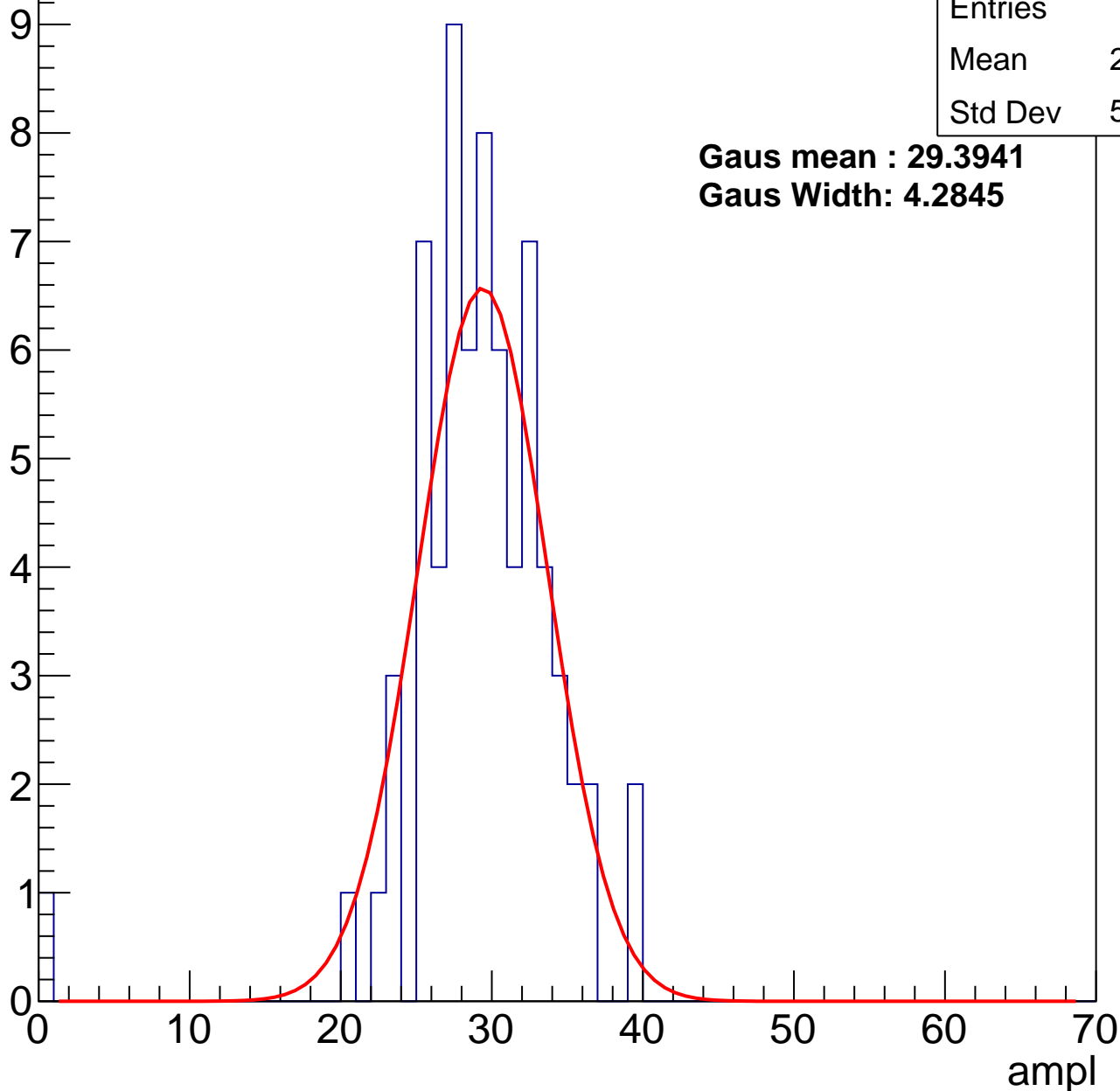
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.79
Std Dev	5.154

Gaus mean : 29.3941

Gaus Width: 4.2845



B1L102S, U6-ch91, adc1

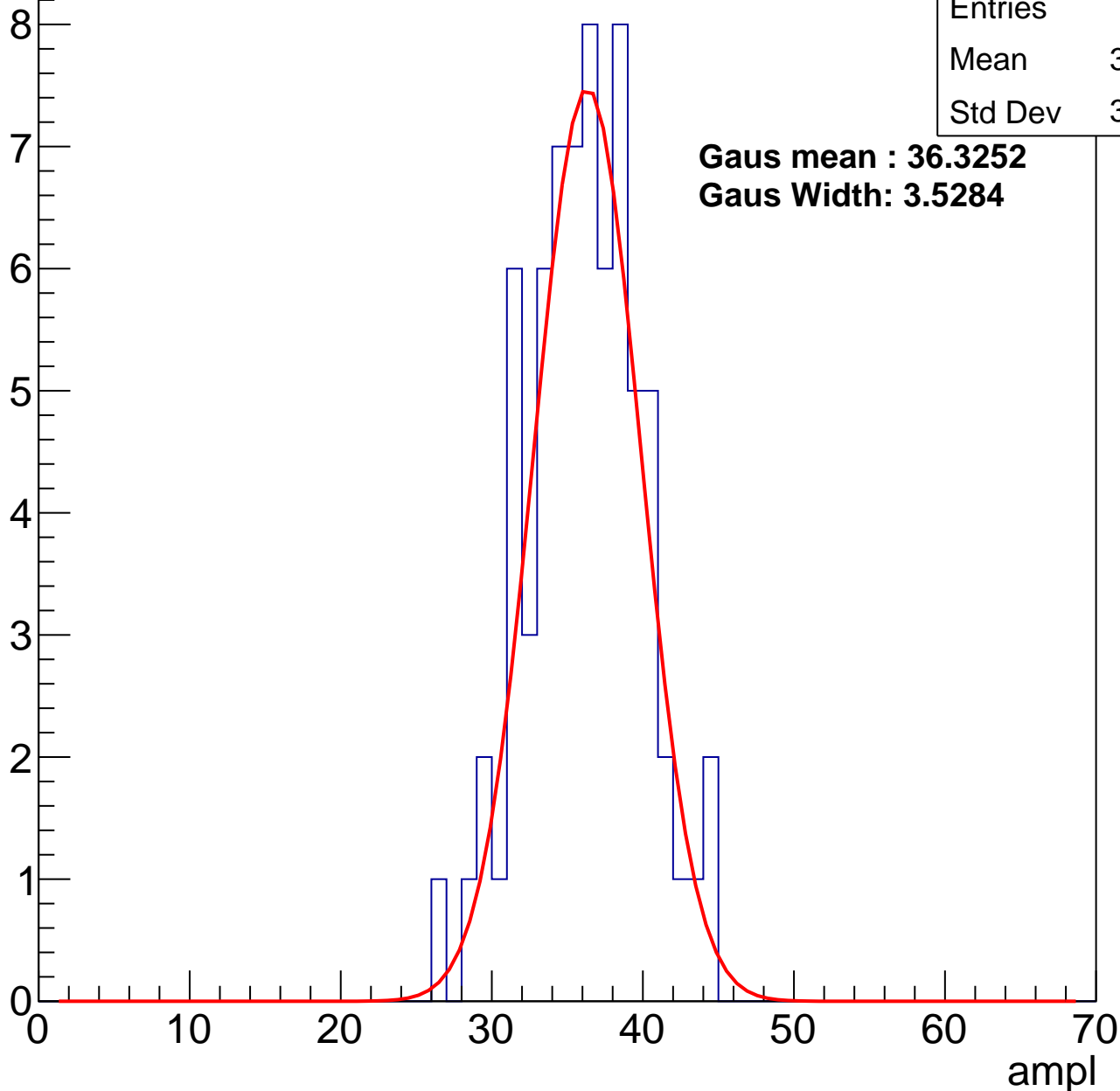
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.68
Std Dev	3.734

Gaus mean : 36.3252

Gaus Width: 3.5284



B1L102S, U6-ch91, adc2

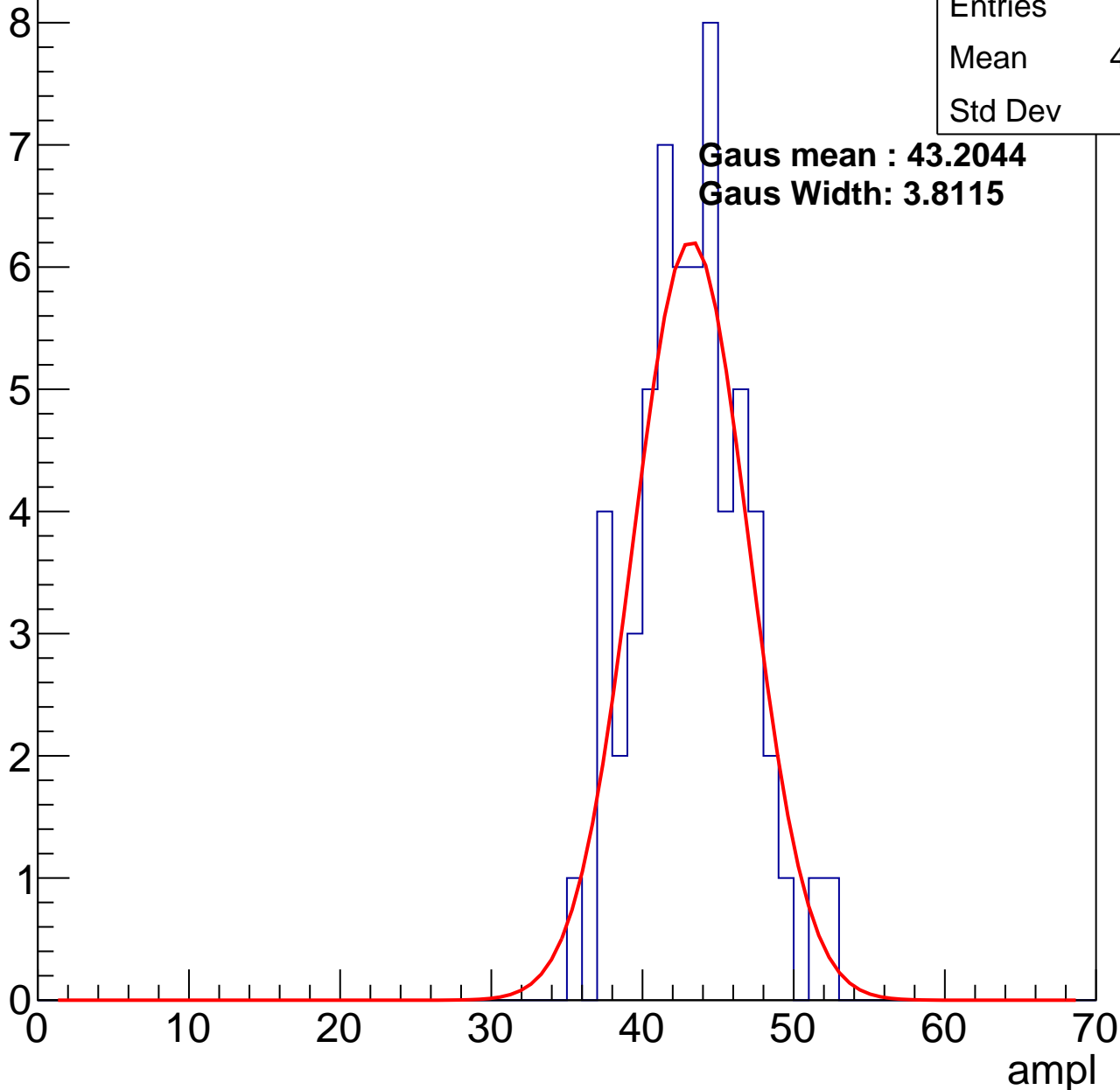
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.85
Std Dev	3.53

Gaus mean : 43.2044

Gaus Width: 3.8115

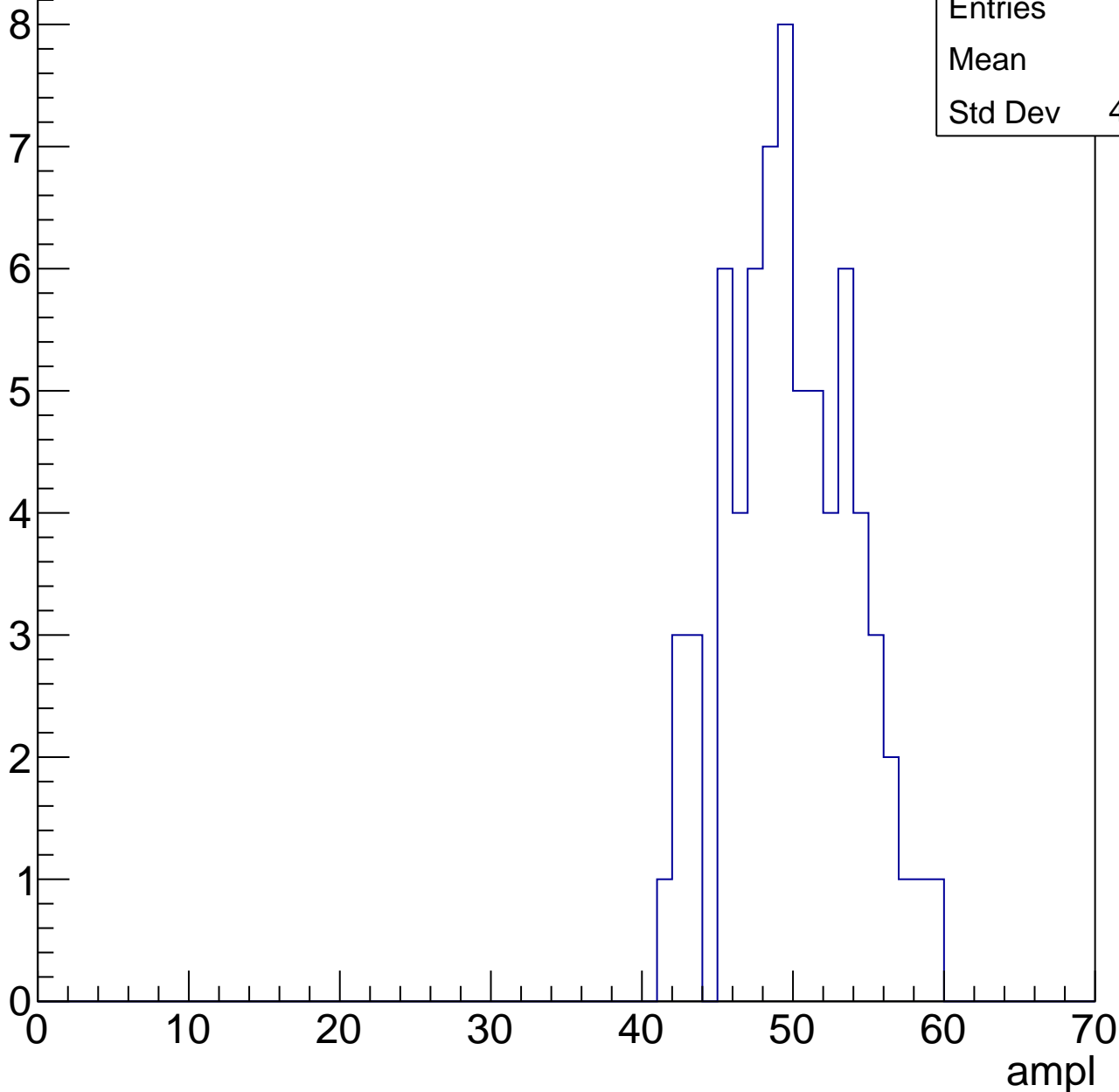


B1L102S, U6-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	49.4
Std Dev	4.104

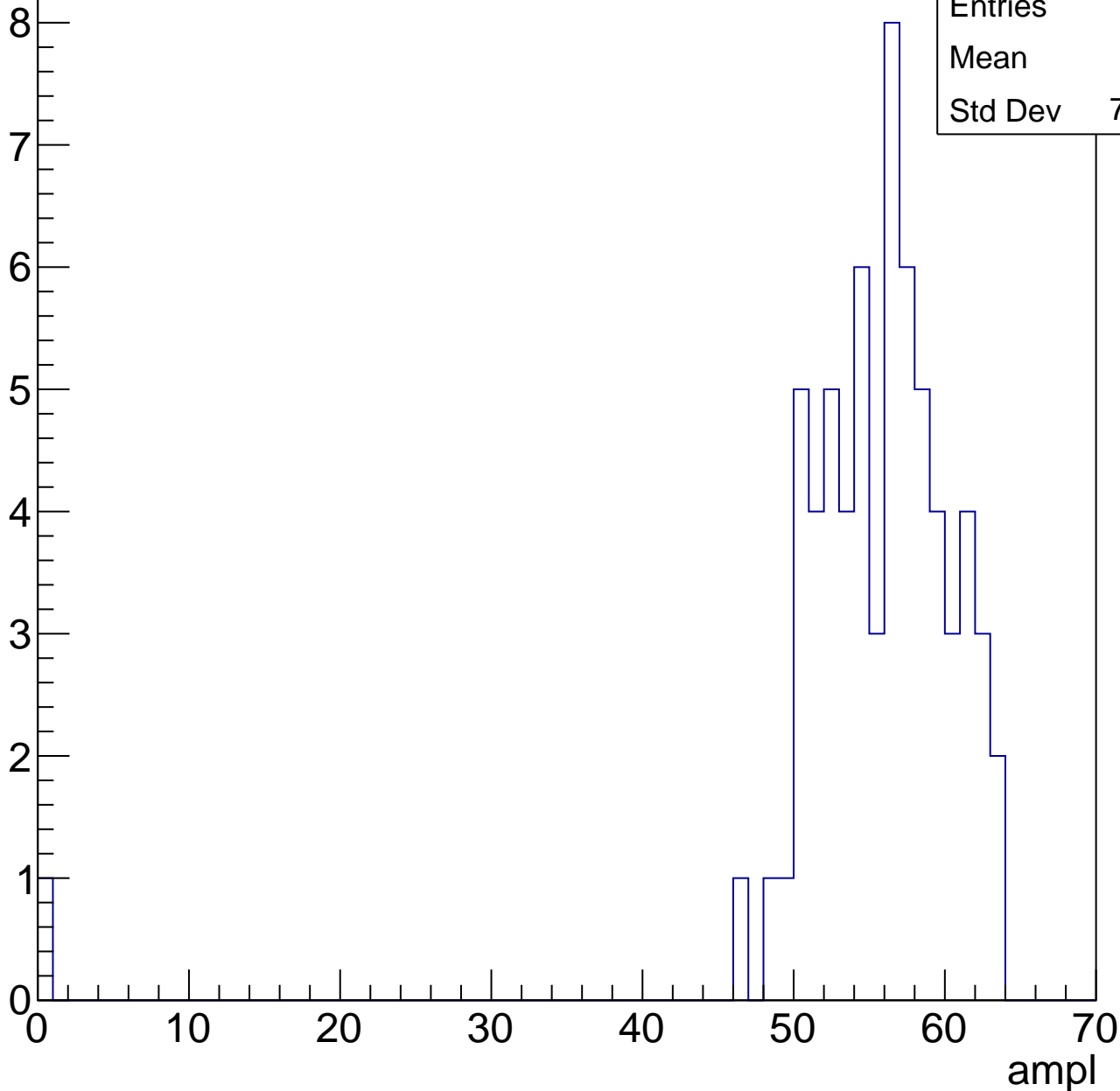


B1L102S, U6-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	54.7
Std Dev	7.859



B1L102S, U6-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

37

Mean

59.68

Std Dev

2.428

0

ampl

0

10

20

30

40

50

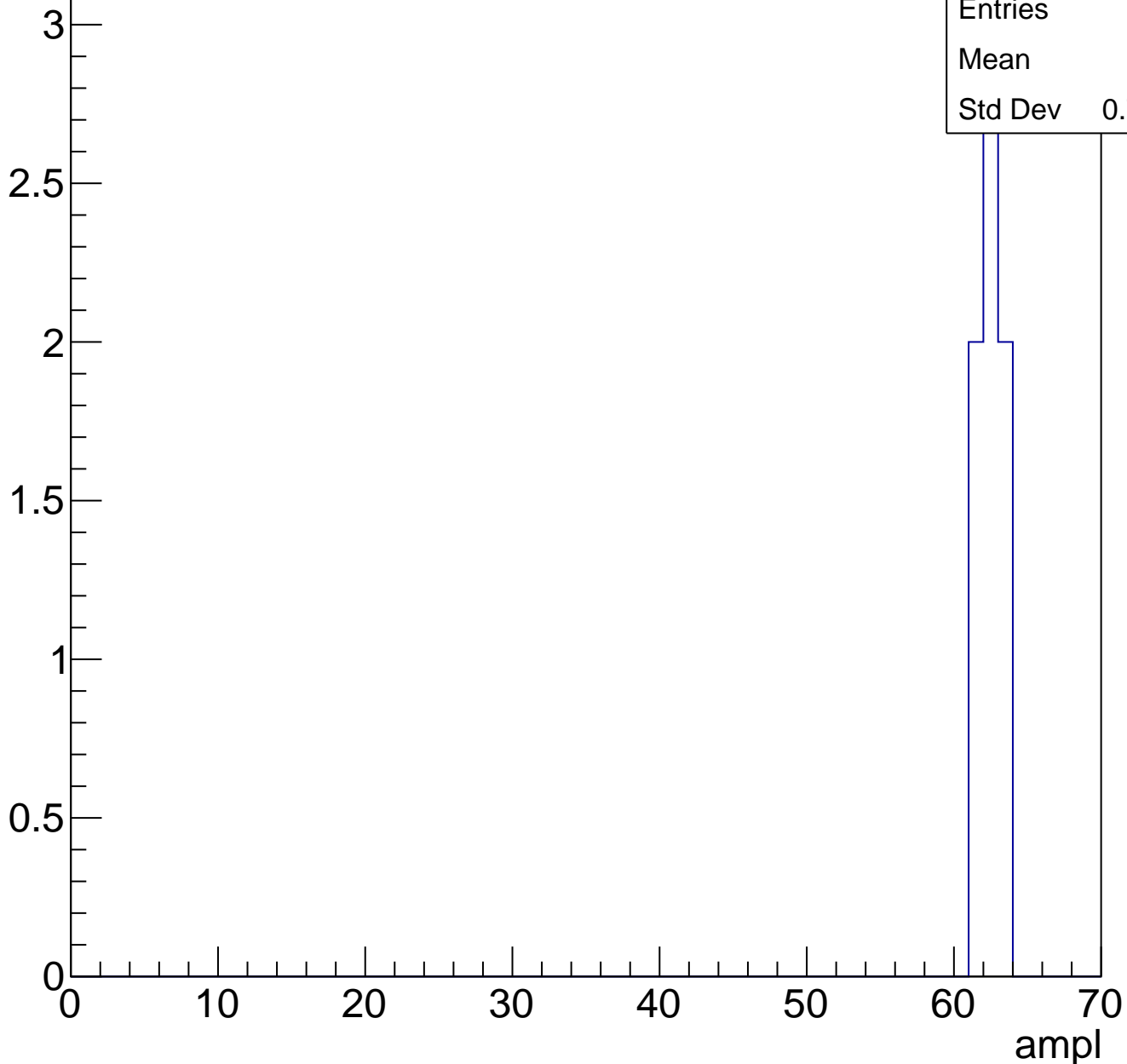
60

70

B1L102S, U6-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch92, adc0

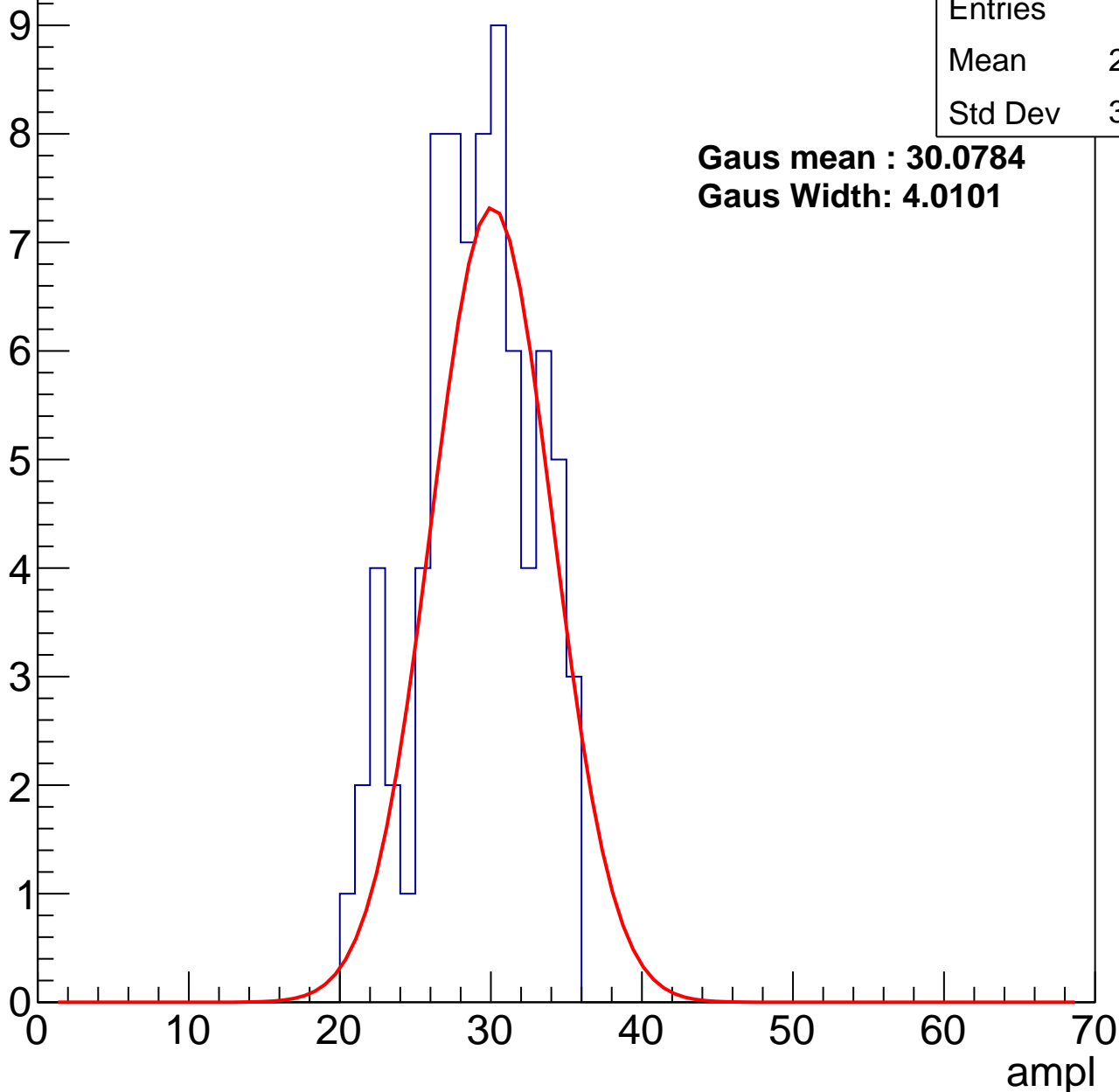
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	28.58
Std Dev	3.688

Gaus mean : 30.0784

Gaus Width: 4.0101



B1L102S, U6-ch92, adc1

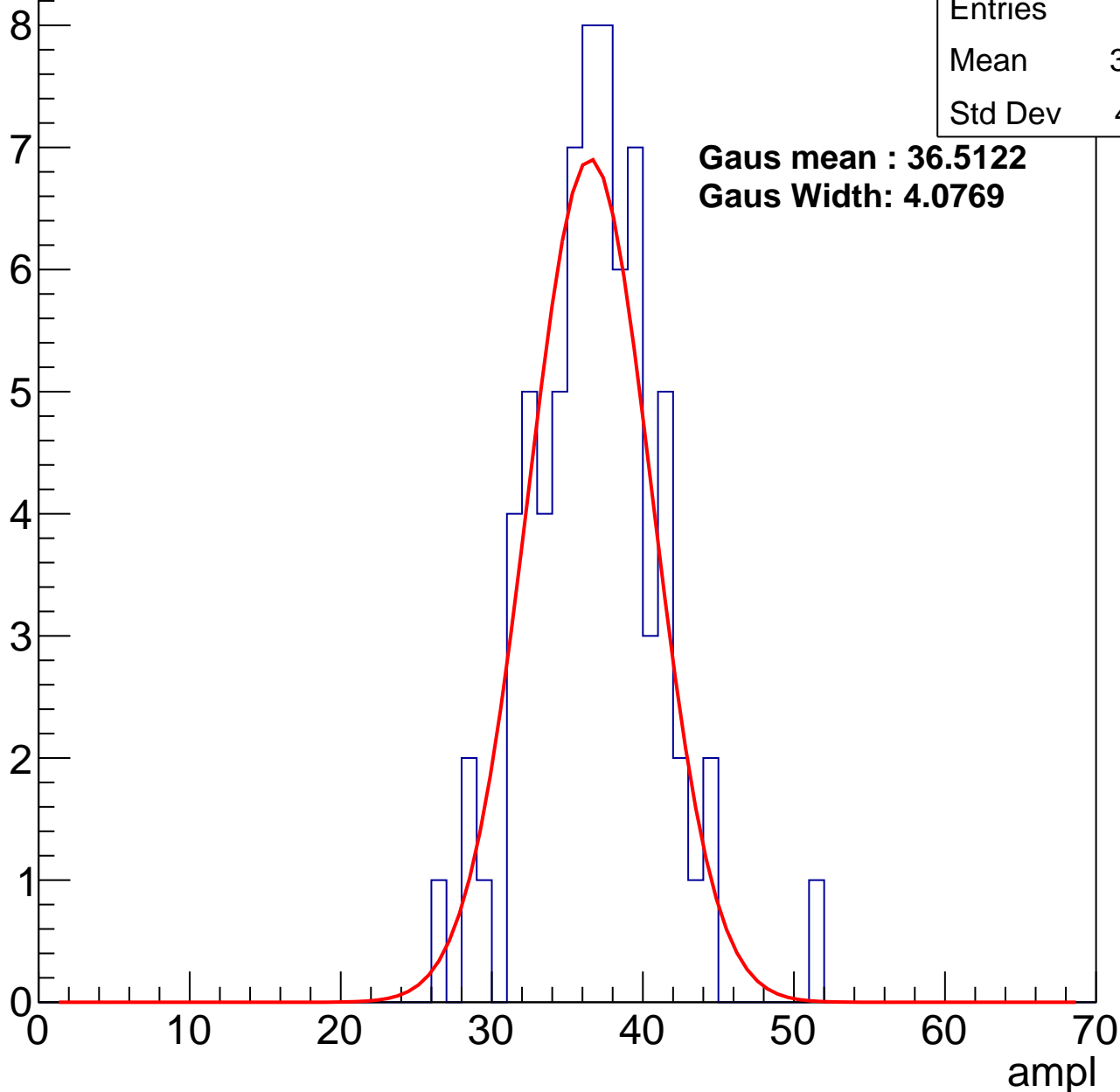
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.36
Std Dev	4.171

Gaus mean : 36.5122

Gaus Width: 4.0769



B1L102S, U6-ch92, adc2

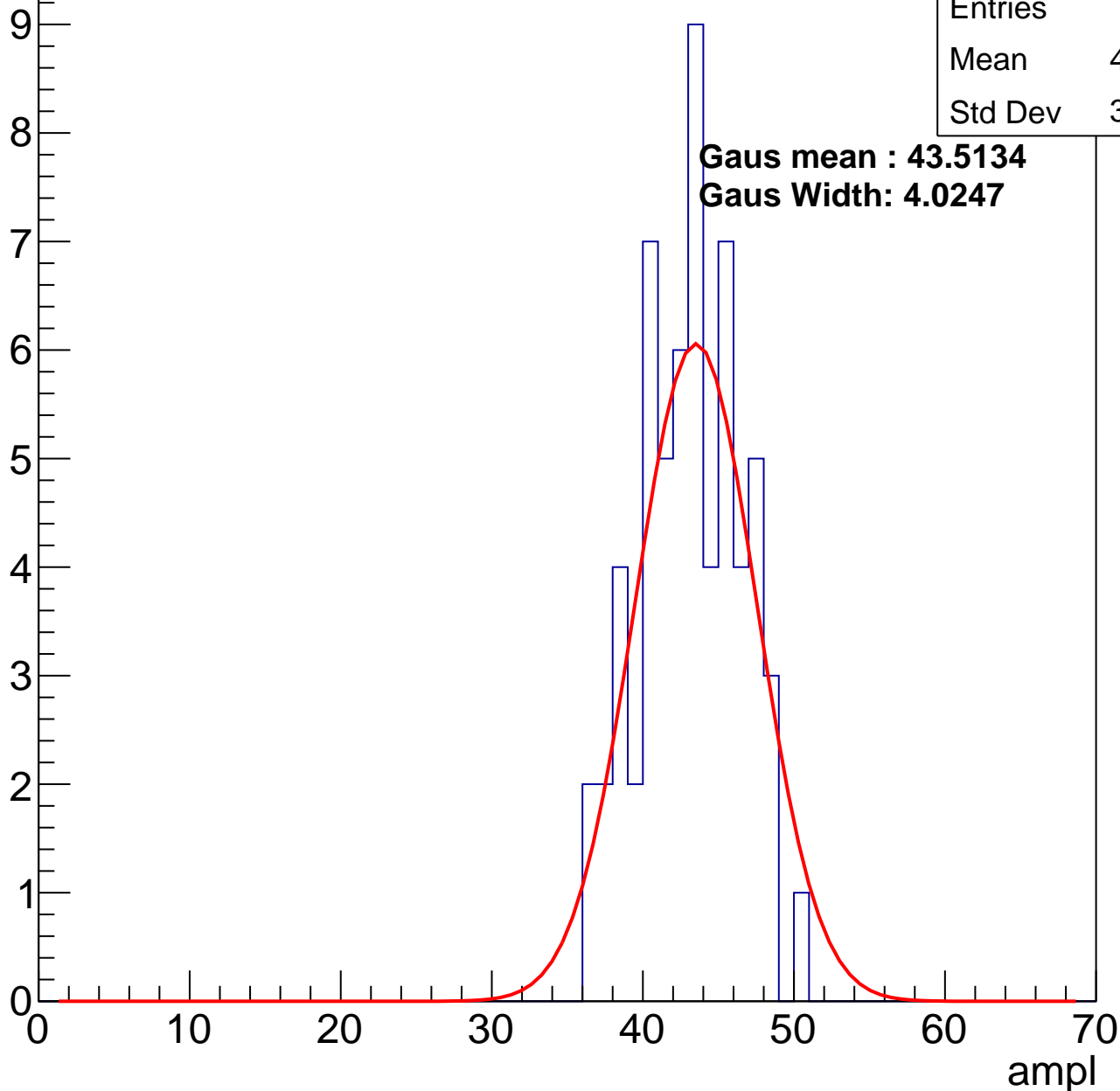
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.69
Std Dev	3.282

Gaus mean : 43.5134

Gaus Width: 4.0247

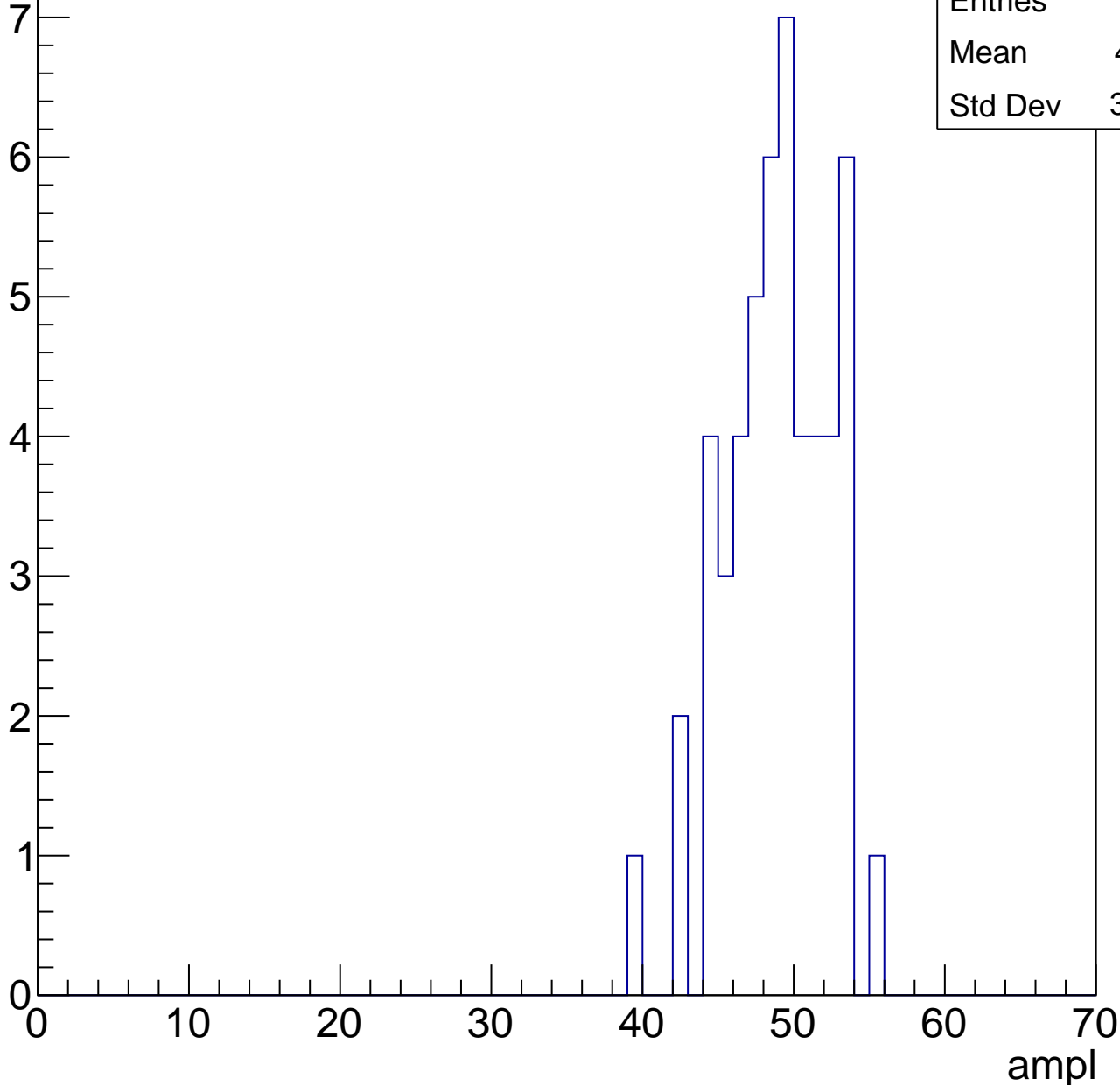


B1L102S, U6-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	48.41
Std Dev	3.368

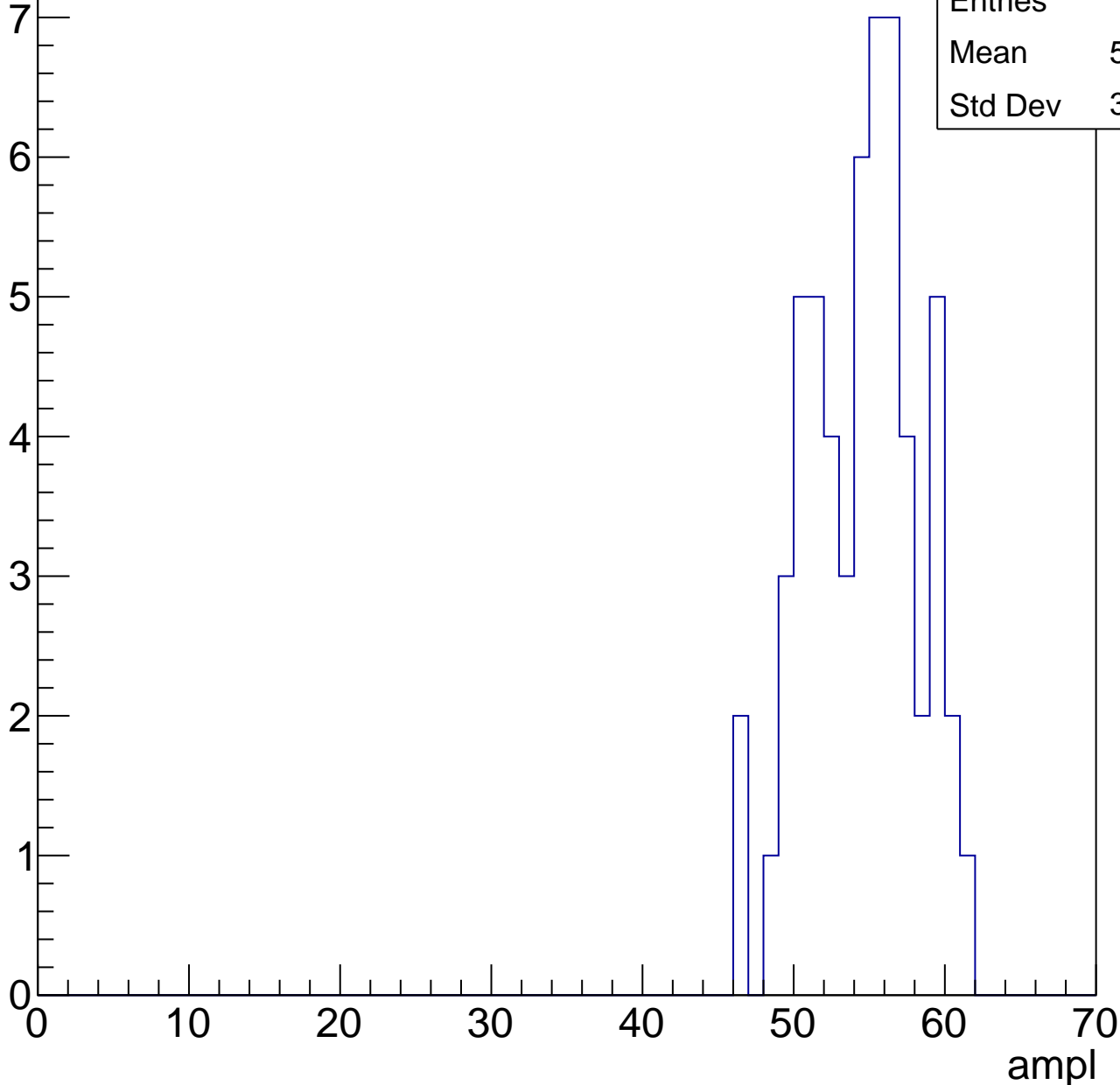


B1L102S, U6-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.04
Std Dev	3.583

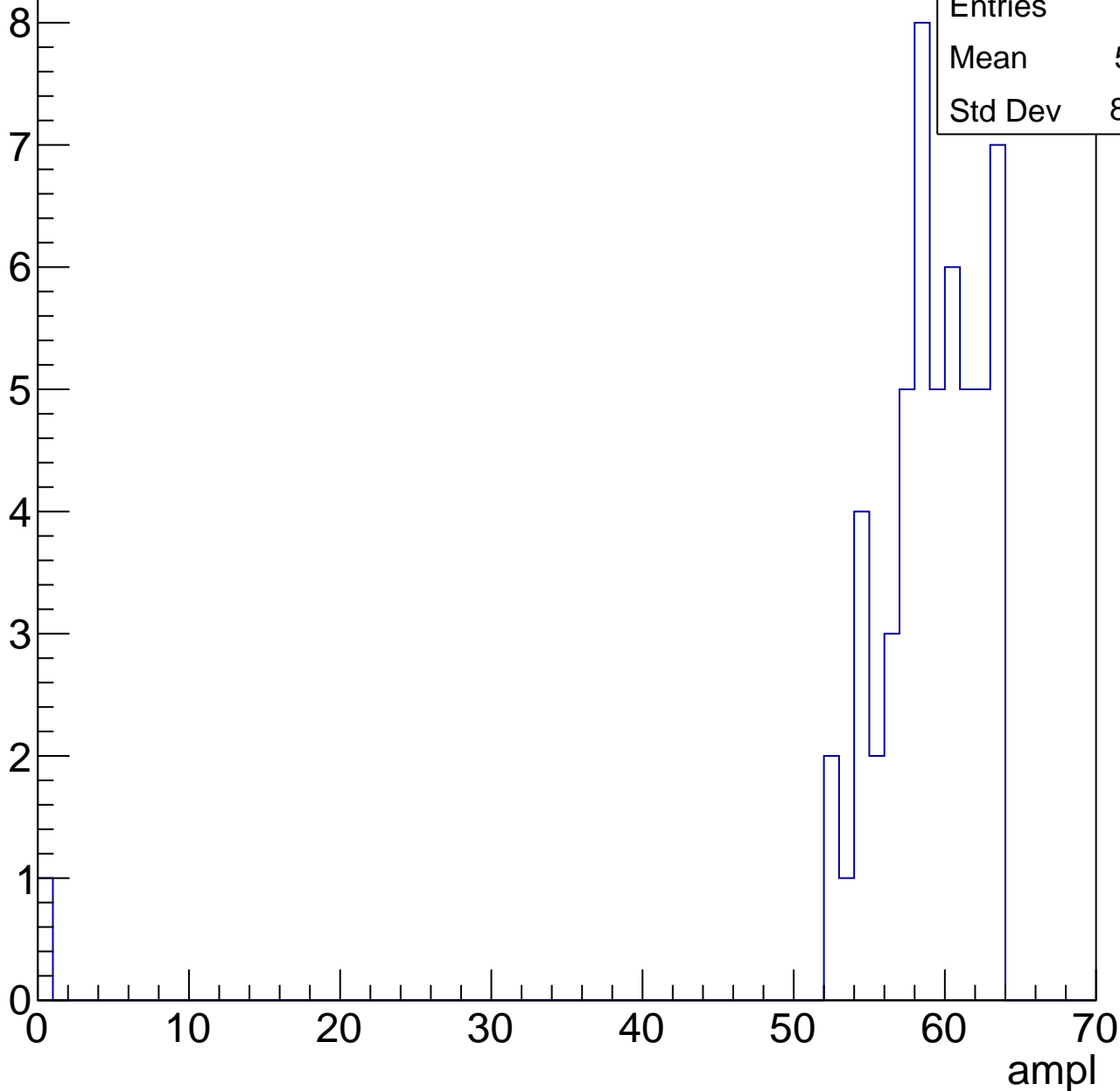


B1L102S, U6-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.61
Std Dev	8.477

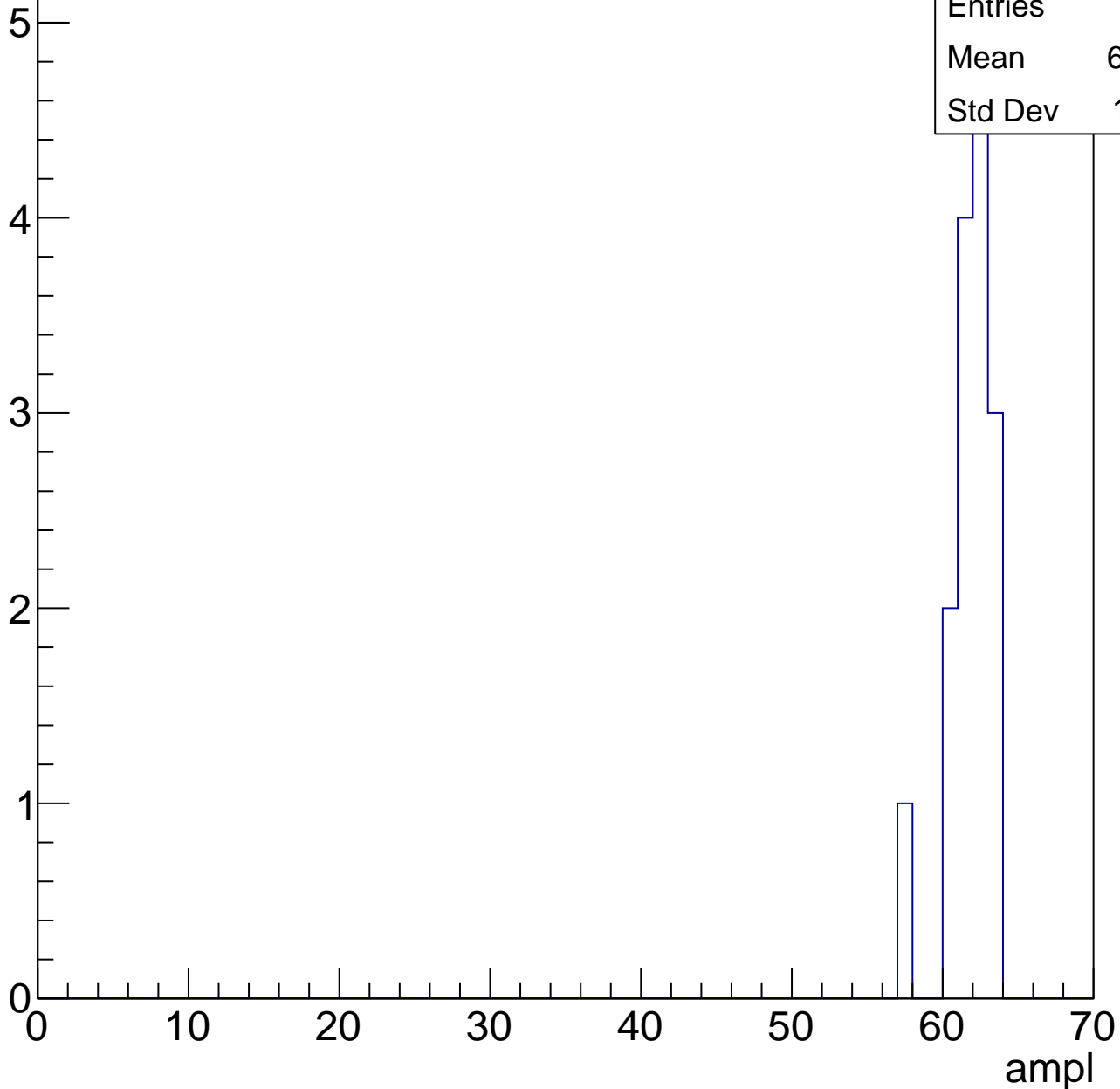


B1L102S, U6-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.33
Std Dev	1.491



B1L102S, U6-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch93, adc0

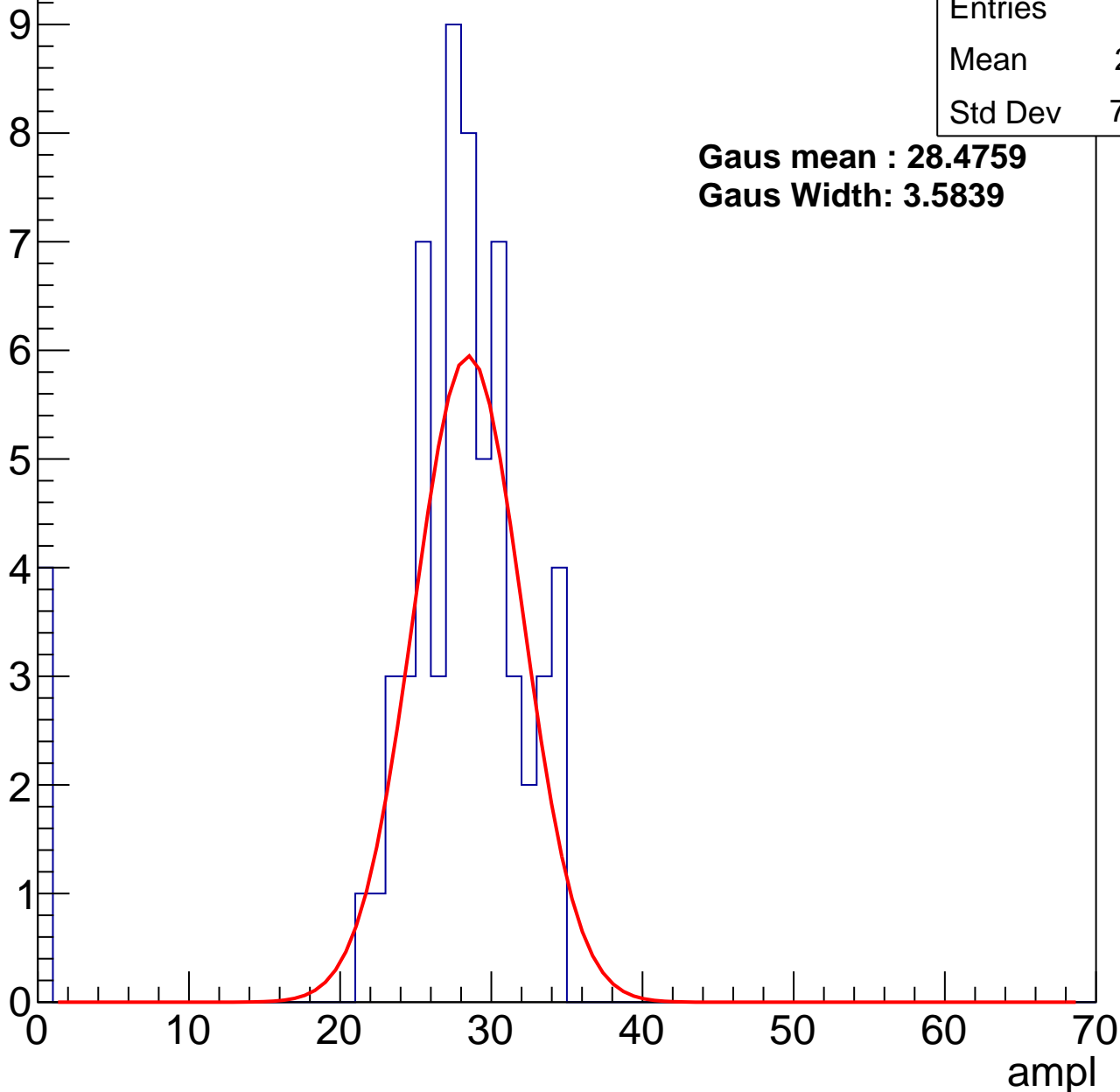
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	26.21
Std Dev	7.492

Gaus mean : 28.4759

Gaus Width: 3.5839



B1L102S, U6-ch93, adc1

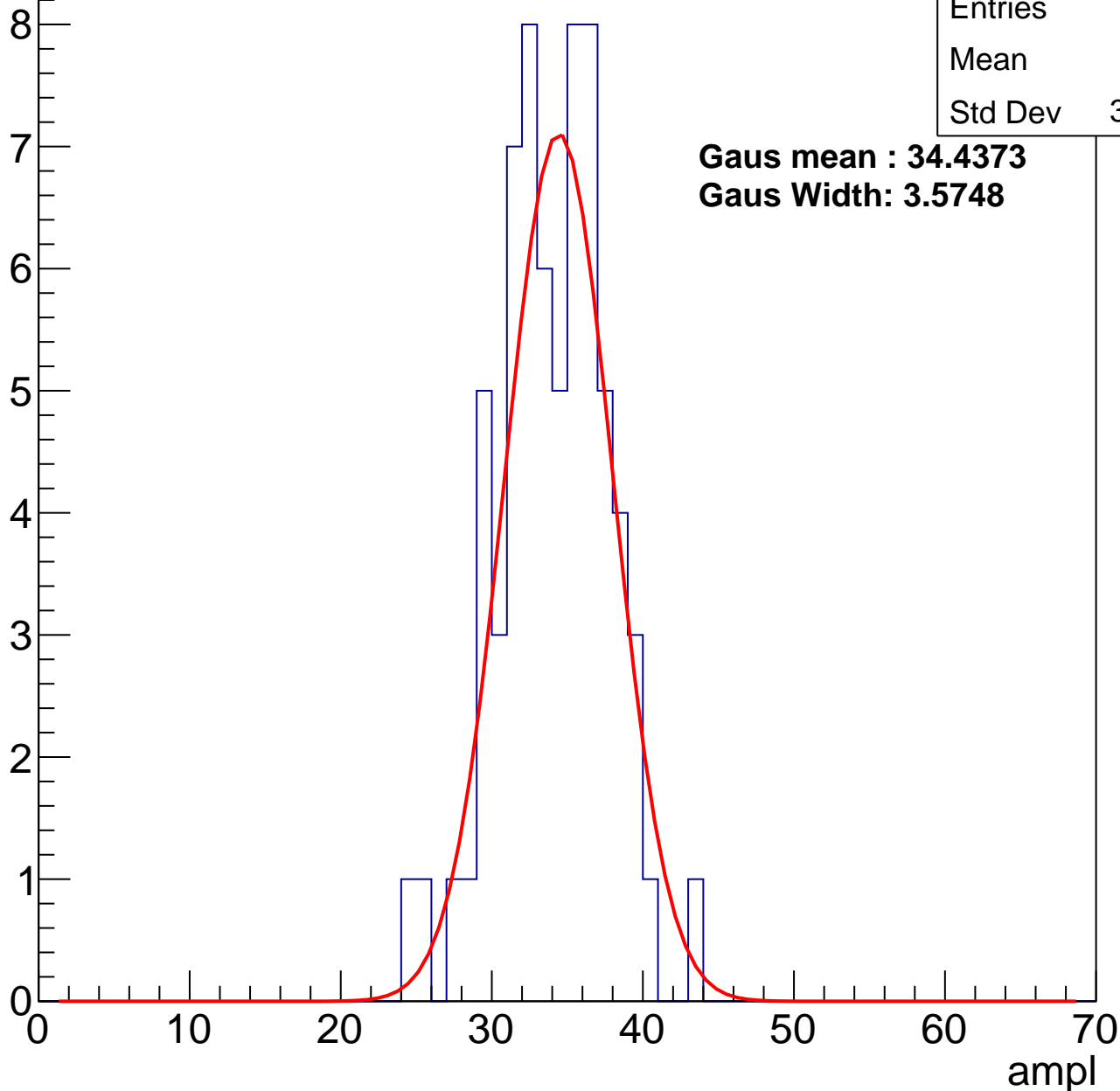
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.6
Std Dev	3.577

Gaus mean : 34.4373

Gaus Width: 3.5748



B1L102S, U6-ch93, adc2

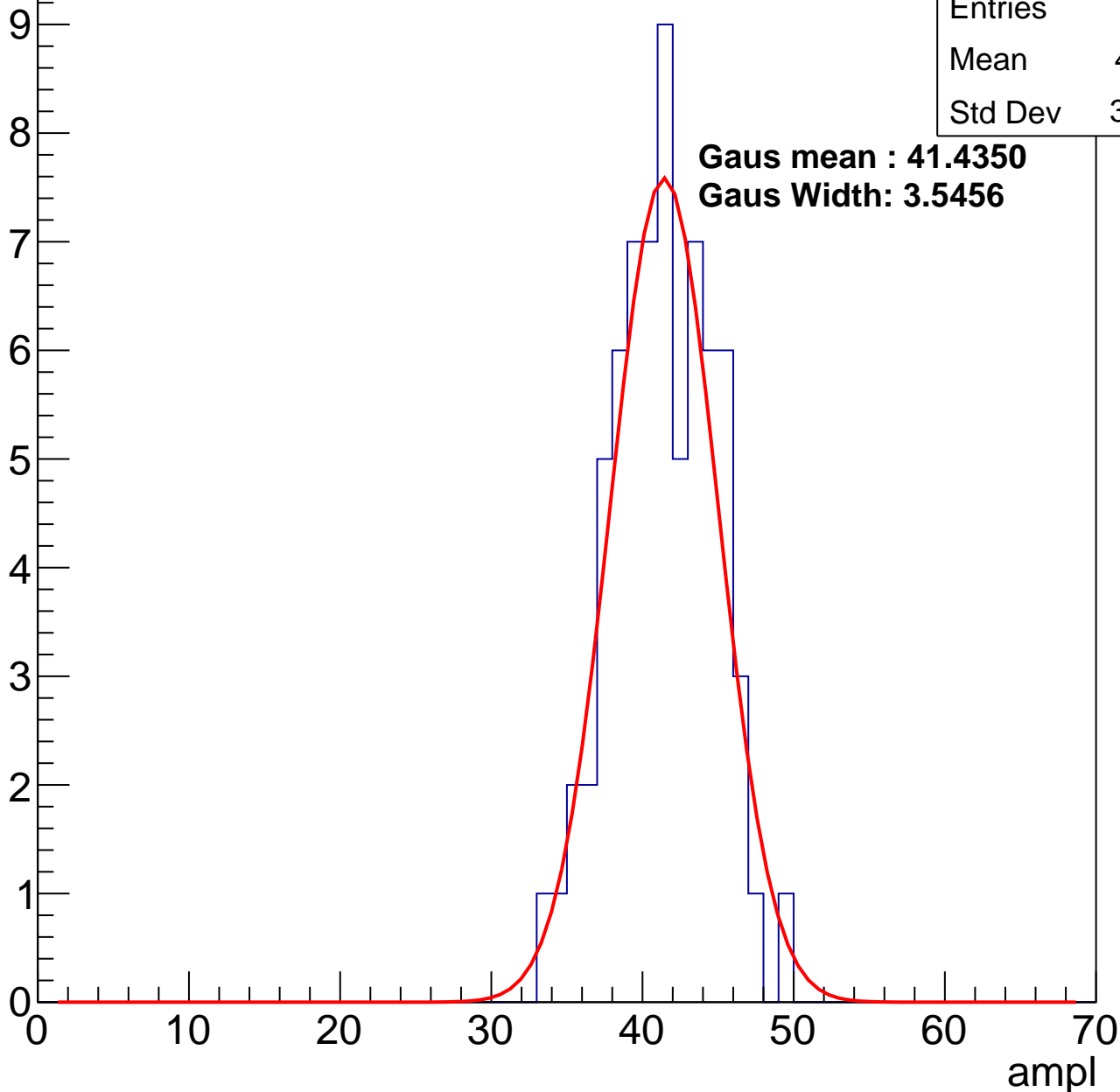
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.91
Std Dev	3.322

Gaus mean : 41.4350

Gaus Width: 3.5456

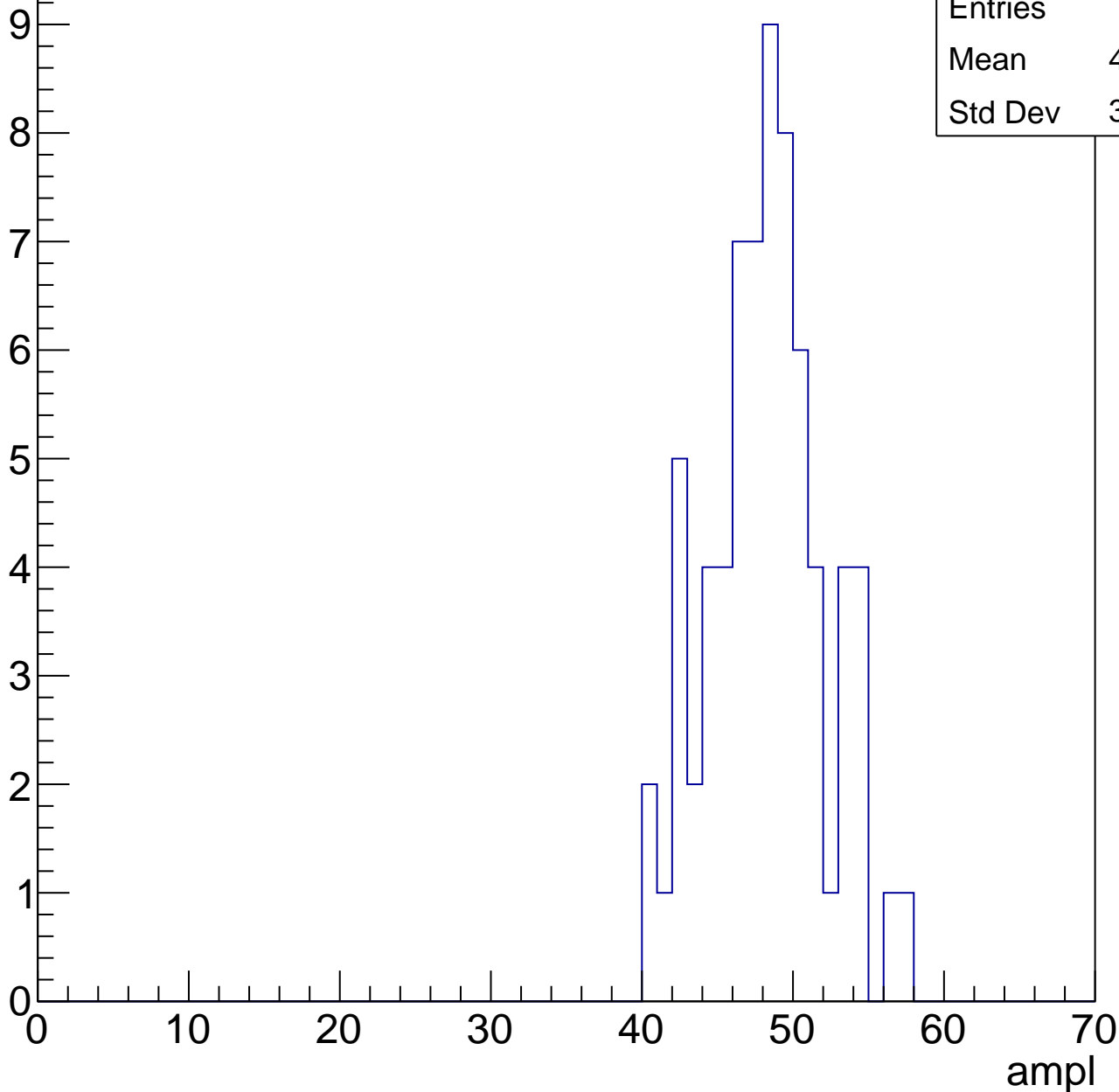


B1L102S, U6-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.79
Std Dev	3.802

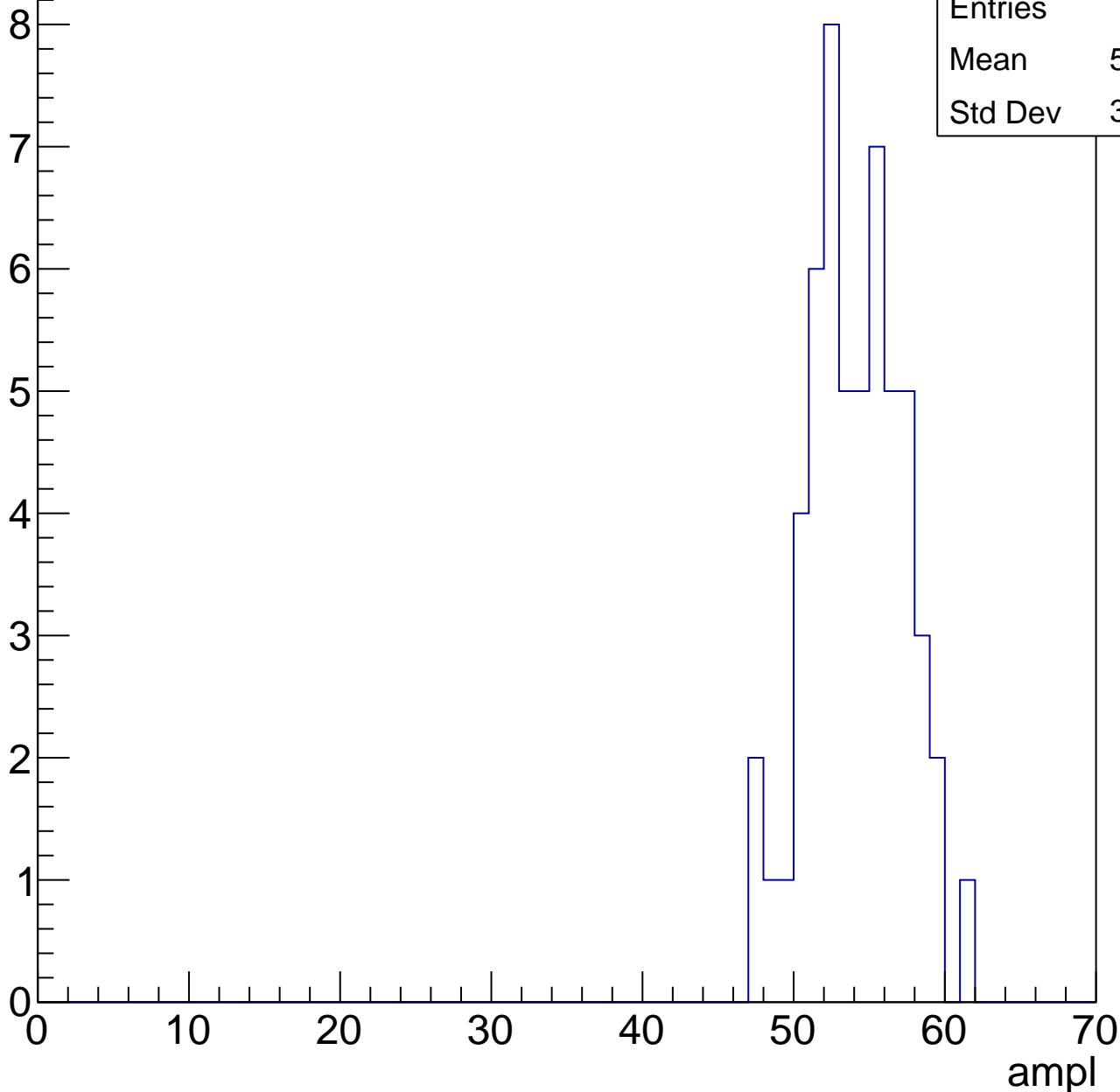


B1L102S, U6-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	53.65
Std Dev	3.094



B1L102S, U6-ch93, adc5

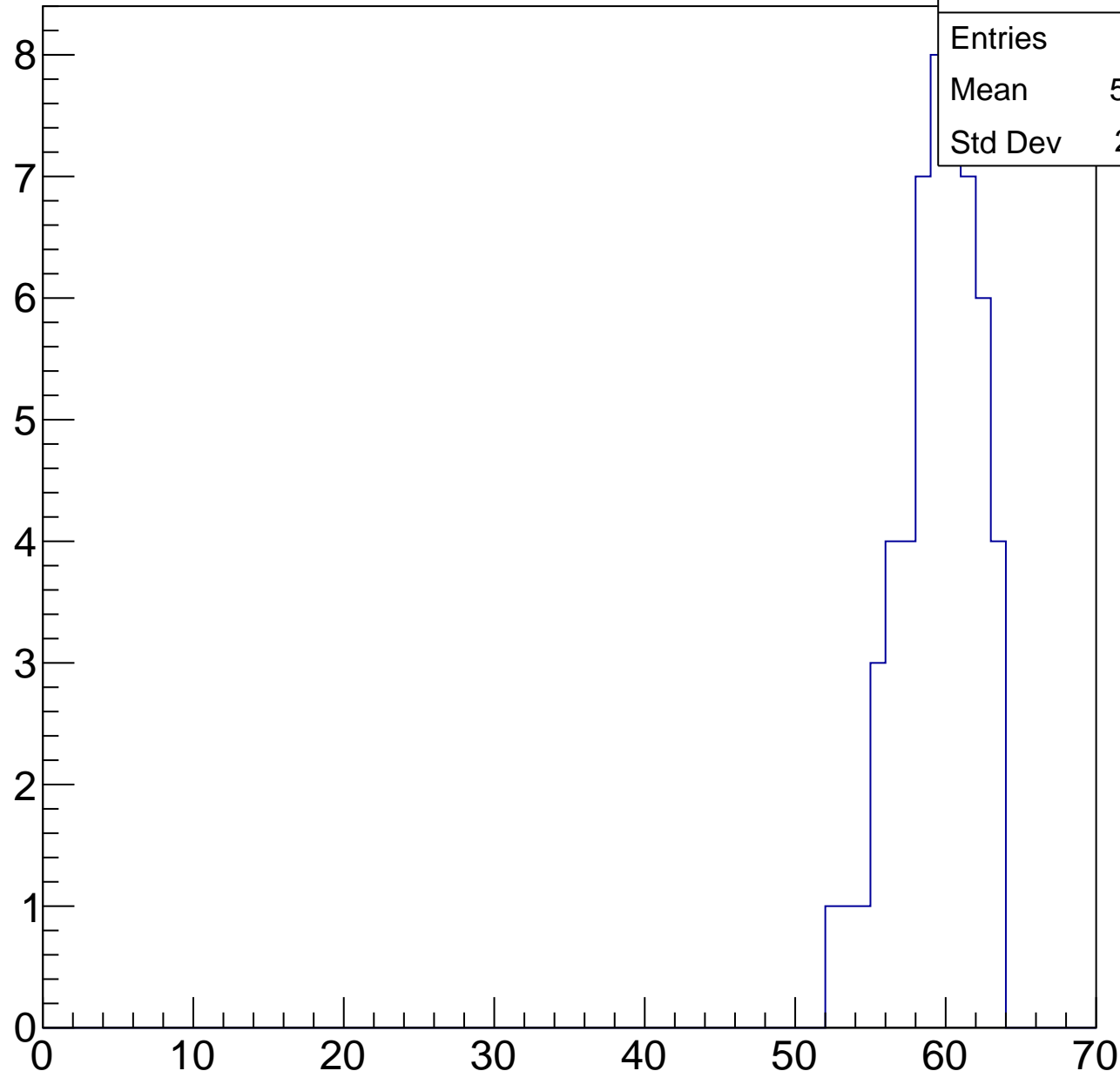
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.98
Std Dev	2.621

ampl

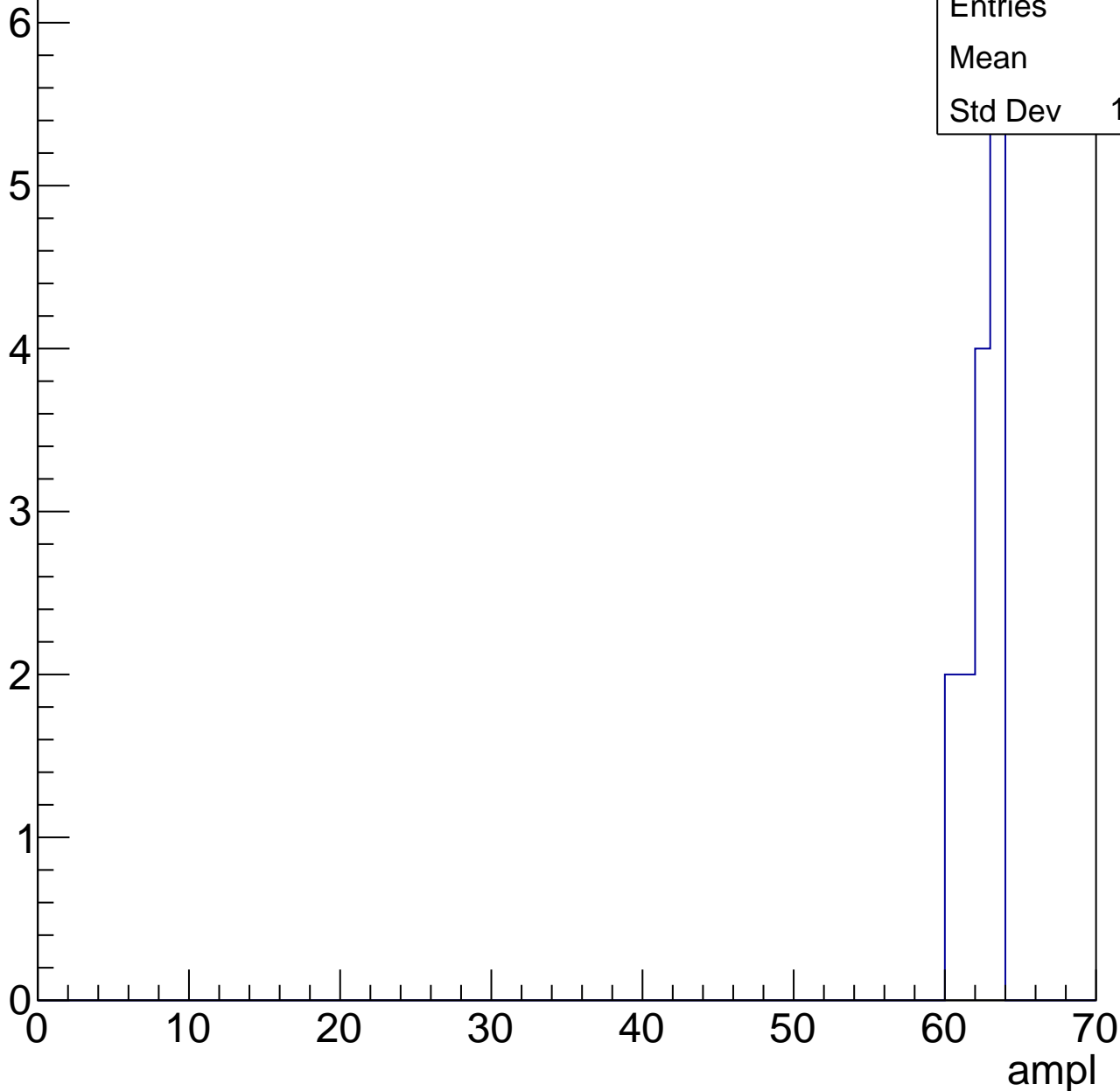


B1L102S, U6-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	62
Std Dev	1.069



B1L102S, U6-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch94, adc0

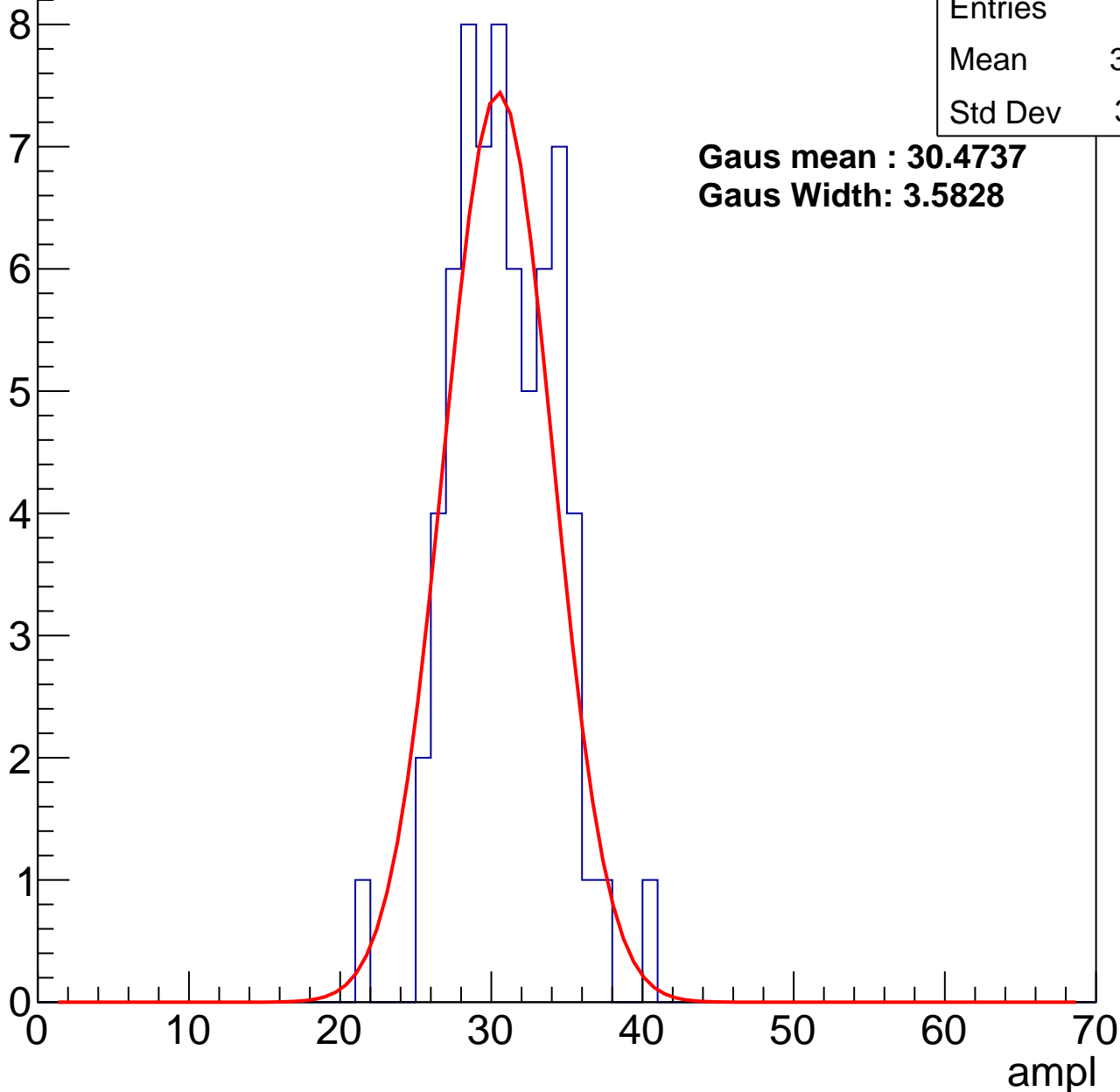
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	30.43
Std Dev	3.351

Gaus mean : 30.4737

Gaus Width: 3.5828



B1L102S, U6-ch94, adc1

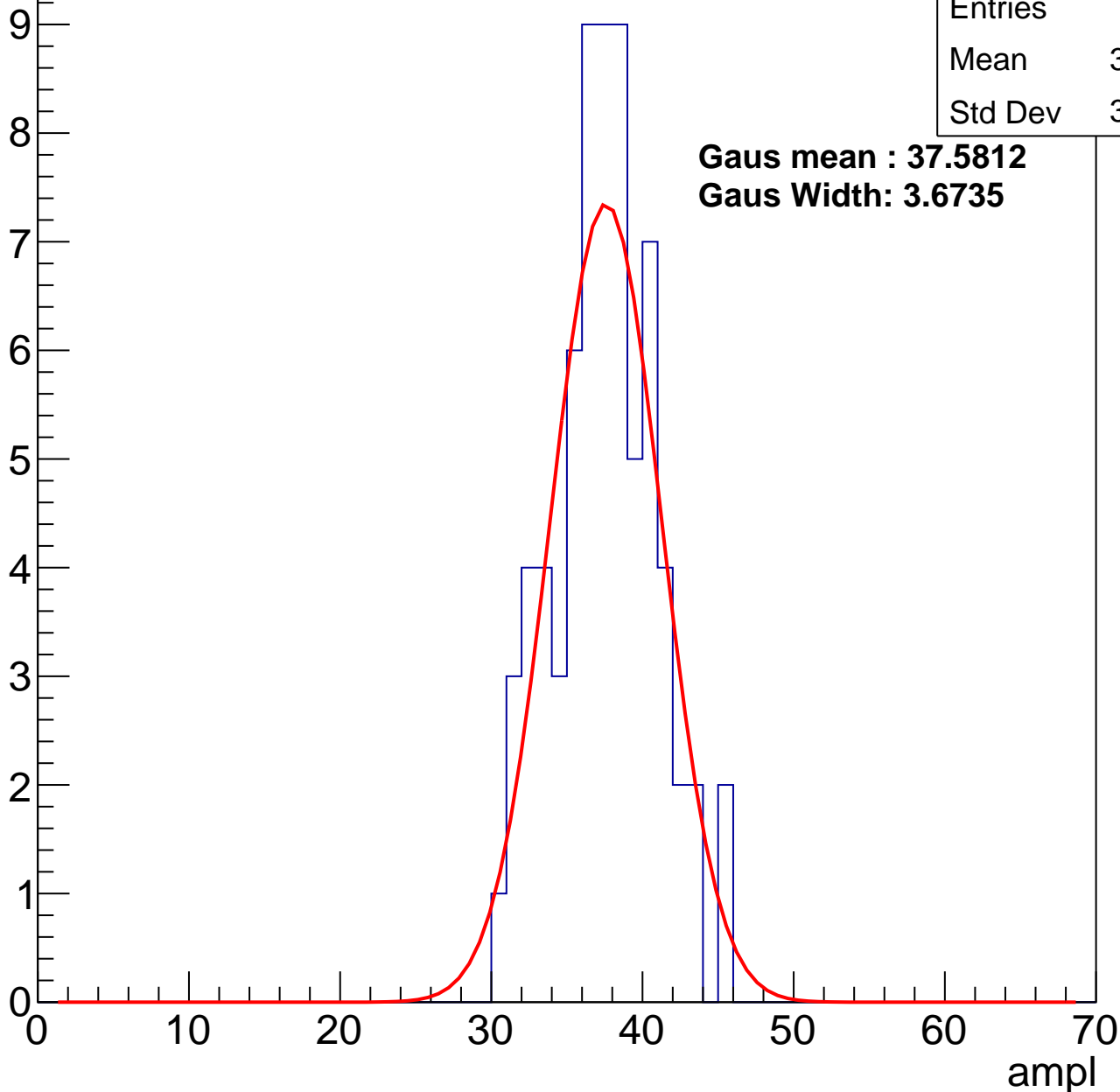
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	37.04
Std Dev	3.353

Gaus mean : 37.5812

Gaus Width: 3.6735



B1L102S, U6-ch94, adc2

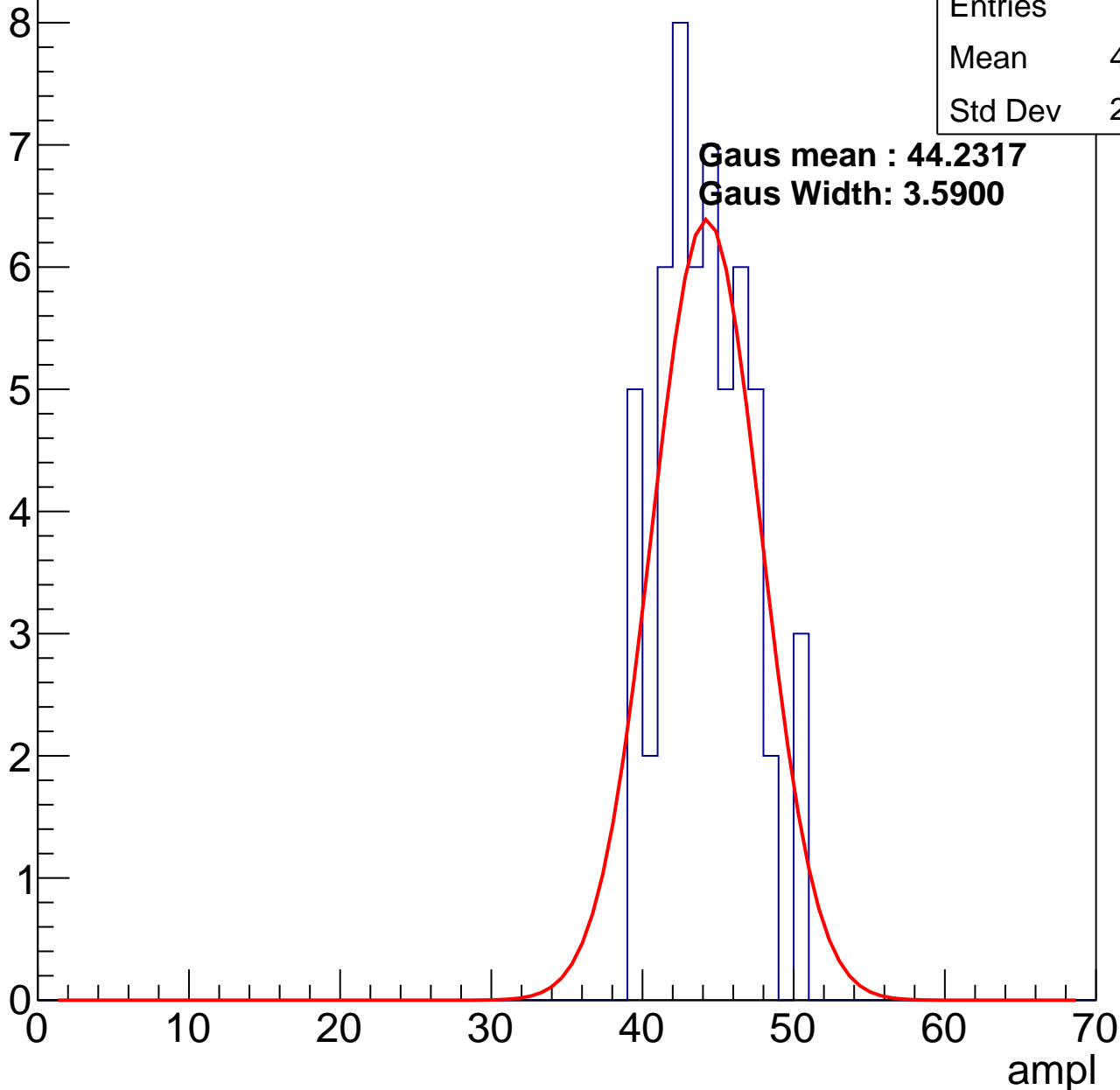
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	43.73
Std Dev	2.889

Gaus mean : 44.2317

Gaus Width: 3.5900

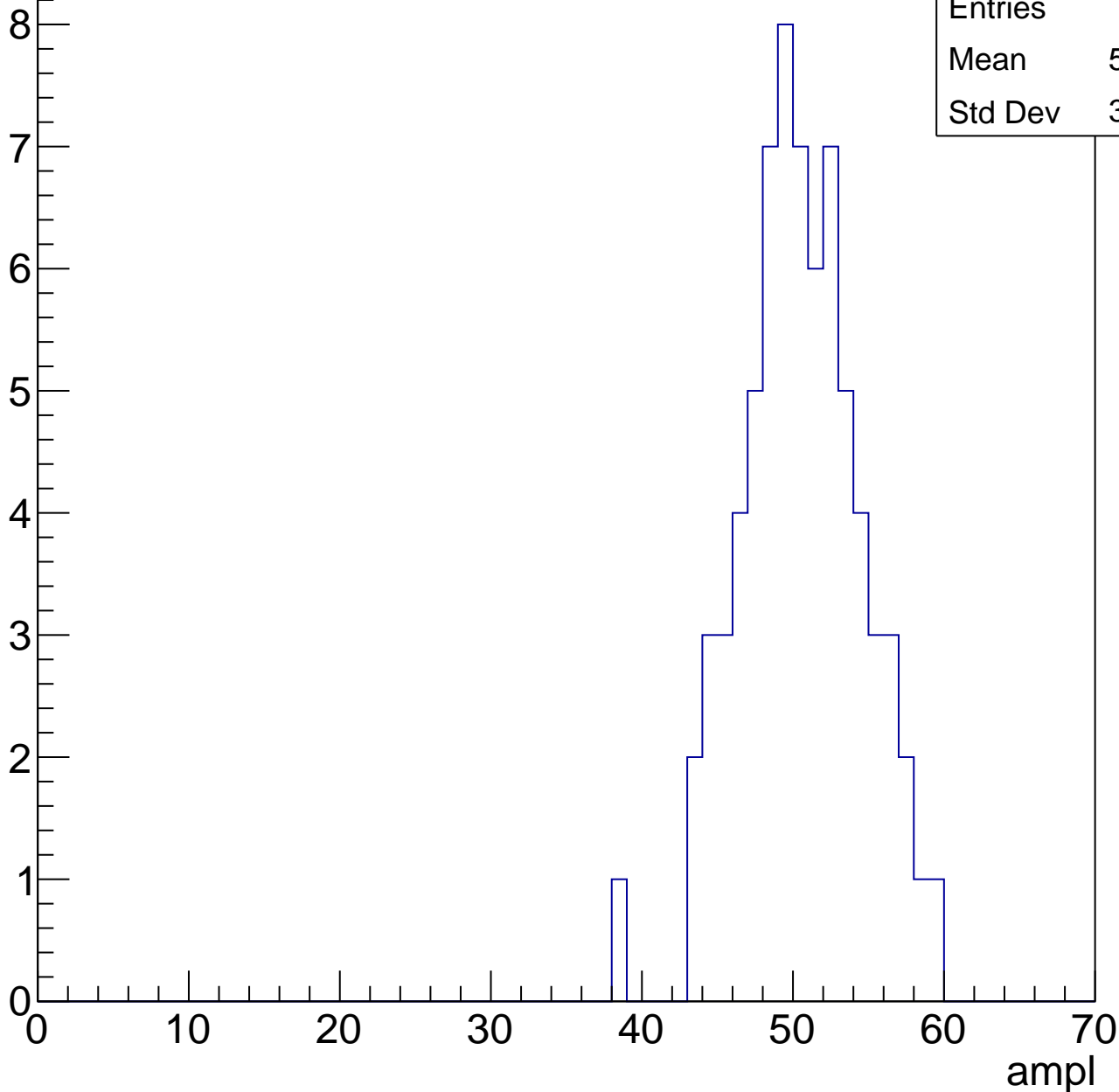


B1L102S, U6-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	50.04
Std Dev	3.977

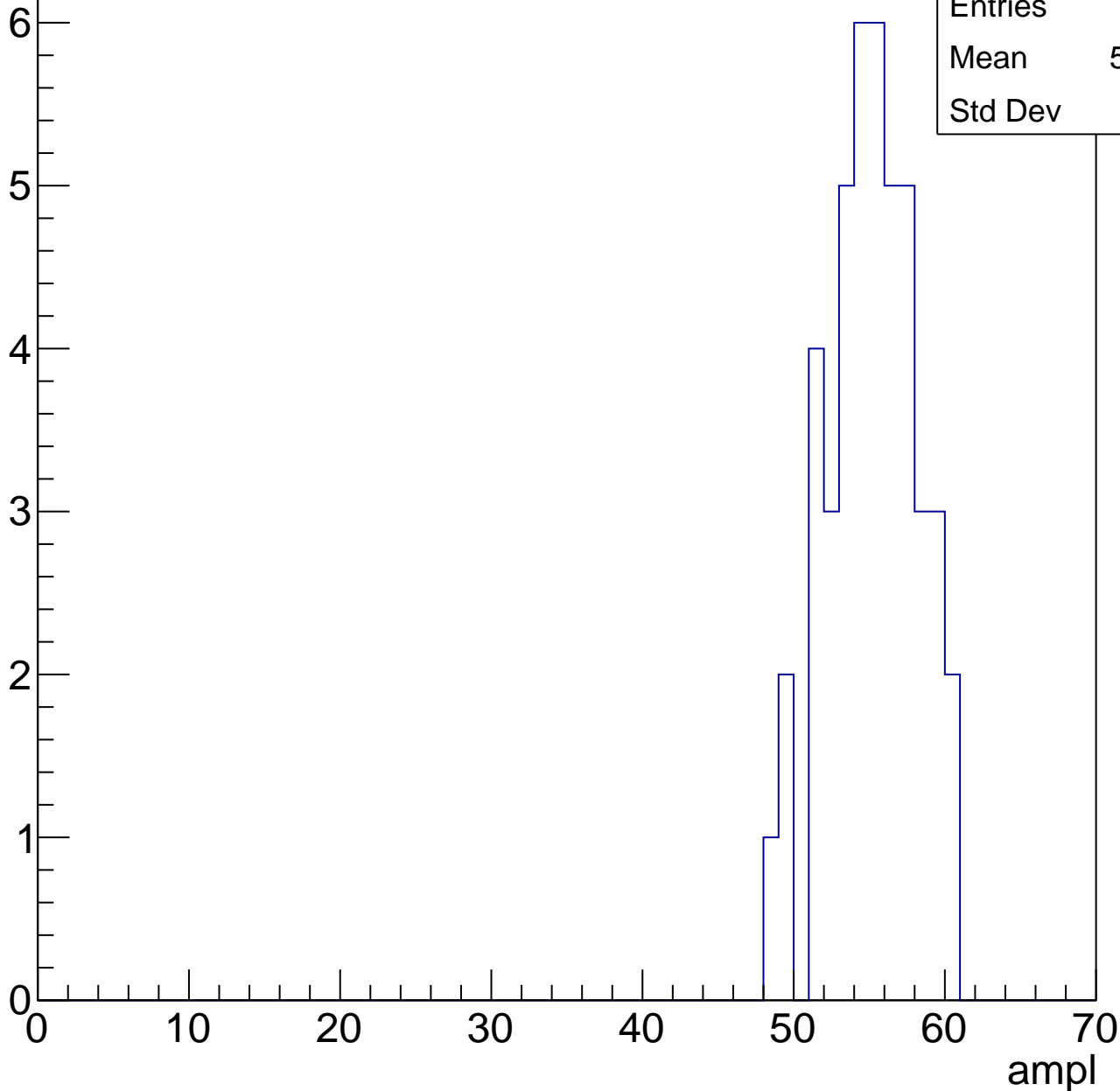


B1L102S, U6-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	54.69
Std Dev	2.92



B1L102S, U6-ch94, adc5

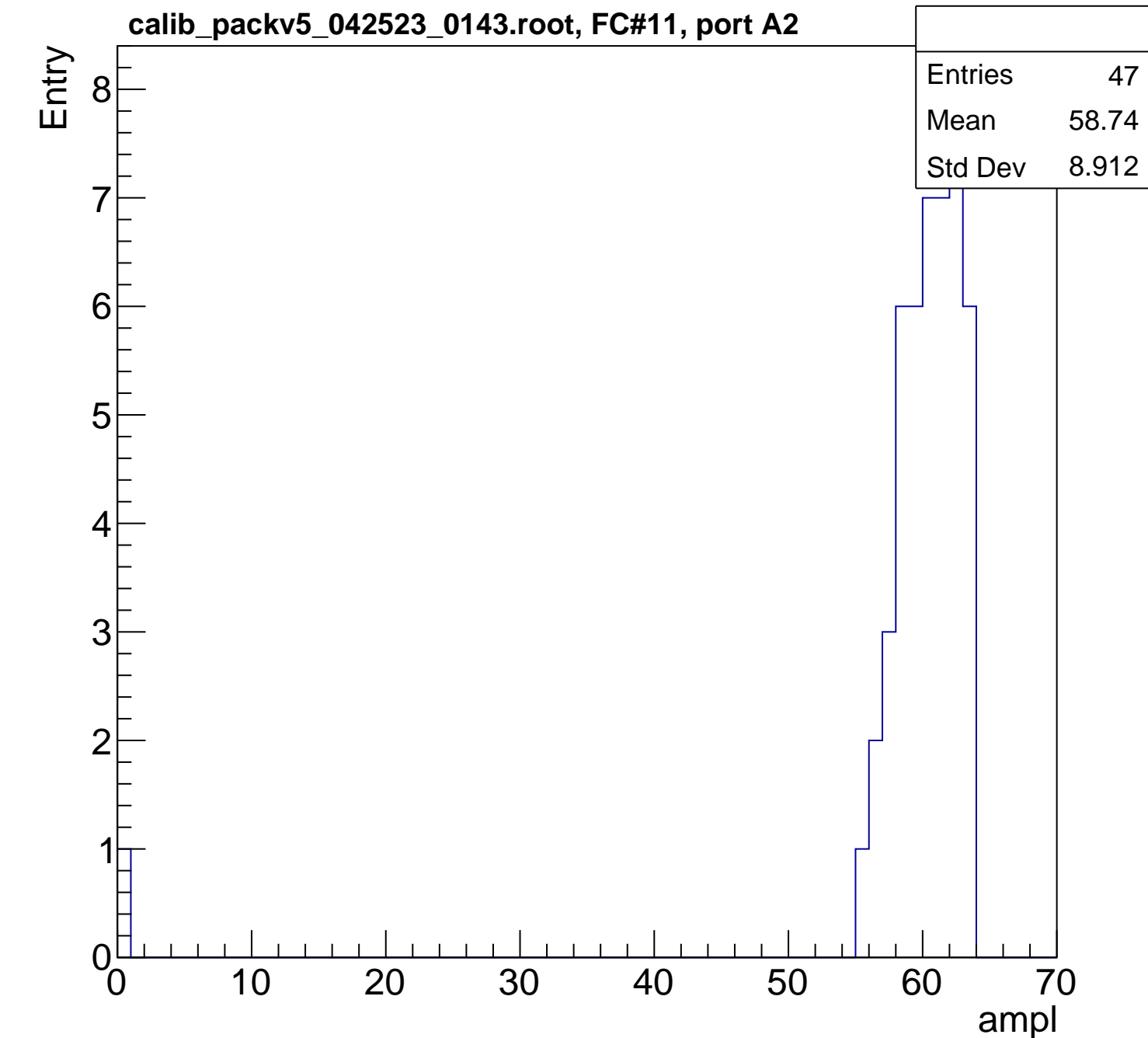
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.74
Std Dev	8.912

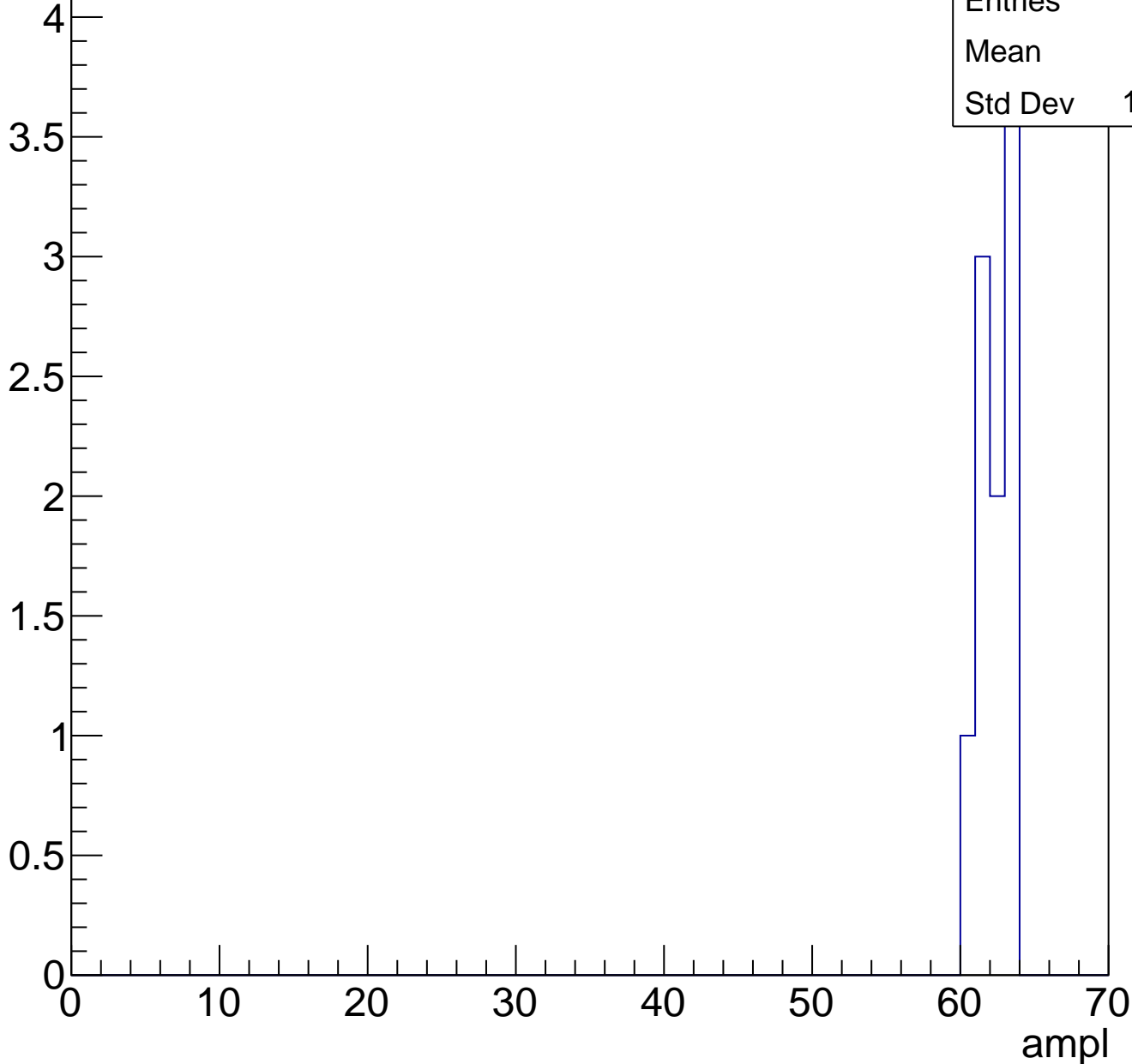
ampl



B1L102S, U6-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch95, adc0

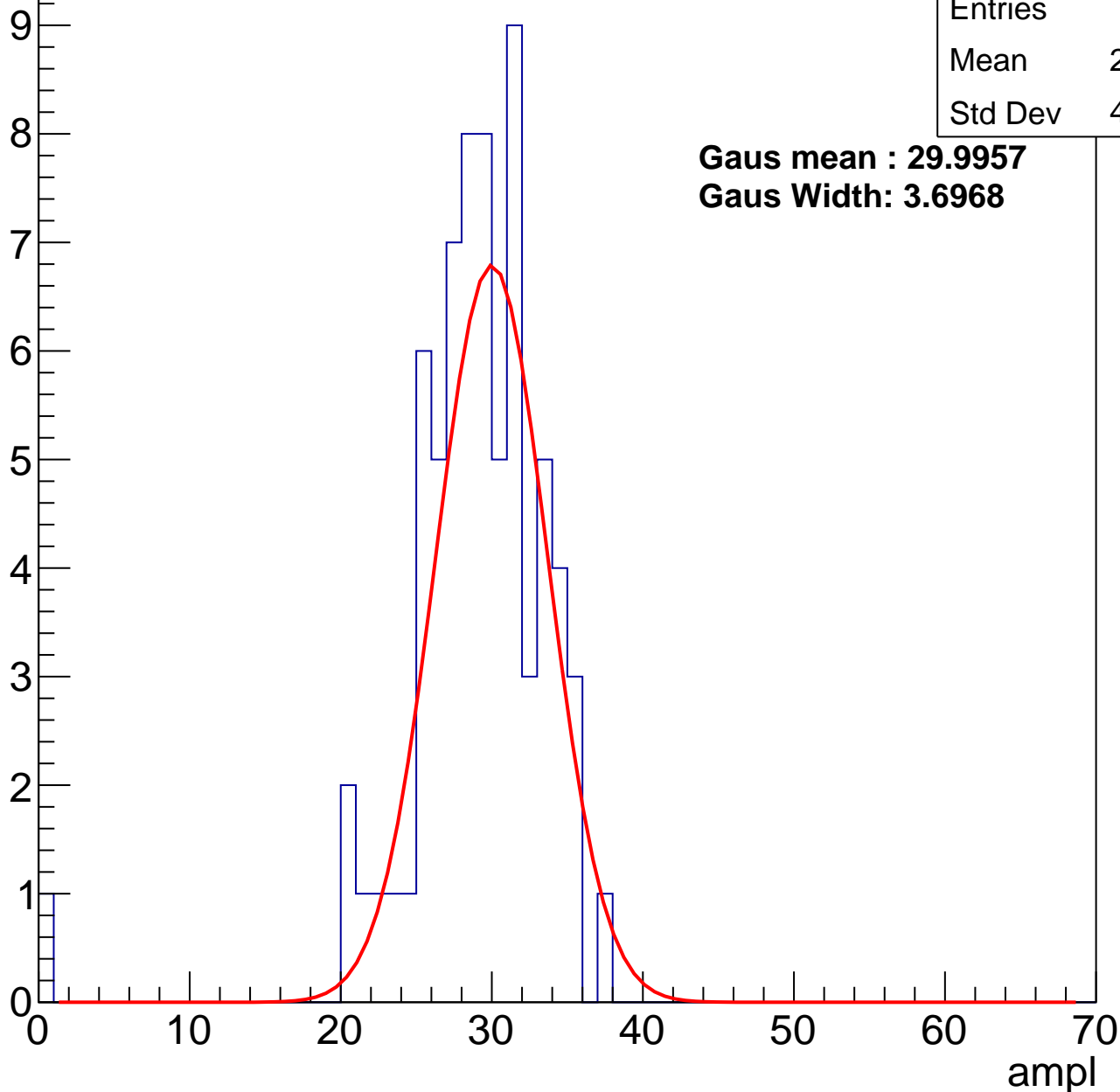
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.49
Std Dev	4.973

Gaus mean : 29.9957

Gaus Width: 3.6968



B1L102S, U6-ch95, adc1

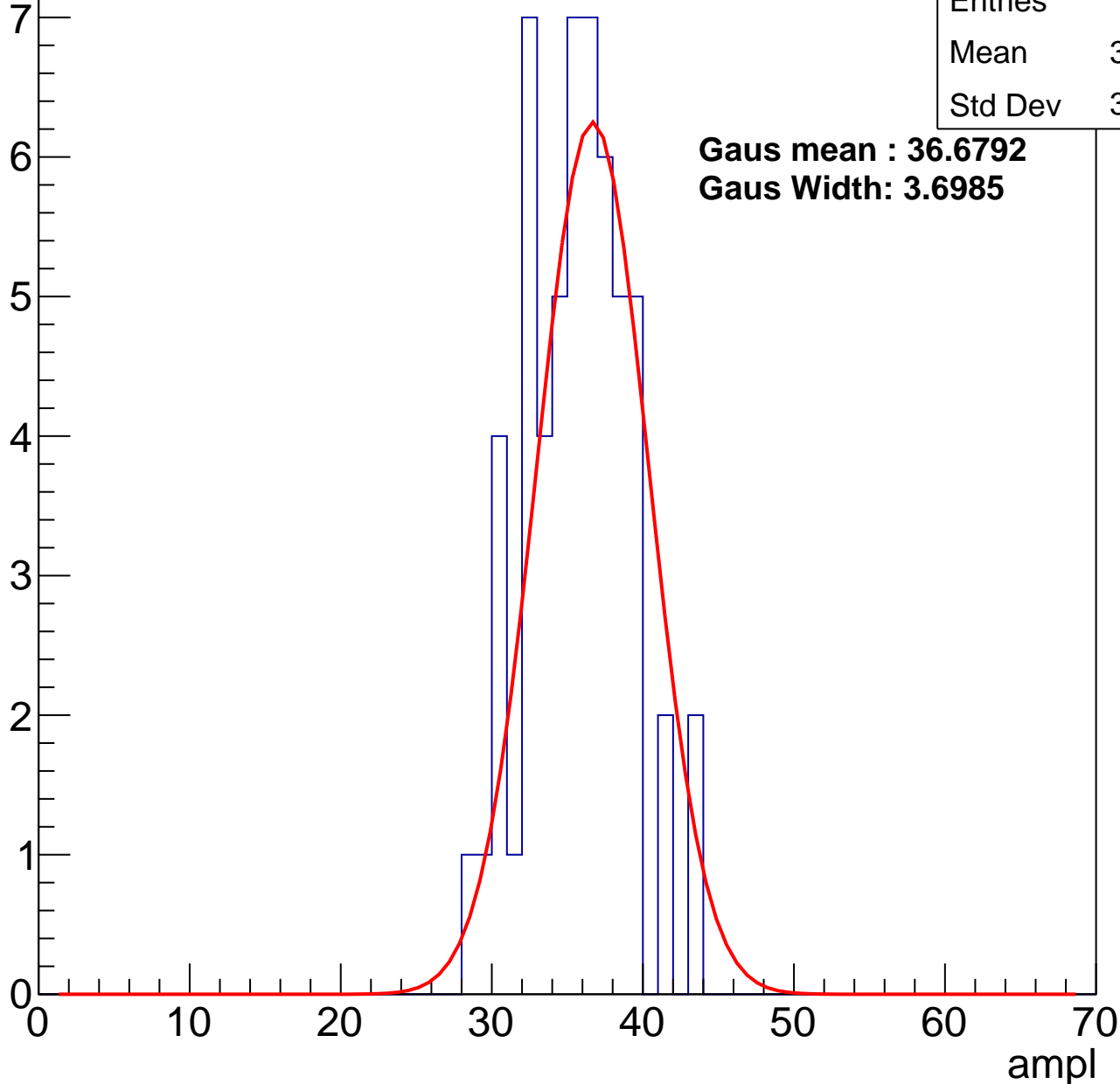
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	35.19
Std Dev	3.348

Gaus mean : 36.6792

Gaus Width: 3.6985



B1L102S, U6-ch95, adc2

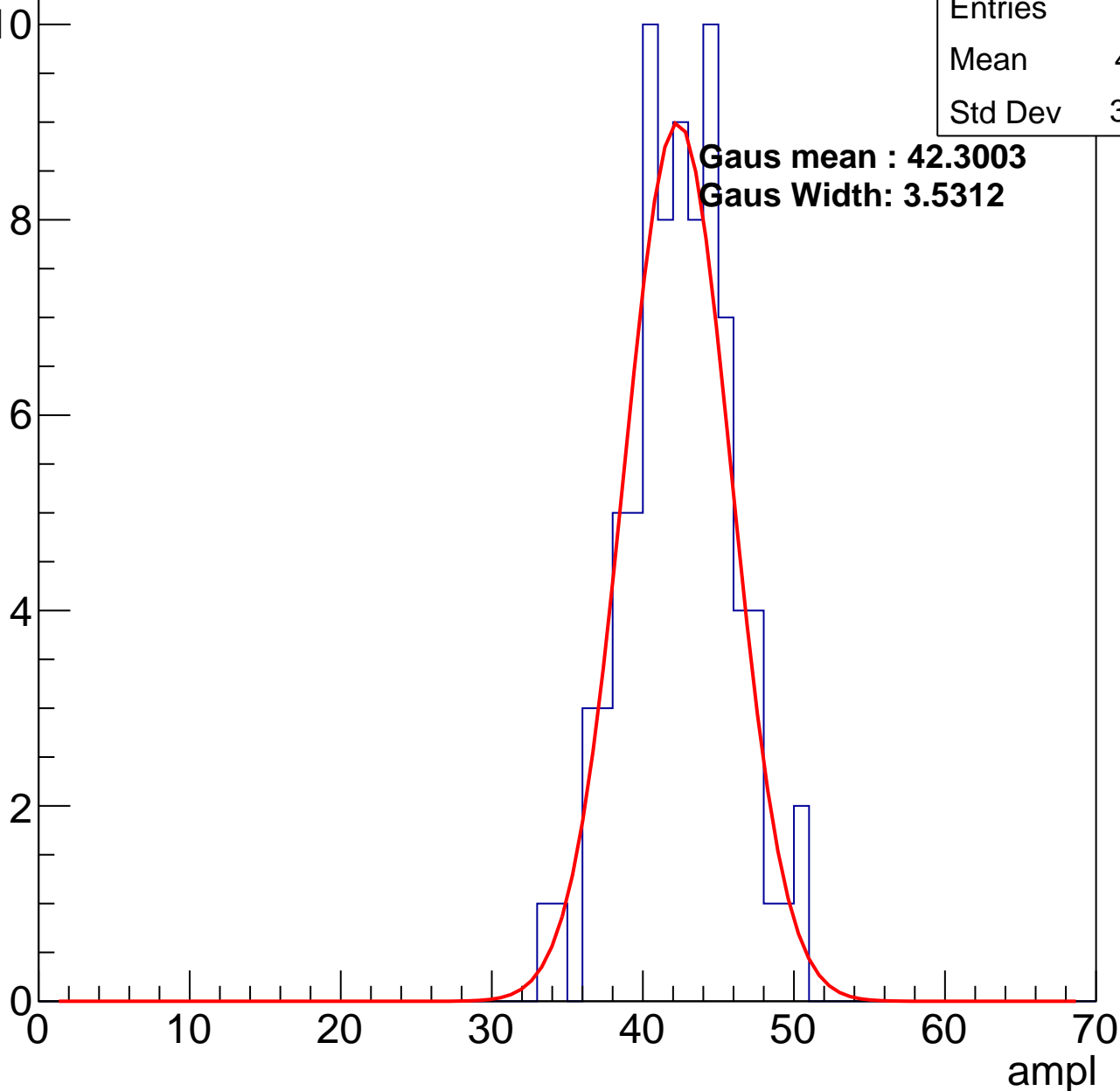
calib_packv5_042523_0143.root, FC#11, port A2

Entries	82
Mean	42.01
Std Dev	3.473

Gaus mean : 42.3003

Gaus Width: 3.5312

Entry

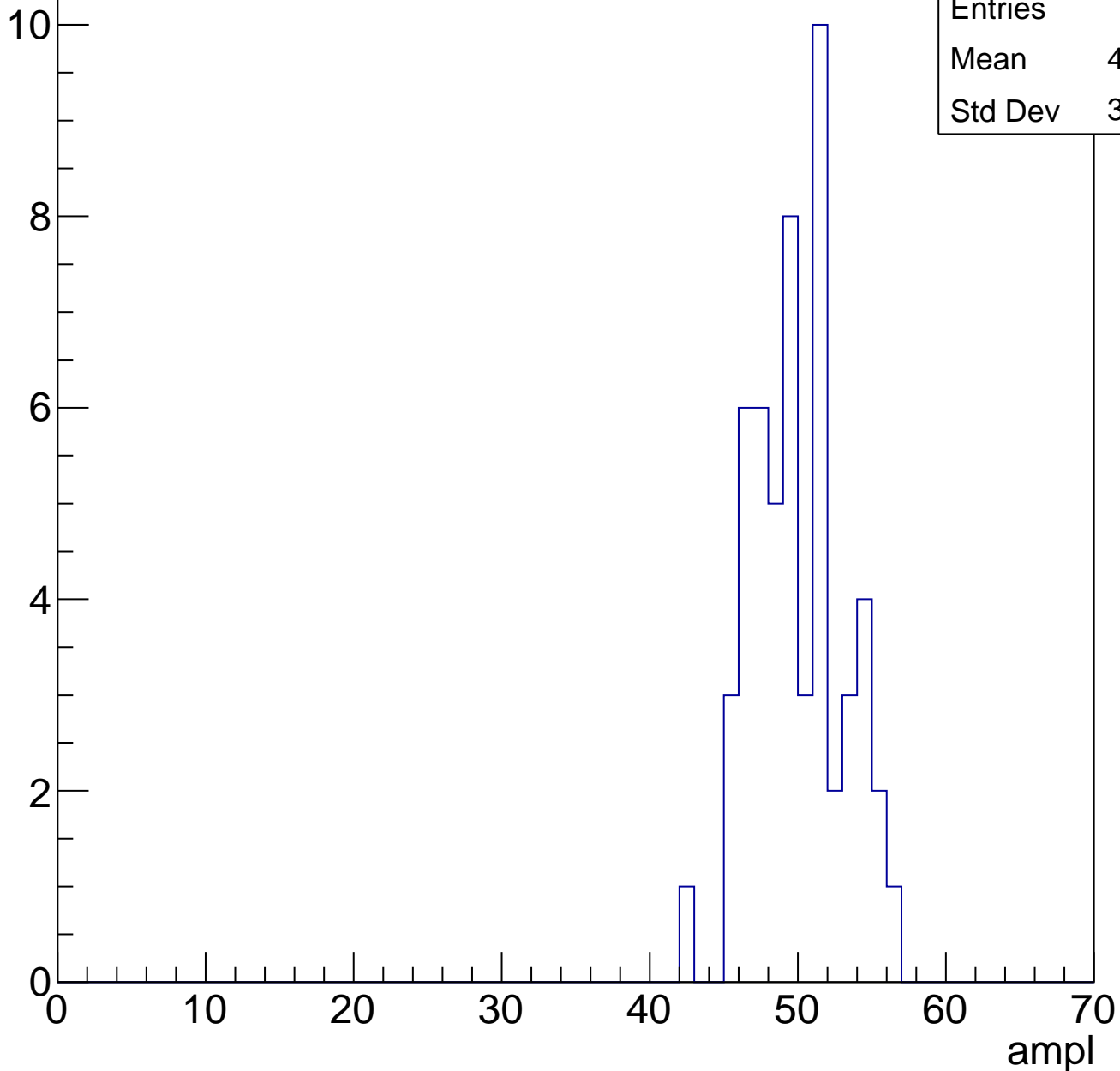


B1L102S, U6-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	54
Mean	49.48
Std Dev	3.035

Entry

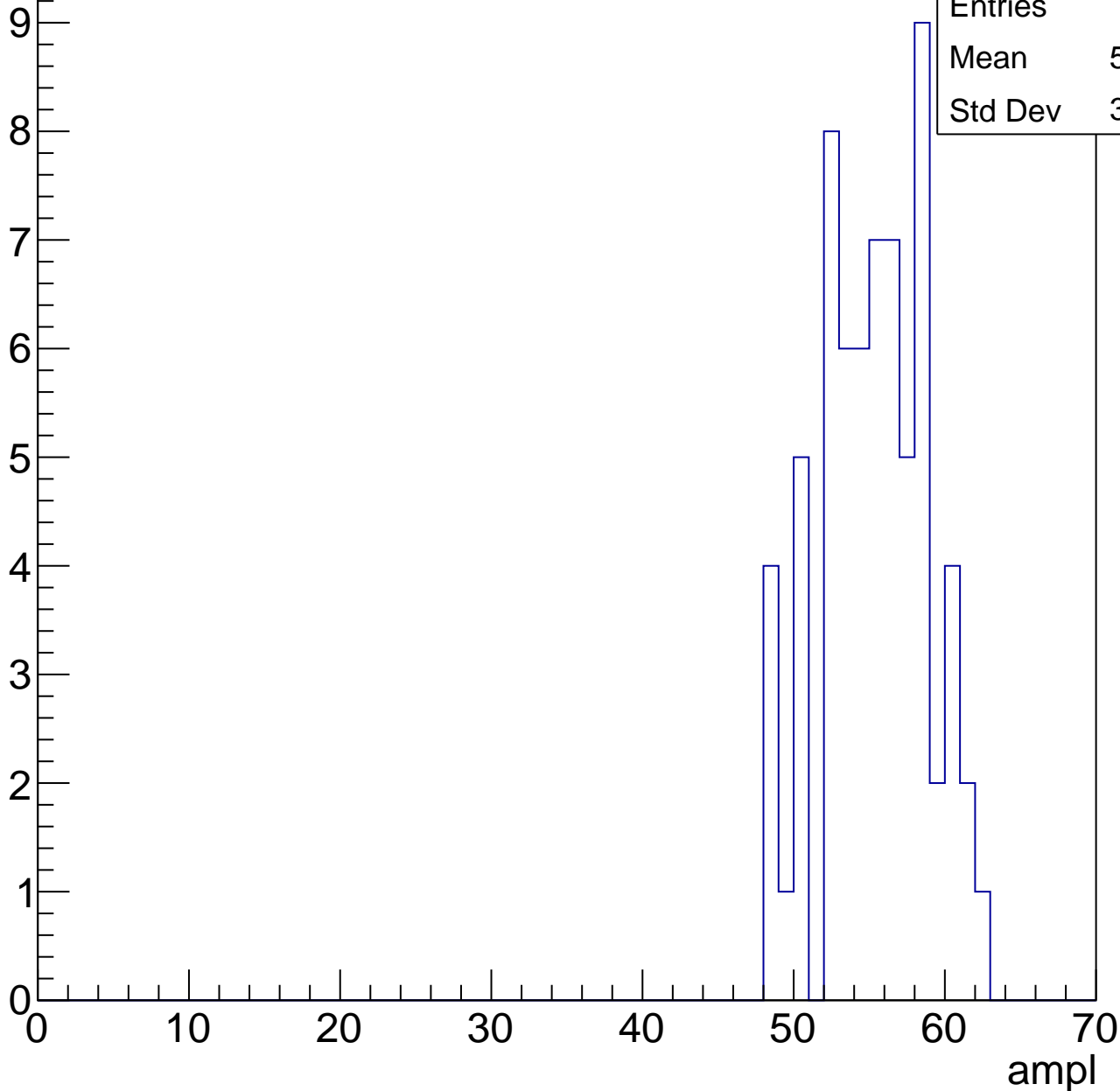


B1L102S, U6-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

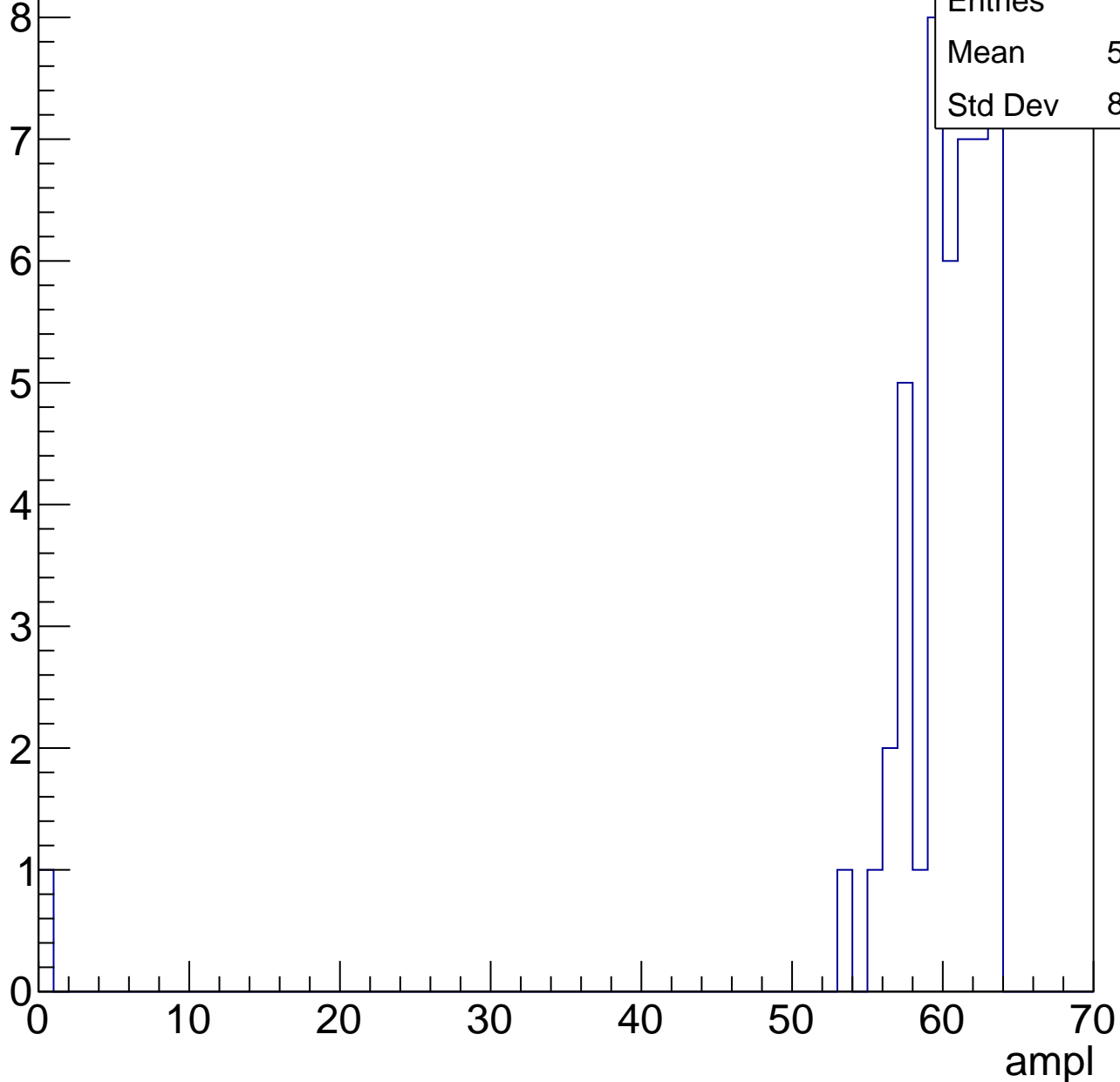
Entries	67
Mean	54.85
Std Dev	3.495



B1L102S, U6-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L102S, U6-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch96, adc0

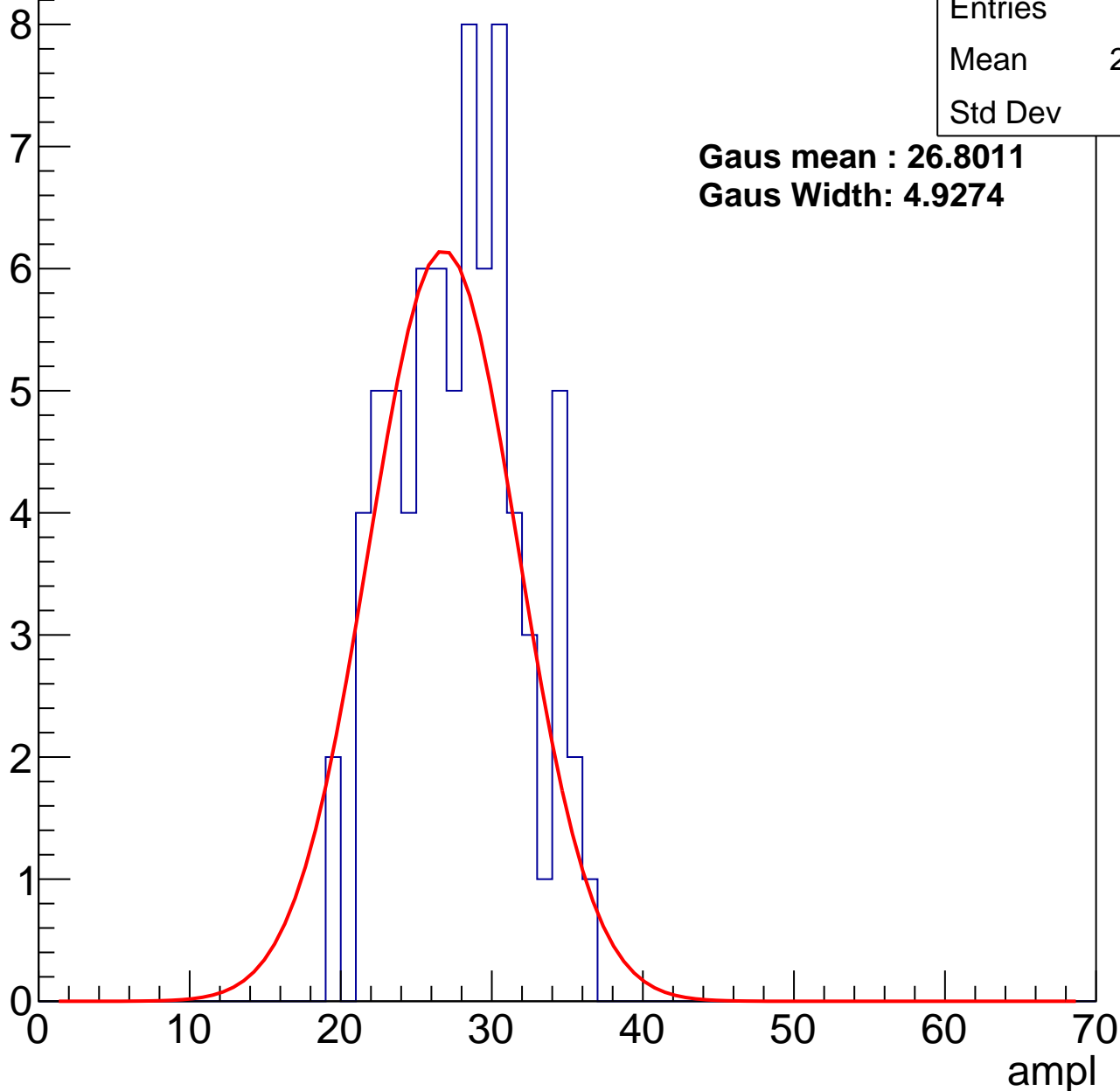
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.35
Std Dev	4.11

Gaus mean : 26.8011

Gaus Width: 4.9274



B1L102S, U6-ch96, adc1

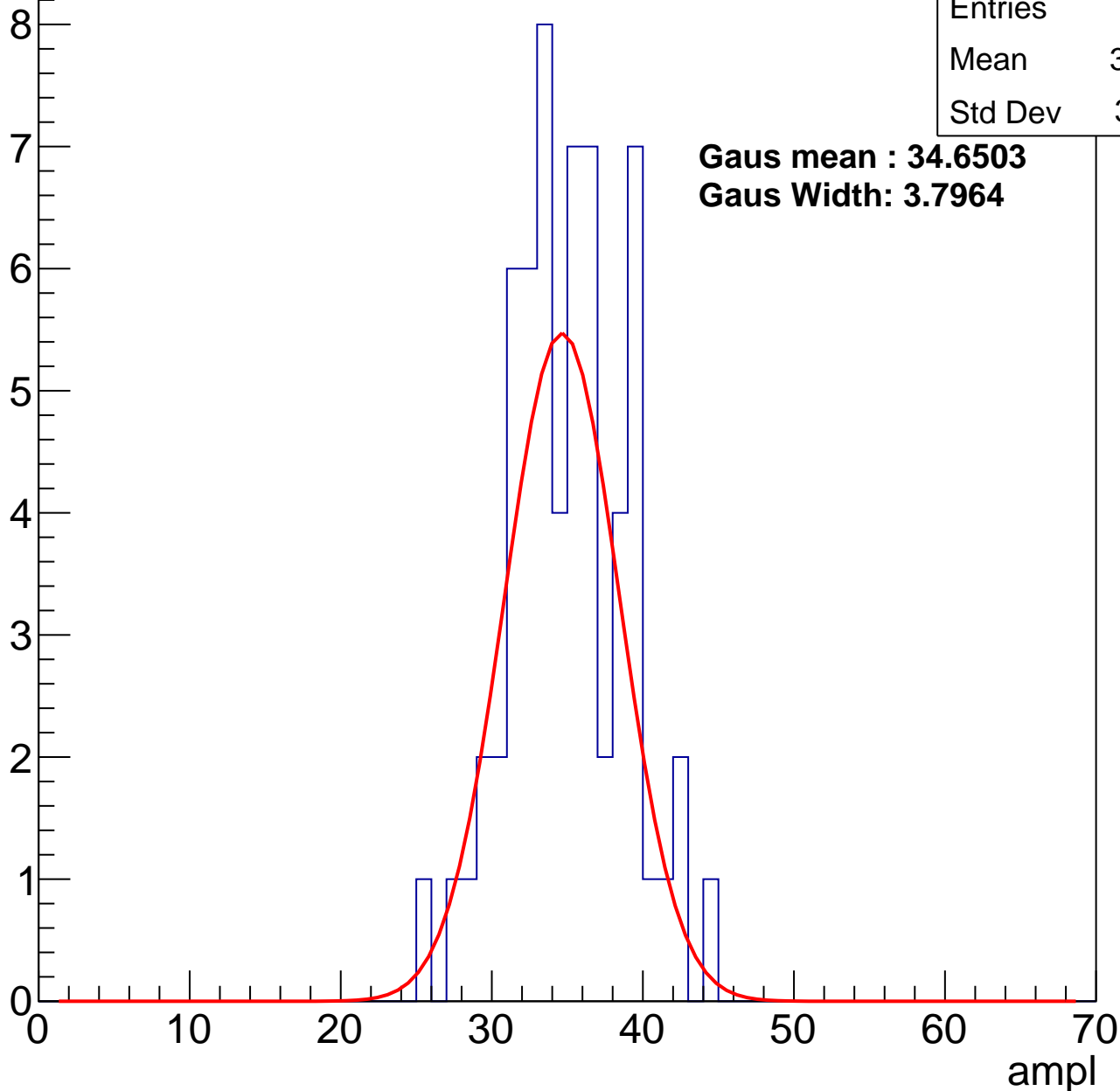
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.62
Std Dev	3.811

Gaus mean : 34.6503

Gaus Width: 3.7964



B1L102S, U6-ch96, adc2

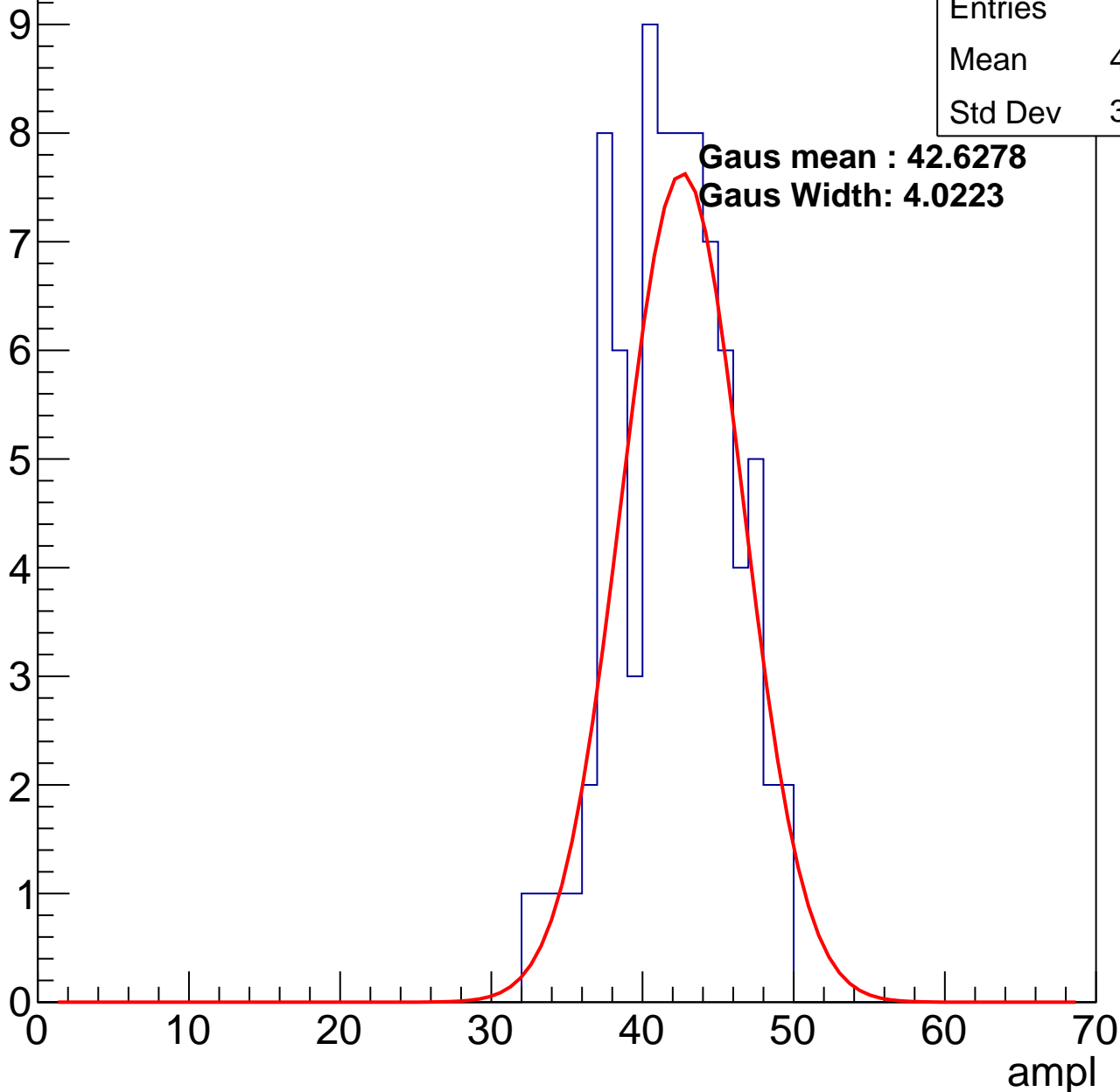
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	41.54
Std Dev	3.768

Gaus mean : 42.6278

Gaus Width: 4.0223

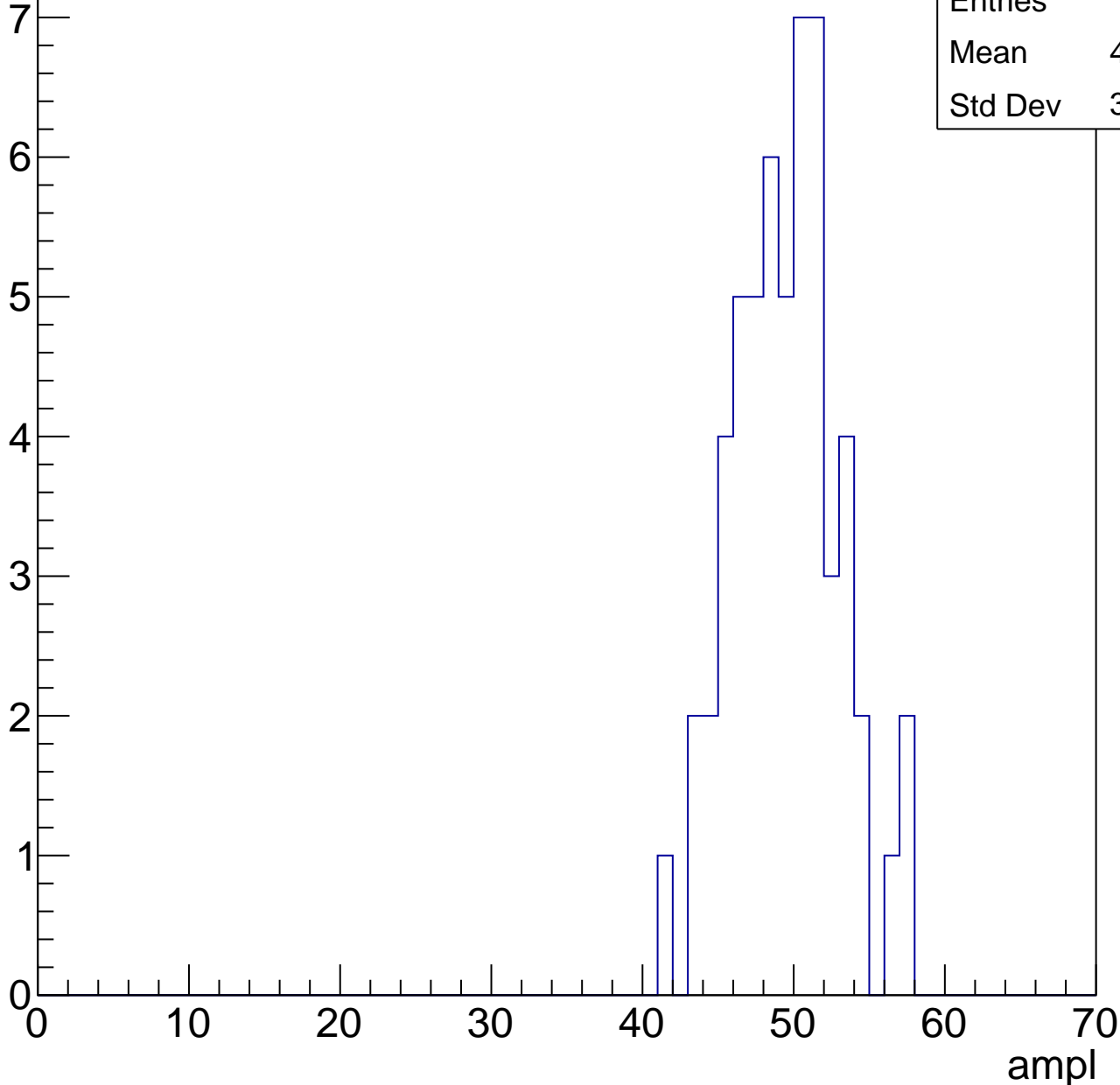


B1L102S, U6-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	49.04
Std Dev	3.469

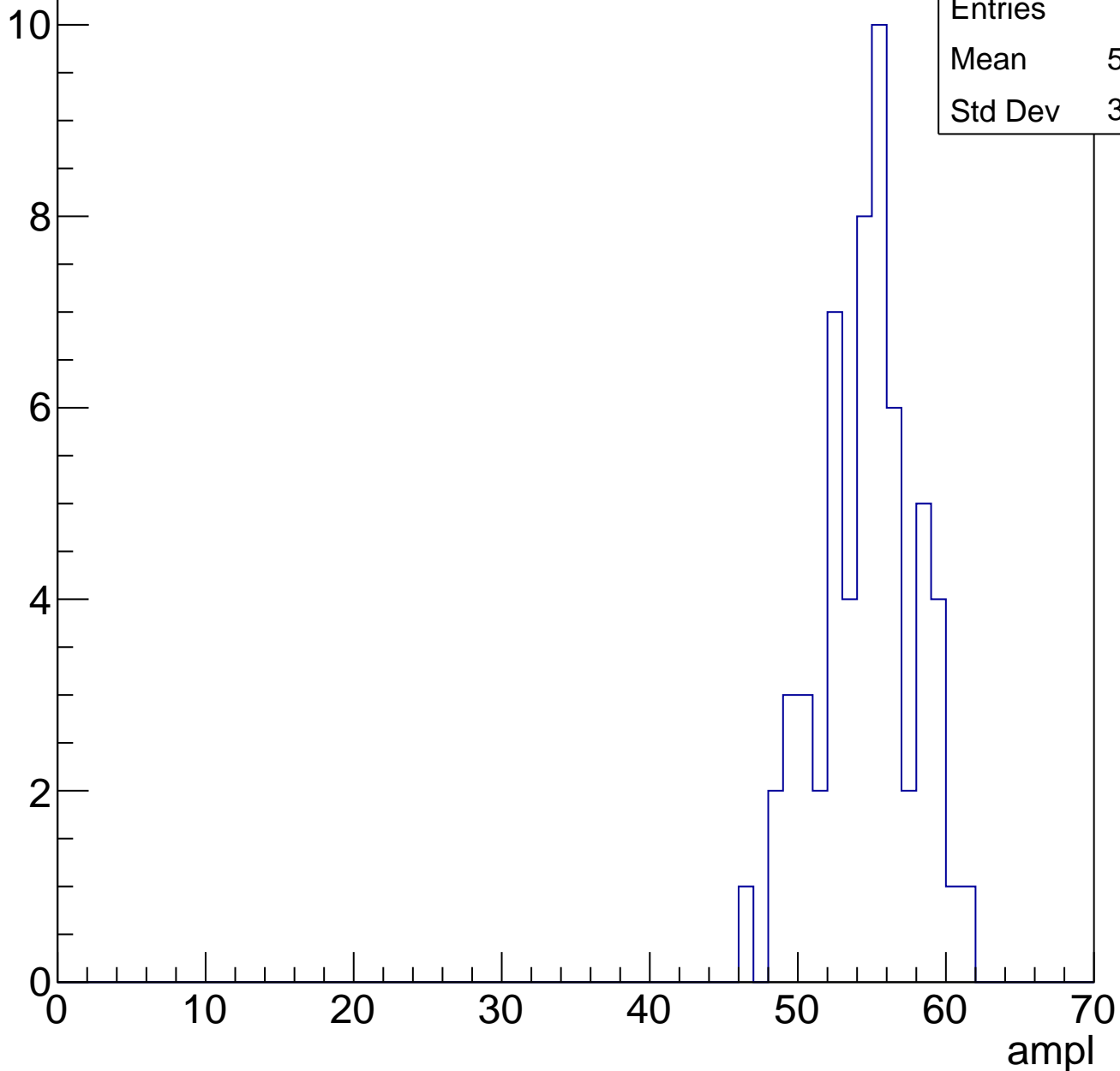


B1L102S, U6-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.17
Std Dev	3.253

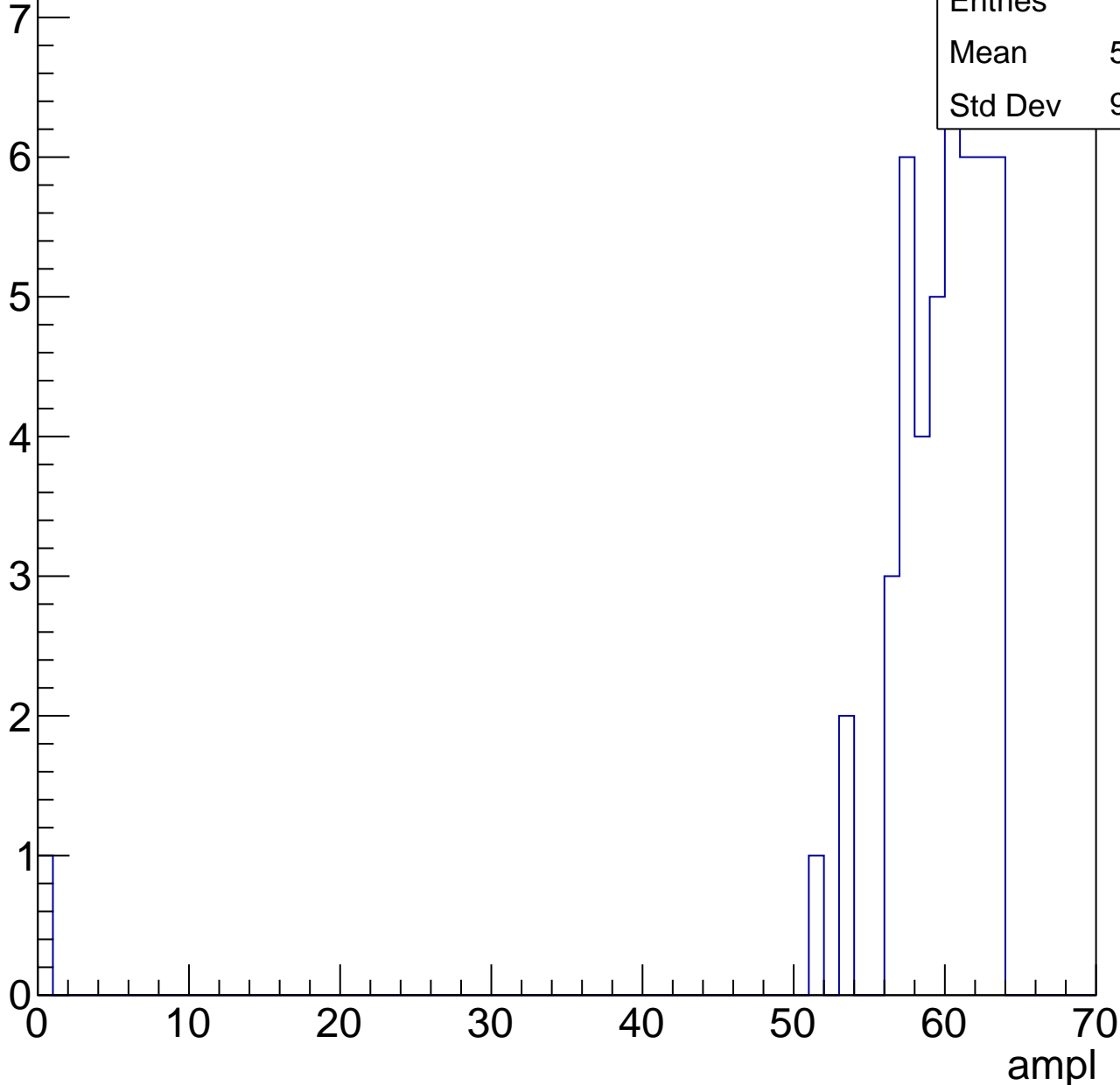


B1L102S, U6-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

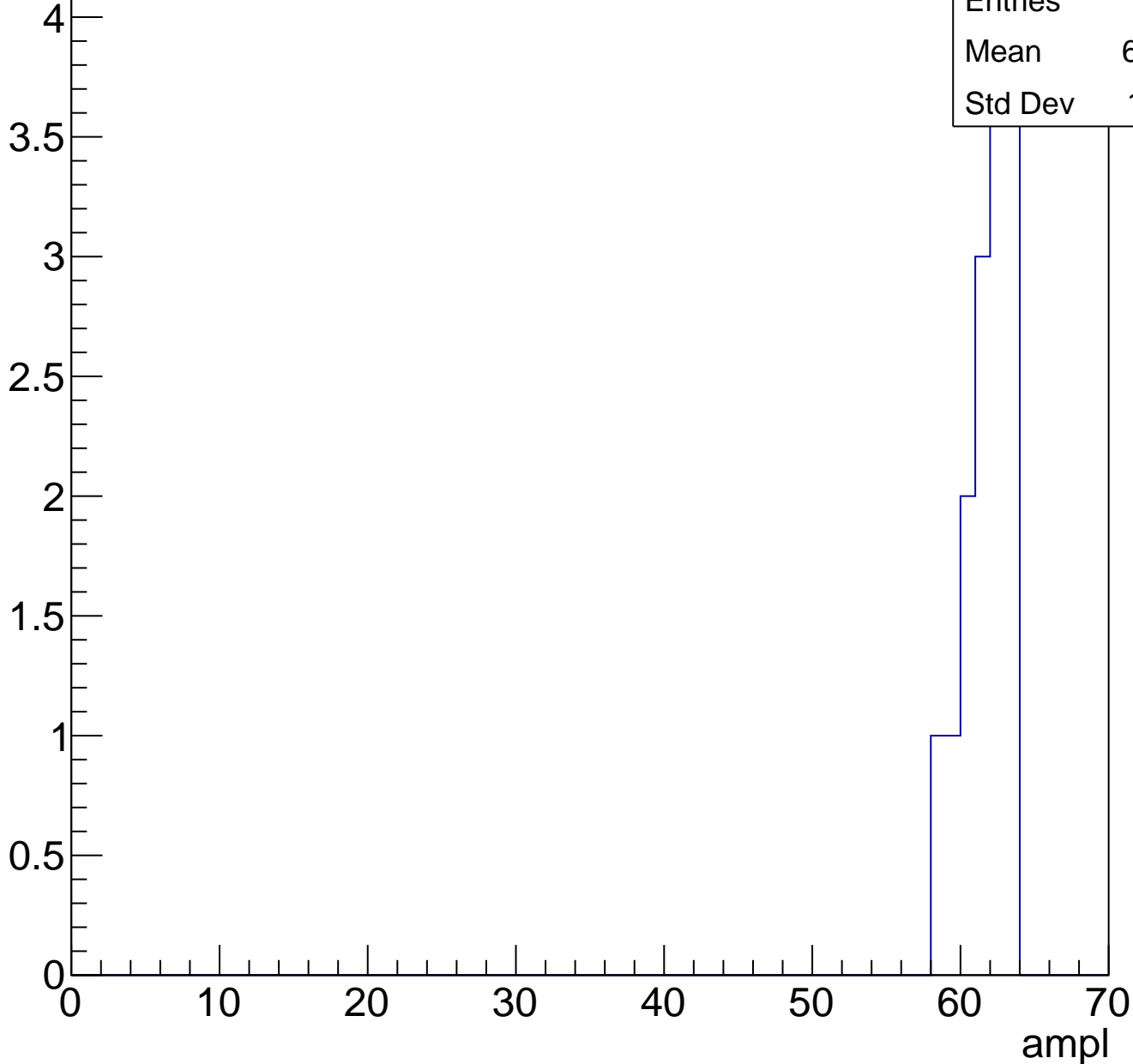
Entries	47
Mean	58.09
Std Dev	9.008



B1L102S, U6-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch97, adc0

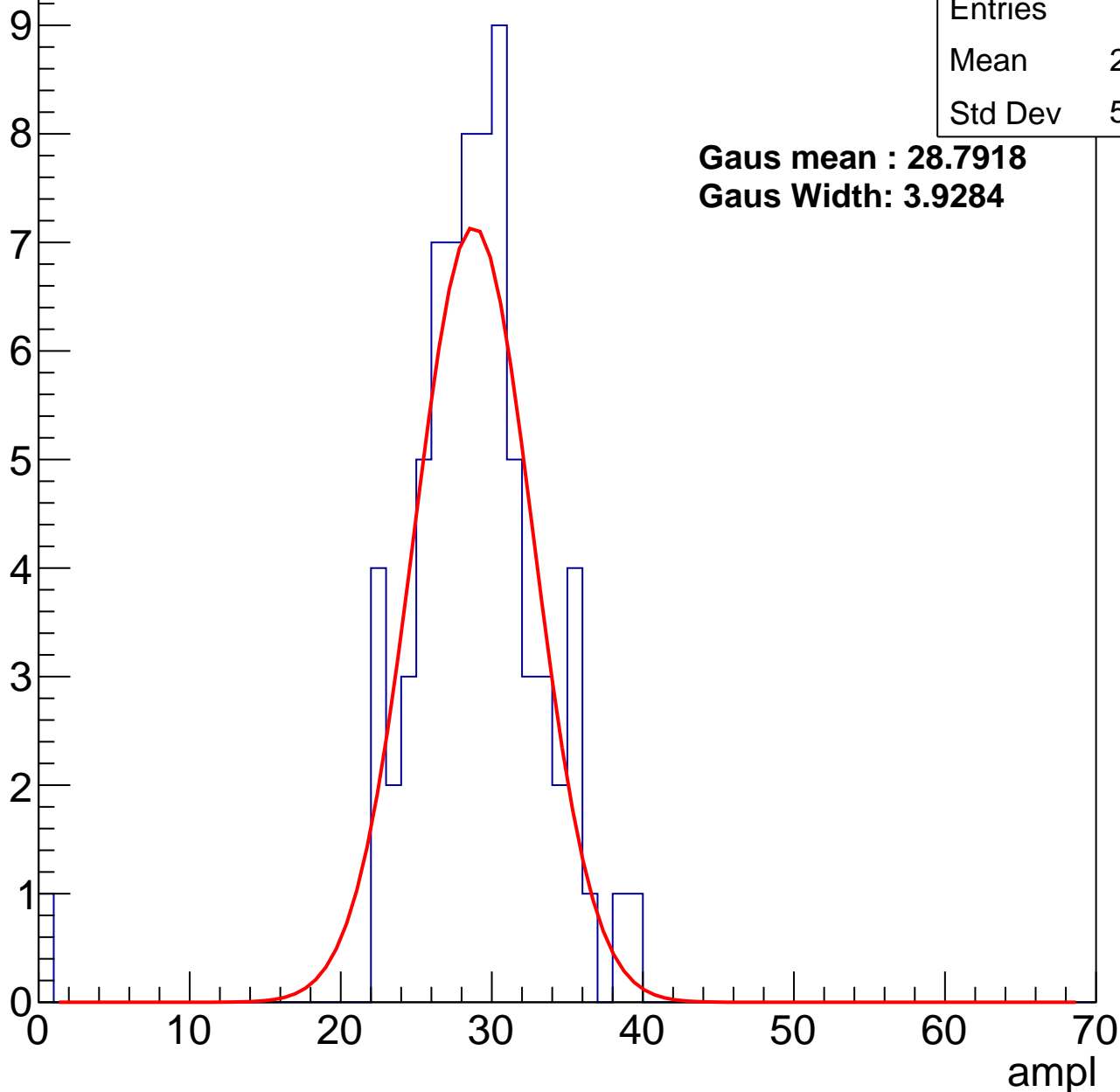
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.36
Std Dev	5.015

Gaus mean : 28.7918

Gaus Width: 3.9284



B1L102S, U6-ch97, adc1

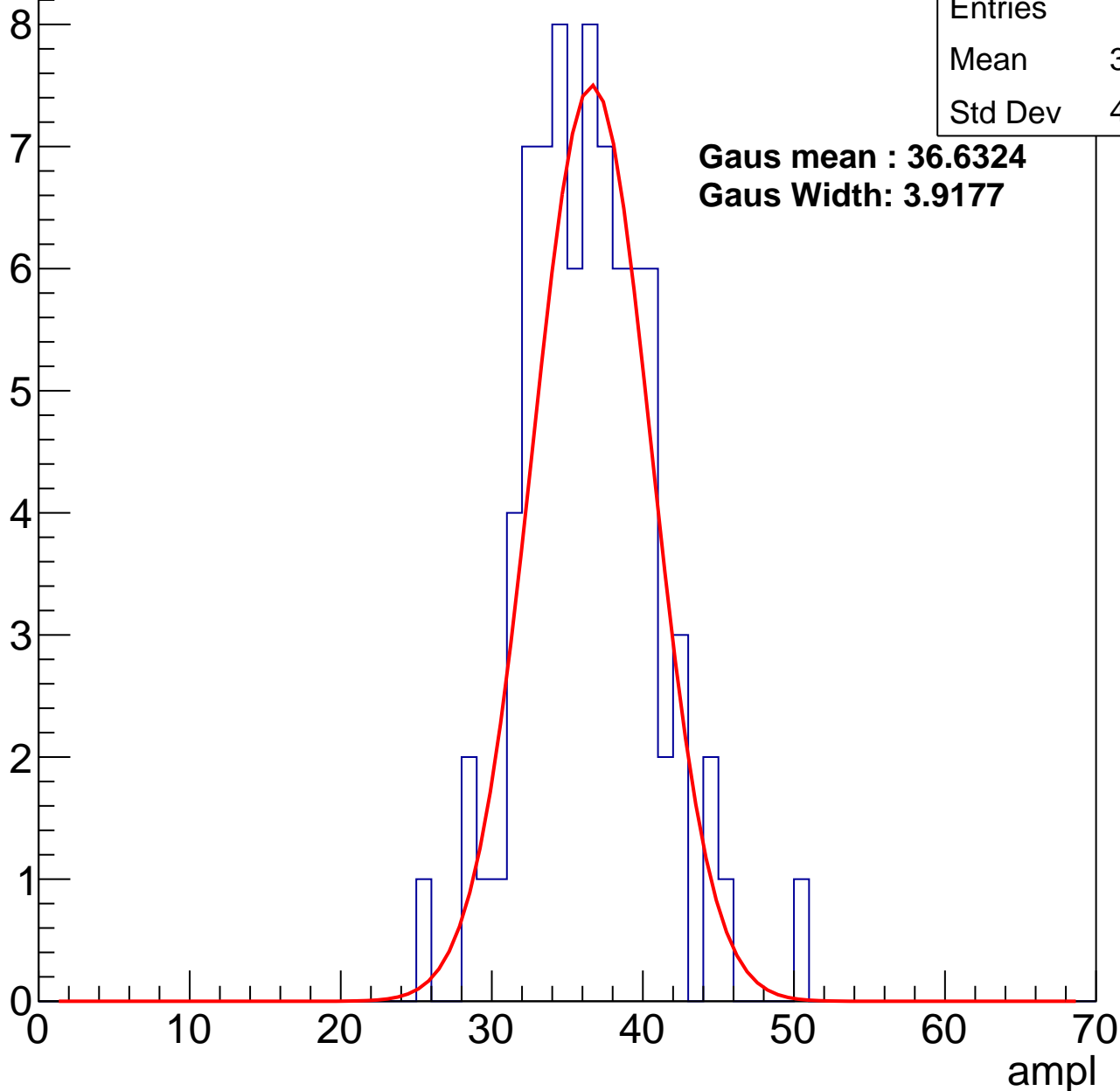
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.96
Std Dev	4.196

Gaus mean : 36.6324

Gaus Width: 3.9177



B1L102S, U6-ch97, adc2

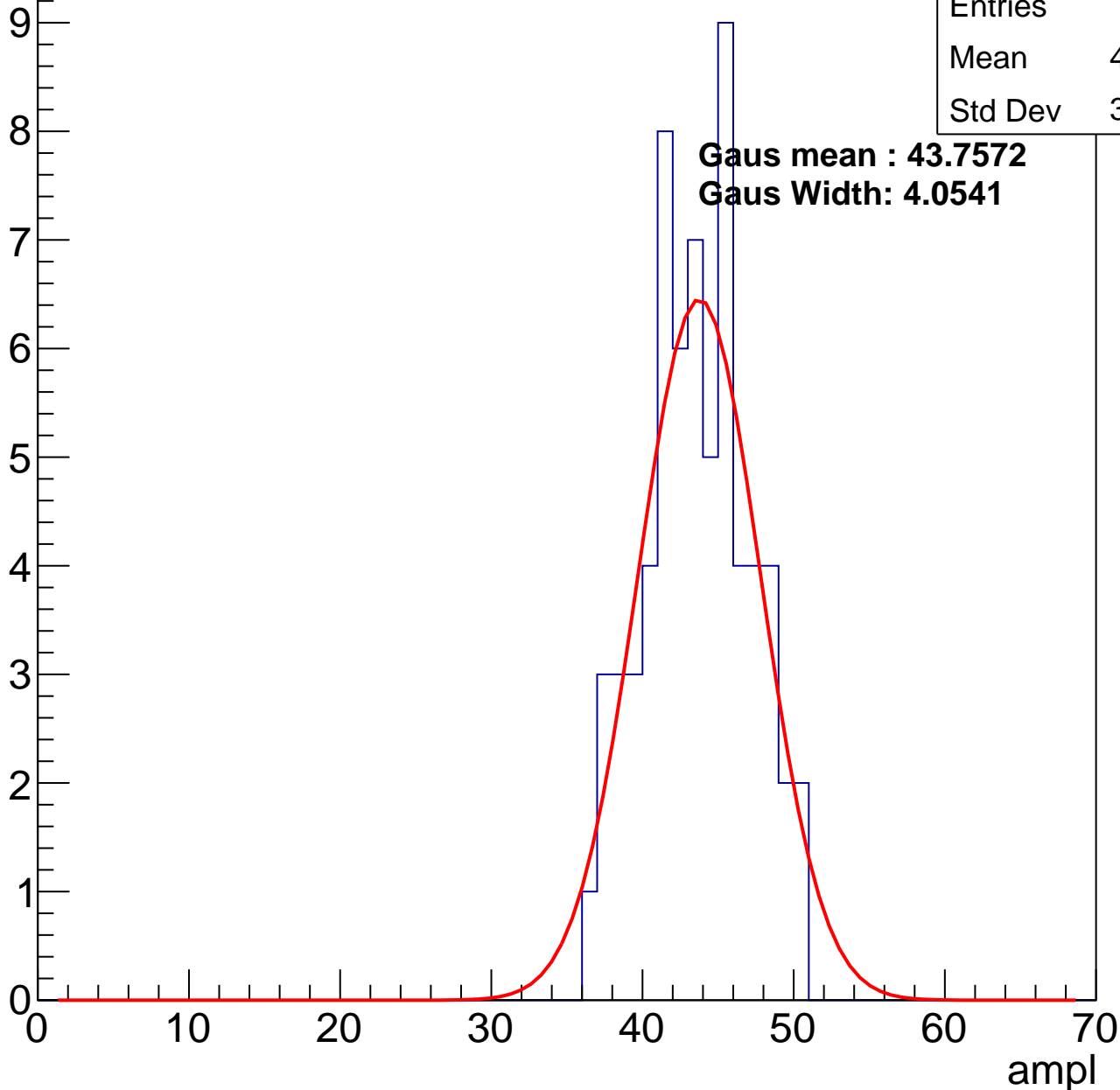
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.17
Std Dev	3.426

Gaus mean : 43.7572

Gaus Width: 4.0541

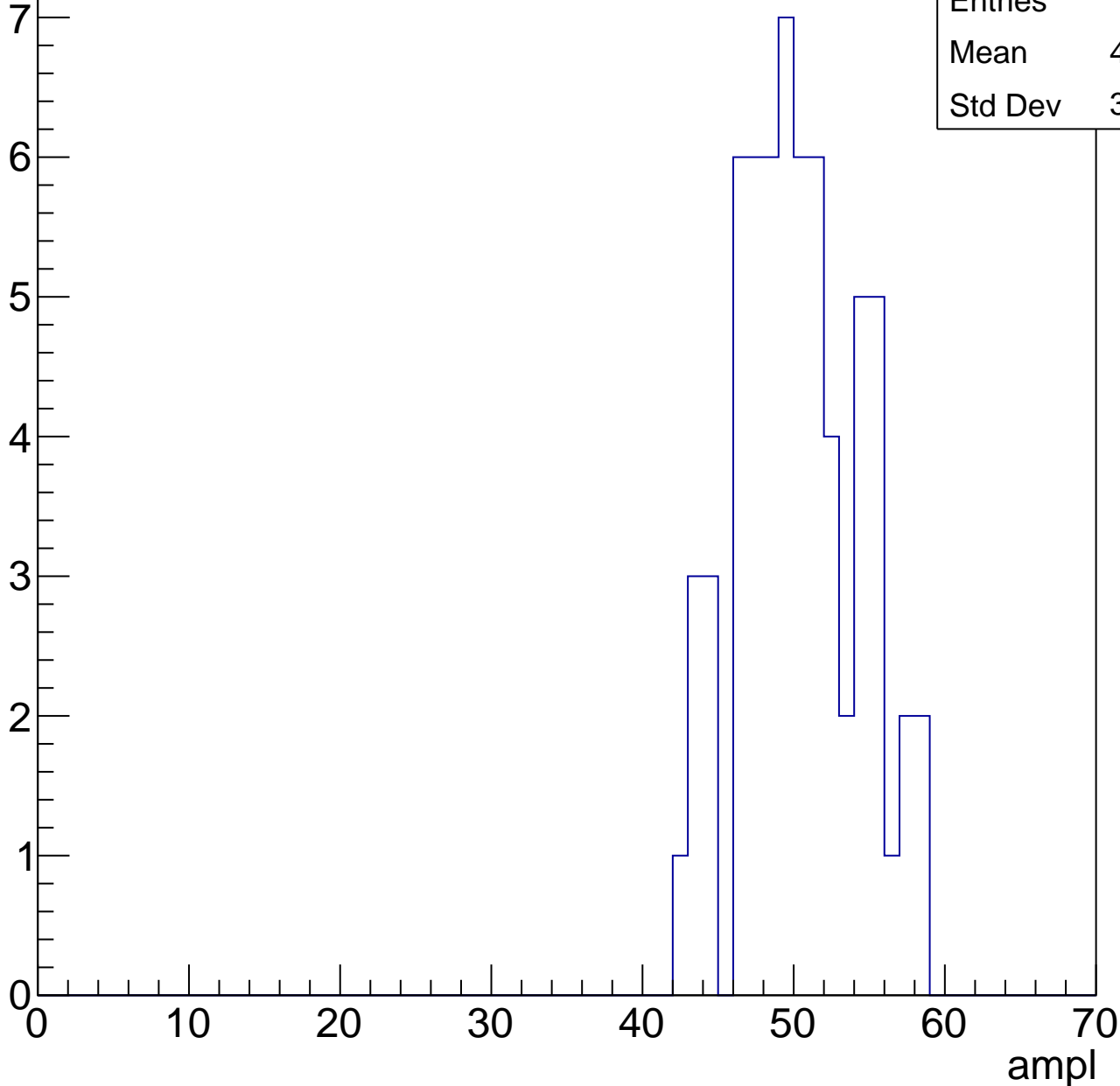


B1L102S, U6-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	49.89
Std Dev	3.935

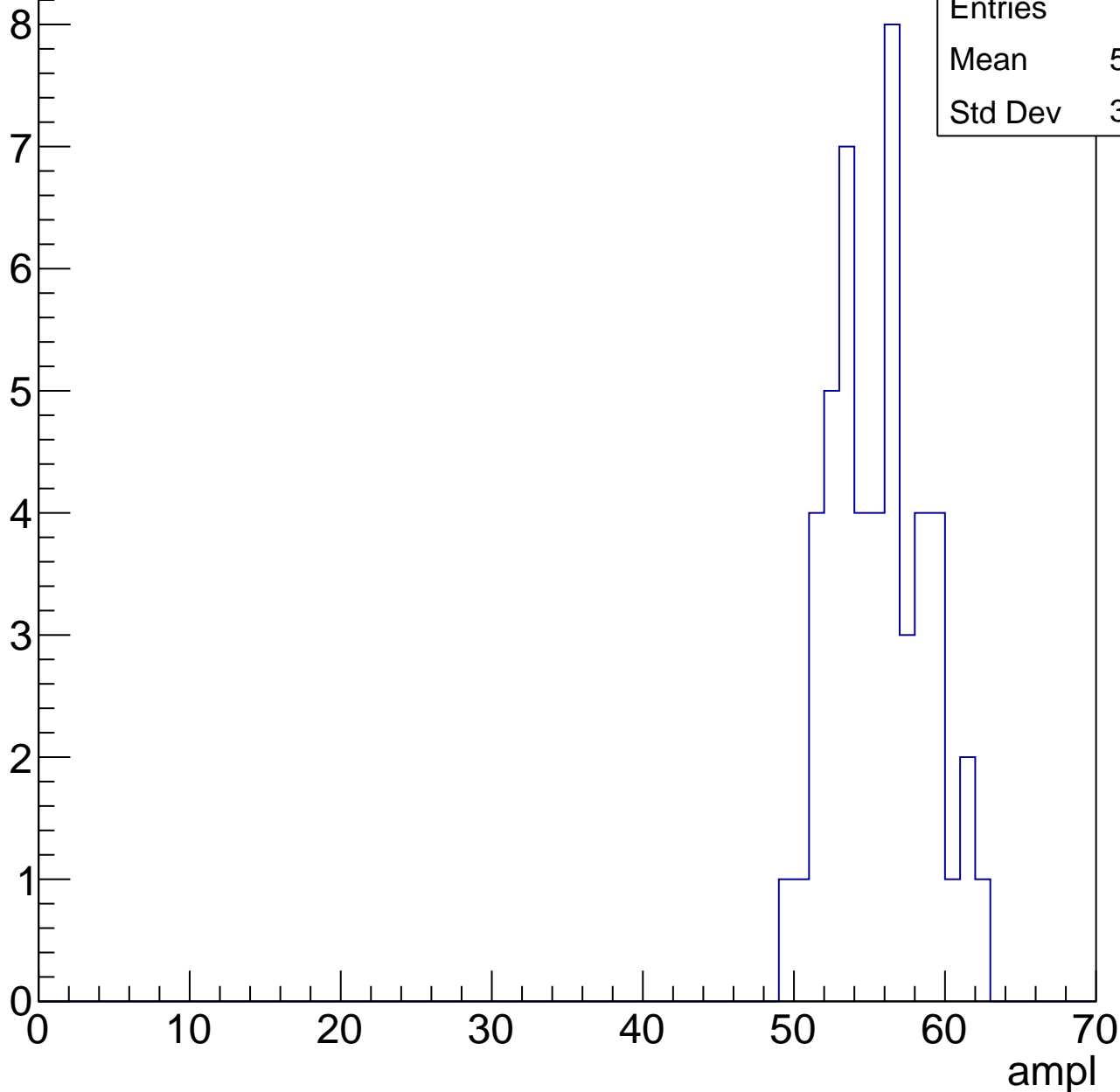


B1L102S, U6-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.12
Std Dev	3.088



B1L102S, U6-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries

47

Mean

58.28

Std Dev

9

ampl

0

10

20

30

40

50

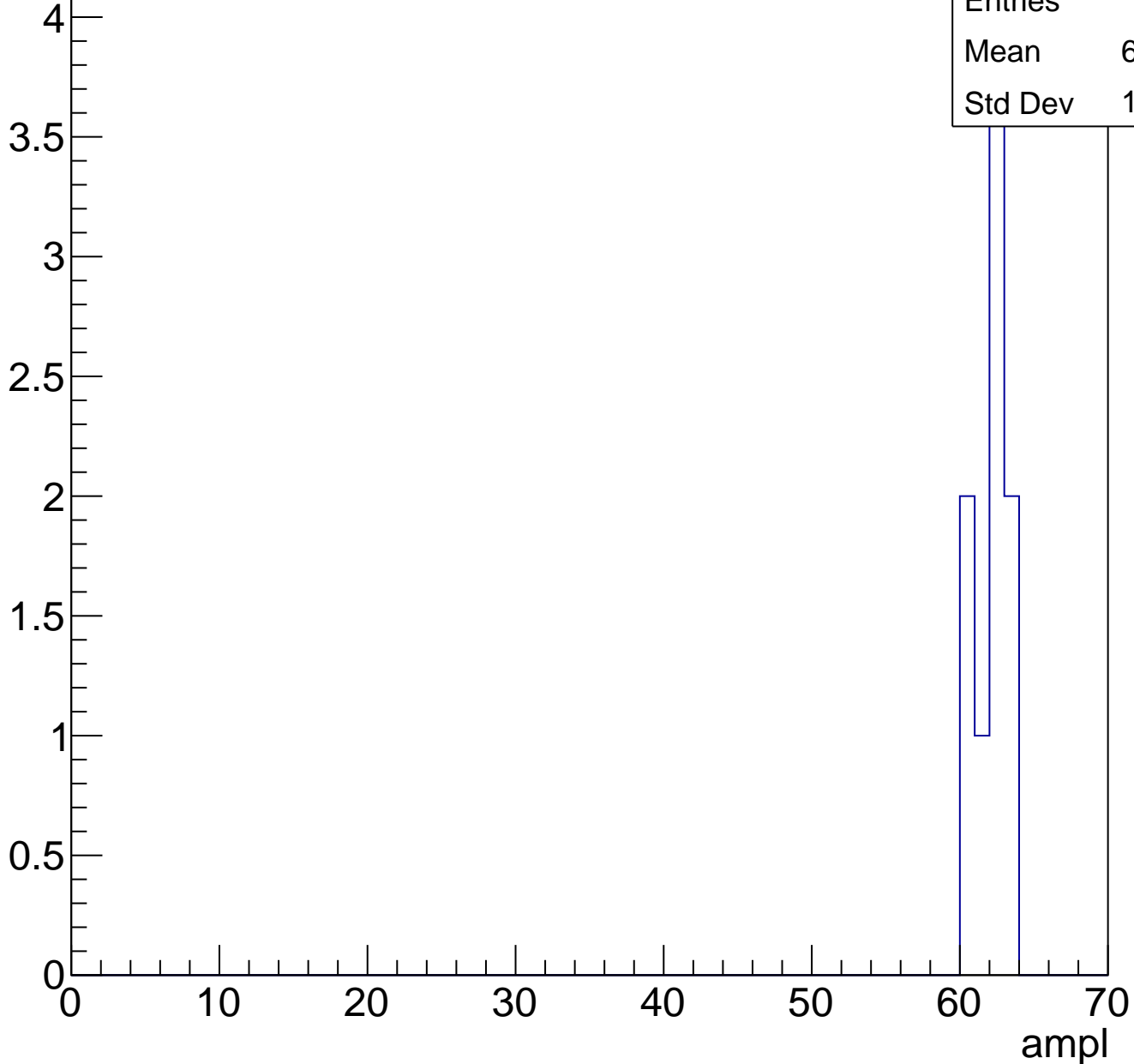
60

70

B1L102S, U6-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch98, adc0

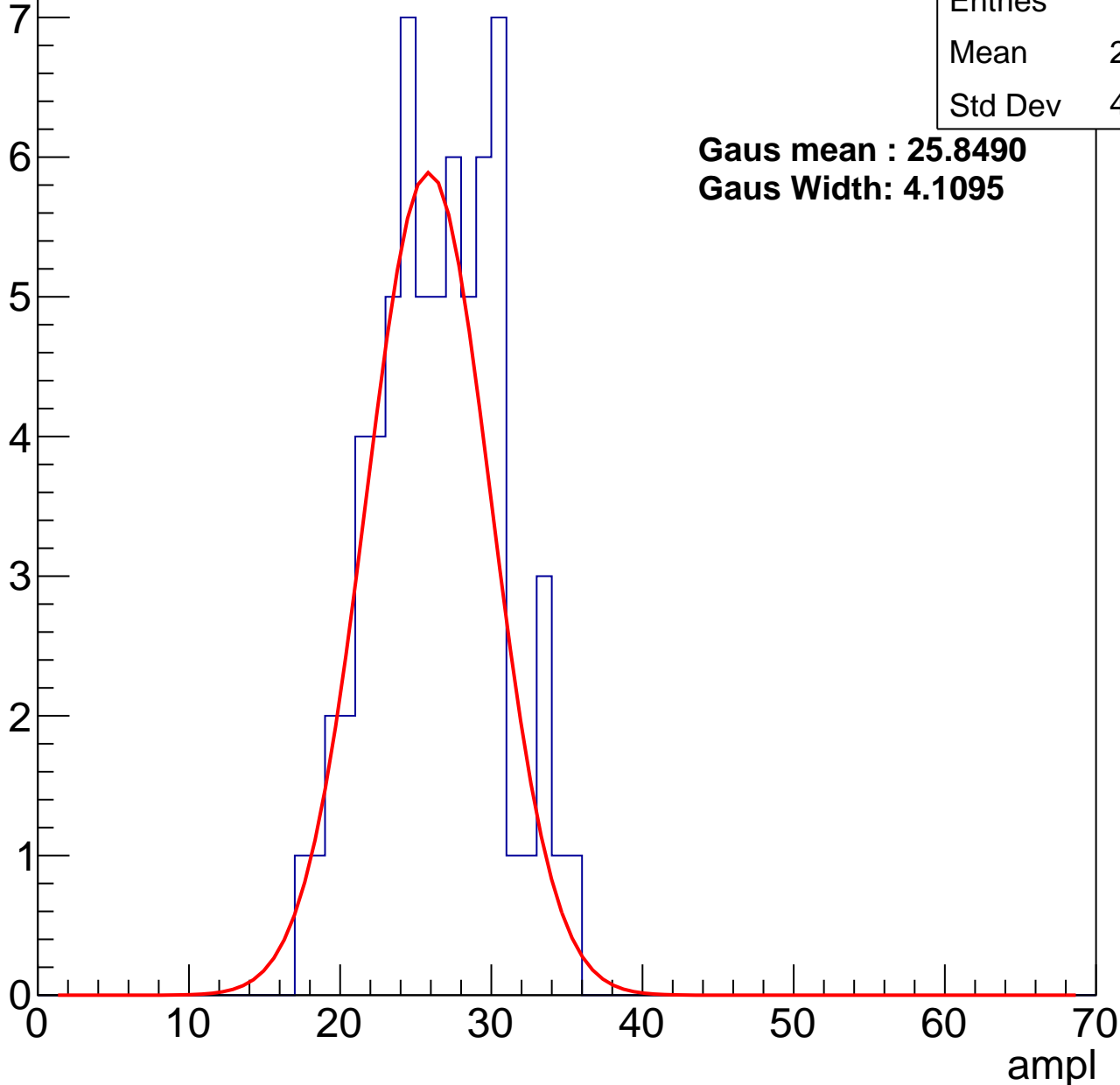
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	25.97
Std Dev	4.055

Gaus mean : 25.8490

Gaus Width: 4.1095



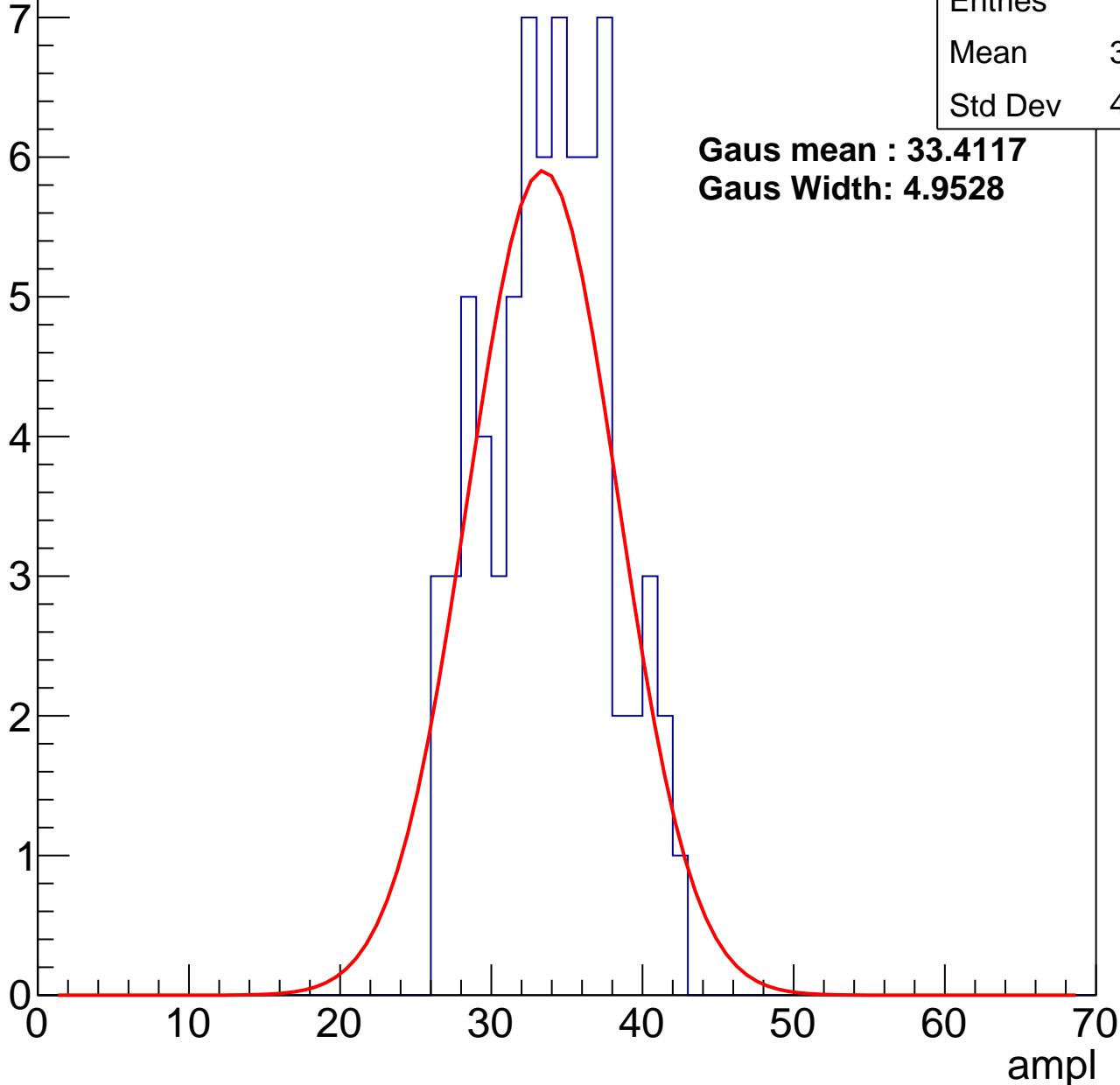
B1L102S, U6-ch98, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	33.38
Std Dev	4.019

Gaus mean : 33.4117
Gaus Width: 4.9528



B1L102S, U6-ch98, adc2

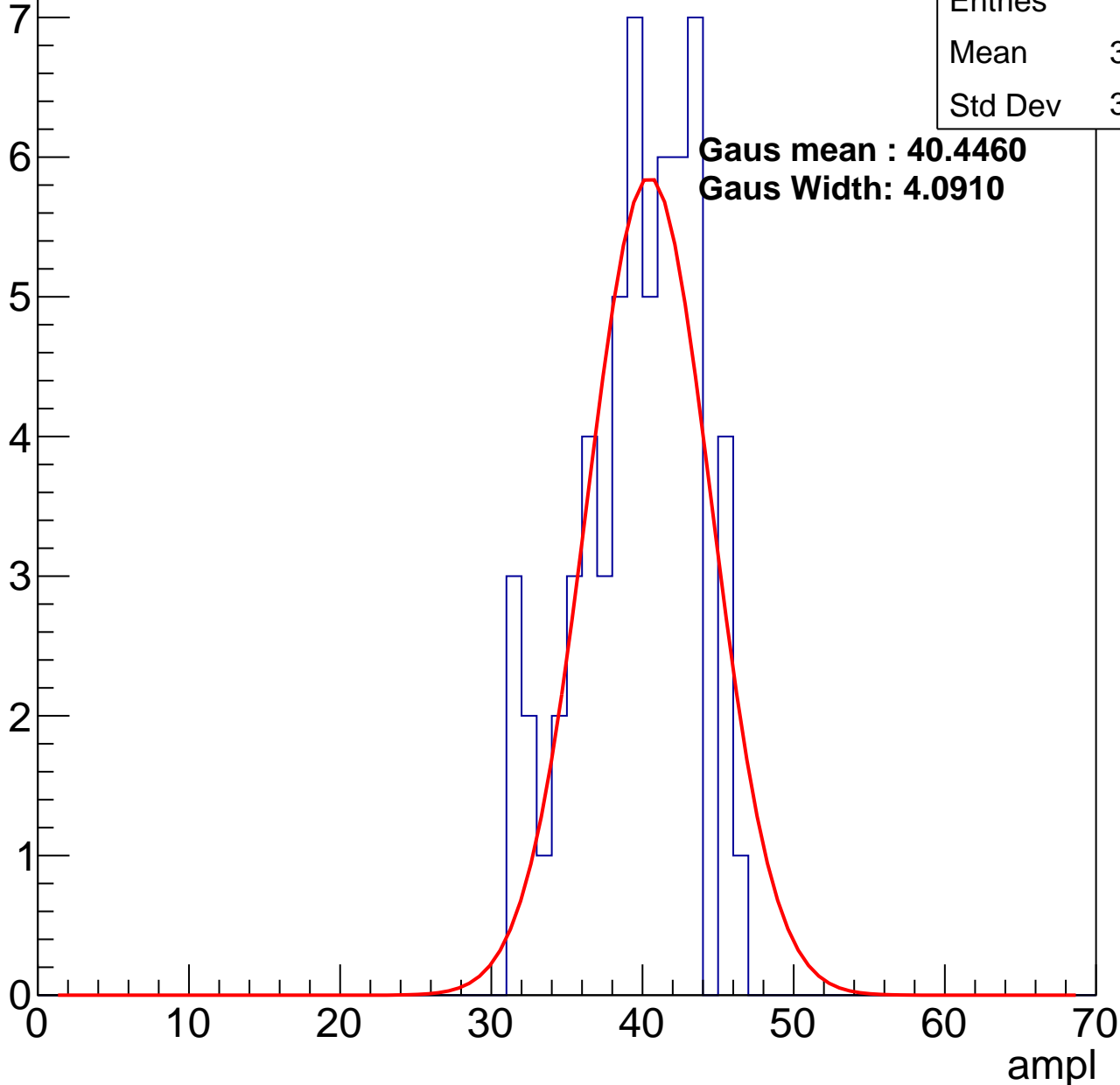
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	39.08
Std Dev	3.828

Gaus mean : 40.4460

Gaus Width: 4.0910



B1L102S, U6-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

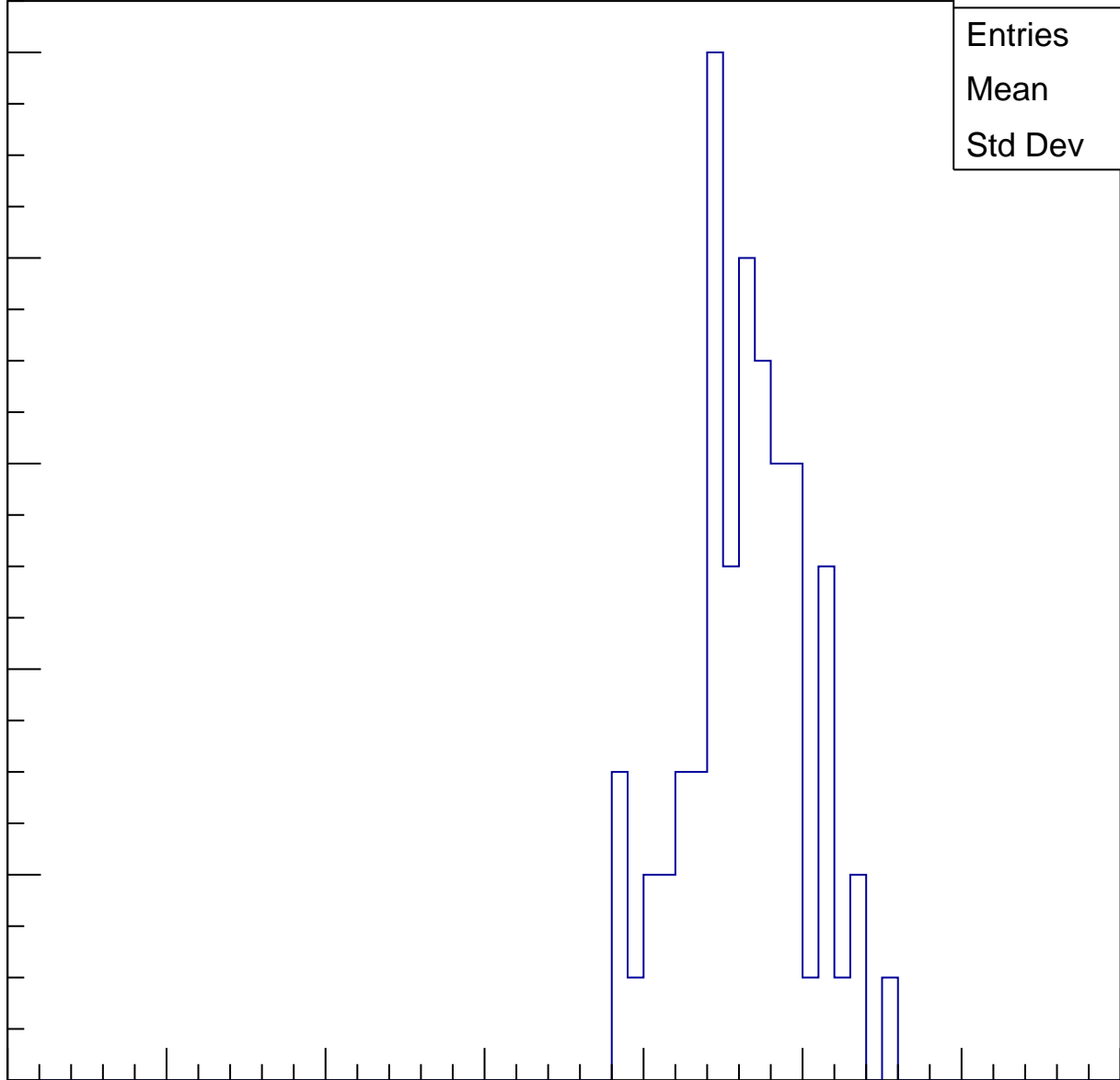
Entries	66
Mean	45.94
Std Dev	3.741

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

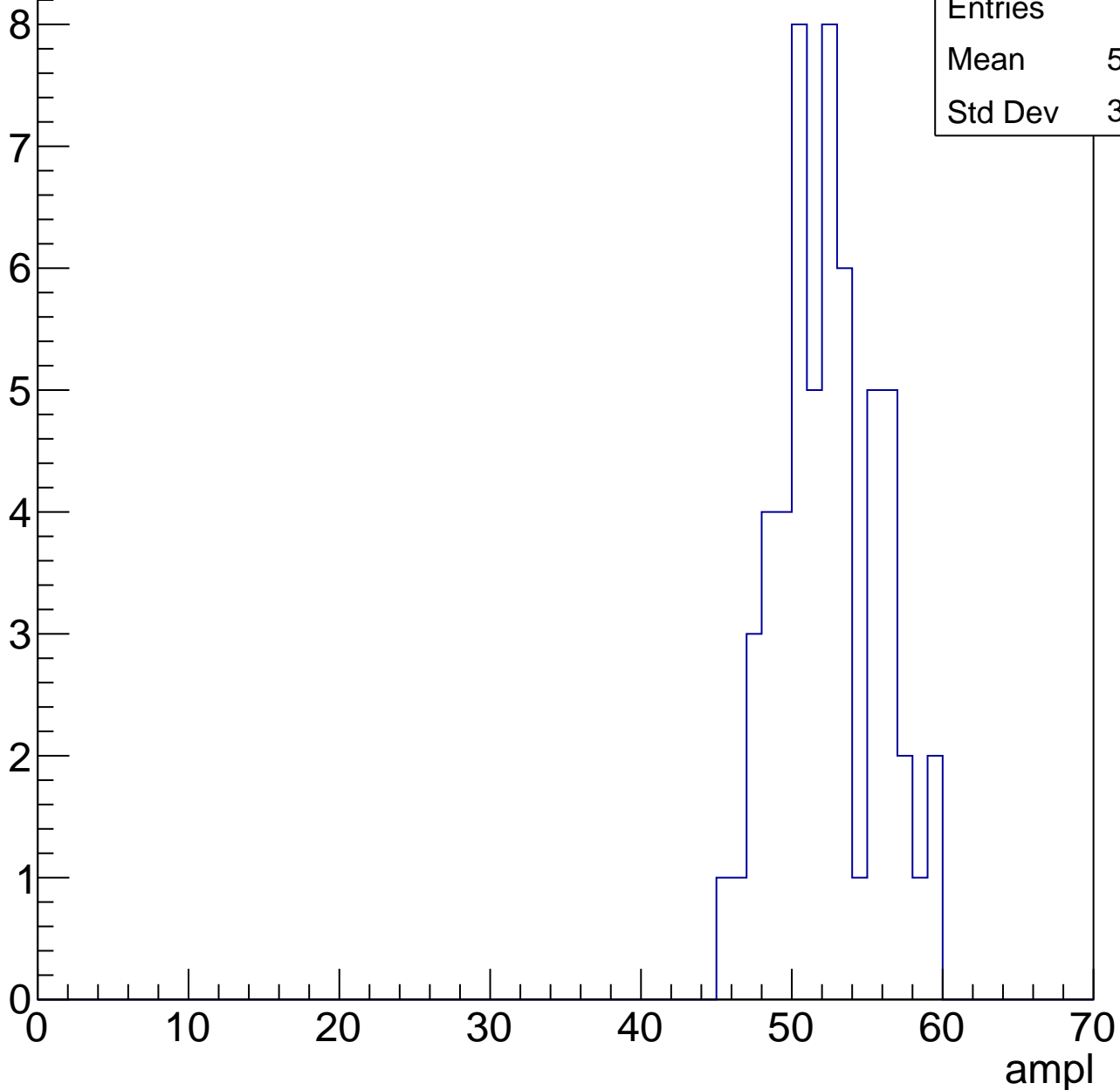


B1L102S, U6-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	51.93
Std Dev	3.316



B1L102S, U6-ch98, adc5

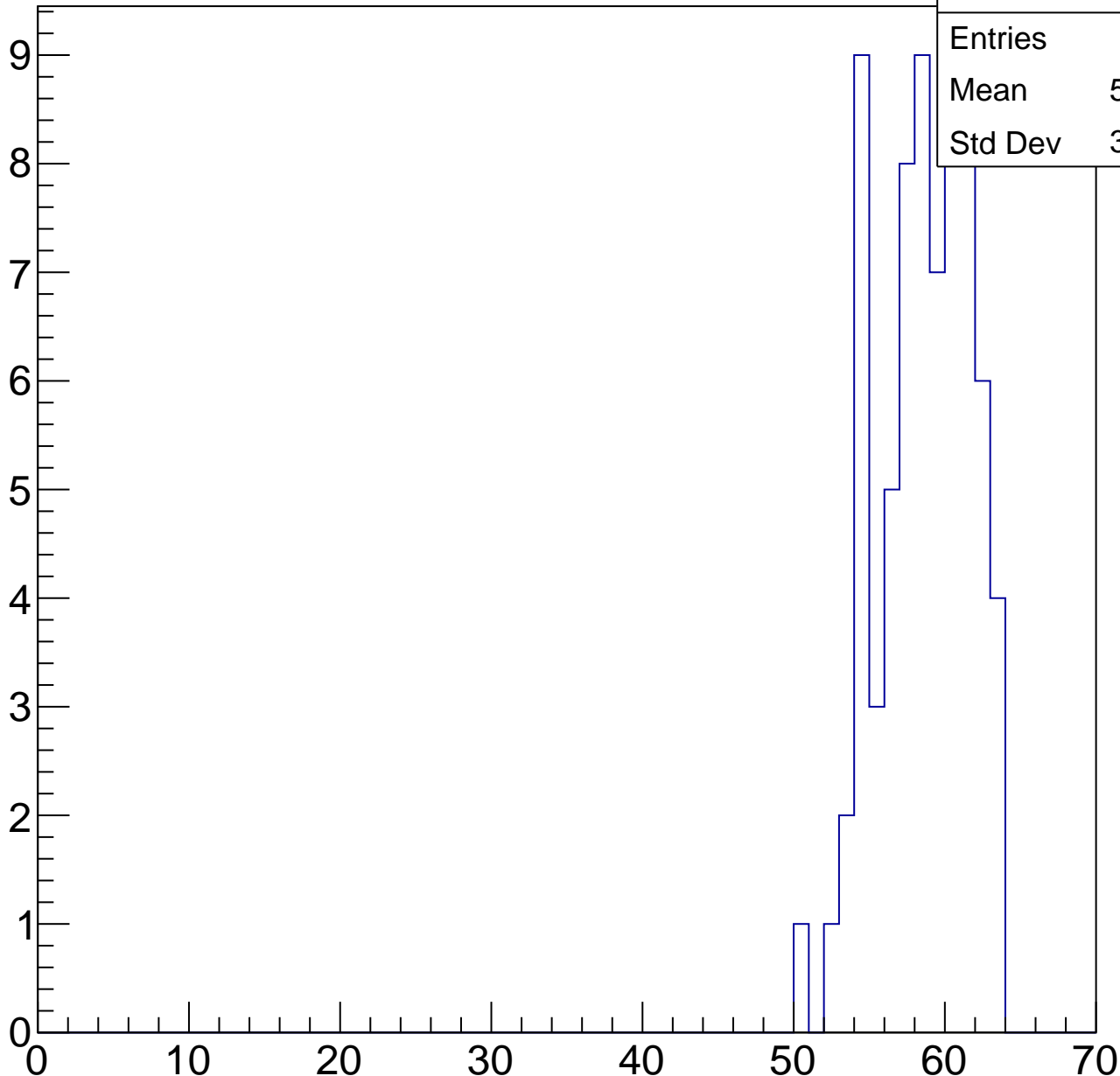
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	72
Mean	58.08
Std Dev	3.027

ampl

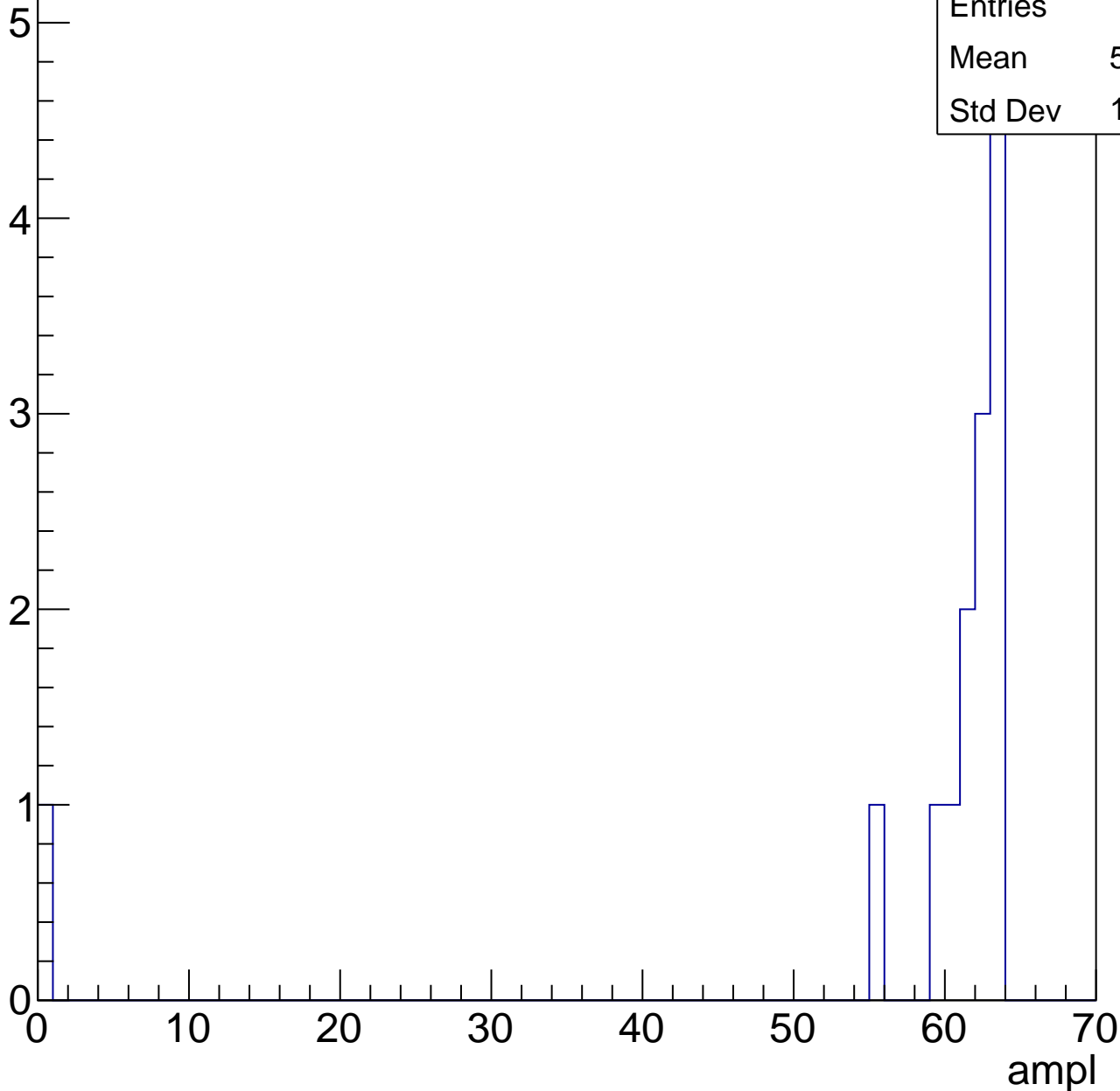


B1L102S, U6-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

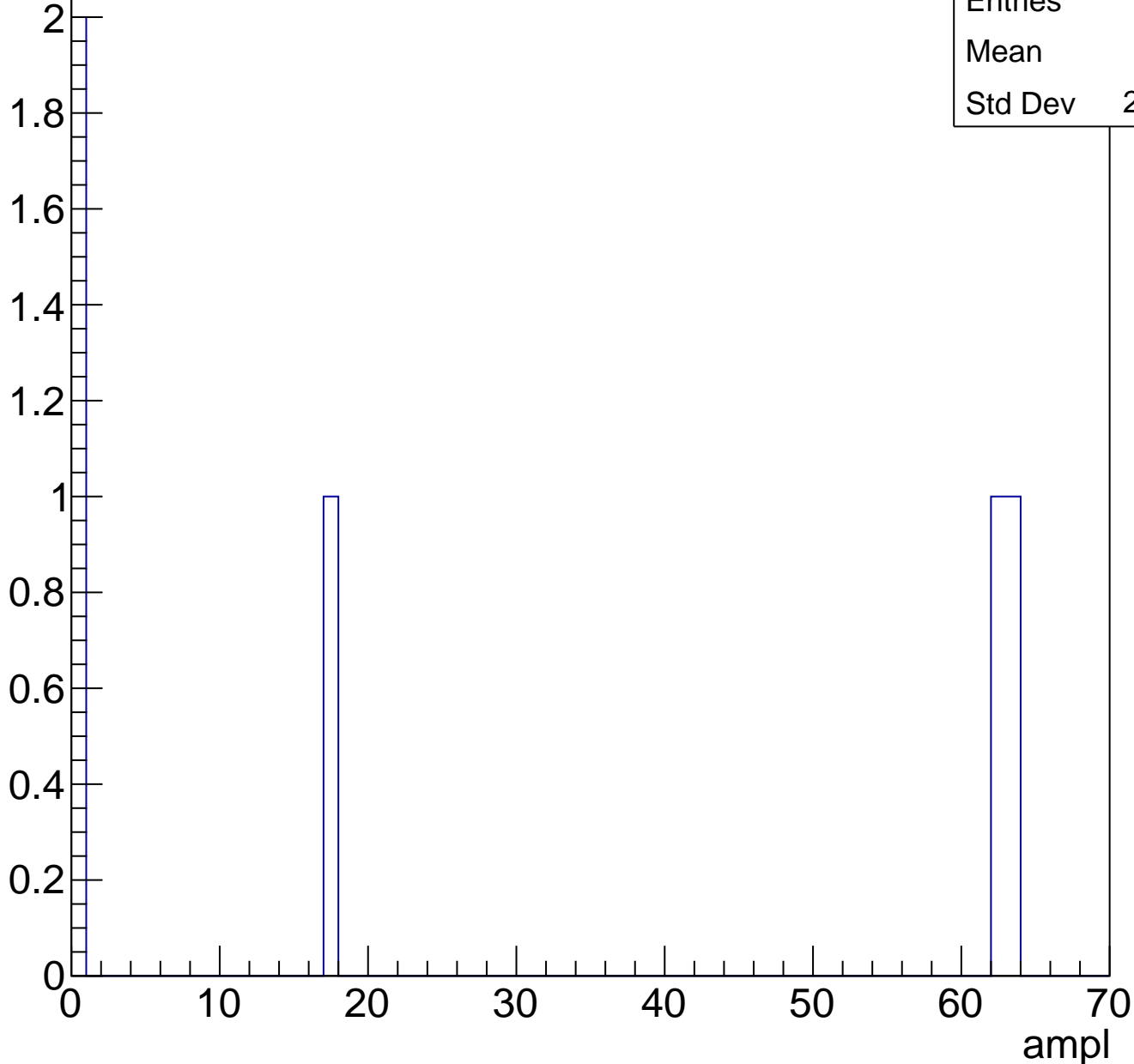
Entries	14
Mean	56.93
Std Dev	15.93



B1L102S, U6-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	28.4
Std Dev	28.53

B1L102S, U6-ch99, adc0

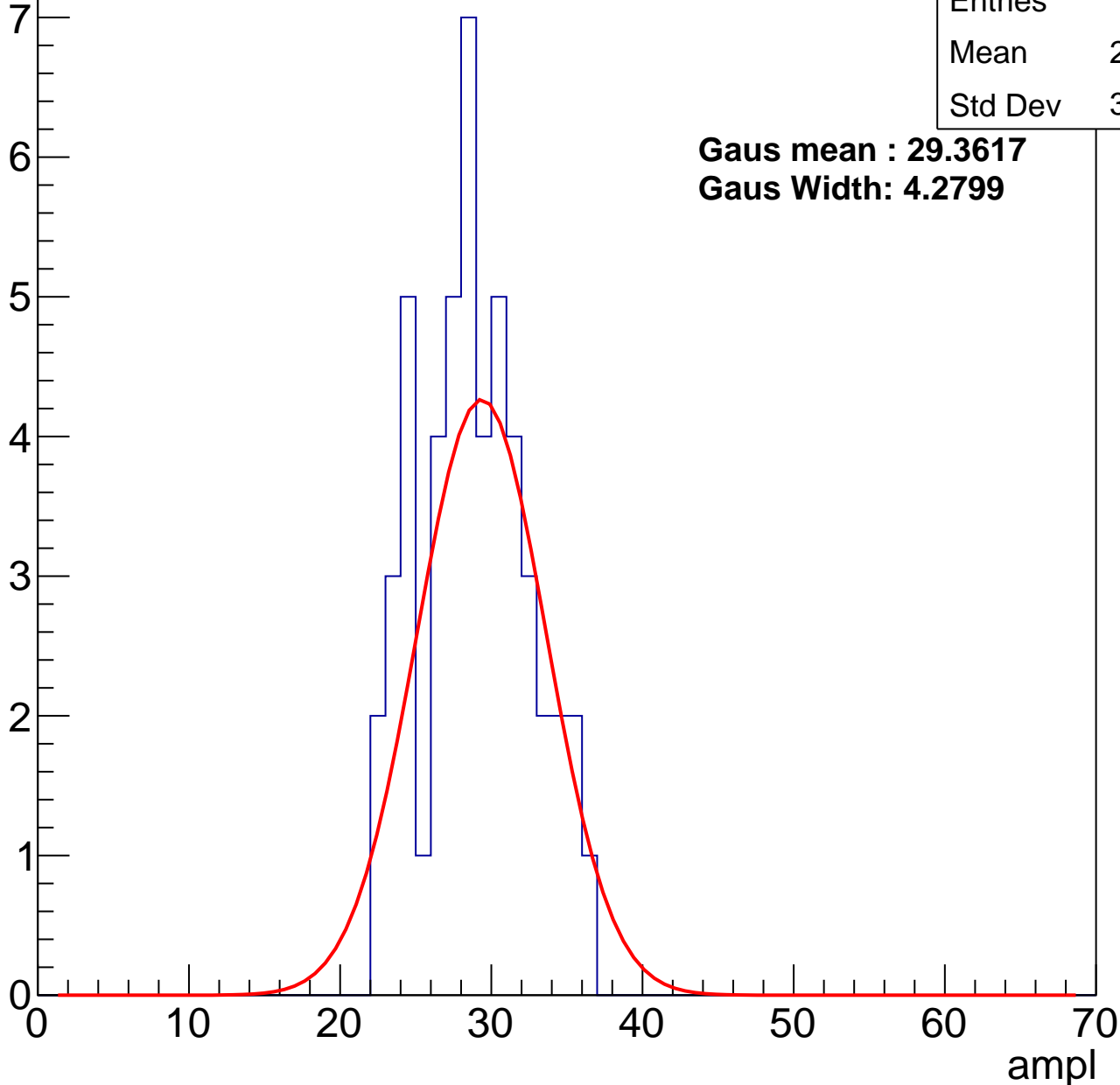
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	28.38
Std Dev	3.583

Gaus mean : 29.3617

Gaus Width: 4.2799



B1L102S, U6-ch99, adc1

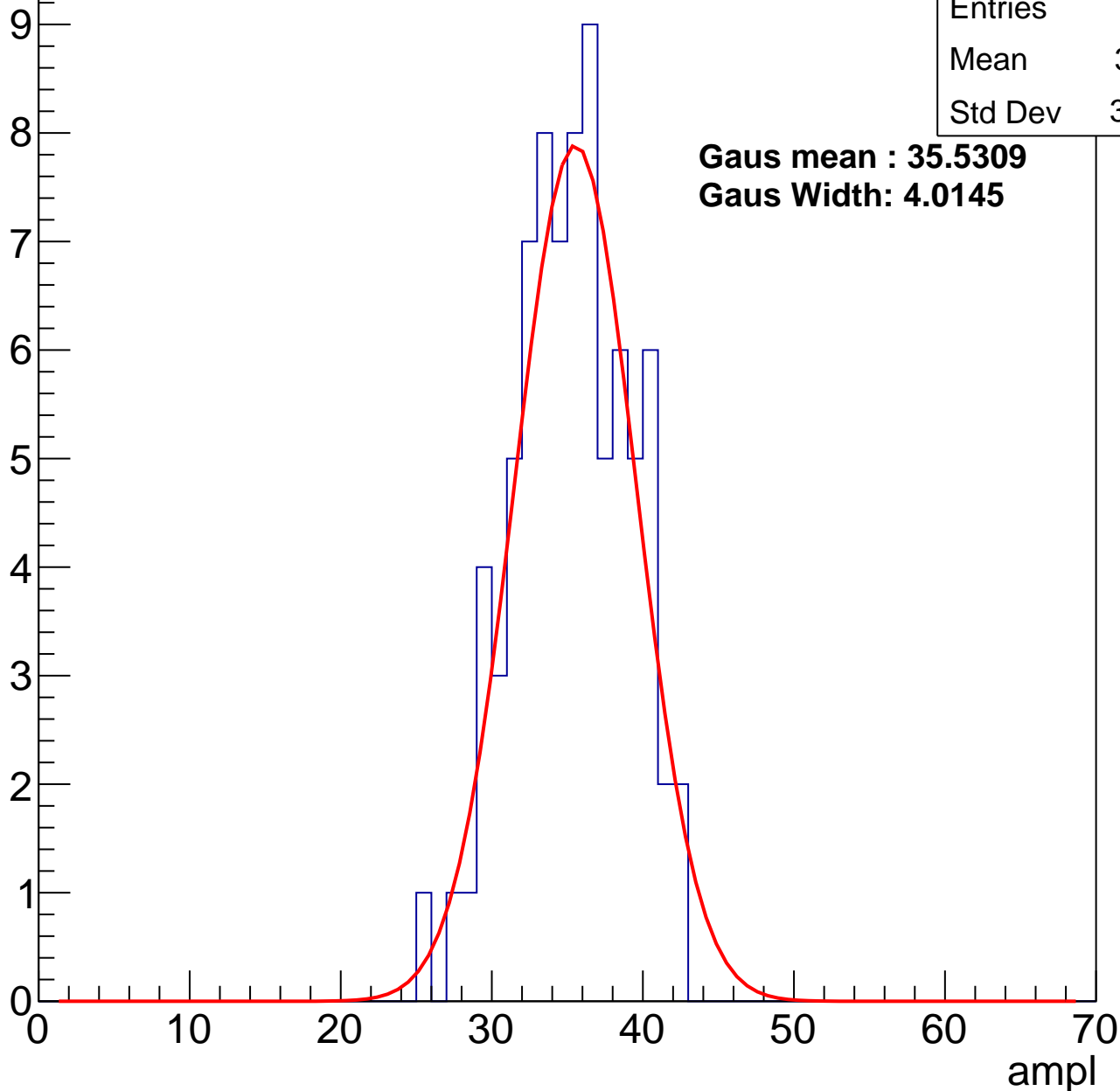
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.81
Std Dev	3.698

Gaus mean : 35.5309

Gaus Width: 4.0145



B1L102S, U6-ch99, adc2

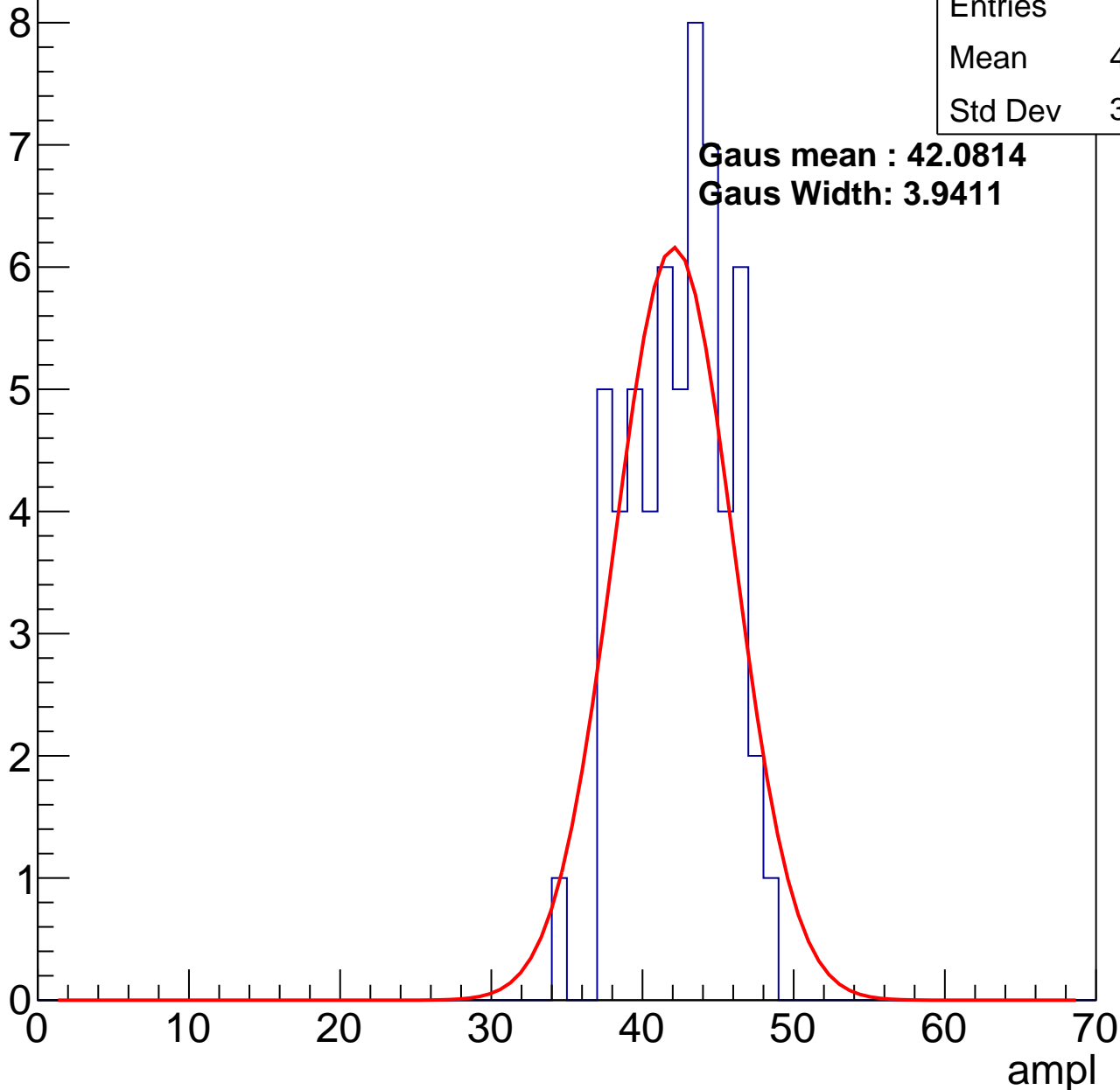
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.93
Std Dev	3.145

Gaus mean : 42.0814

Gaus Width: 3.9411

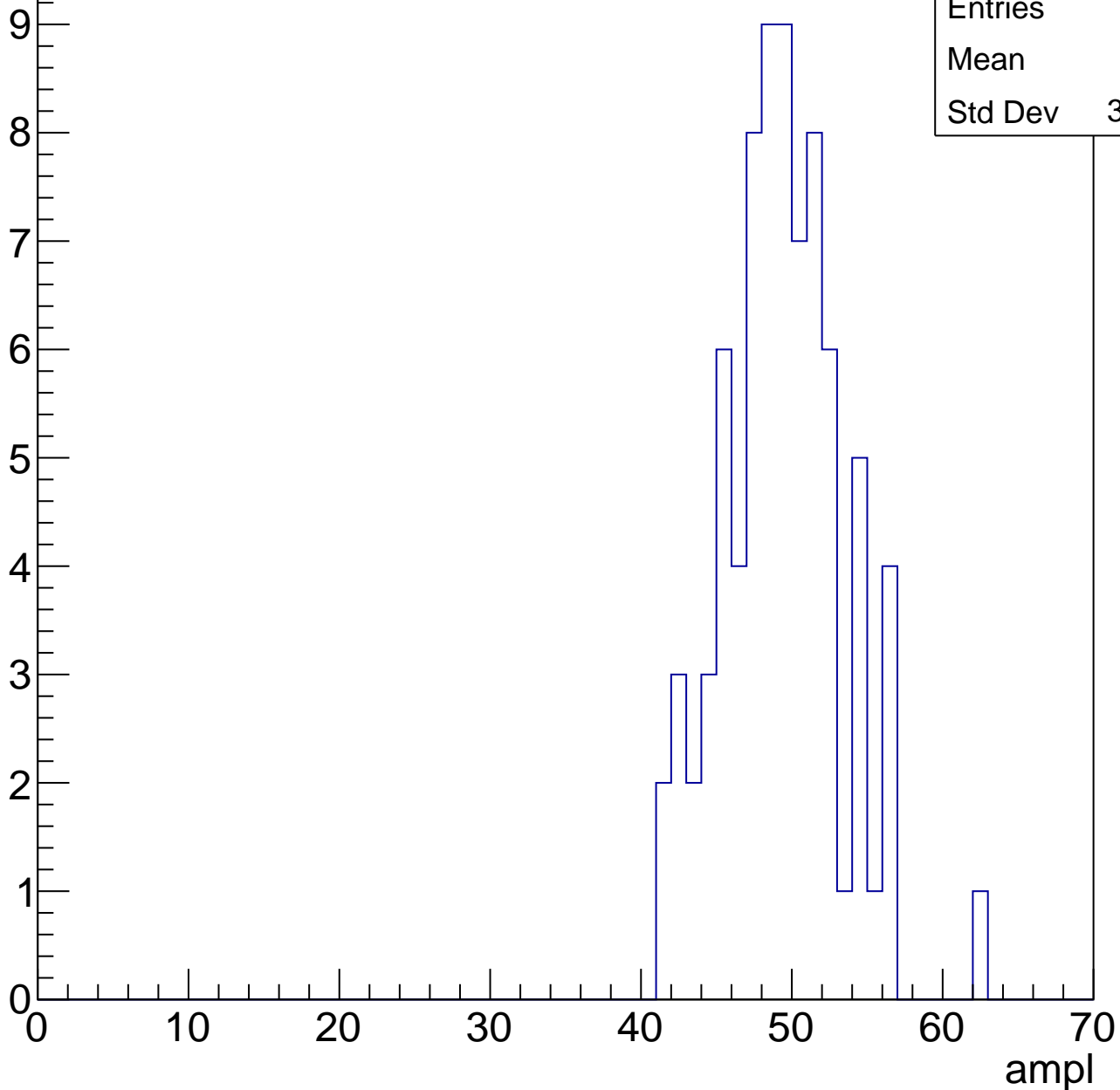


B1L102S, U6-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	48.9
Std Dev	3.945

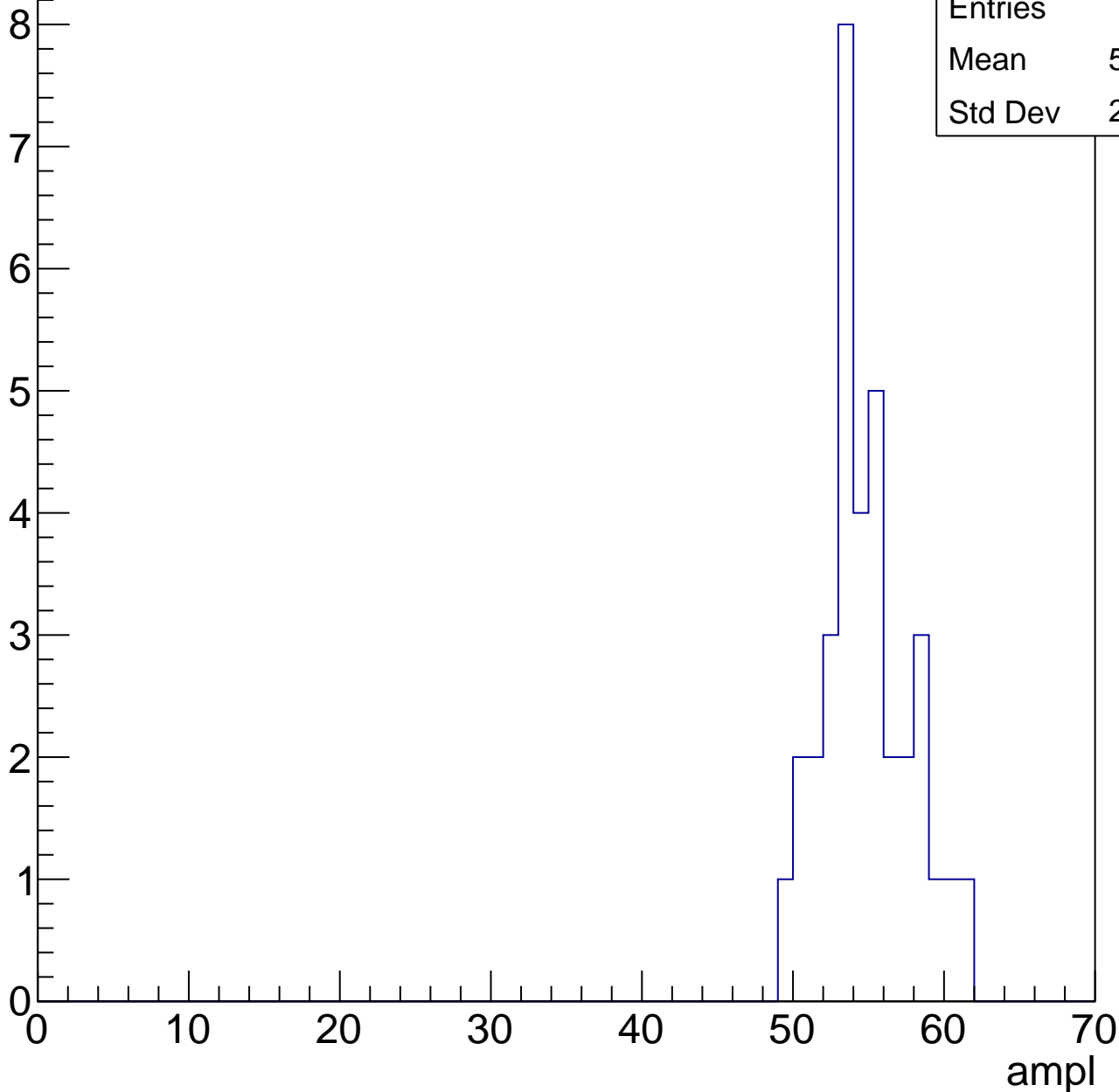


B1L102S, U6-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	35
Mean	54.34
Std Dev	2.828

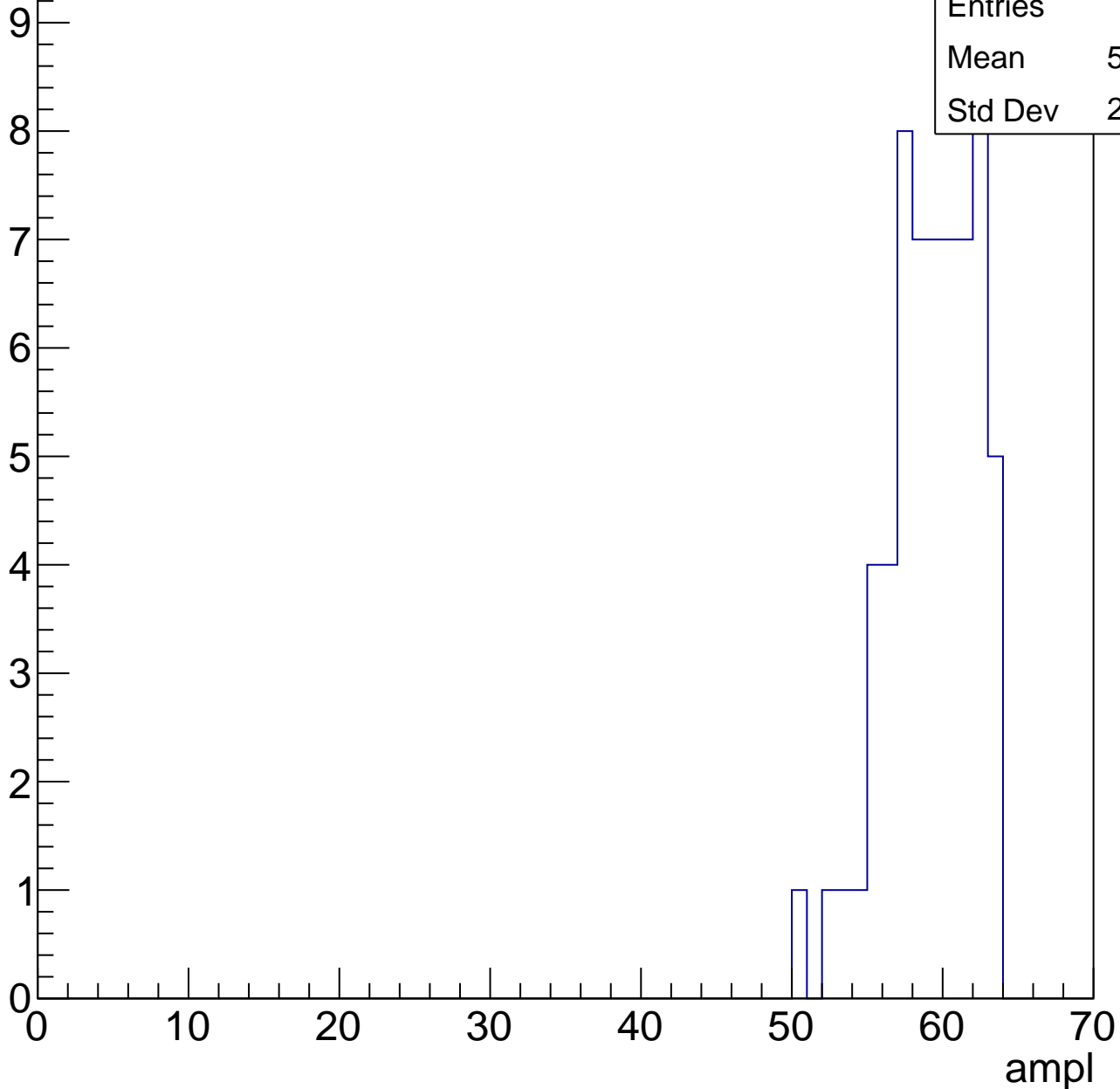


B1L102S, U6-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

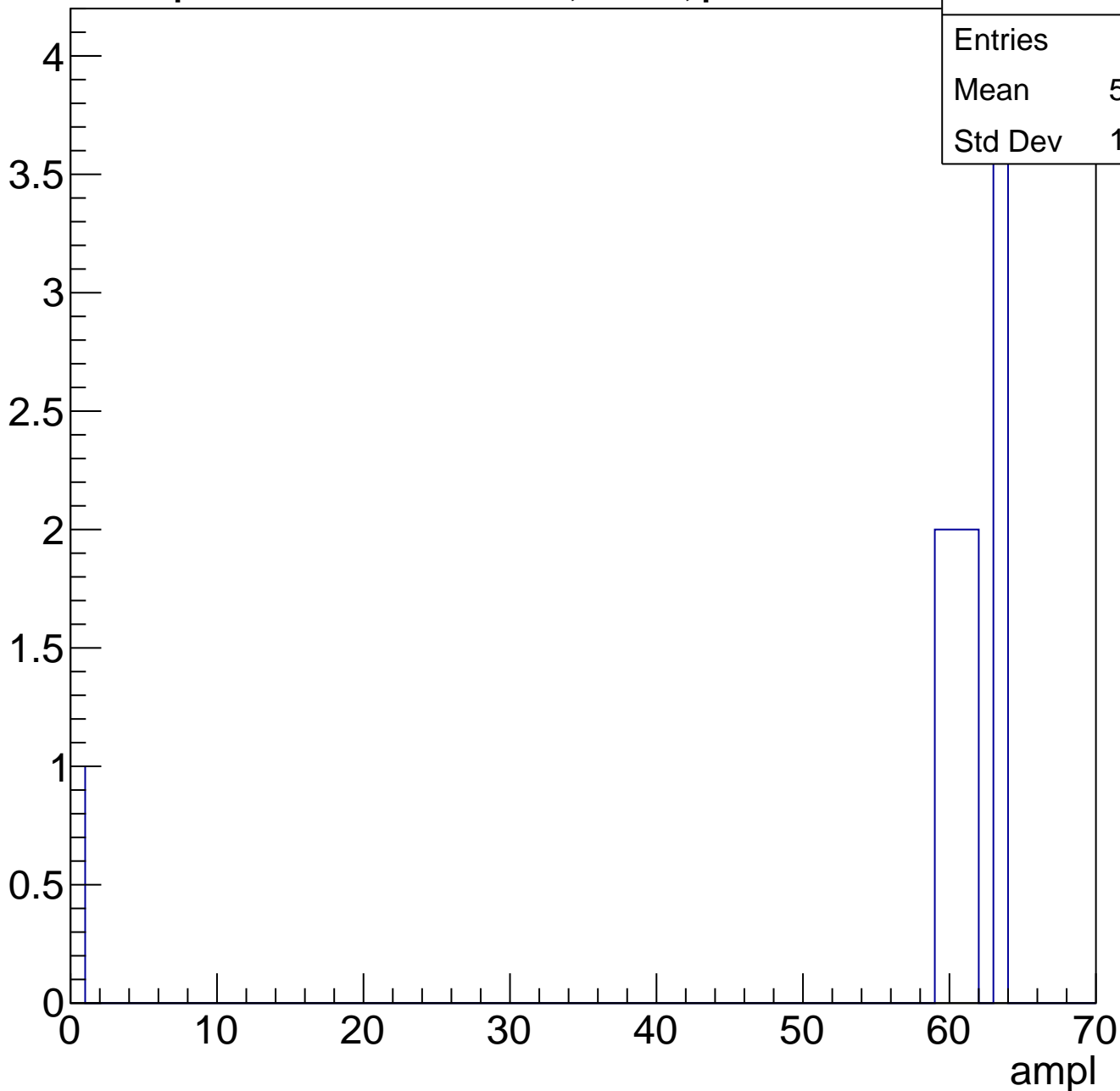
Entries	62
Mean	58.84
Std Dev	2.908



B1L102S, U6-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L102S, U6-ch100, adc0

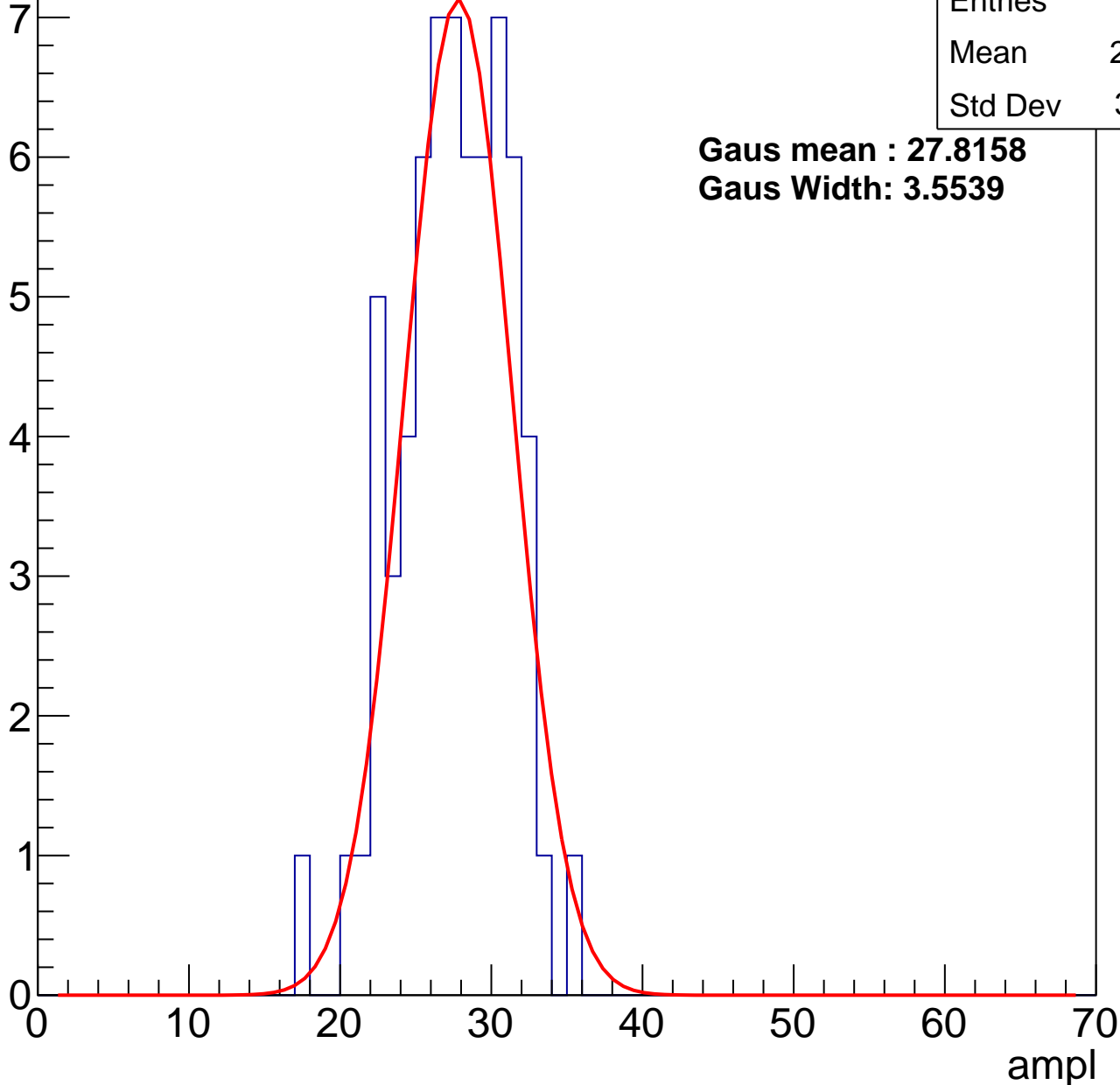
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.09
Std Dev	3.511

Gaus mean : 27.8158

Gaus Width: 3.5539



B1L102S, U6-ch100, adc1

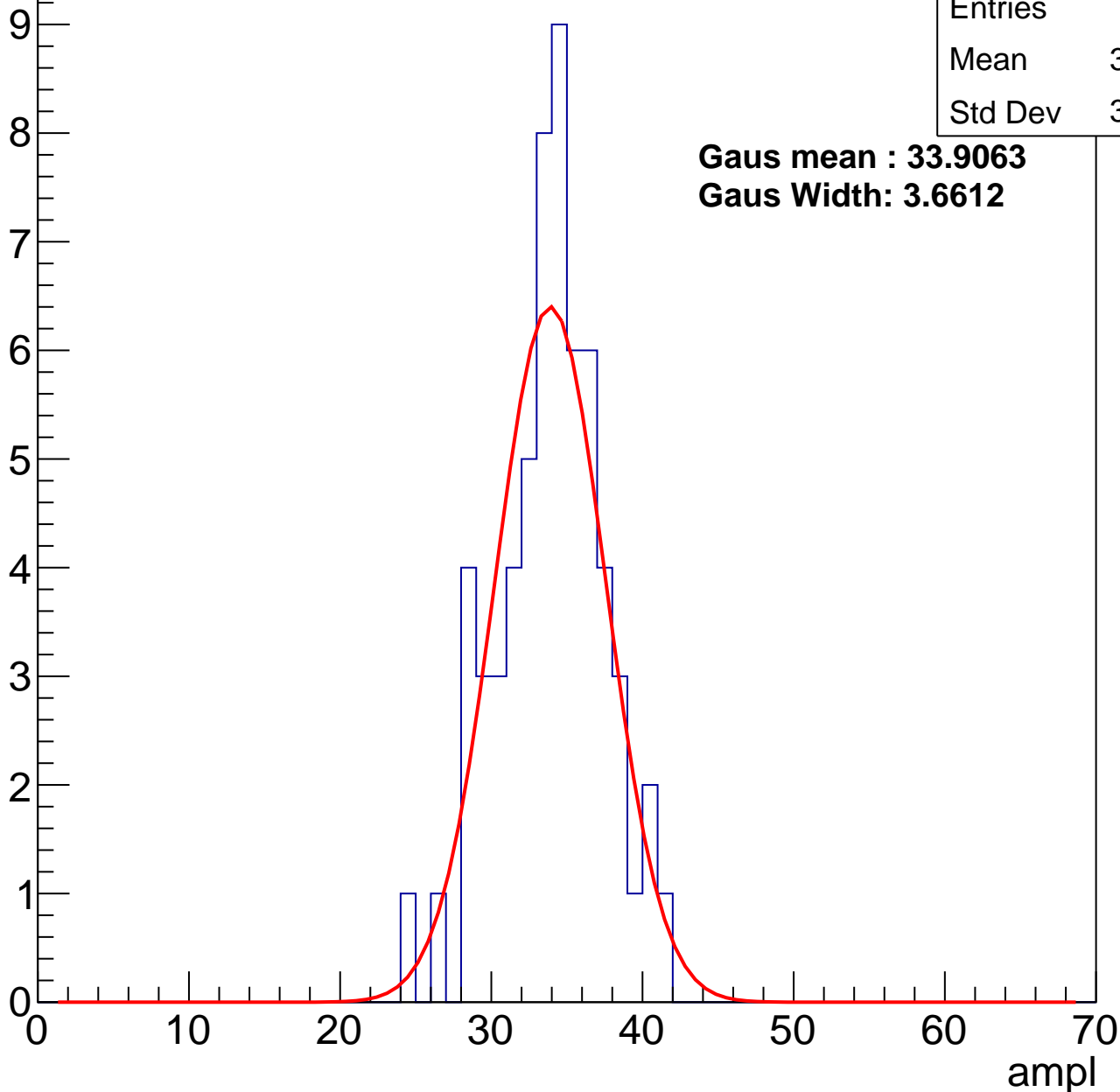
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	33.46
Std Dev	3.476

Gaus mean : 33.9063

Gaus Width: 3.6612



B1L102S, U6-ch100, adc2

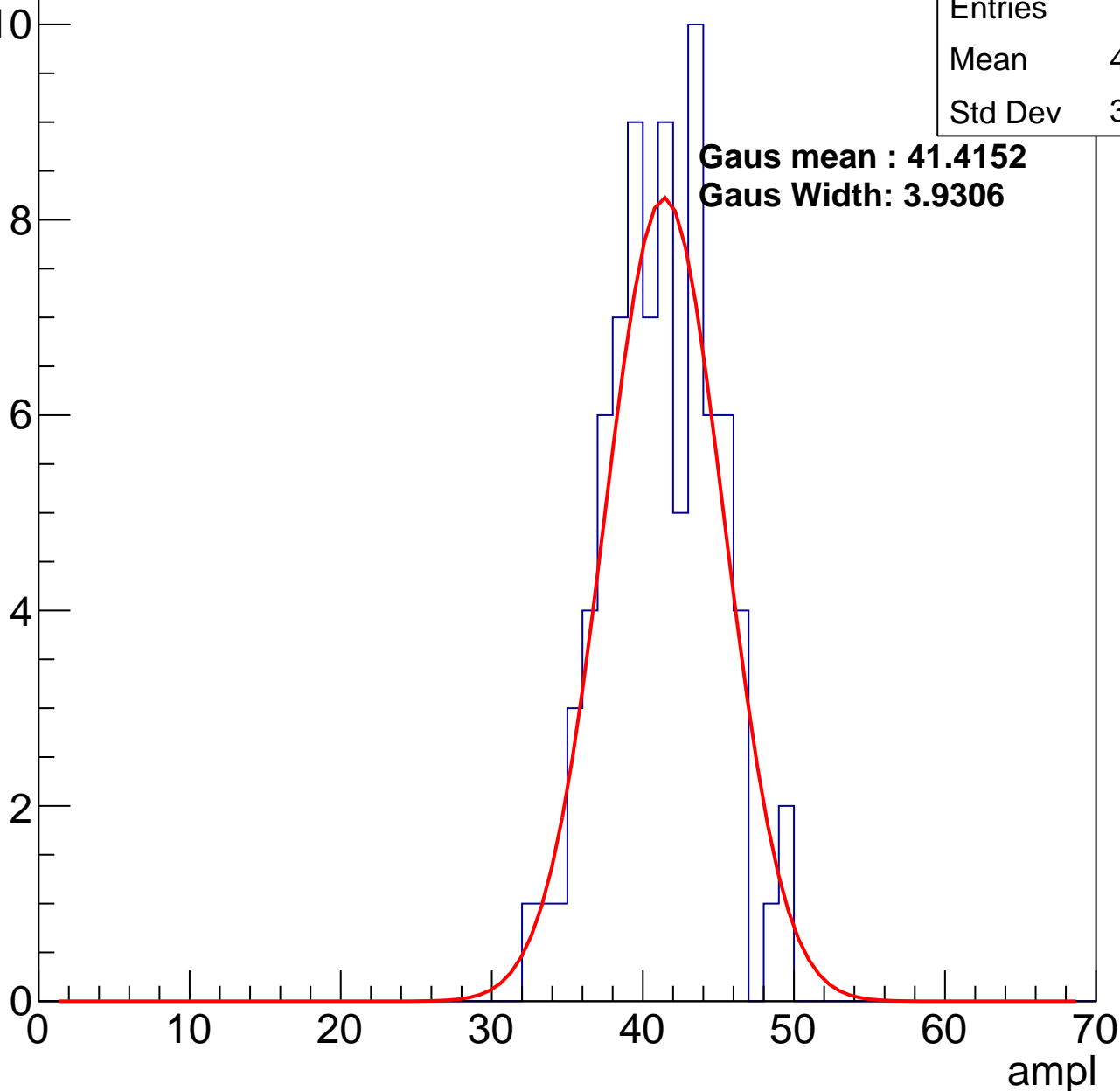
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	40.73
Std Dev	3.612

Gaus mean : 41.4152

Gaus Width: 3.9306

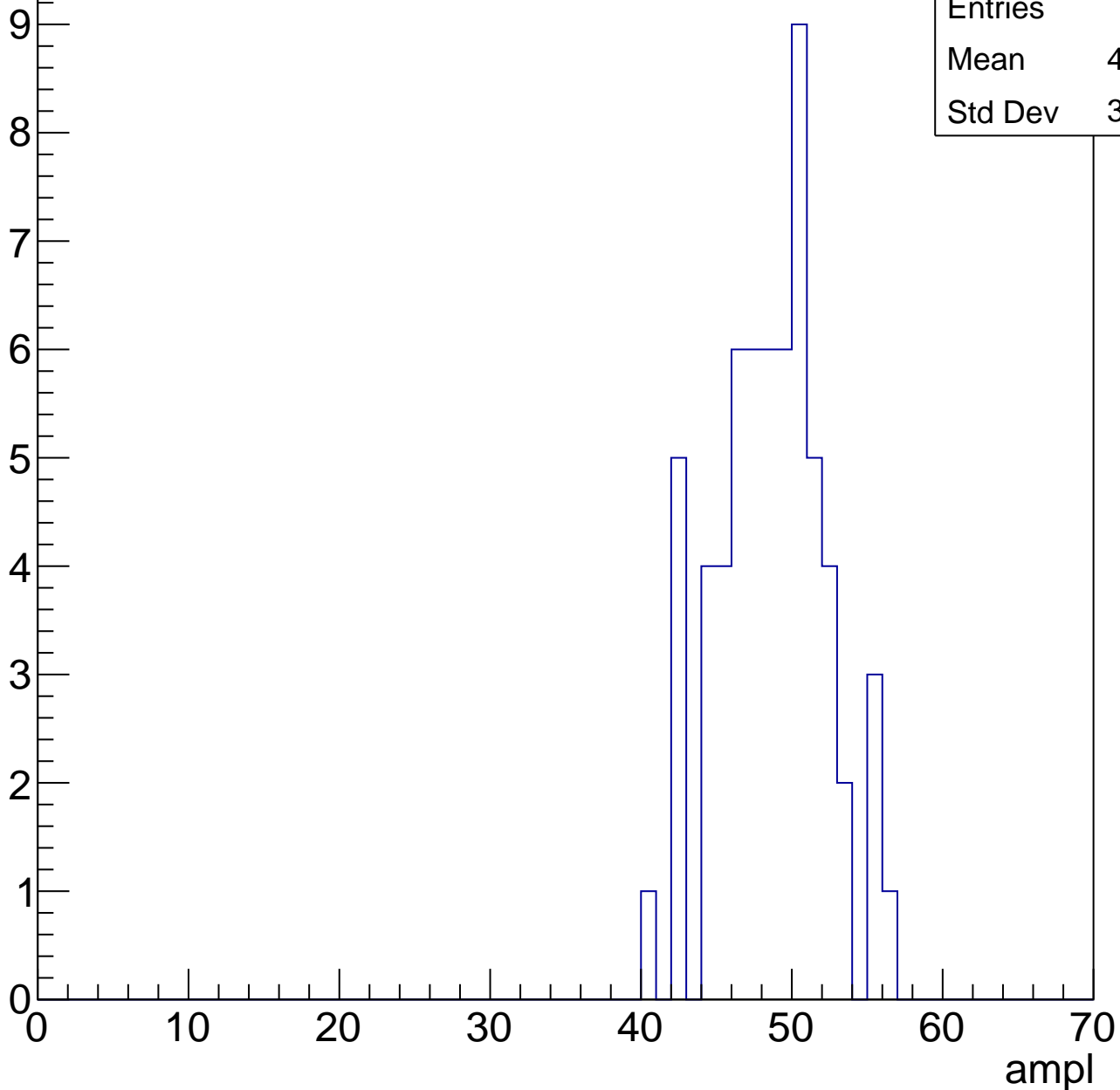


B1L102S, U6-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	48.16
Std Dev	3.557

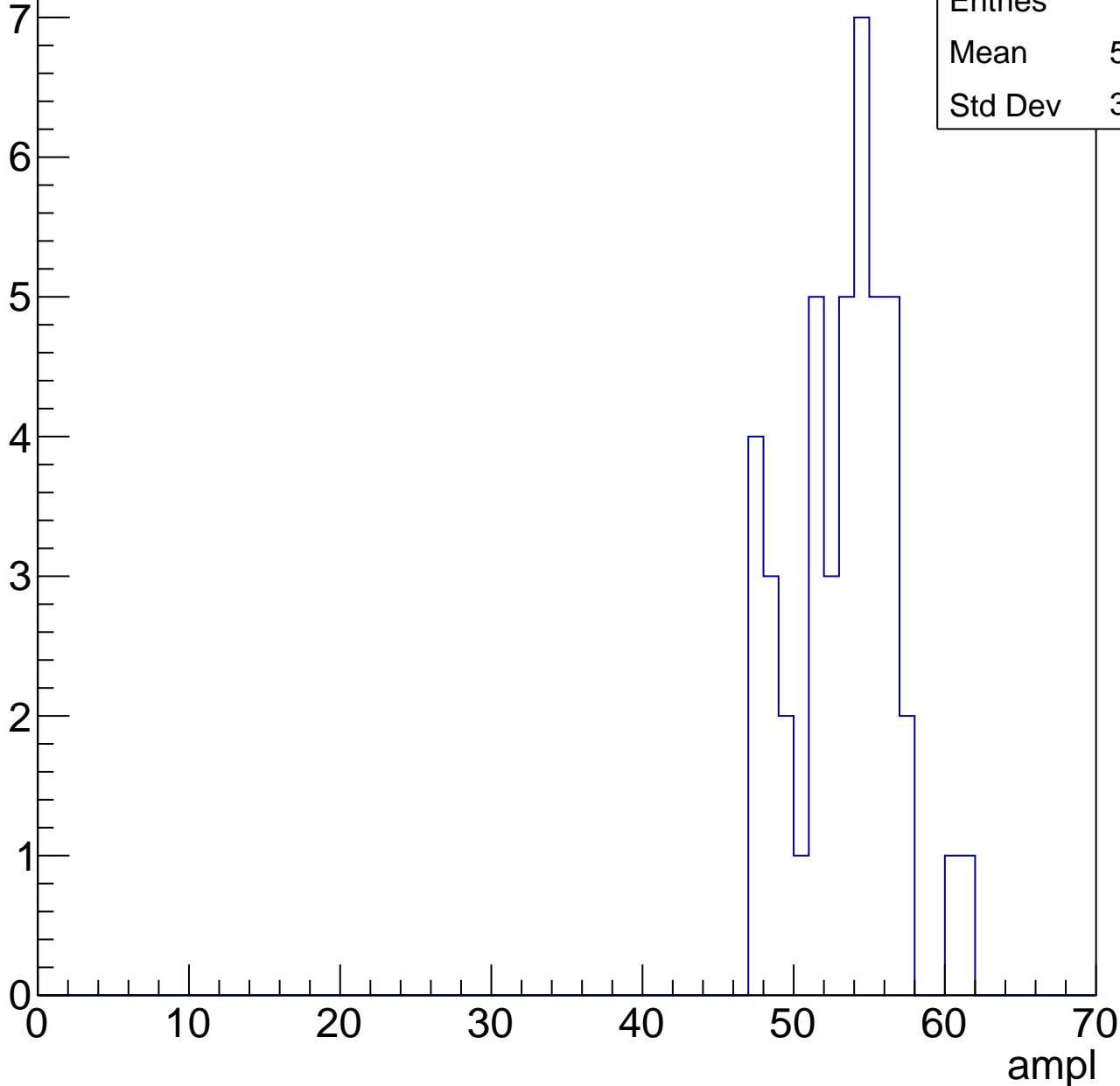


B1L102S, U6-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	52.82
Std Dev	3.366

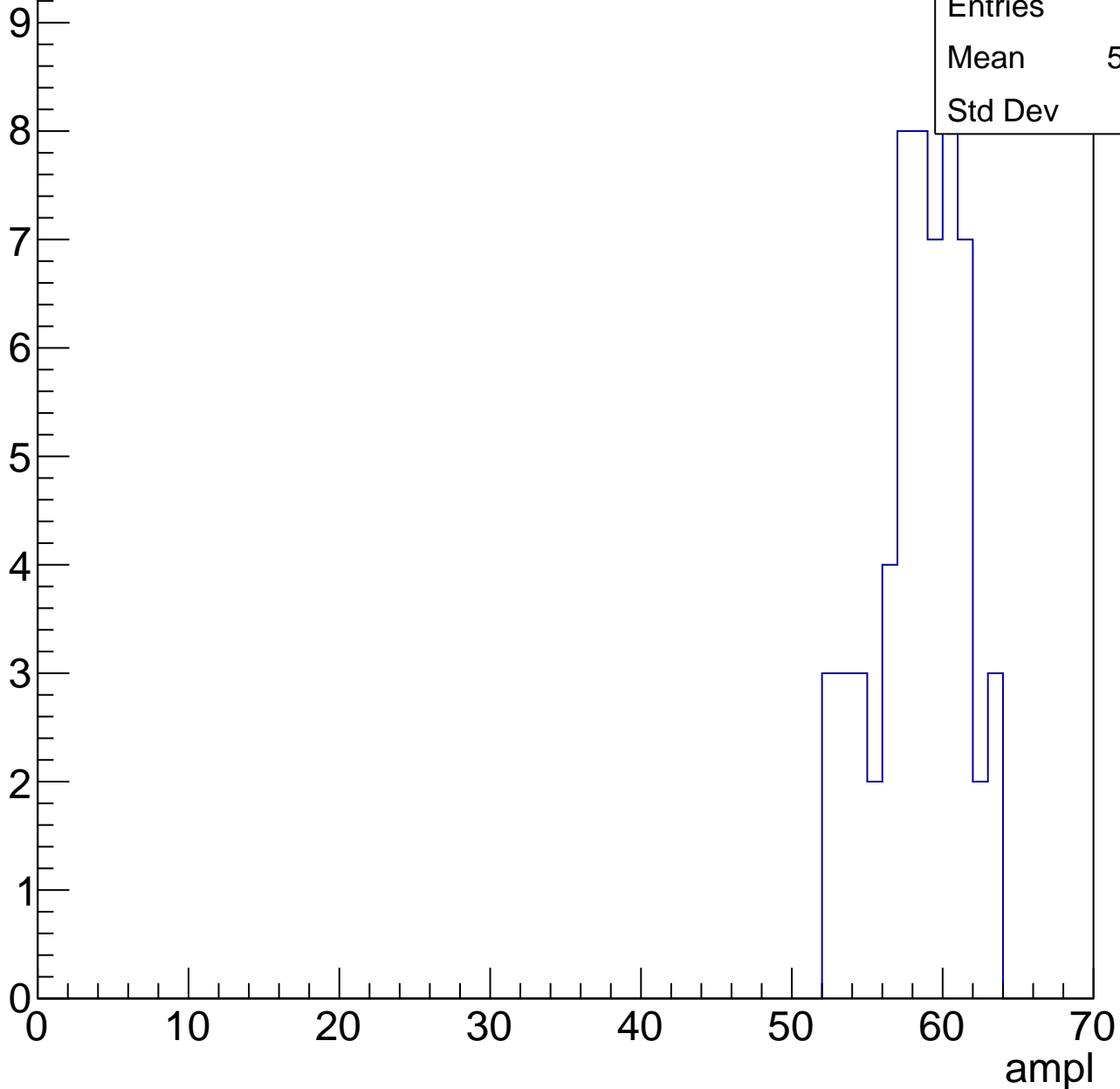


B1L102S, U6-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	58.03
Std Dev	2.87

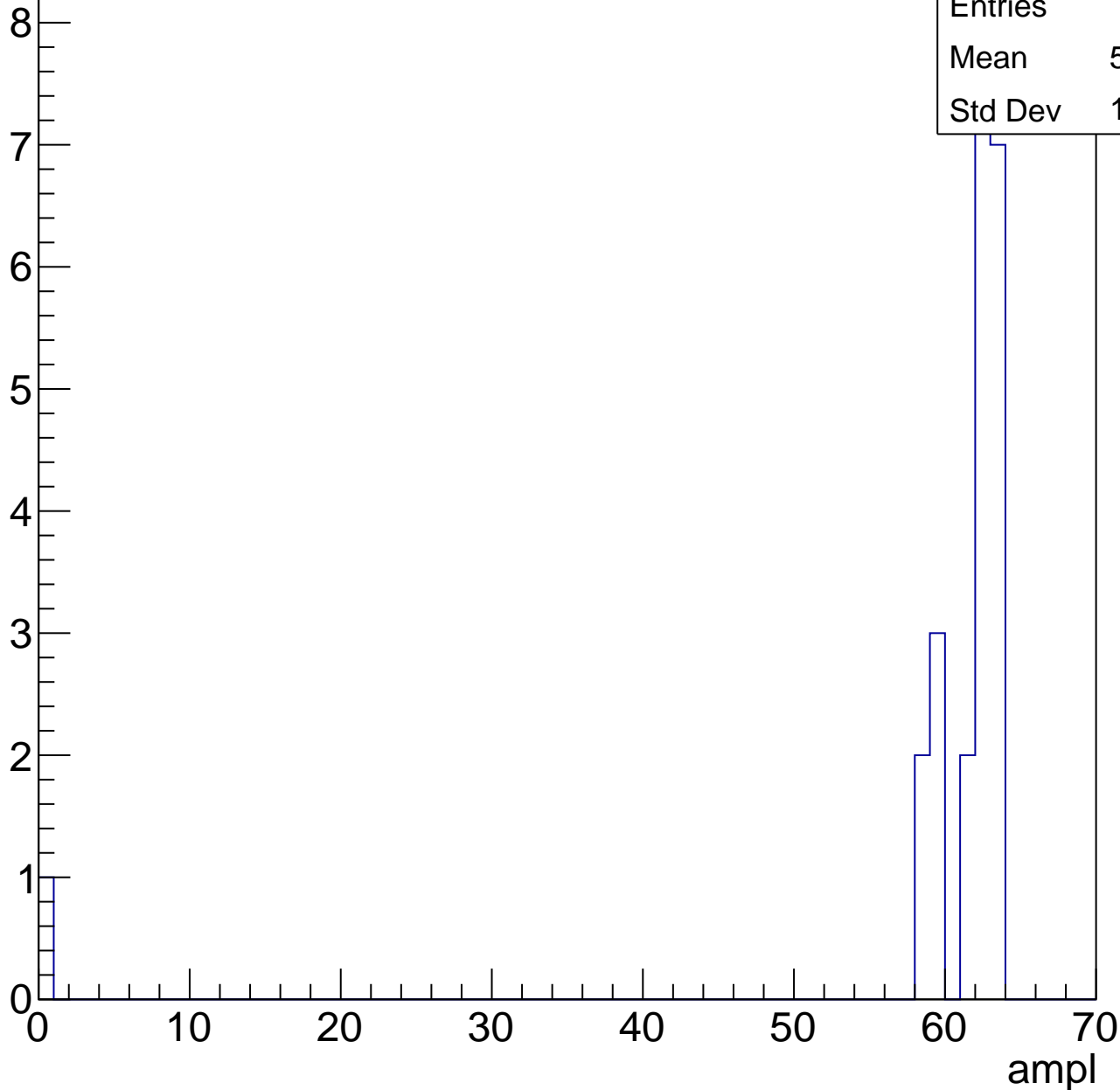


B1L102S, U6-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	58.78
Std Dev	12.64



B1L102S, U6-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch101, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	29.29
Std Dev	3.704

Gaus mean : 29.5813

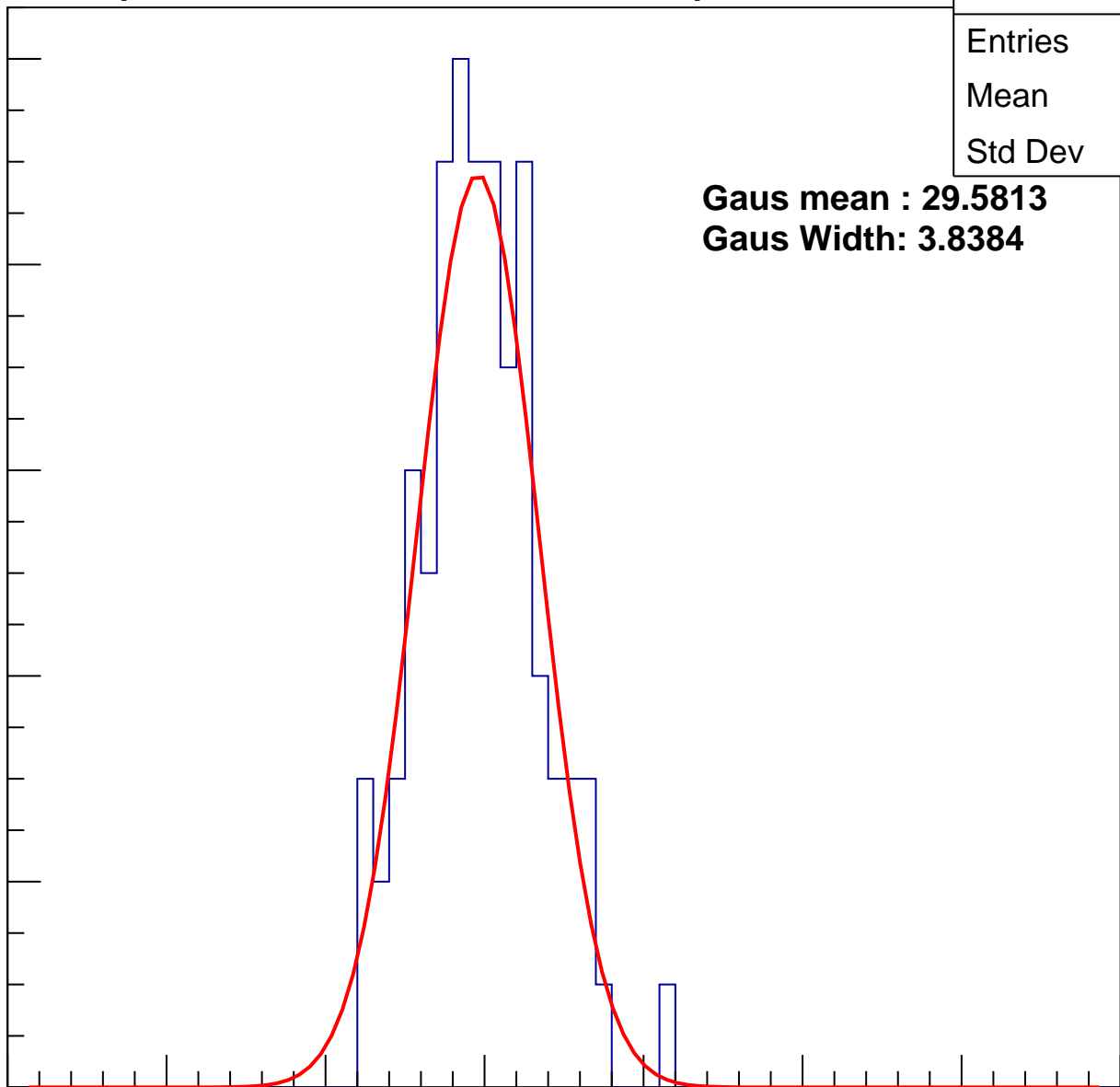
Gaus Width: 3.8384

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch101, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	73
Mean	37.14
Std Dev	3.597

Gaus mean : 38.1880

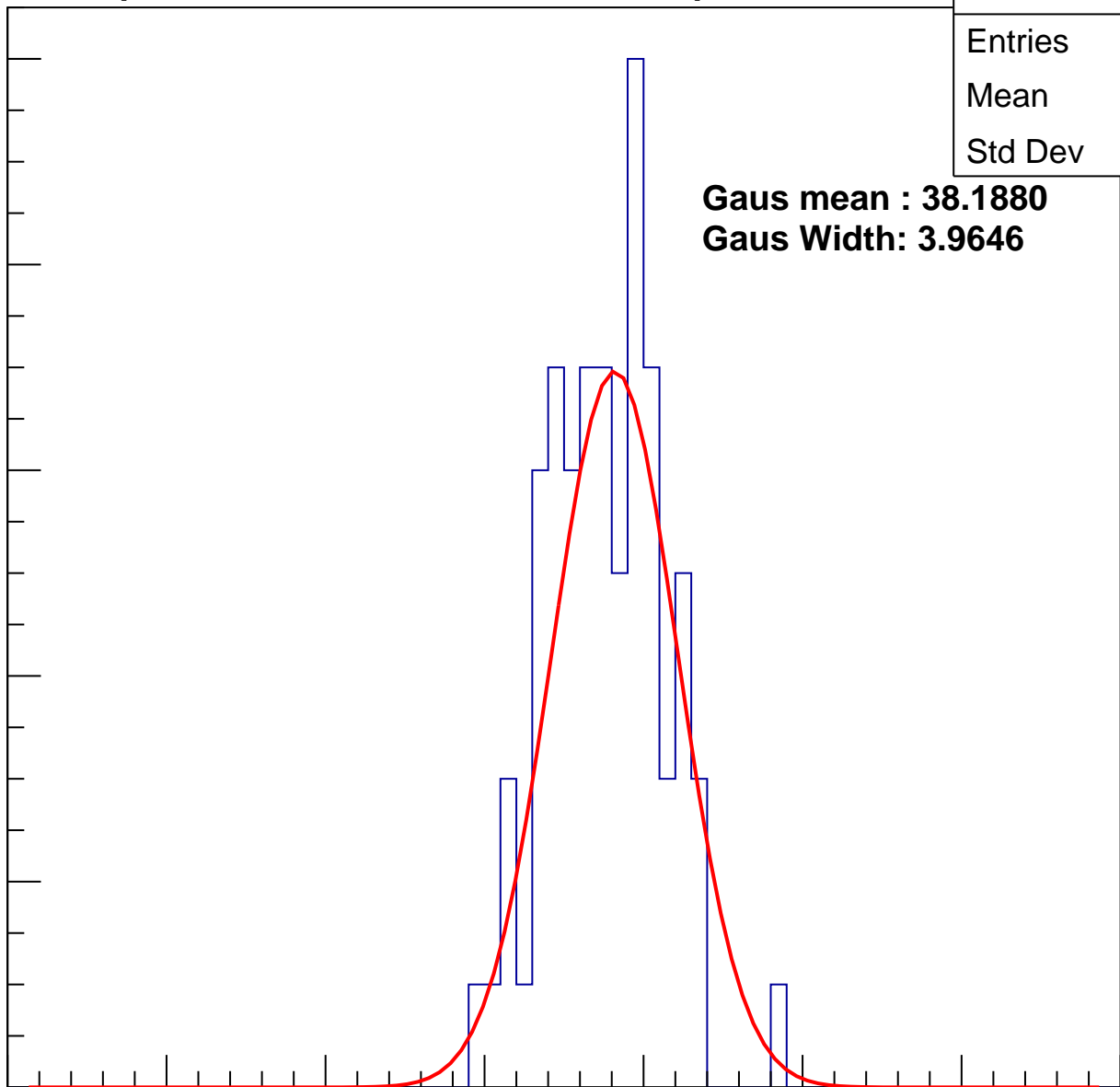
Gaus Width: 3.9646

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch101, adc2

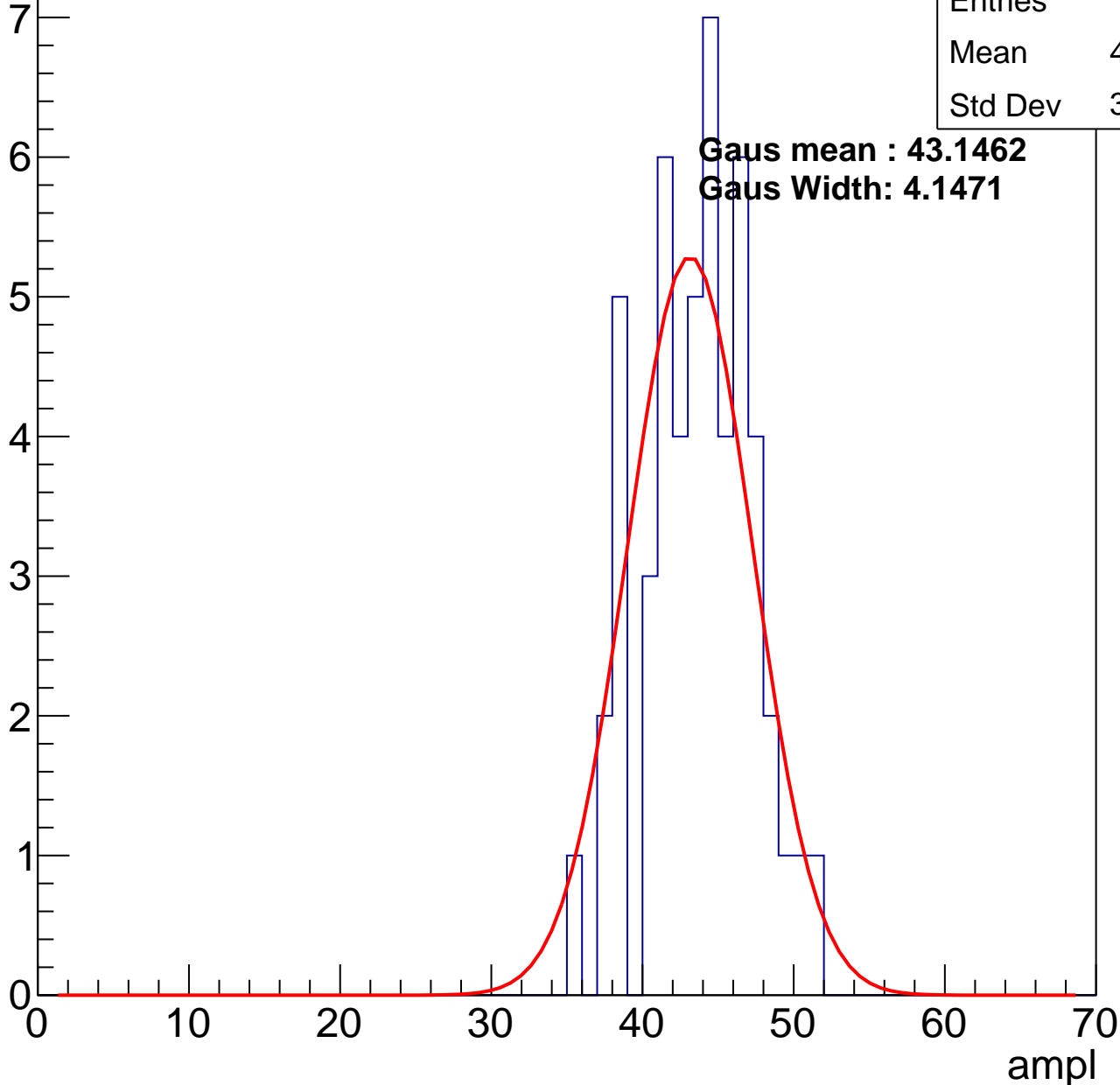
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.19
Std Dev	3.547

Gaus mean : 43.1462

Gaus Width: 4.1471

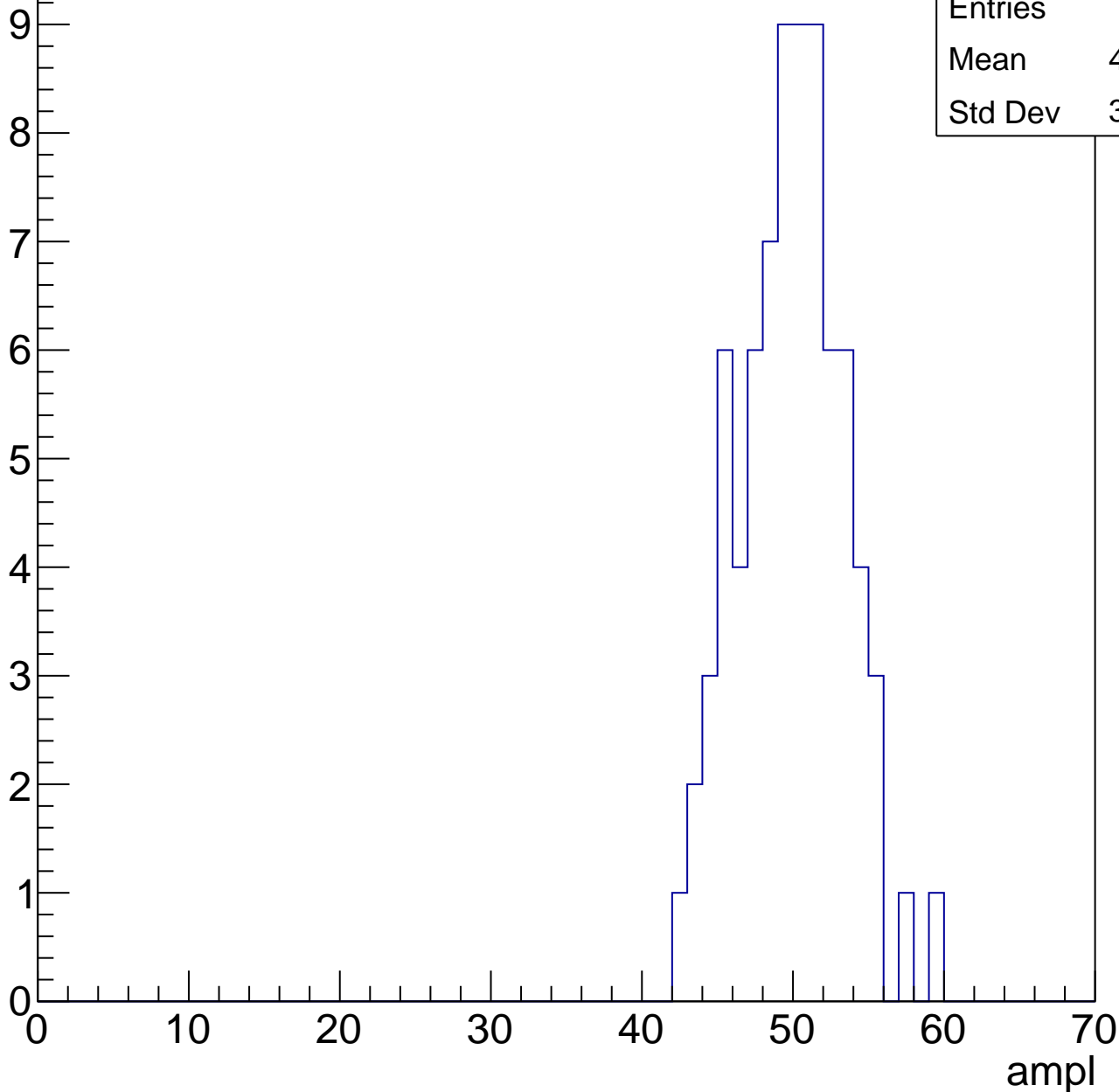


B1L102S, U6-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	49.47
Std Dev	3.436

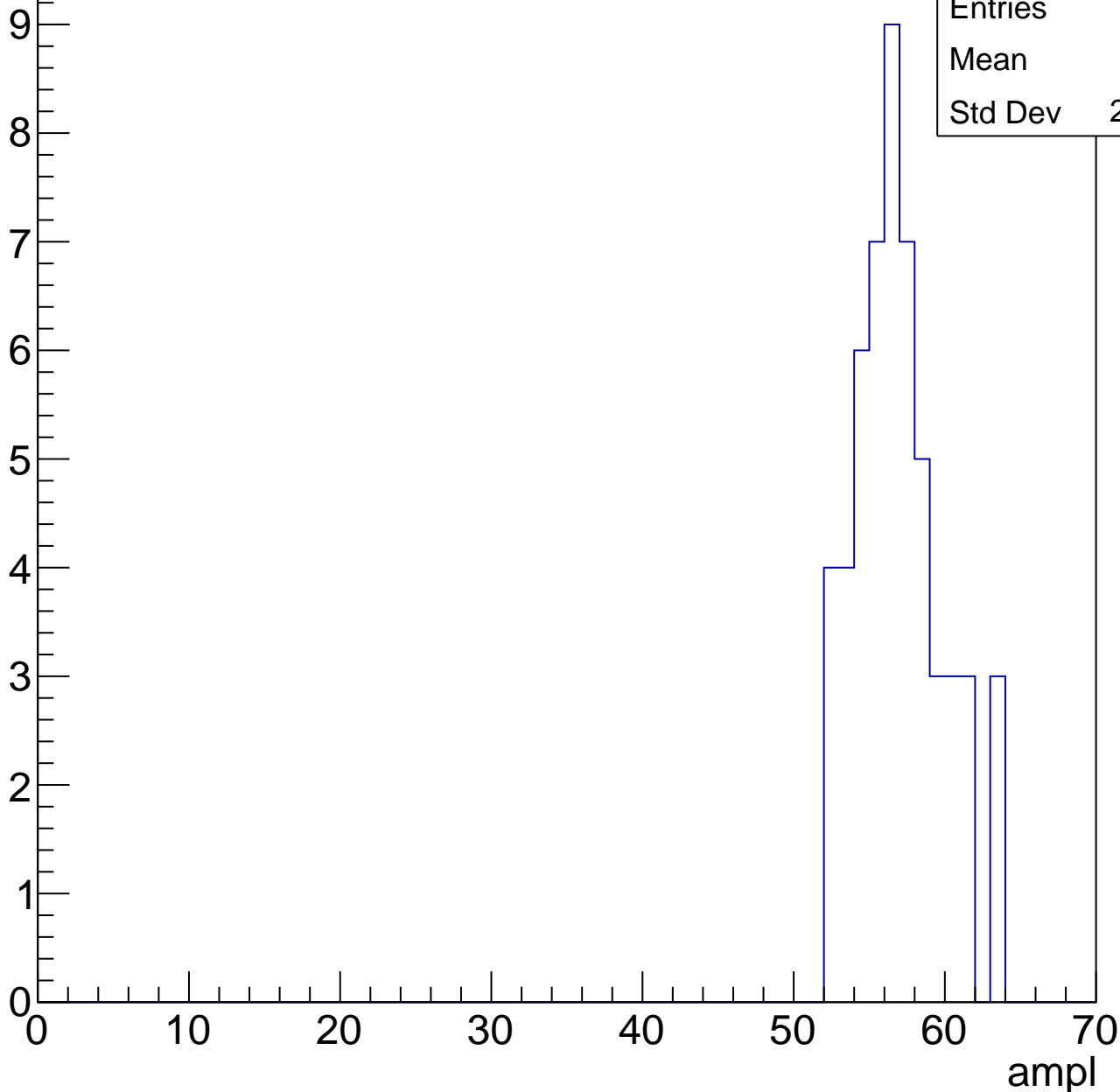


B1L102S, U6-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	56.5
Std Dev	2.859

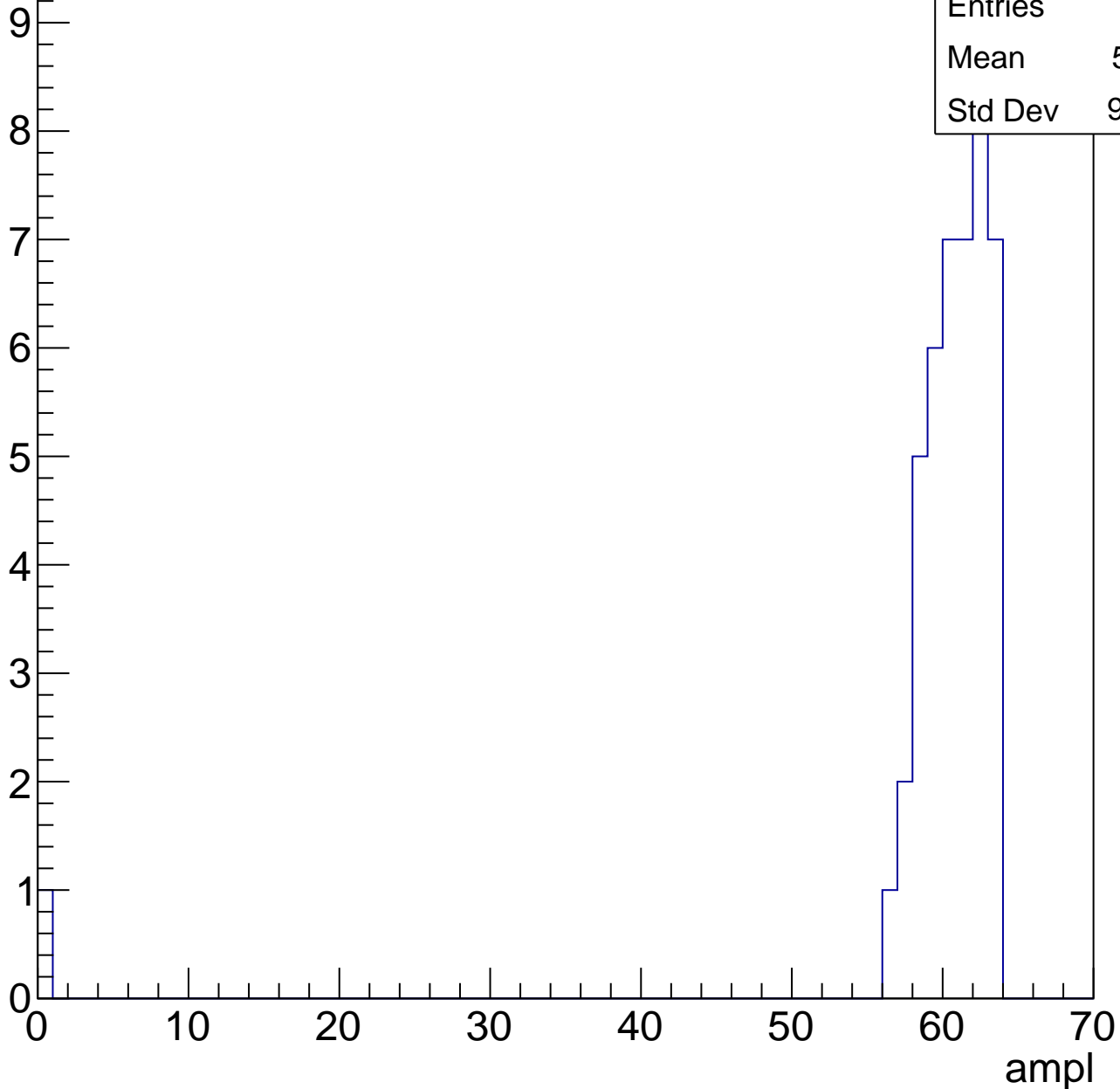


B1L102S, U6-ch101, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.11
Std Dev	9.105



B1L102S, U6-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U6-ch102, adc0

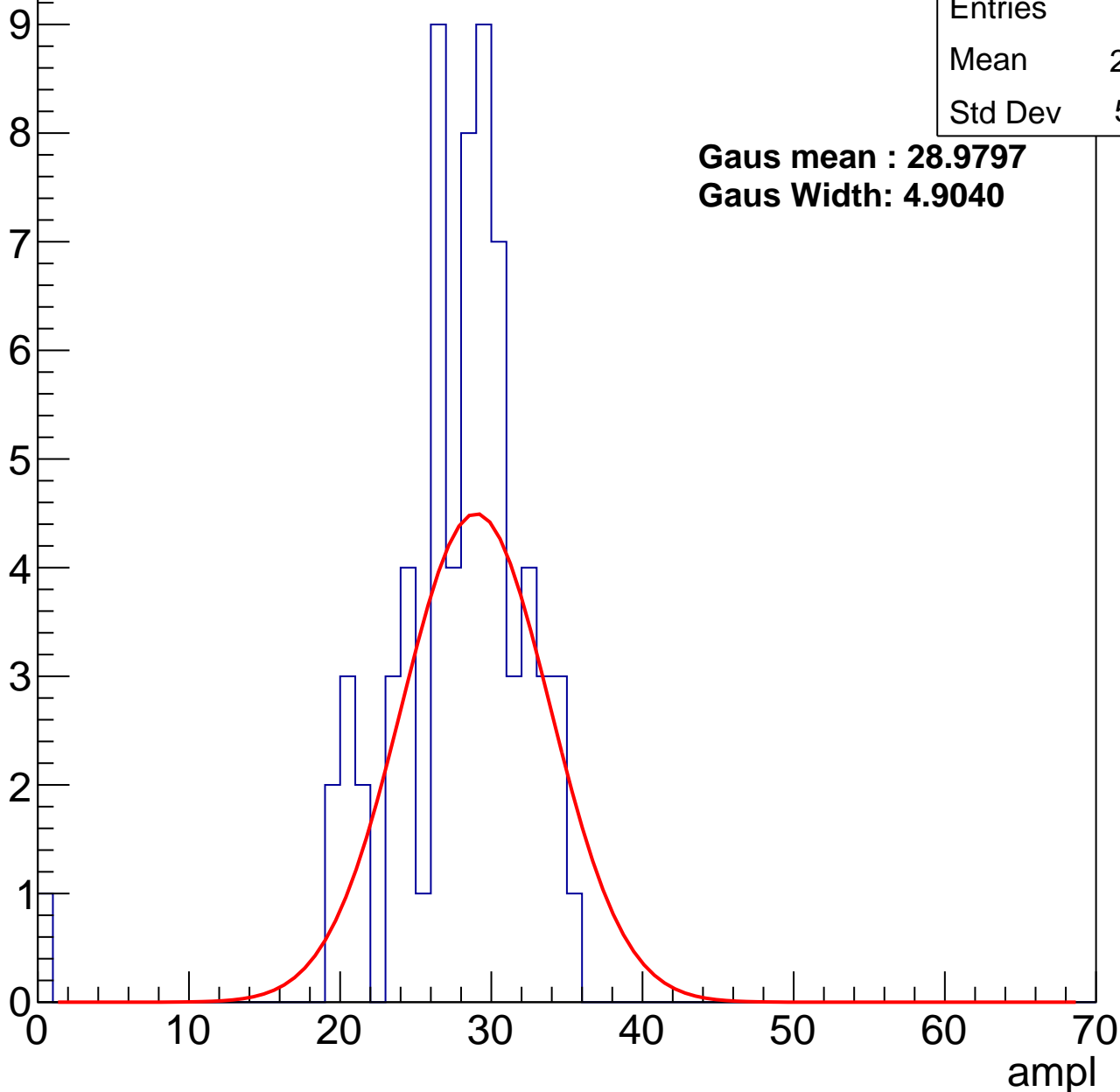
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	27.22
Std Dev	5.101

Gaus mean : 28.9797

Gaus Width: 4.9040



B1L102S, U6-ch102, adc1

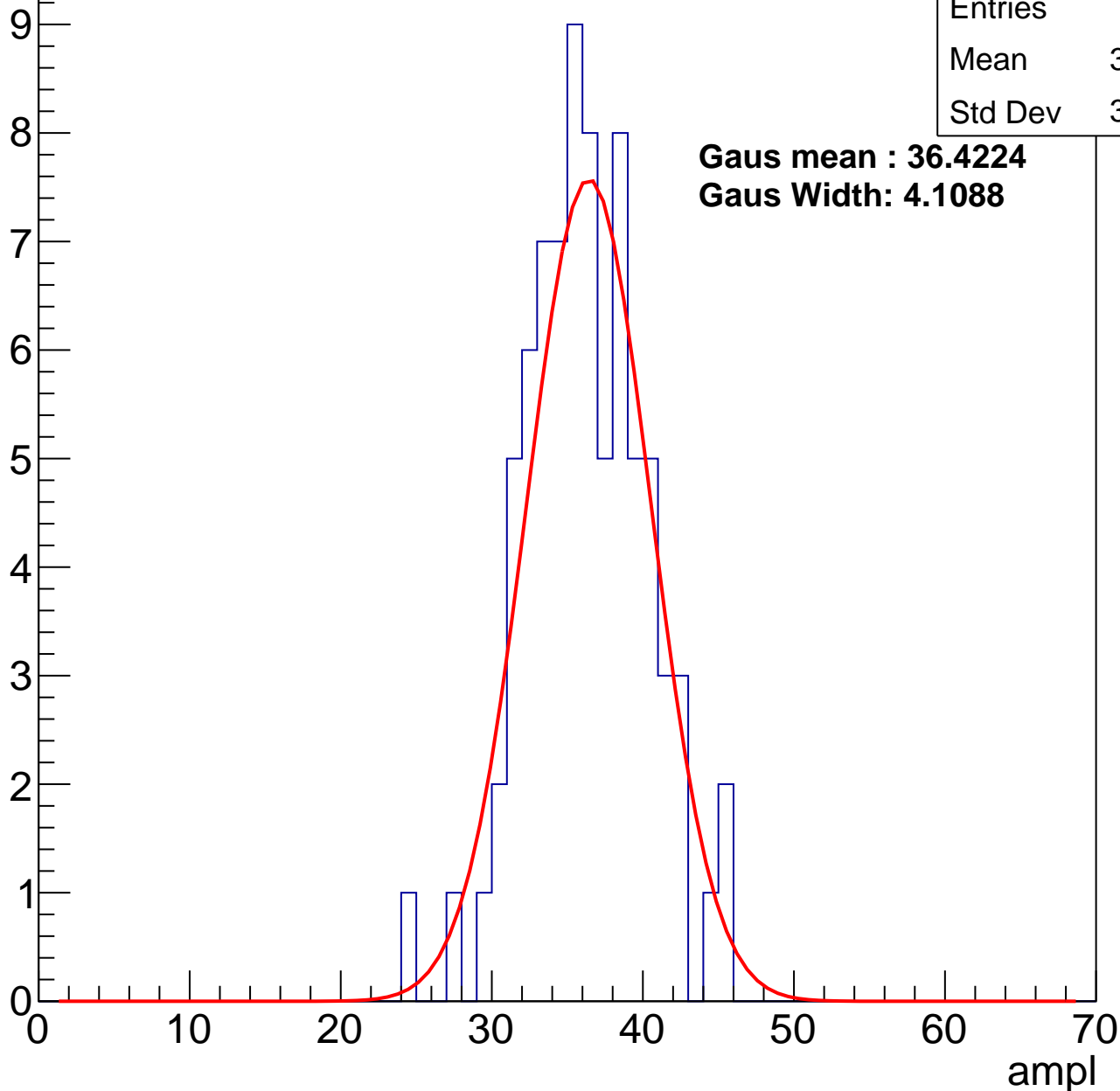
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.77
Std Dev	3.952

Gaus mean : 36.4224

Gaus Width: 4.1088



B1L102S, U6-ch102, adc2

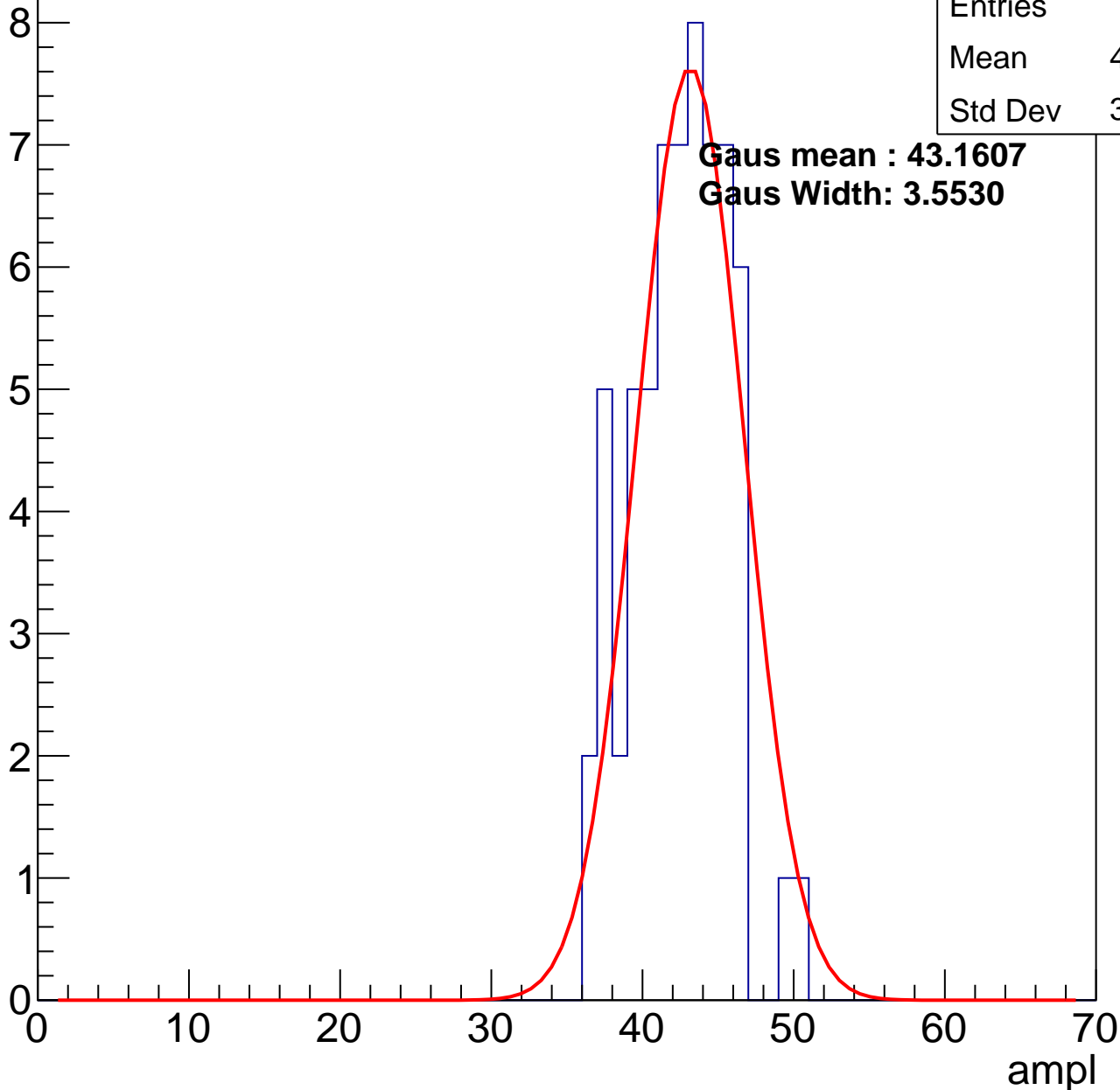
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.08
Std Dev	3.108

Gaus mean : 43.1607

Gaus Width: 3.5530

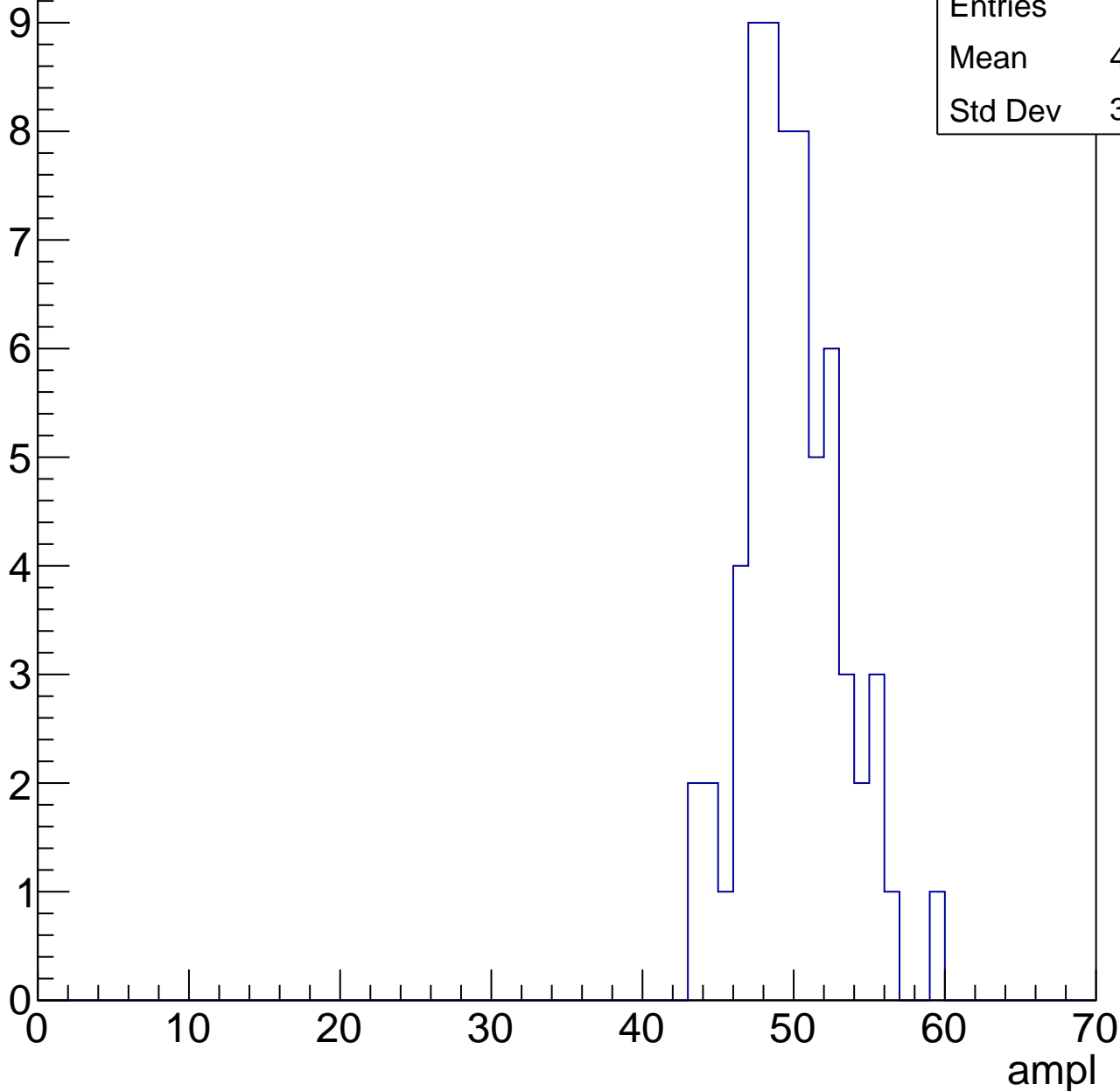


B1L102S, U6-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.44
Std Dev	3.172

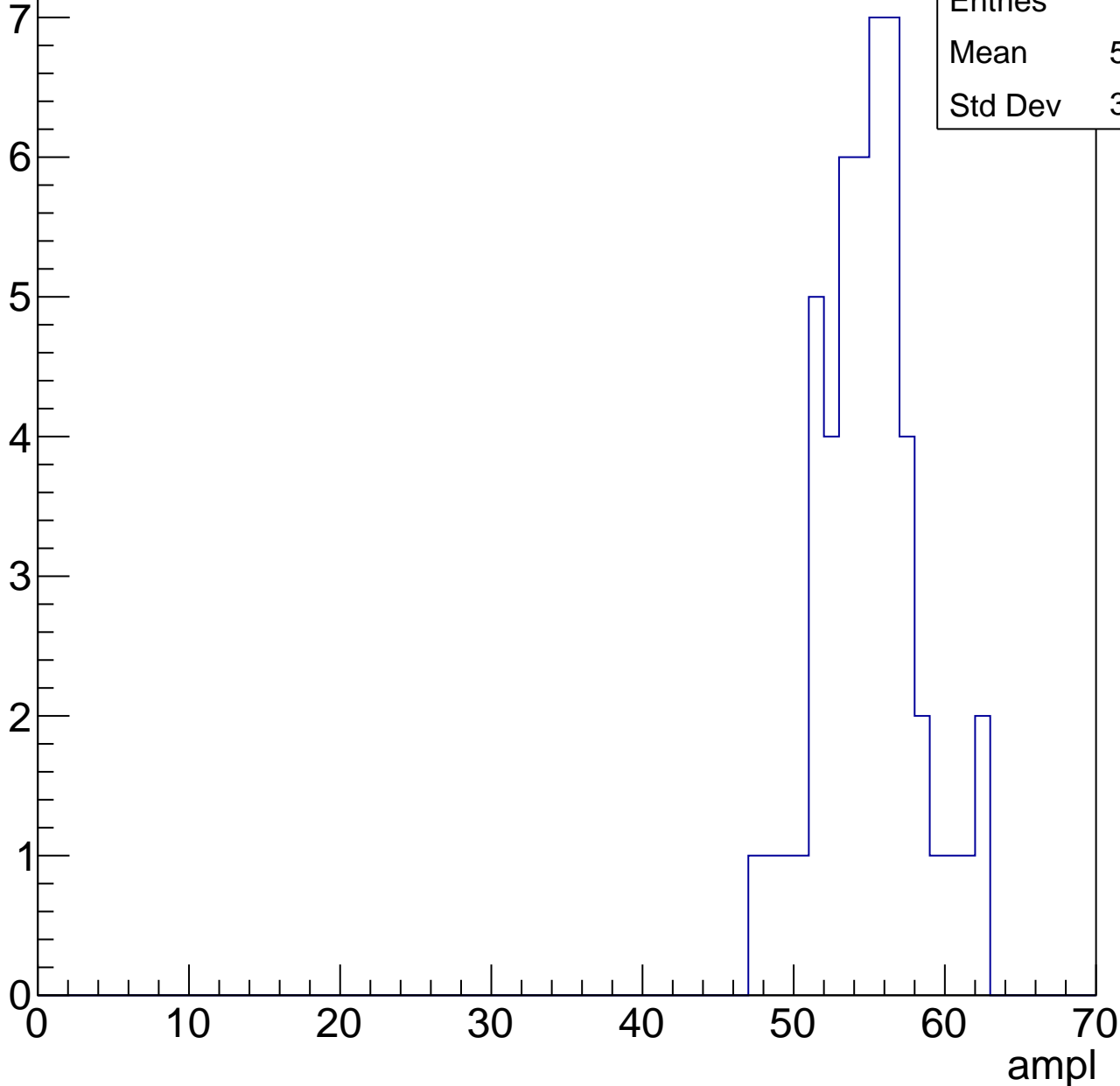


B1L102S, U6-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.48
Std Dev	3.233



B1L102S, U6-ch102, adc5

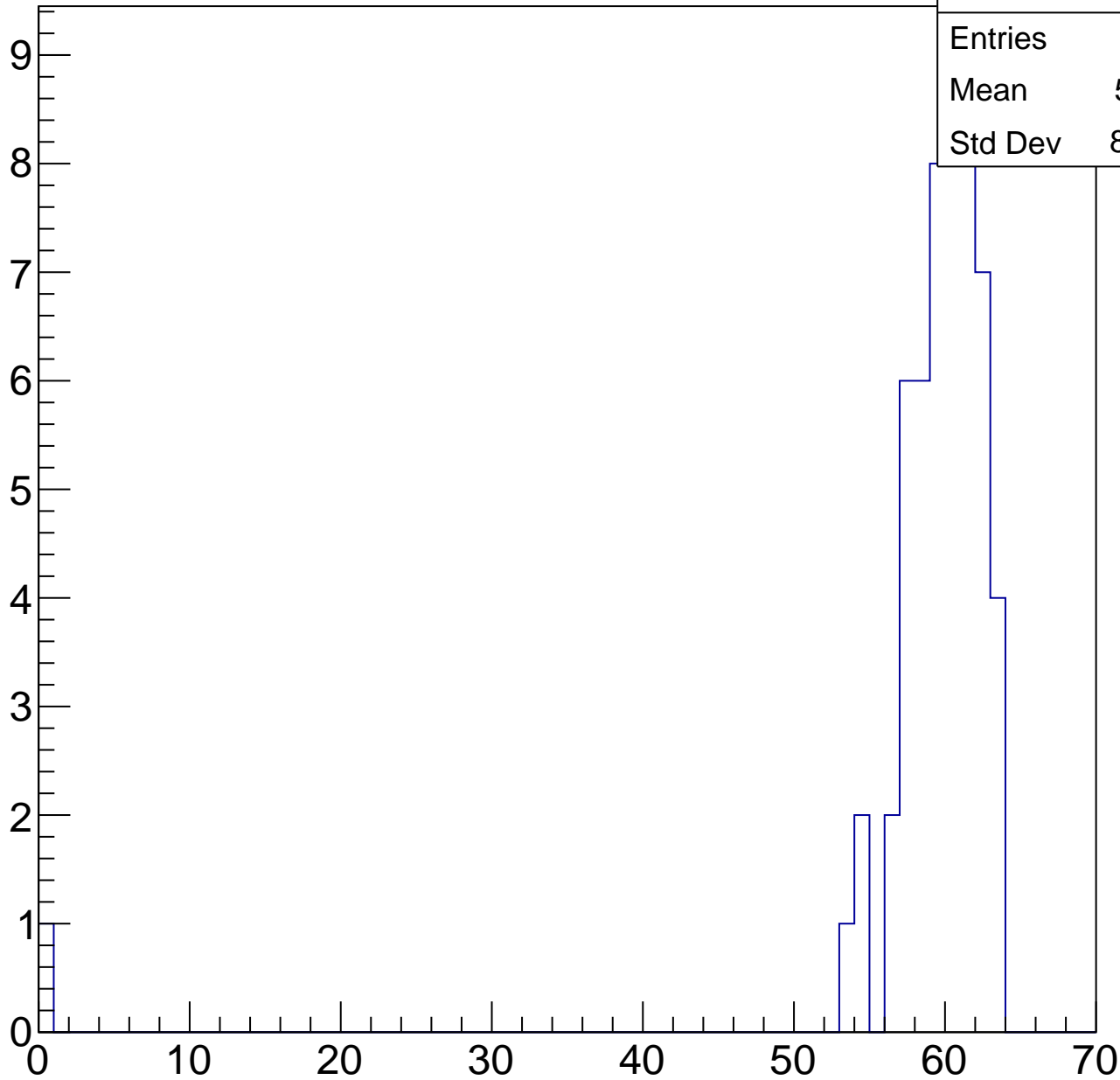
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.31
Std Dev	8.342

ampl

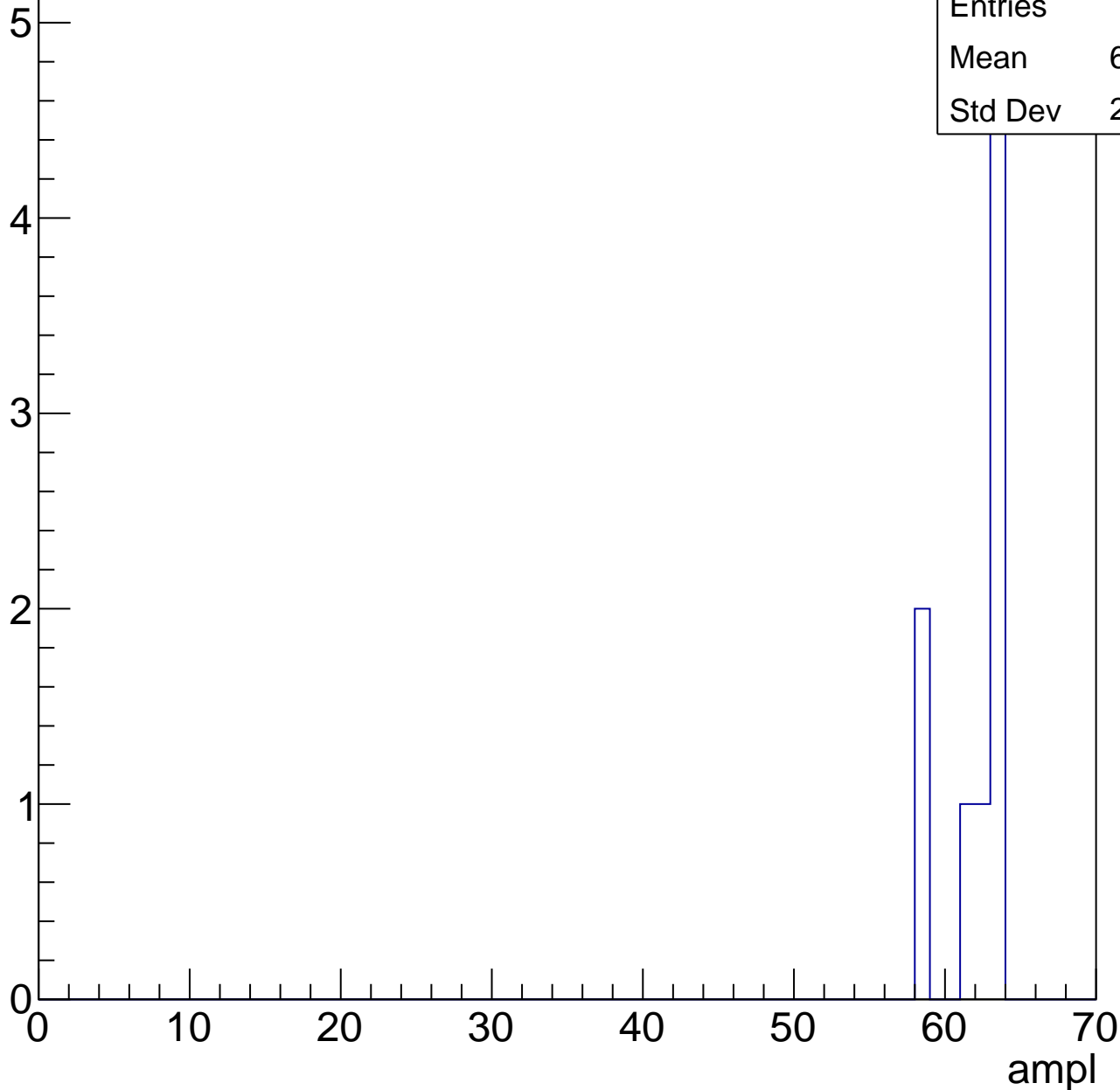


B1L102S, U6-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	61.56
Std Dev	2.006



B1L102S, U6-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch103, adc0

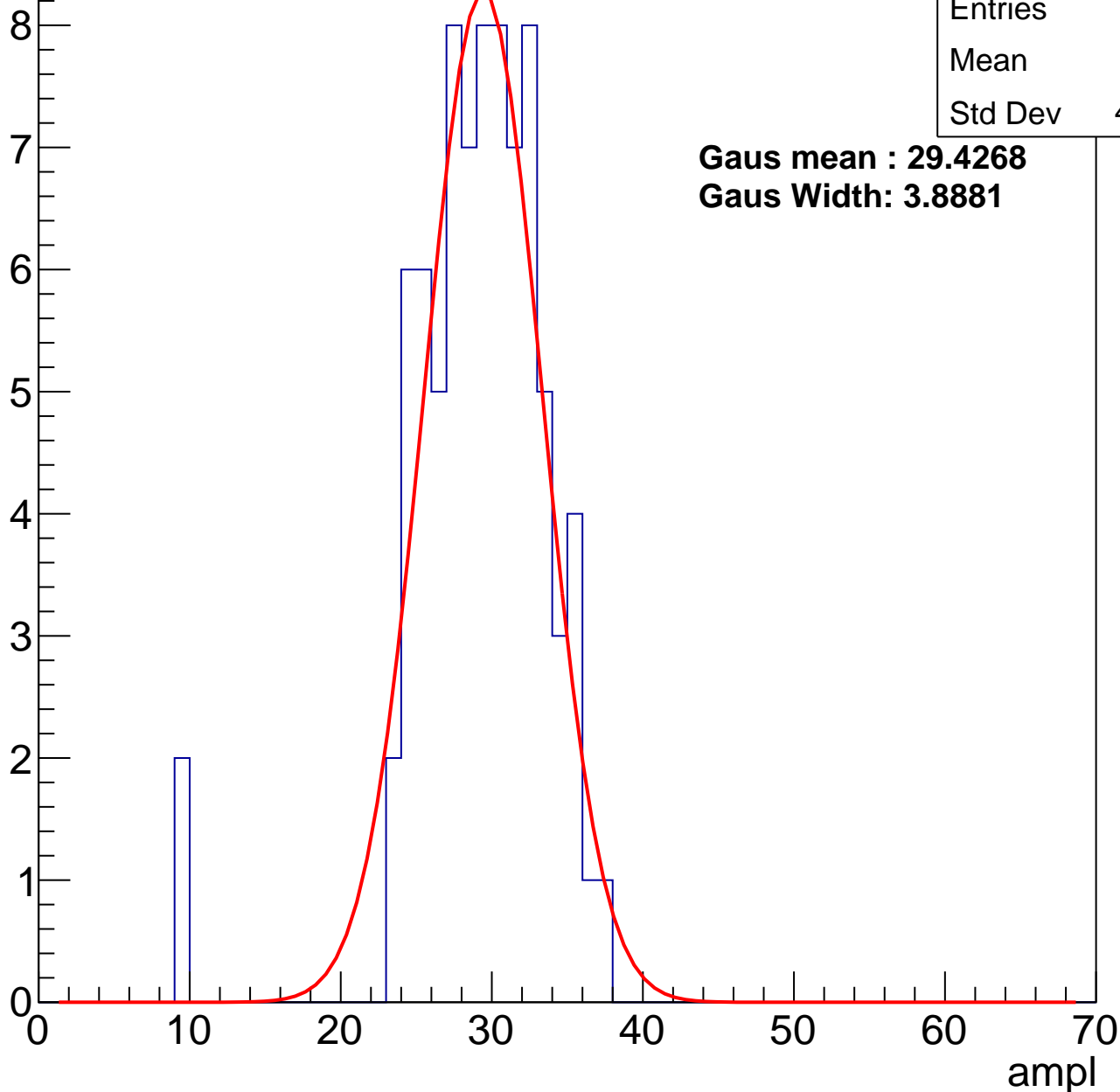
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	28.7
Std Dev	4.601

Gaus mean : 29.4268

Gaus Width: 3.8881



B1L102S, U6-ch103, adc1

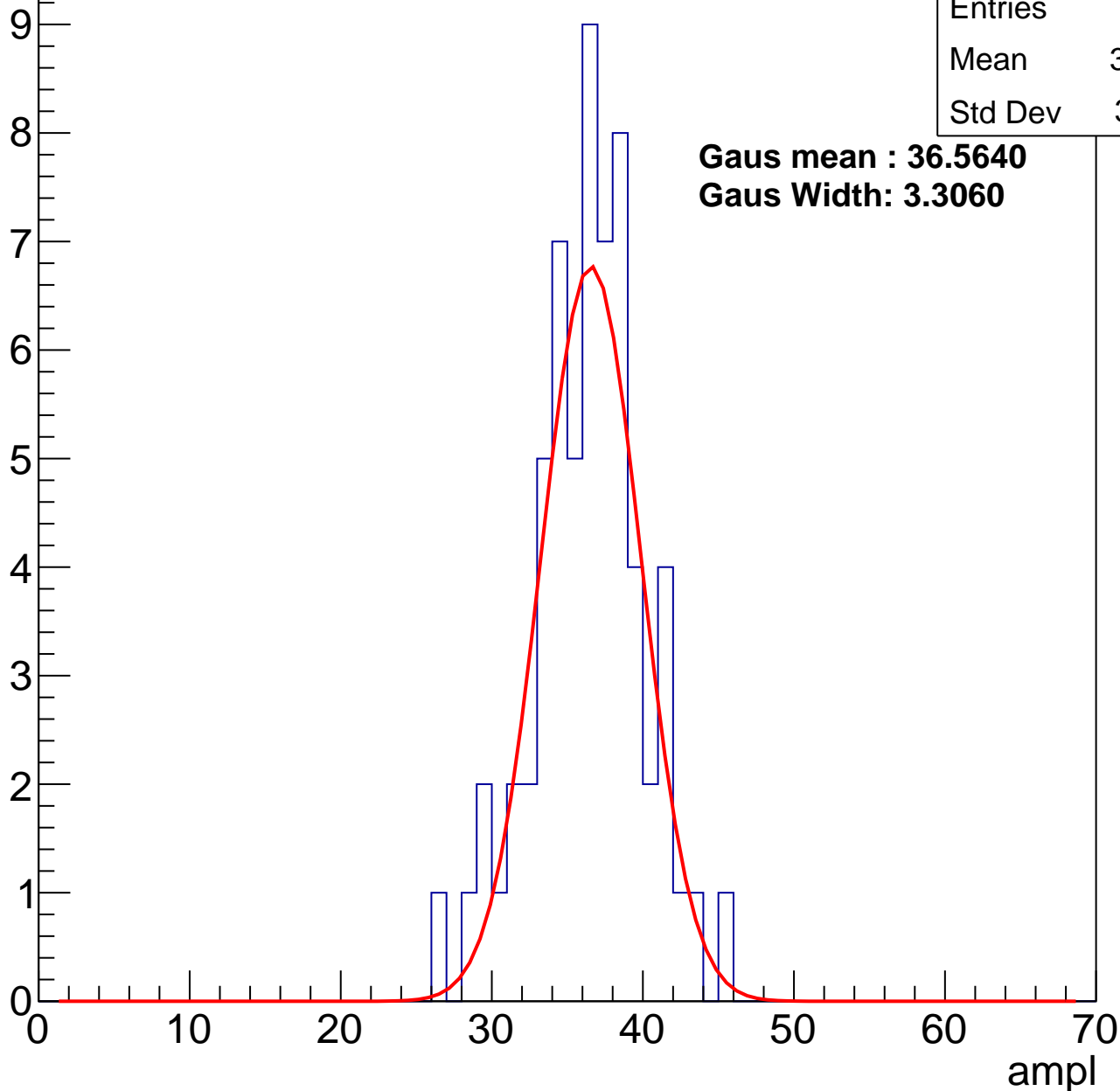
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.92
Std Dev	3.631

Gaus mean : 36.5640

Gaus Width: 3.3060



B1L102S, U6-ch103, adc2

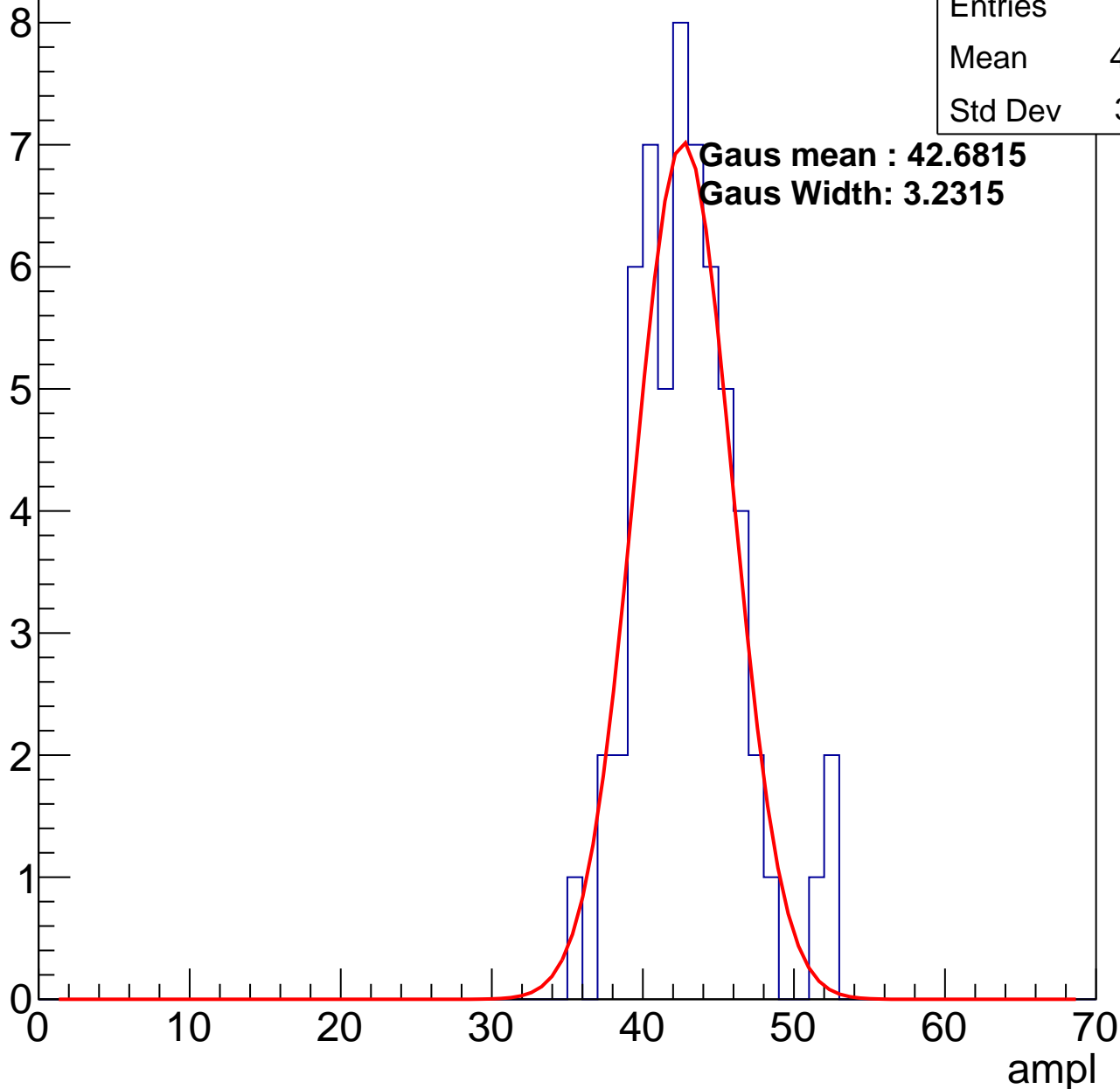
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.56
Std Dev	3.461

Gaus mean : 42.6815

Gaus Width: 3.2315

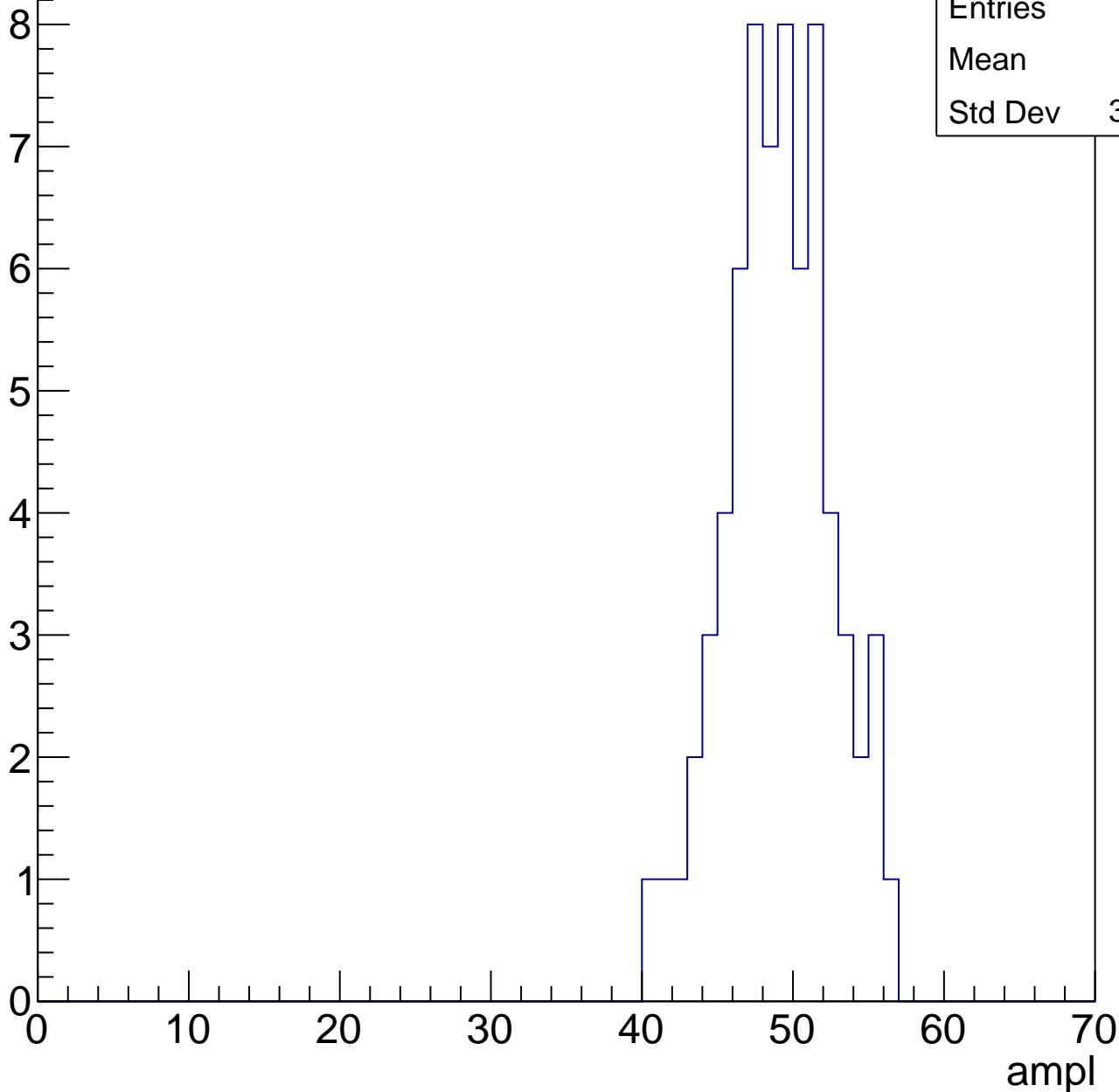


B1L102S, U6-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.6
Std Dev	3.465

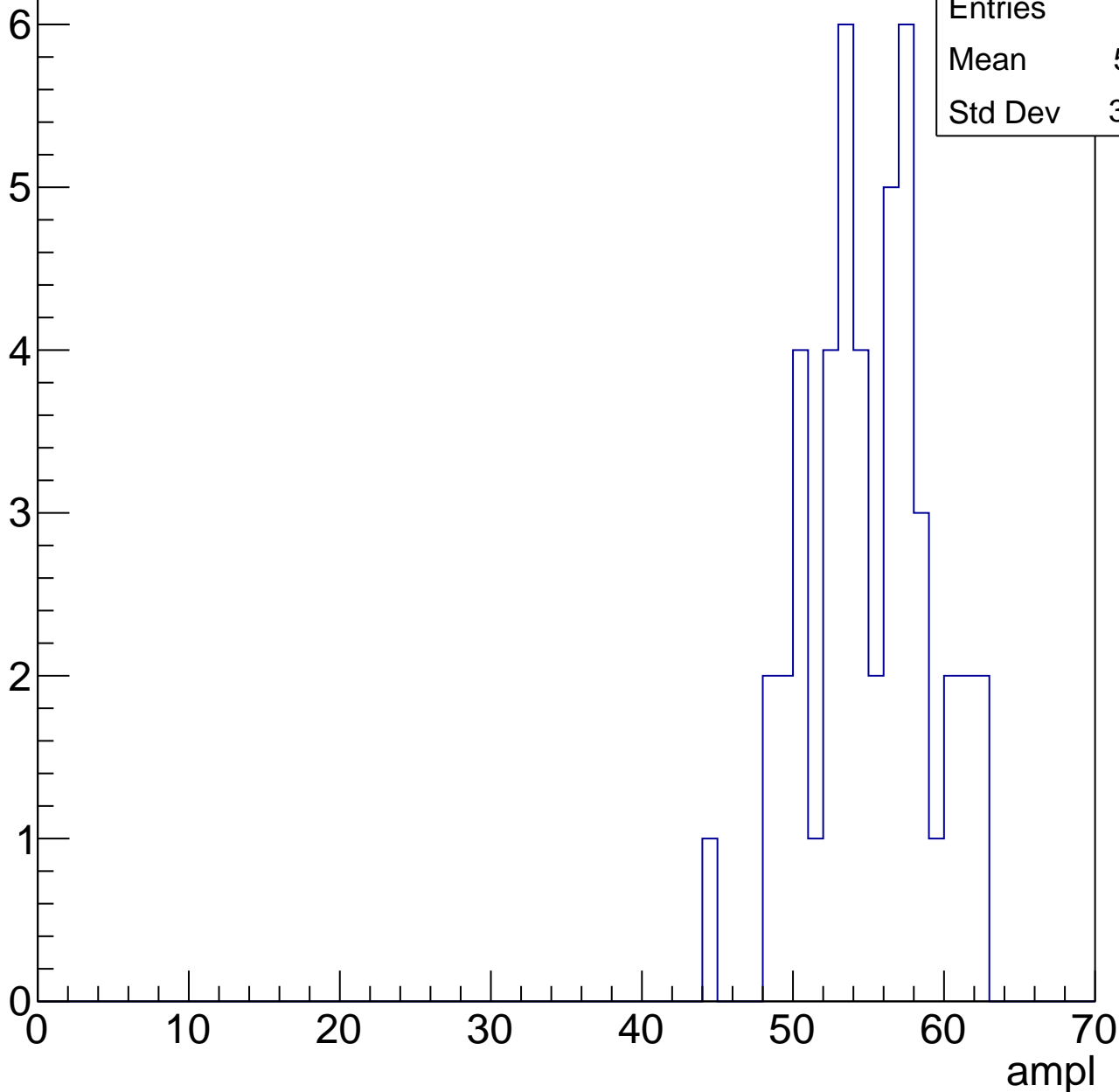


B1L102S, U6-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

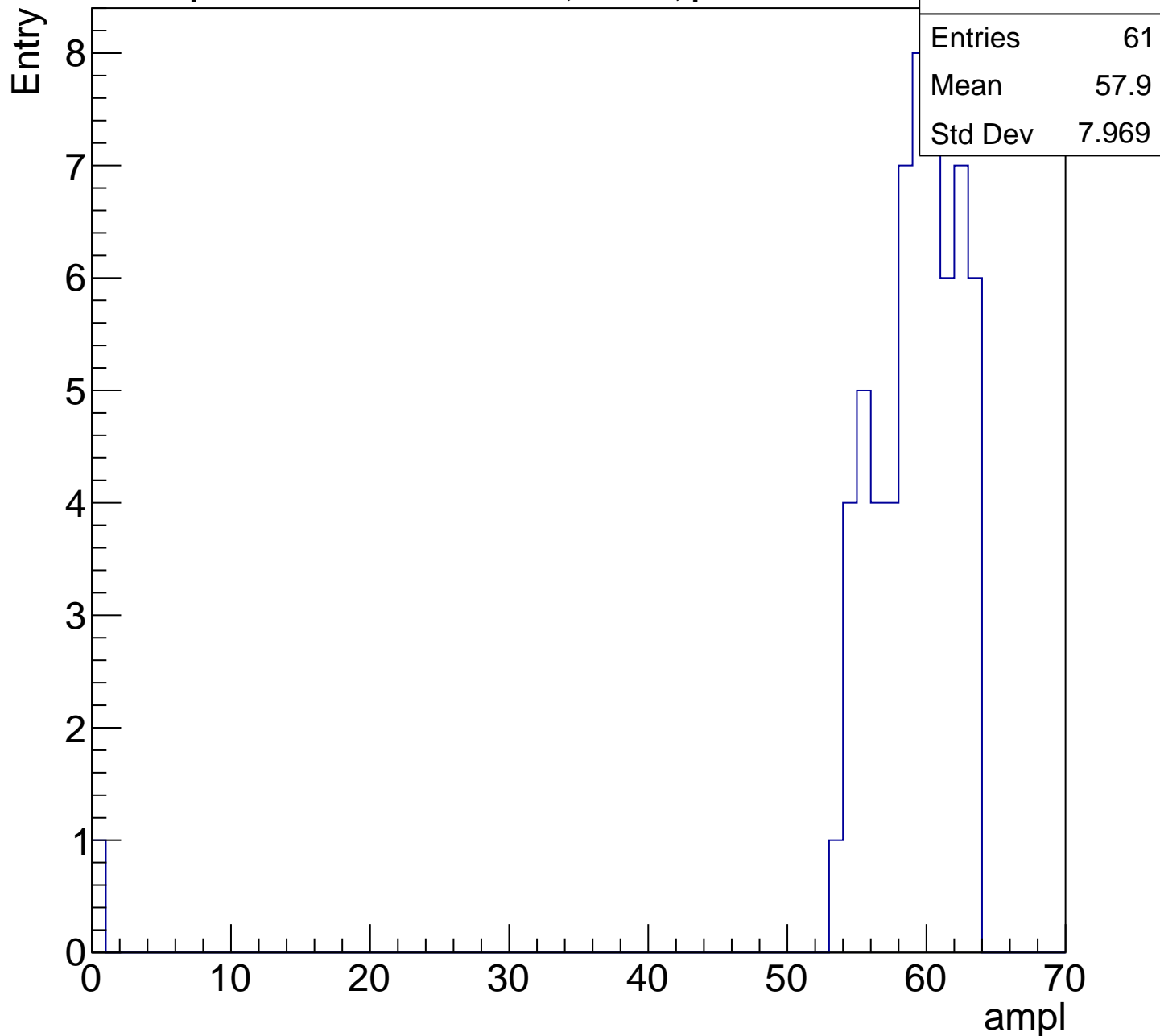
Entry

Entries	47
Mean	54.51
Std Dev	3.999



B1L102S, U6-ch103, adc5

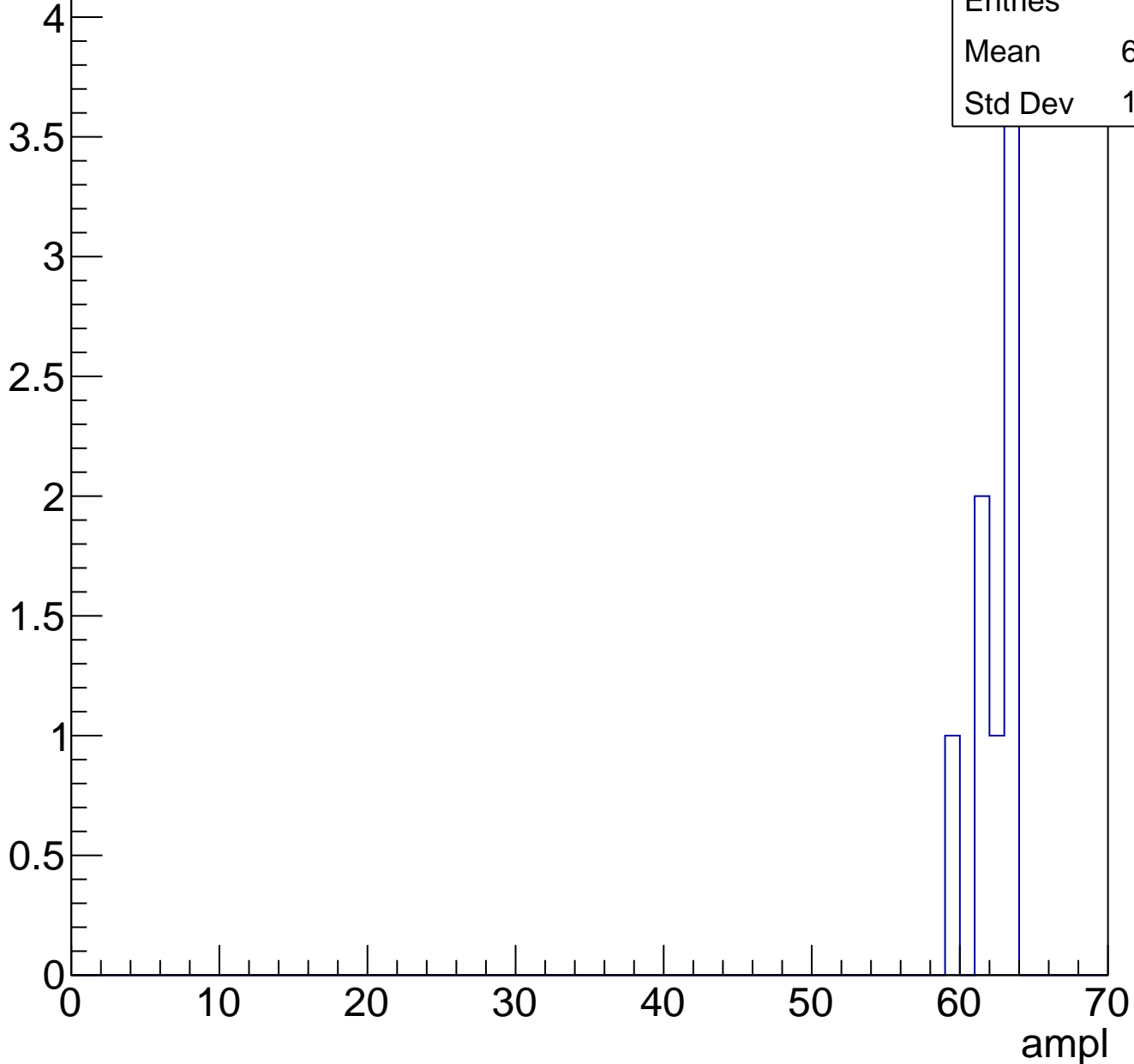
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U6-ch103, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch104, adc0

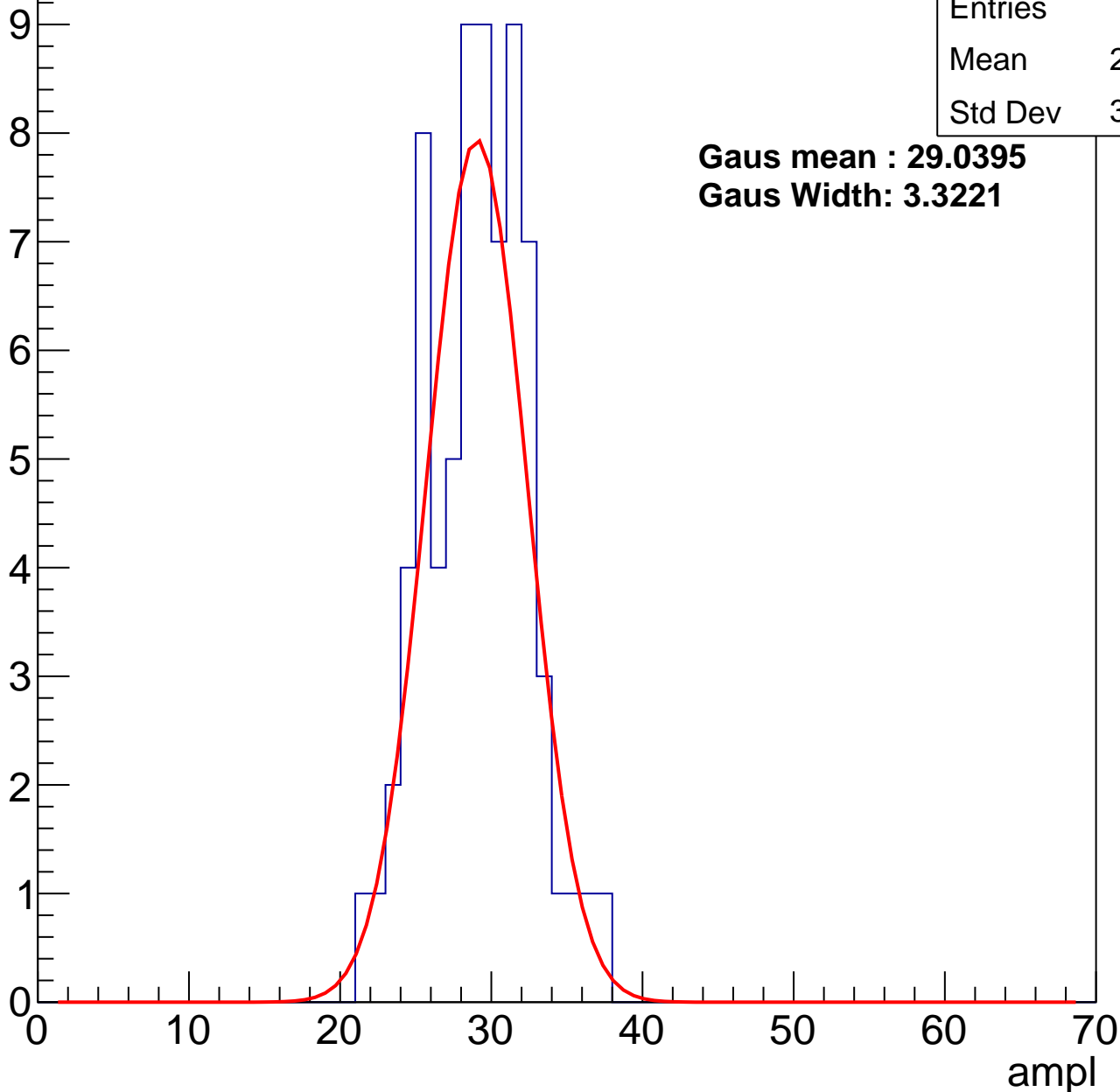
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.64
Std Dev	3.304

Gaus mean : 29.0395

Gaus Width: 3.3221



B1L102S, U6-ch104, adc1

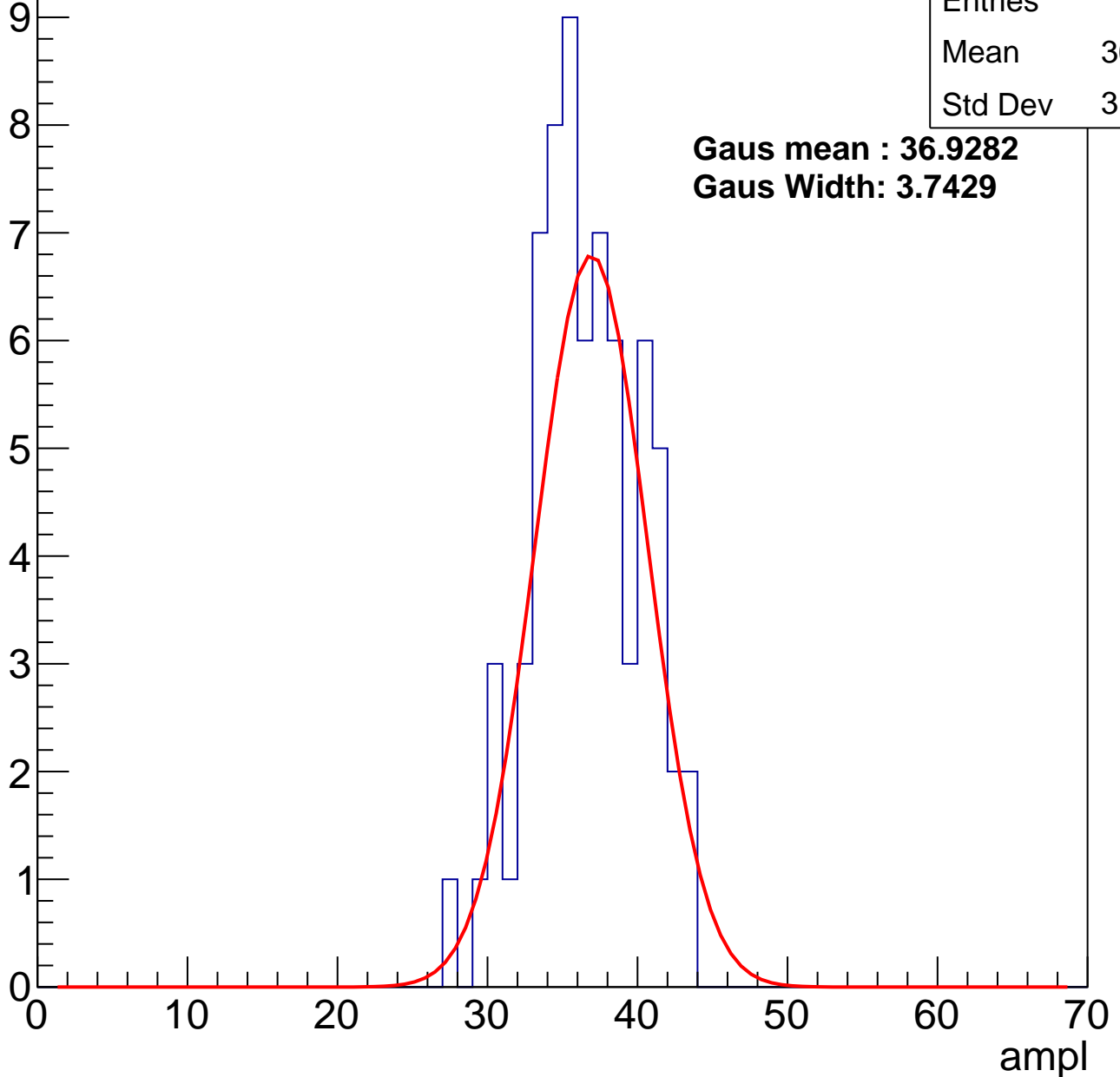
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	36.09
Std Dev	3.512

Gaus mean : 36.9282

Gaus Width: 3.7429



B1L102S, U6-ch104, adc2

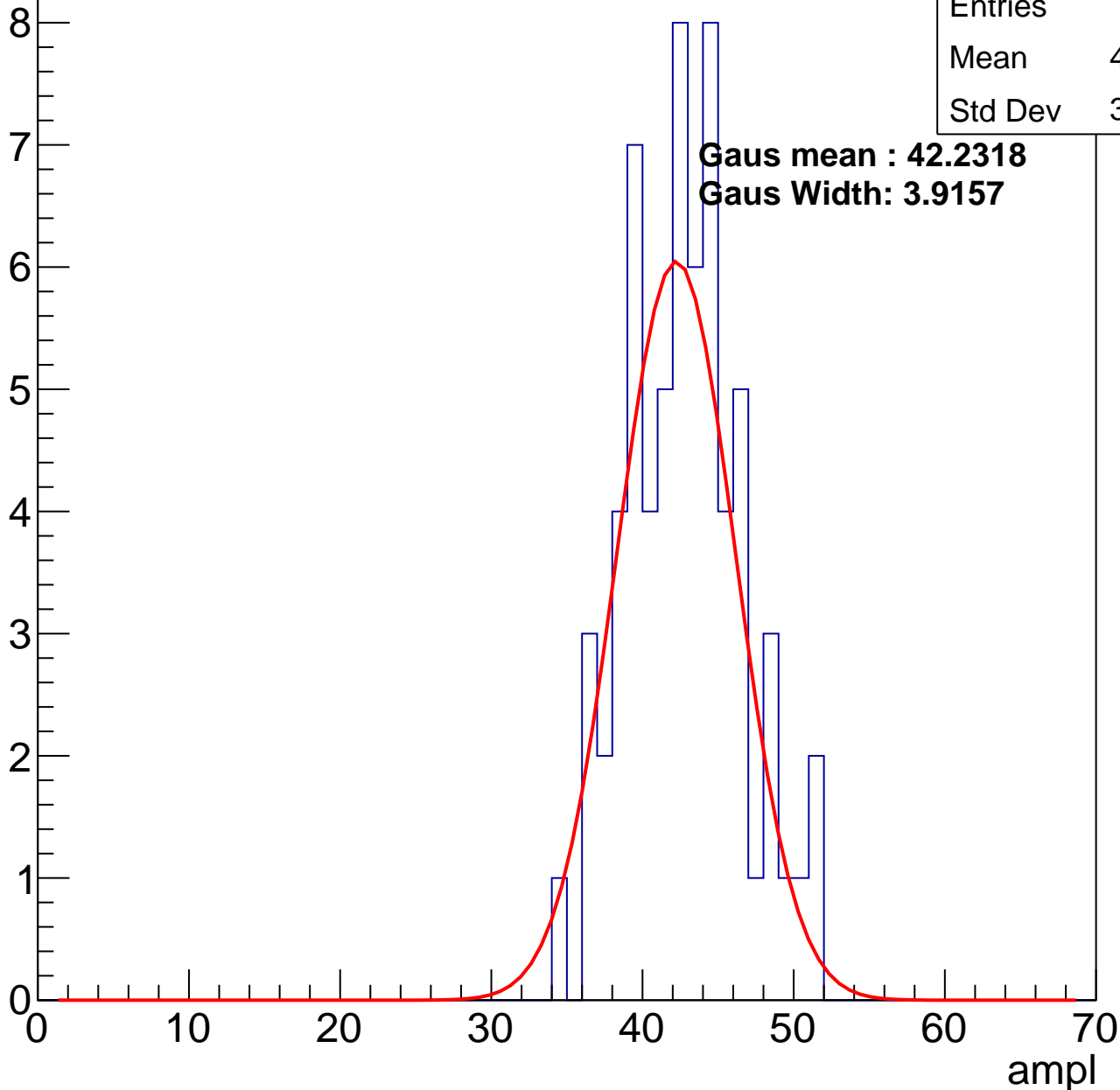
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.37
Std Dev	3.769

Gaus mean : 42.2318

Gaus Width: 3.9157

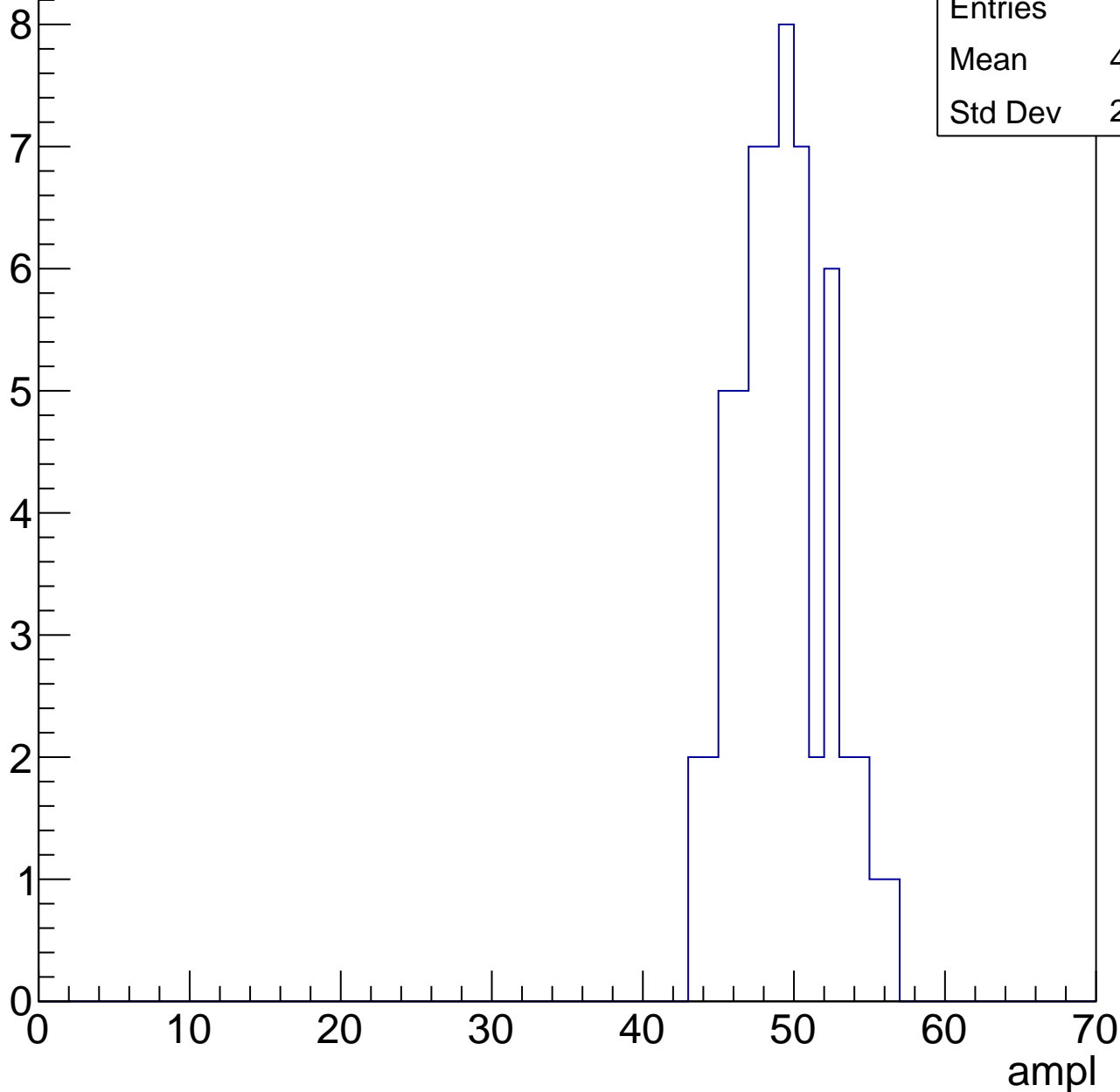


B1L102S, U6-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	48.68
Std Dev	2.998

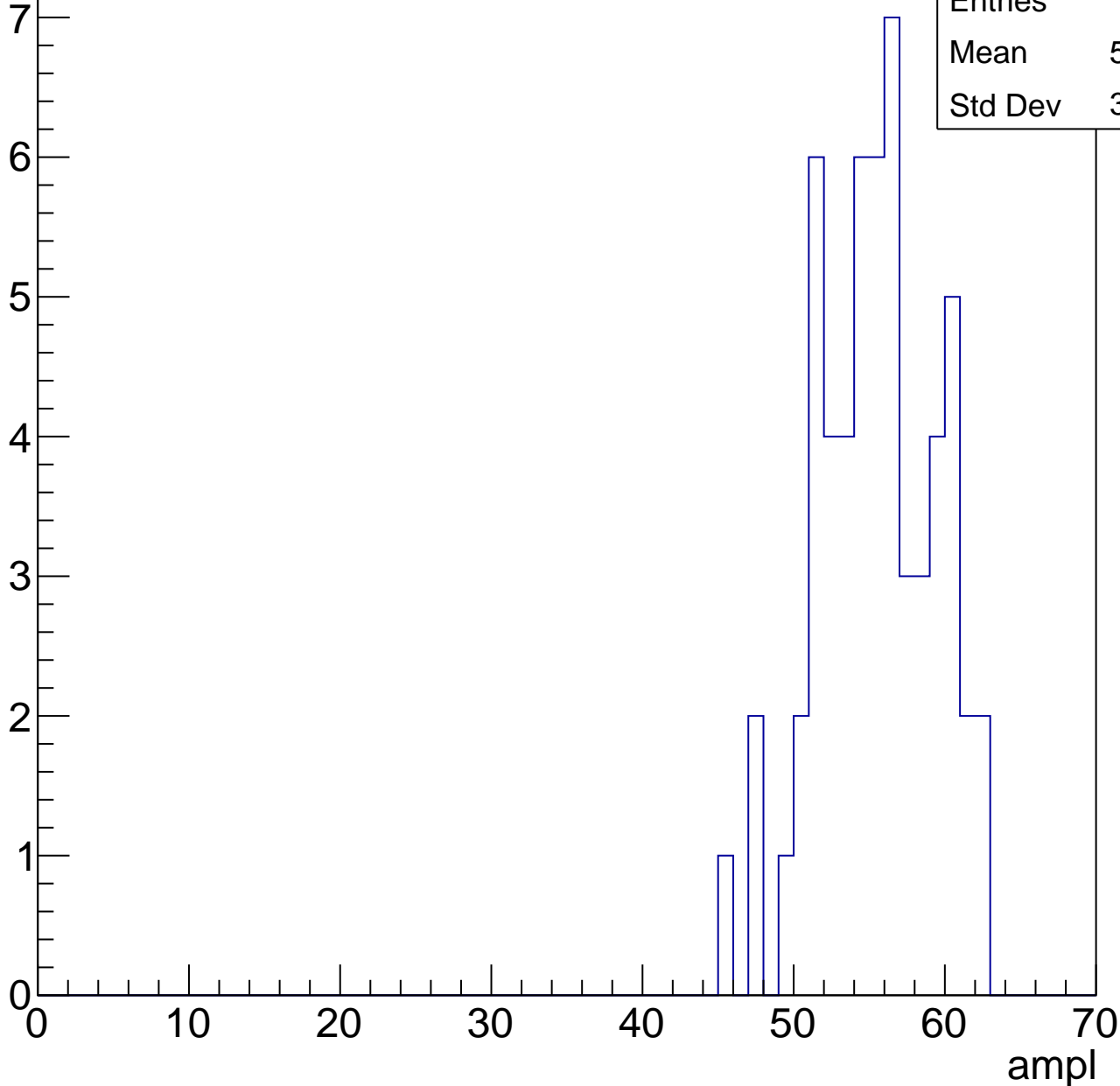


B1L102S, U6-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.95
Std Dev	3.879

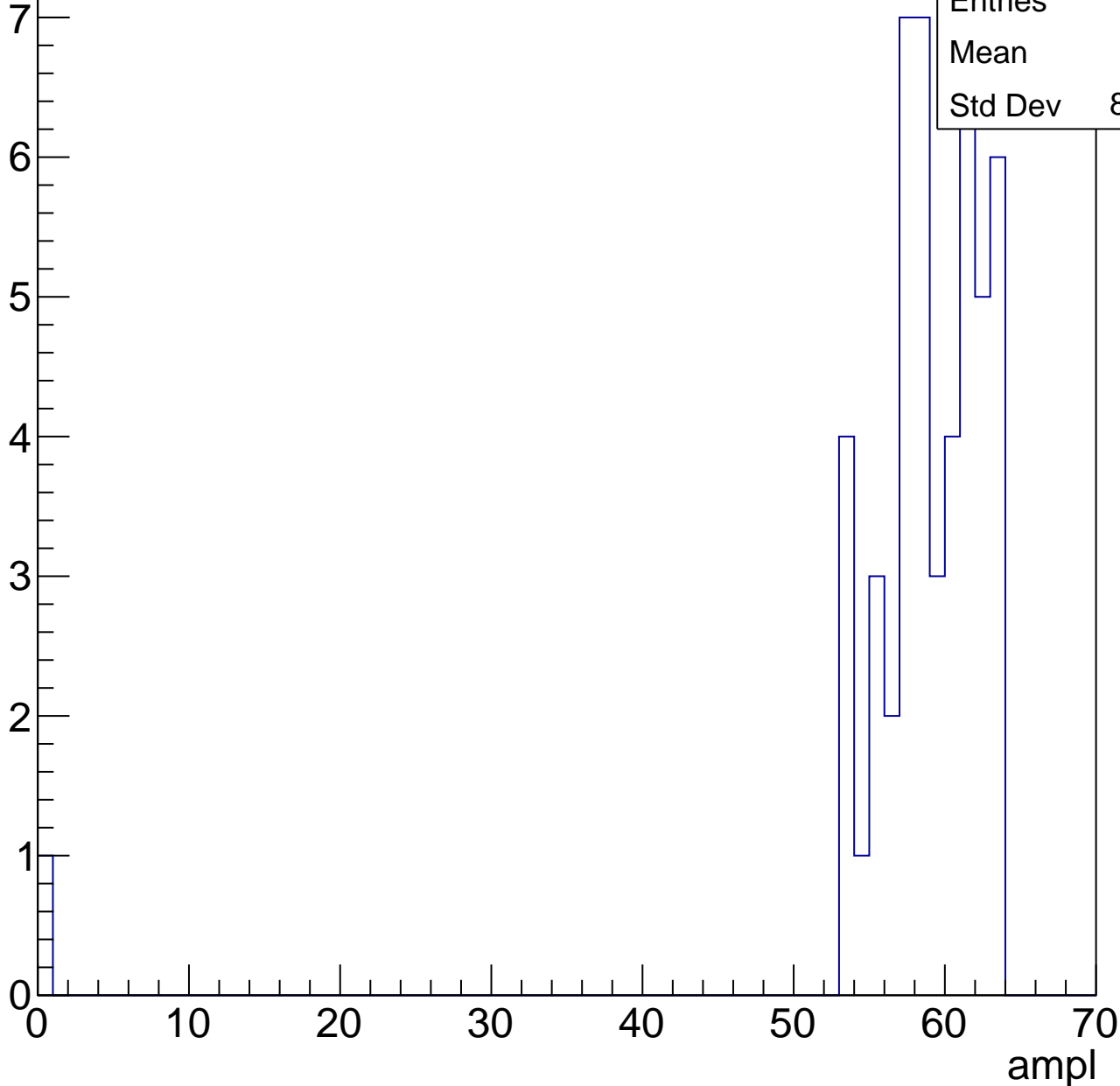


B1L102S, U6-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

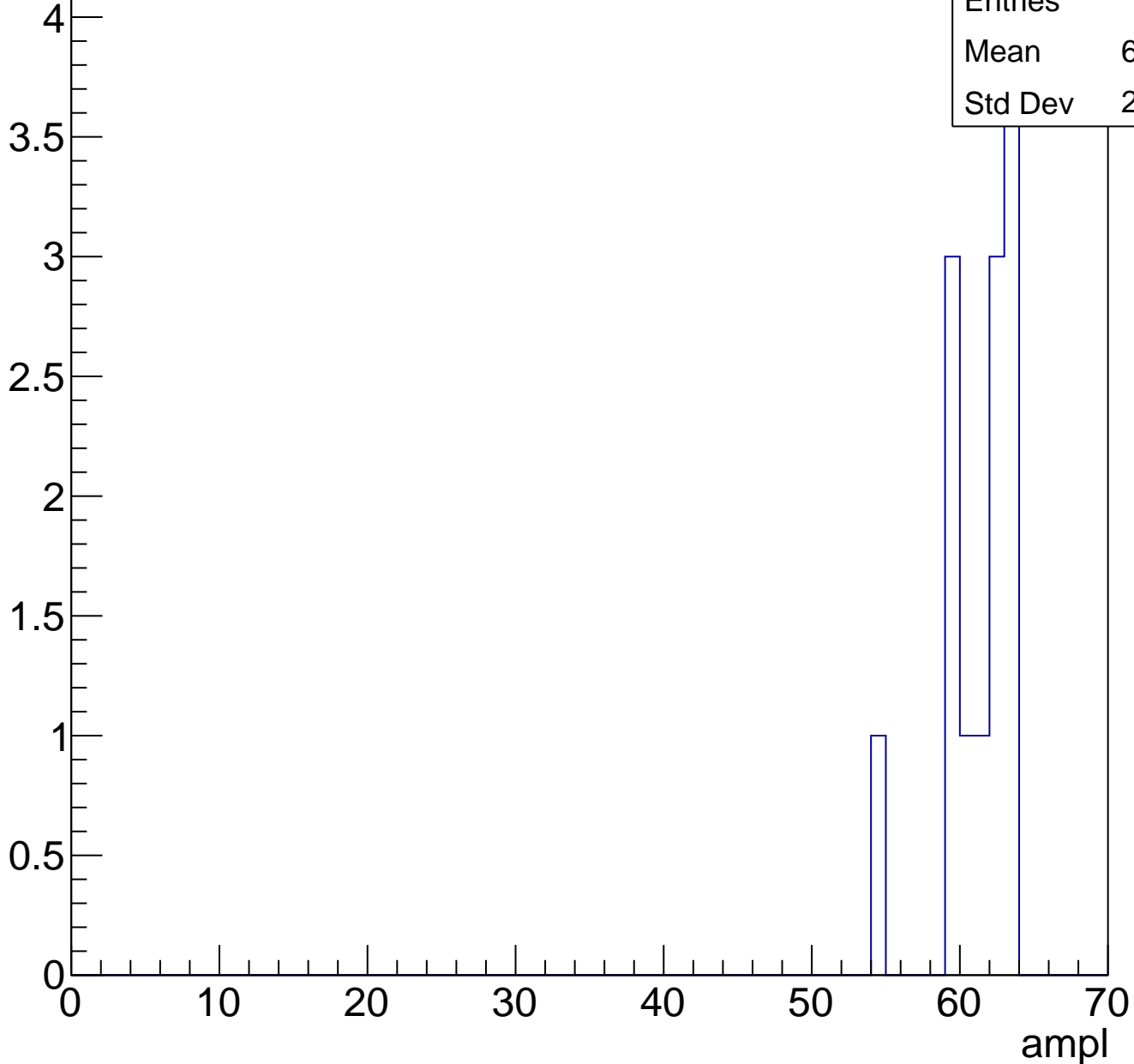
Entries	50
Mean	57.6
Std Dev	8.748



B1L102S, U6-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch105, adc0

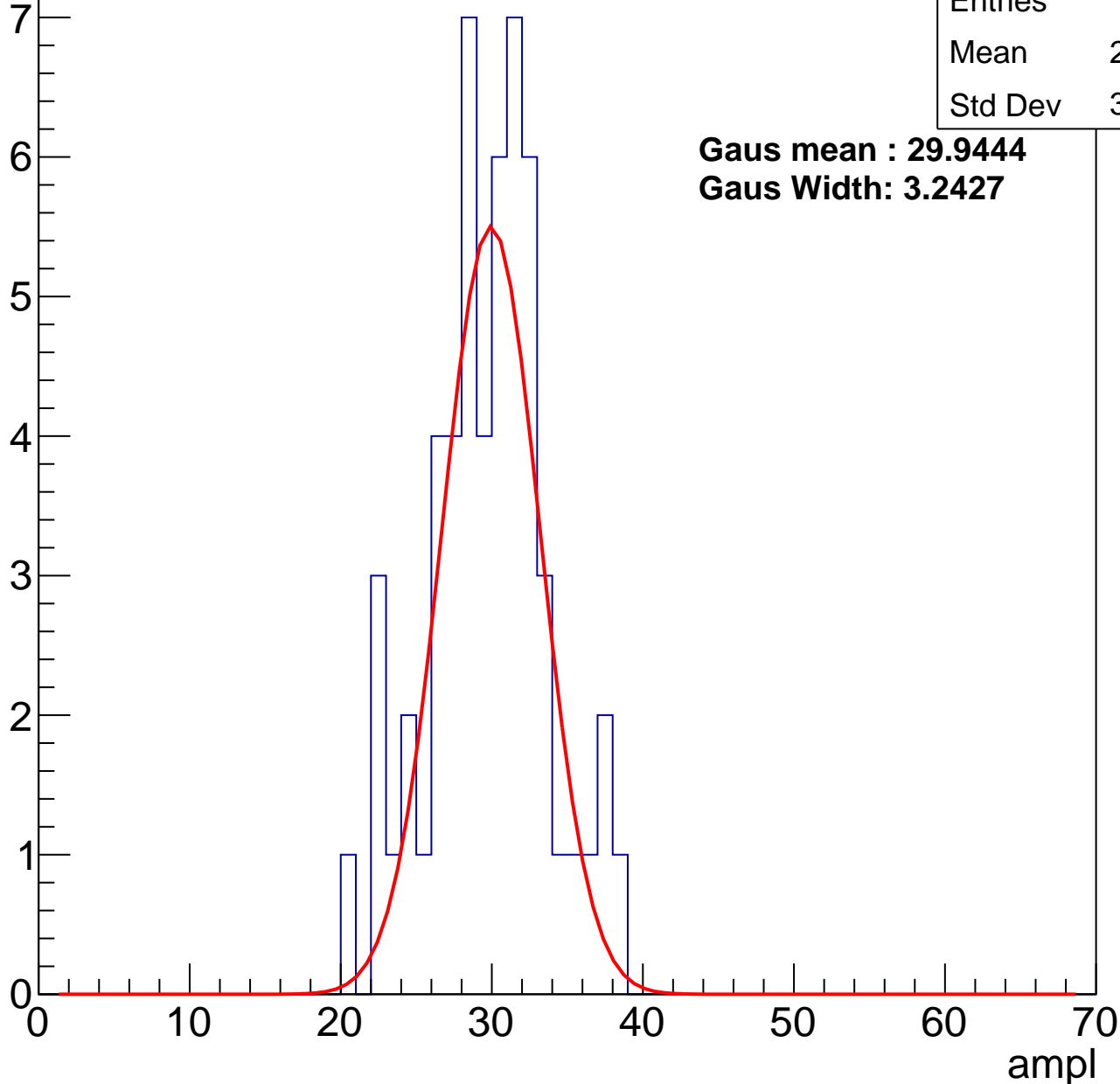
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	29.29
Std Dev	3.907

Gaus mean : 29.9444

Gaus Width: 3.2427



B1L102S, U6-ch105, adc1

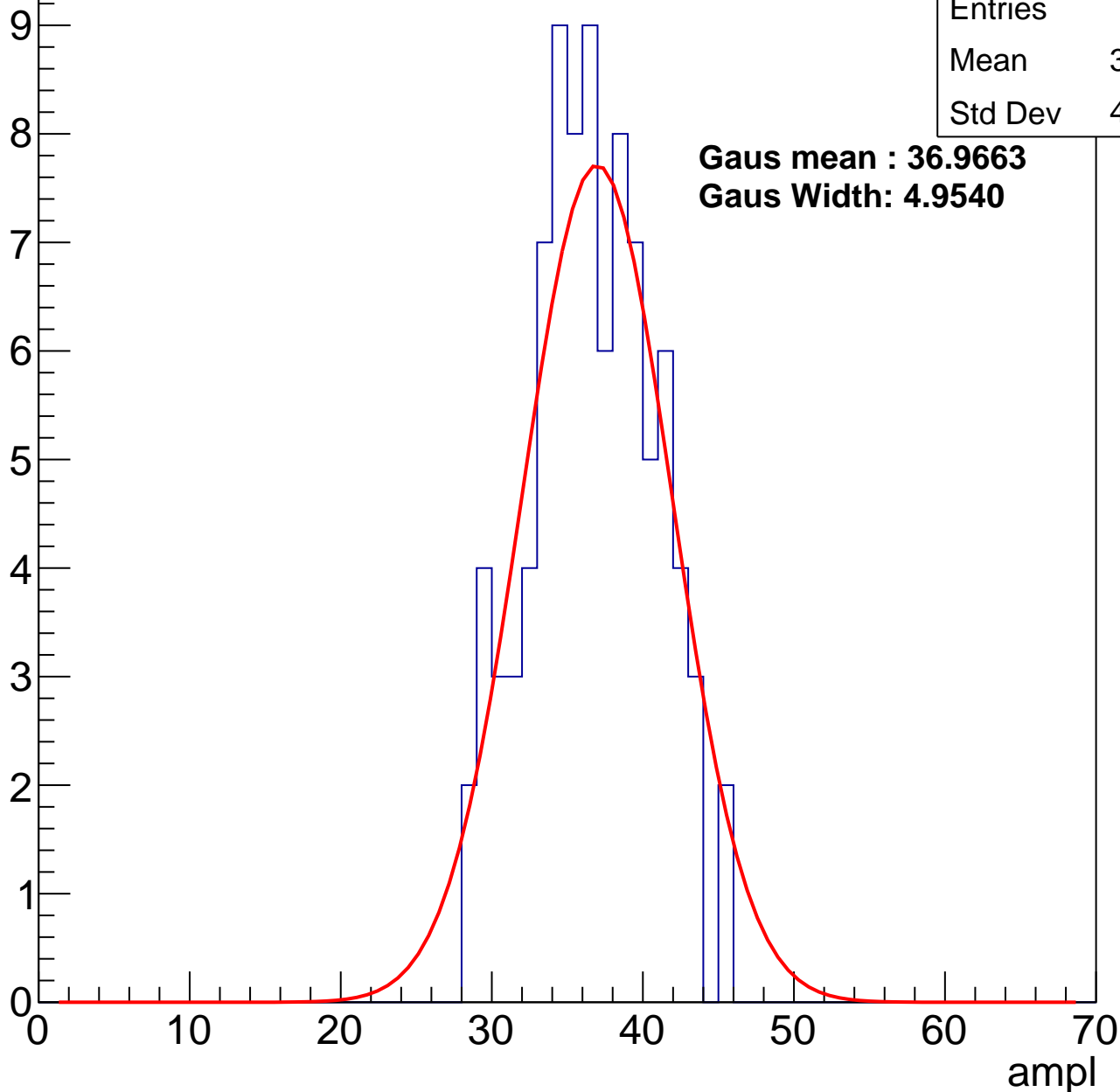
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	90
Mean	36.18
Std Dev	4.027

Gaus mean : 36.9663

Gaus Width: 4.9540

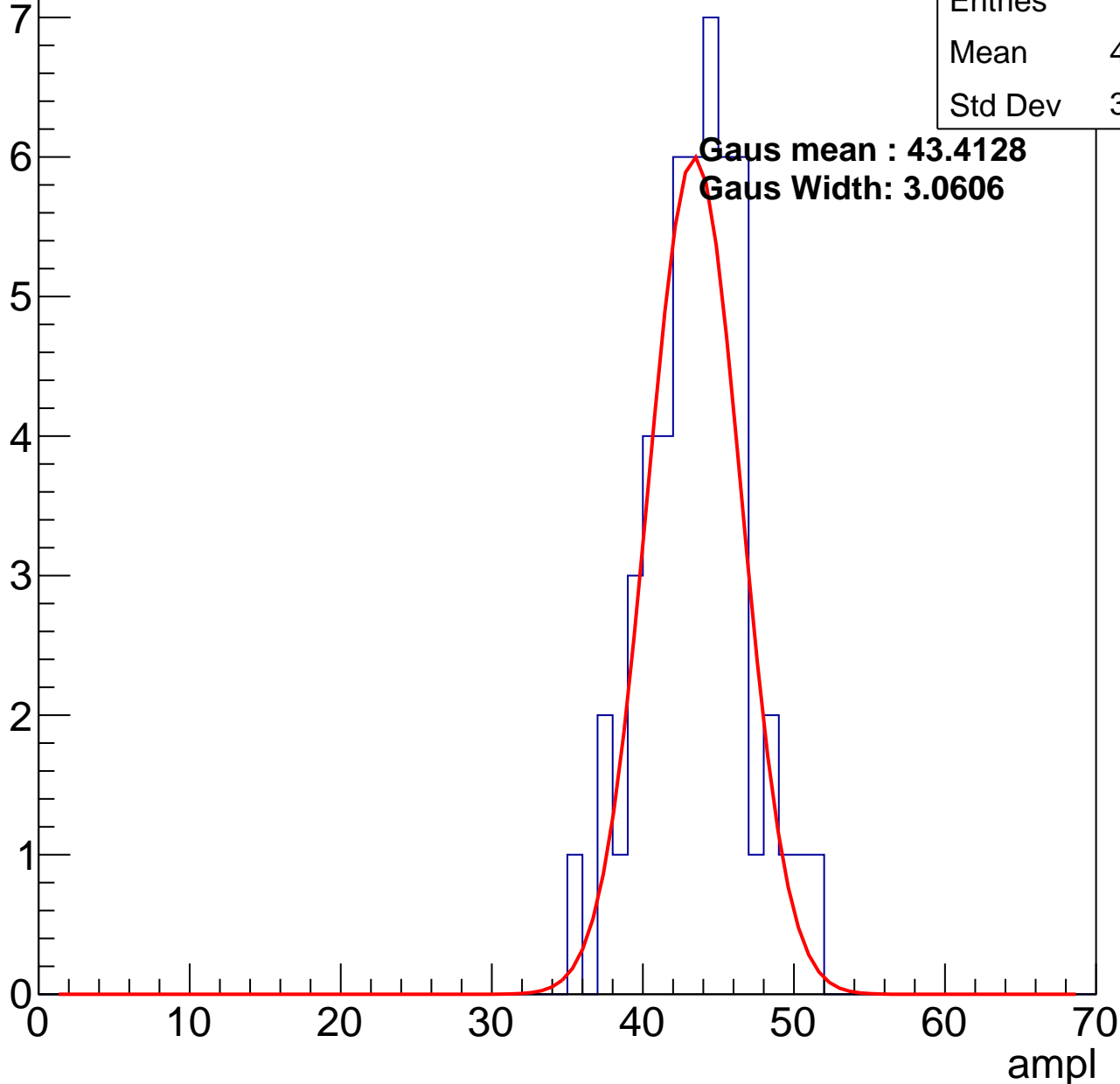


B1L102S, U6-ch105, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	43.17
Std Dev	3.298

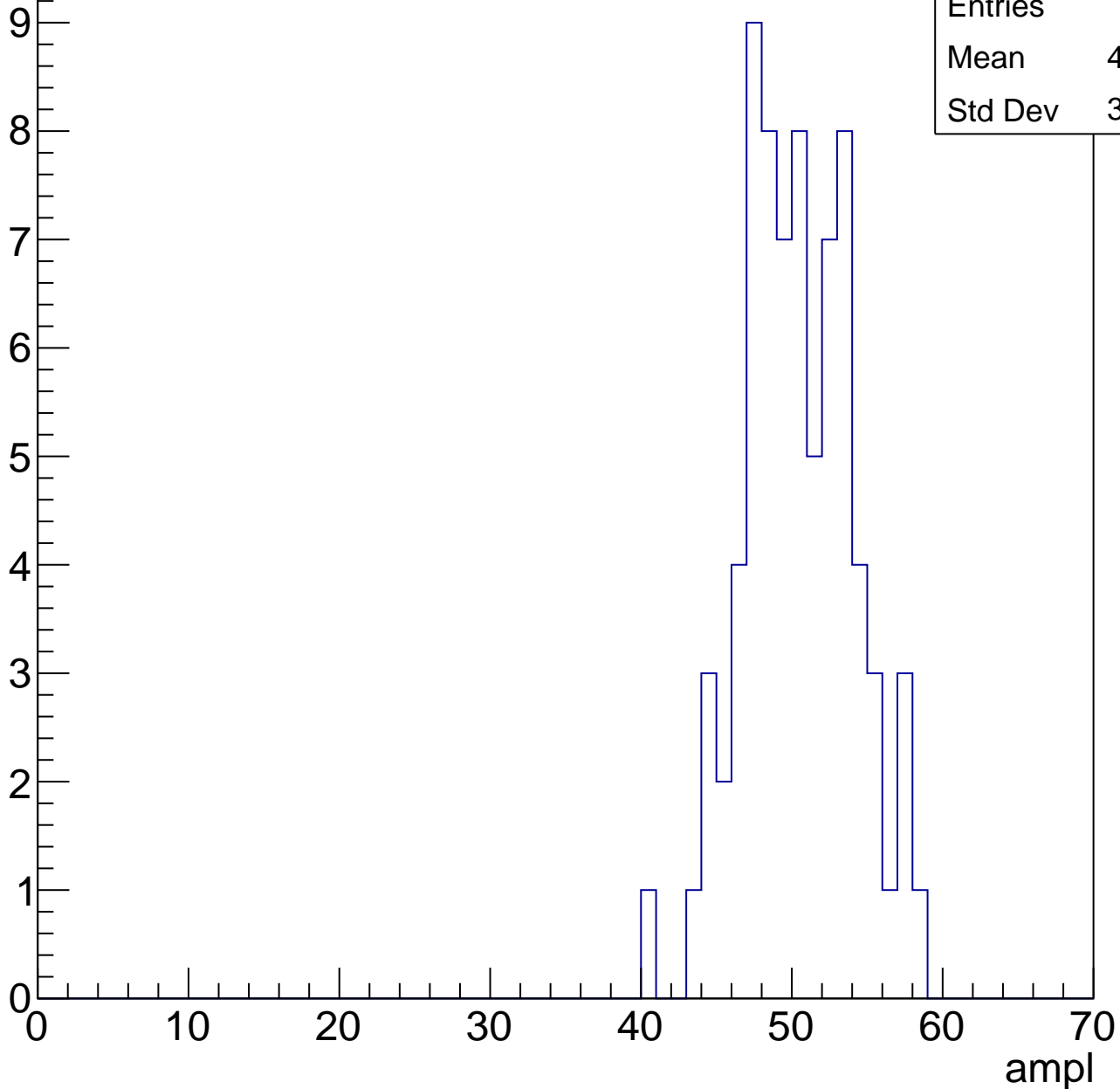


B1L102S, U6-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

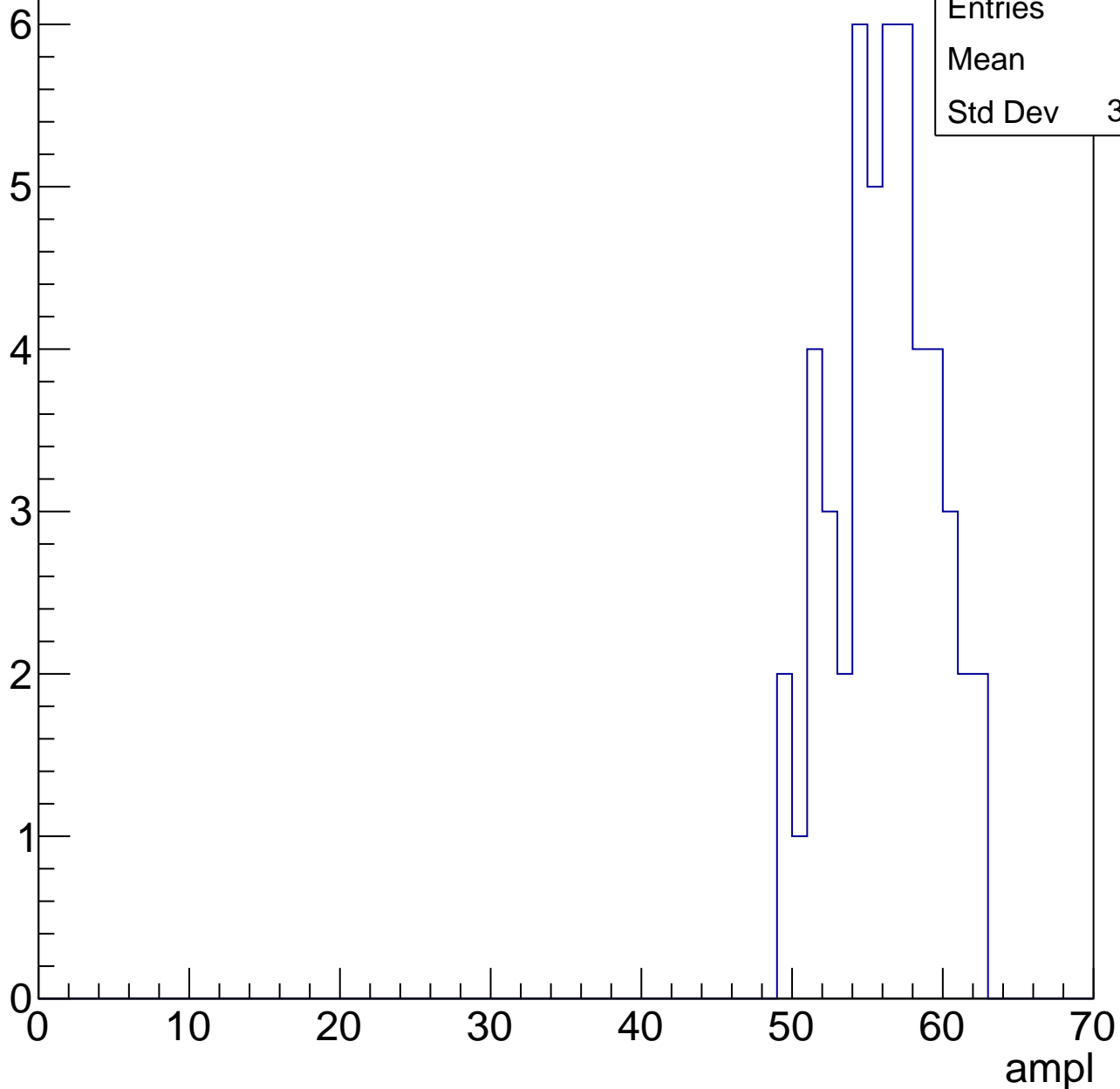
Entries	75
Mean	49.97
Std Dev	3.618



B1L102S, U6-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	50
Mean	55.7
Std Dev	3.324

B1L102S, U6-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

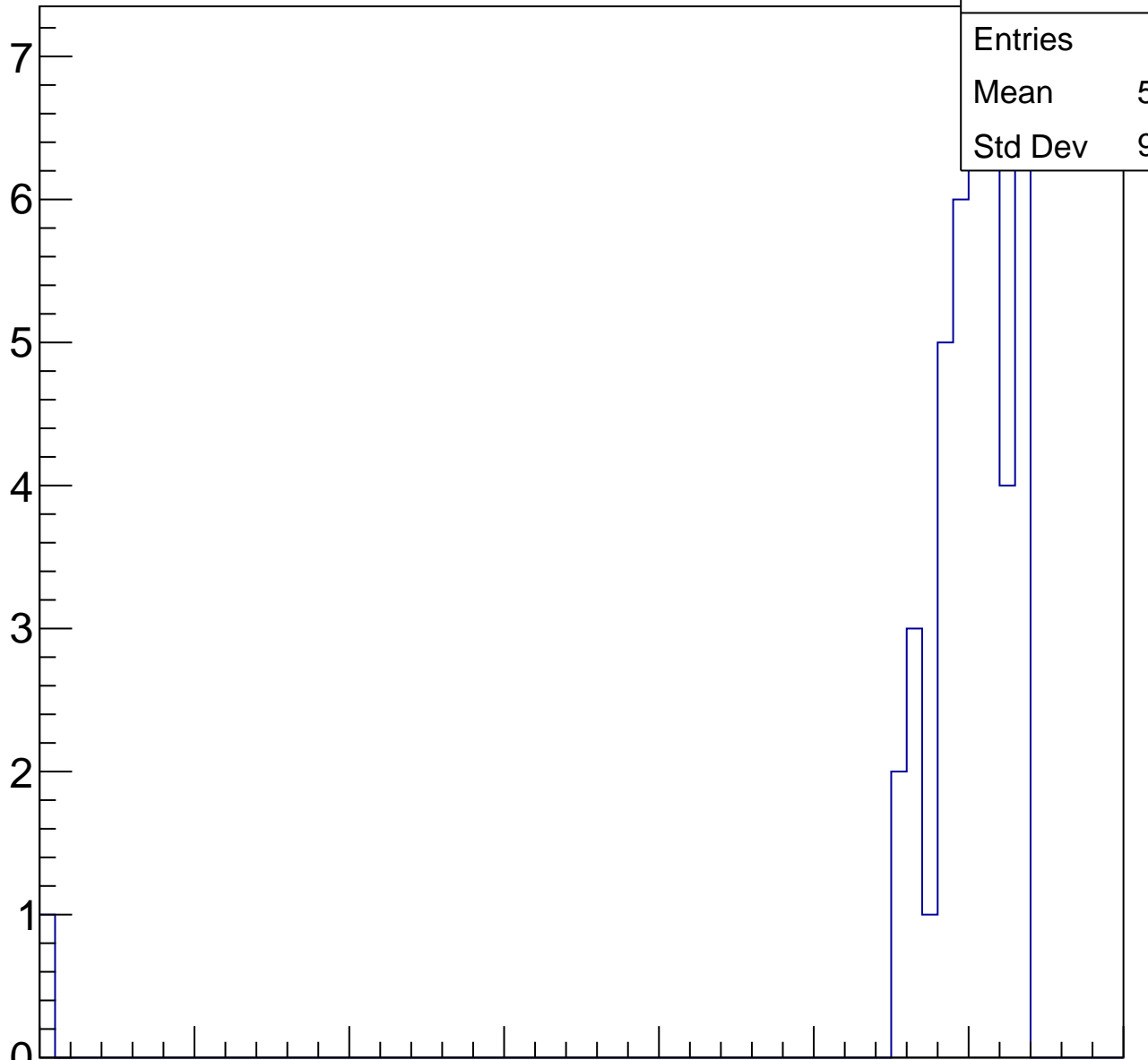
Entry

7
6
5
4
3
2
1
0

Entries	43
Mean	58.49
Std Dev	9.302

ampl

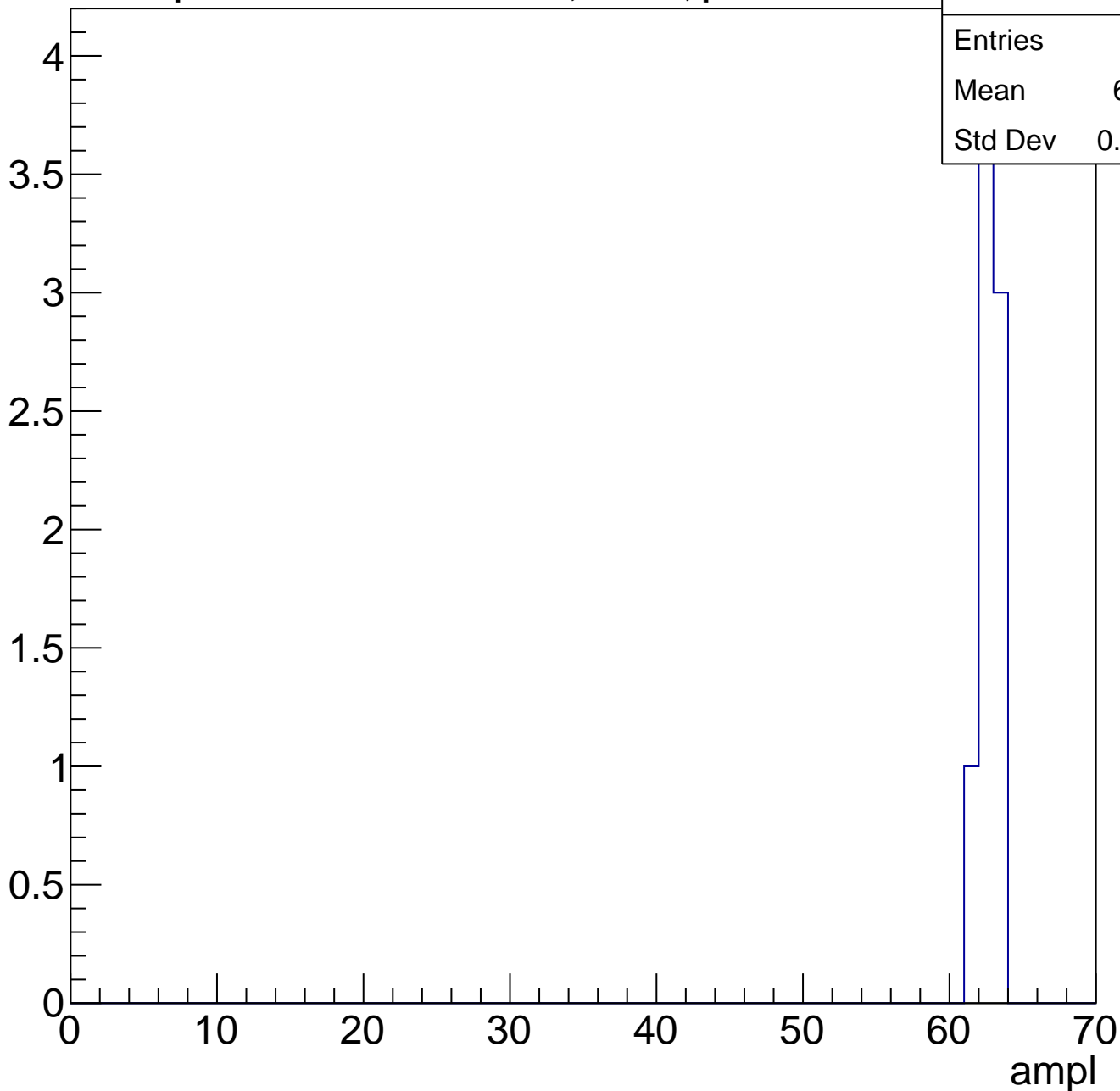
0 10 20 30 40 50 60 70



B1L102S, U6-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch106, adc0

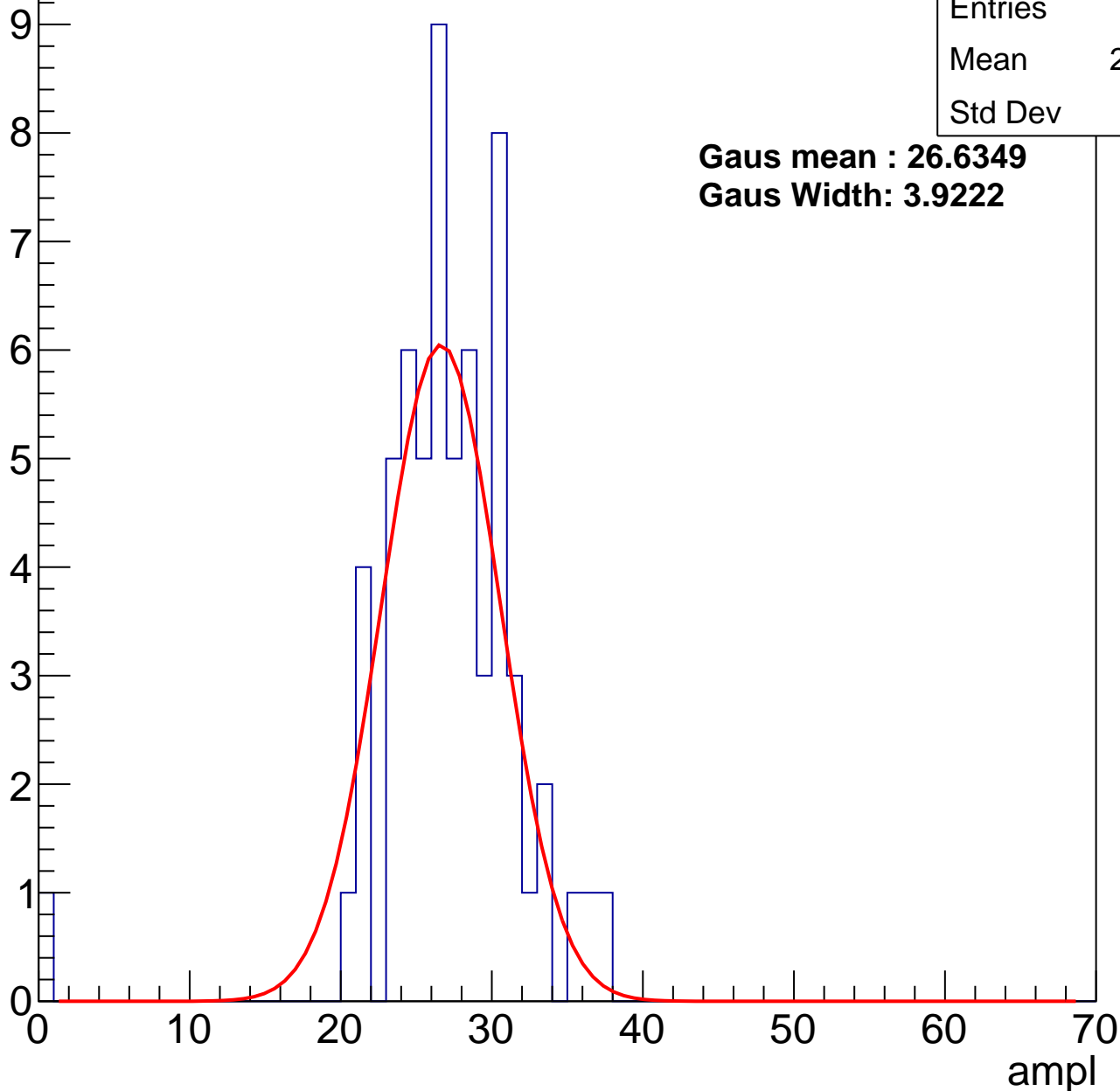
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	26.63
Std Dev	5.01

Gaus mean : 26.6349

Gaus Width: 3.9222



B1L102S, U6-ch106, adc1

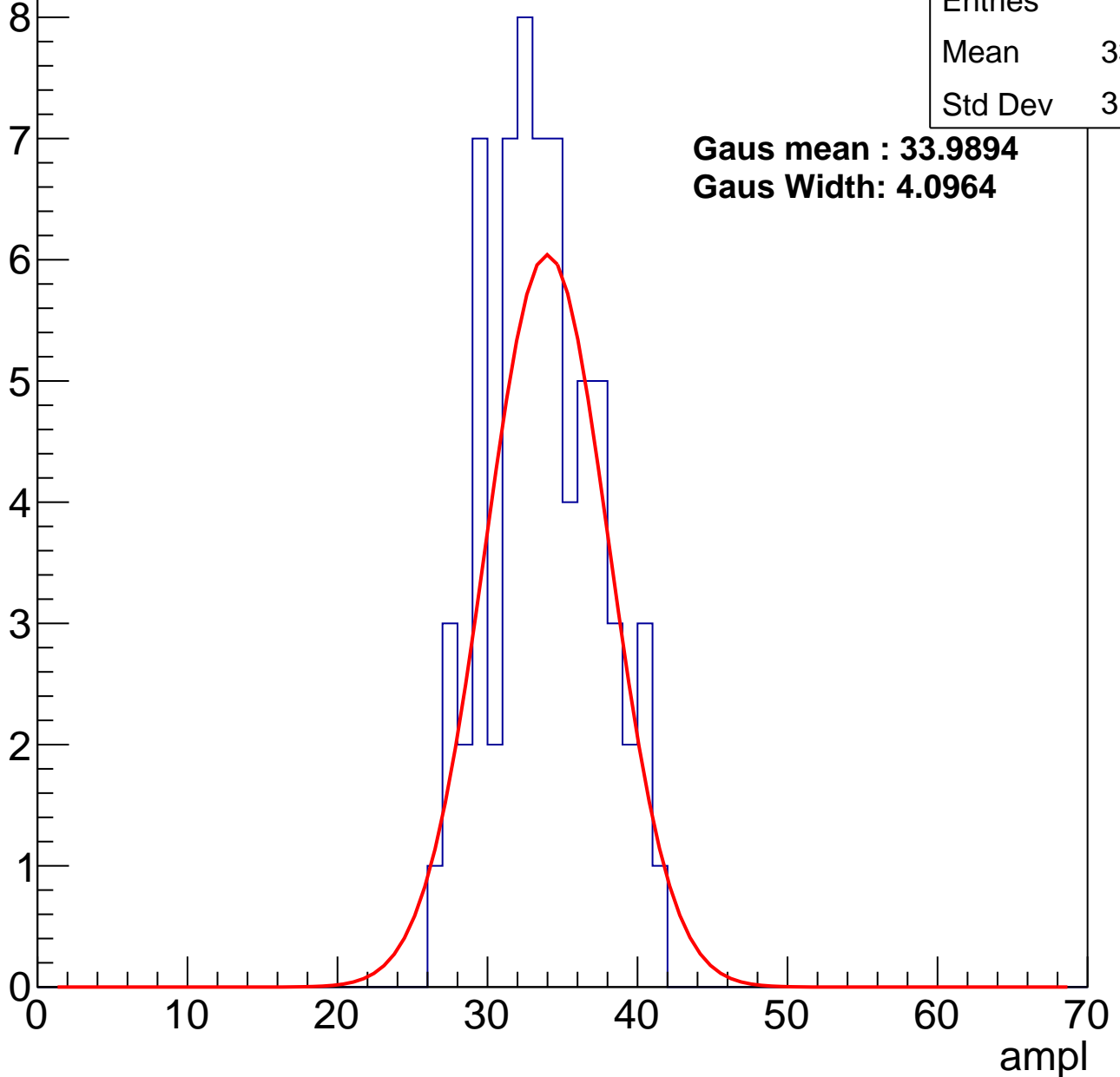
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	33.22
Std Dev	3.615

Gaus mean : 33.9894

Gaus Width: 4.0964



B1L102S, U6-ch106, adc2

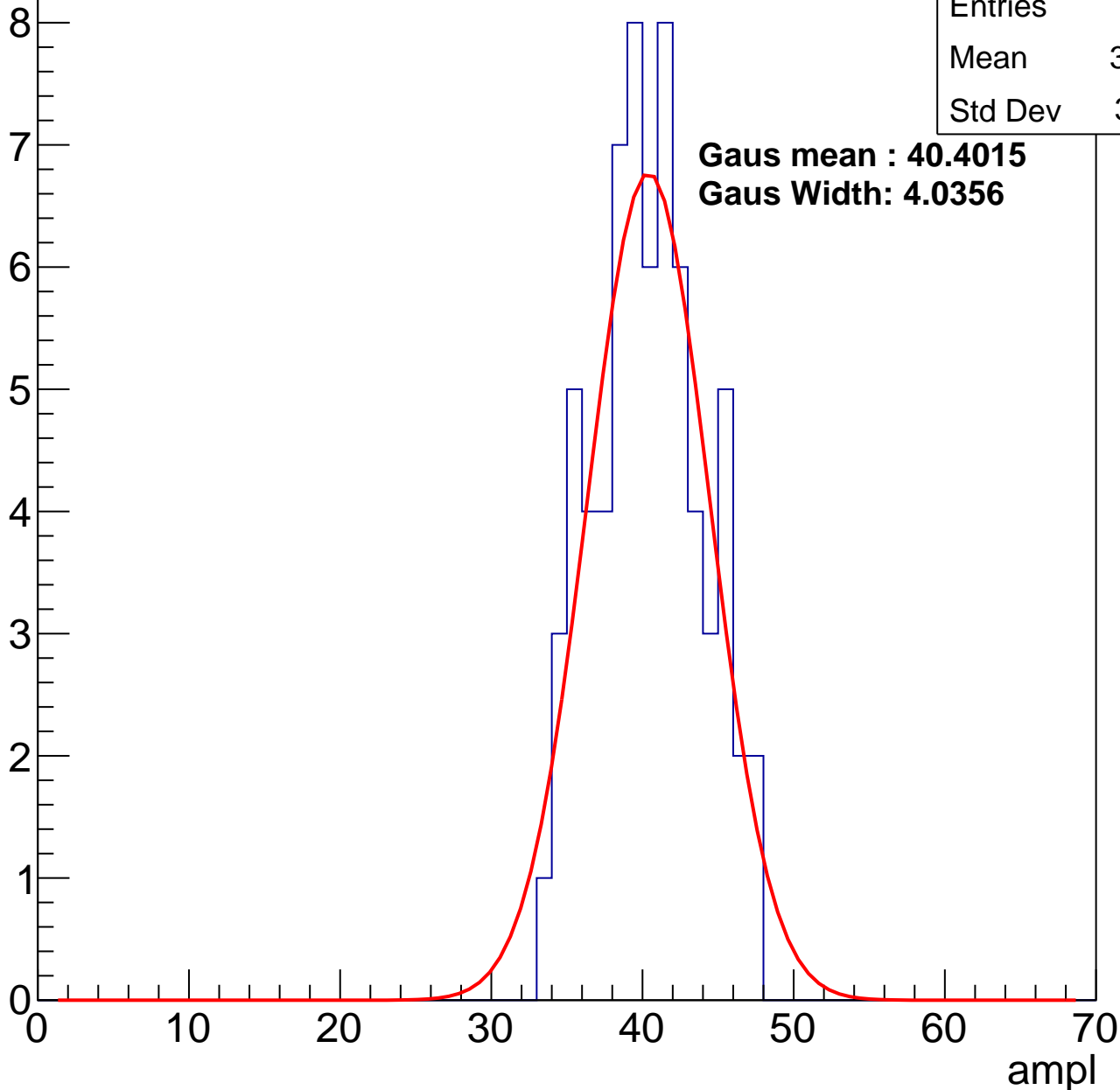
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	39.93
Std Dev	3.491

Gaus mean : 40.4015

Gaus Width: 4.0356

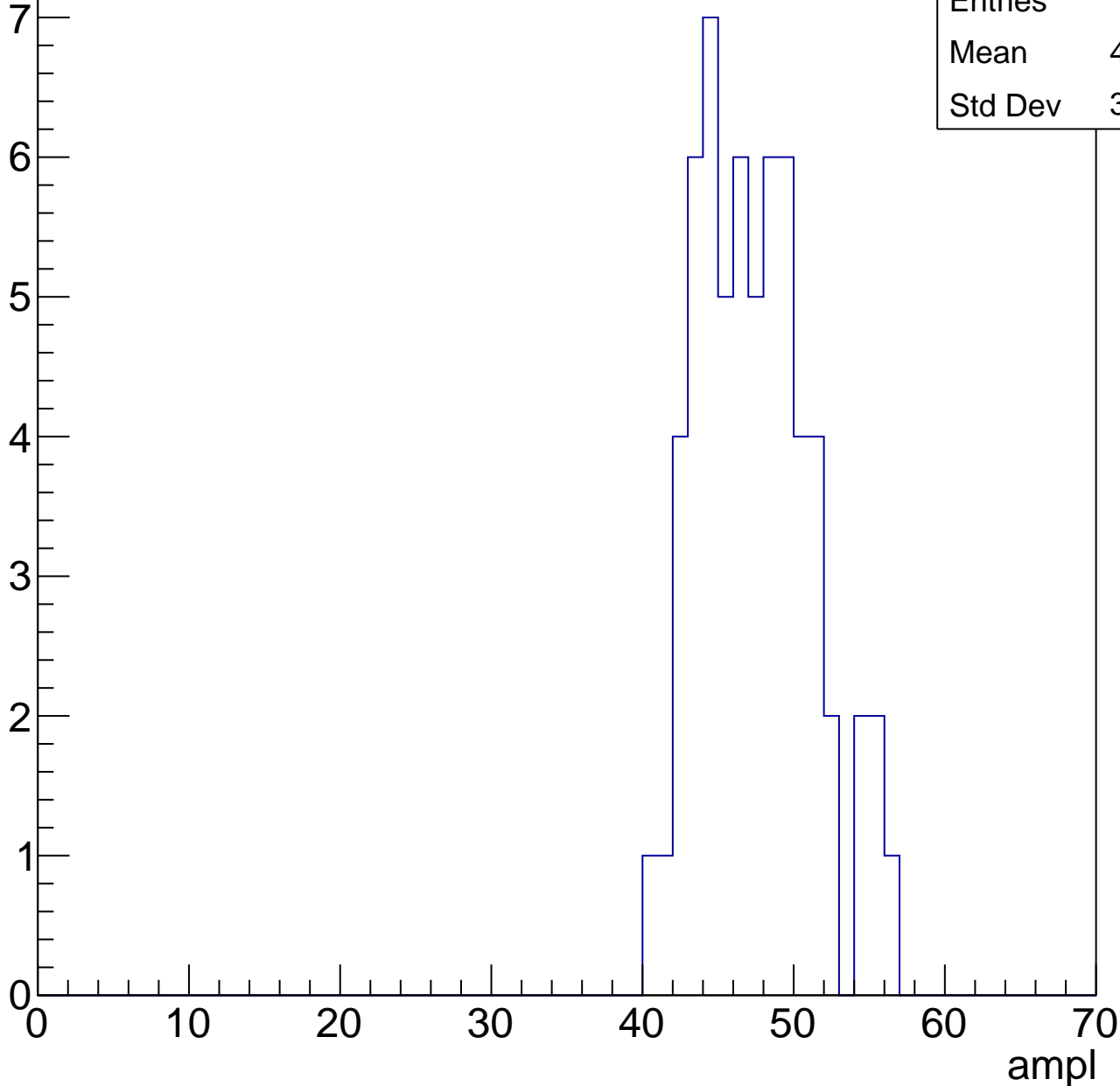


B1L102S, U6-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.02
Std Dev	3.718

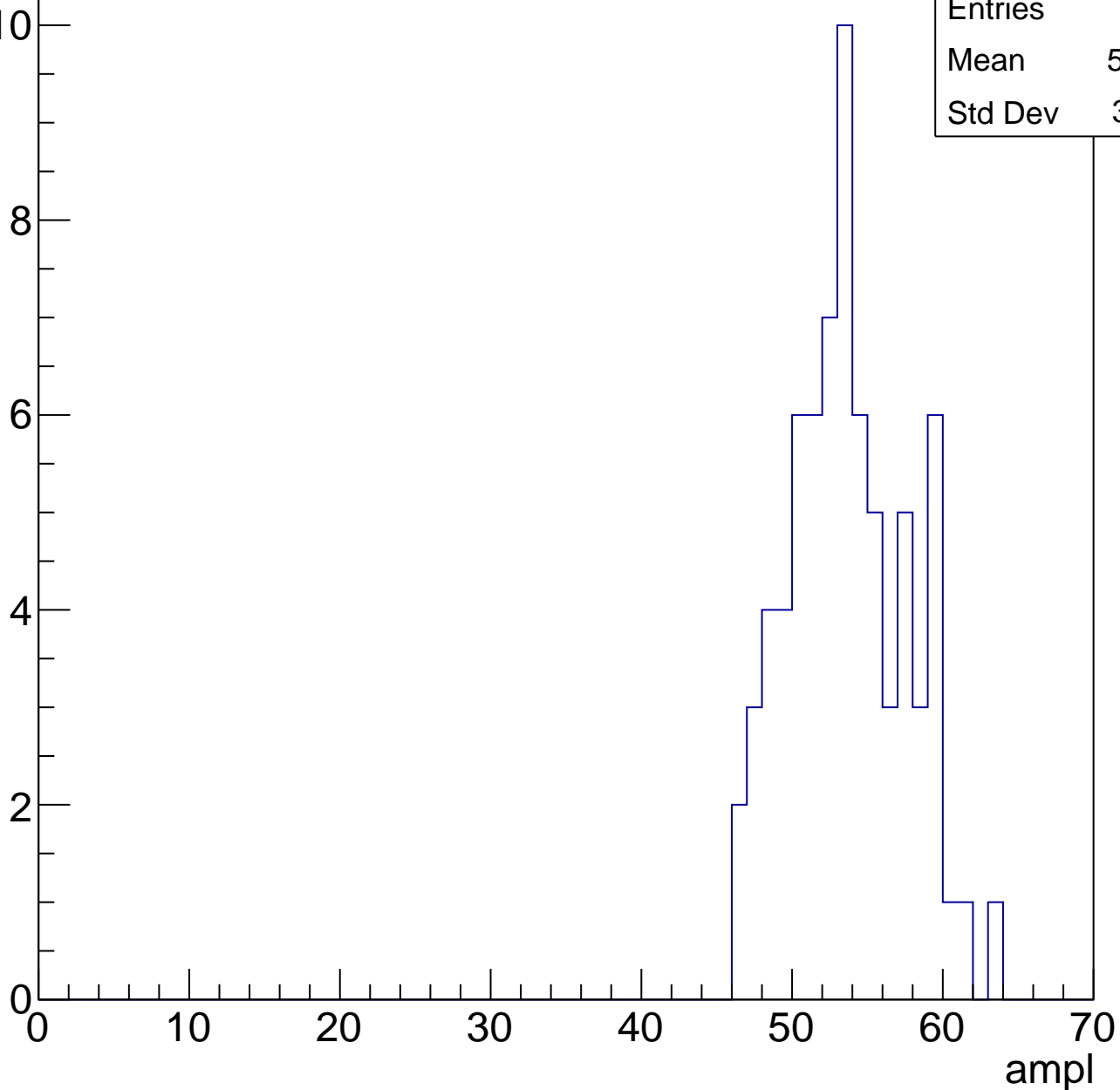


B1L102S, U6-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	53.22
Std Dev	3.861

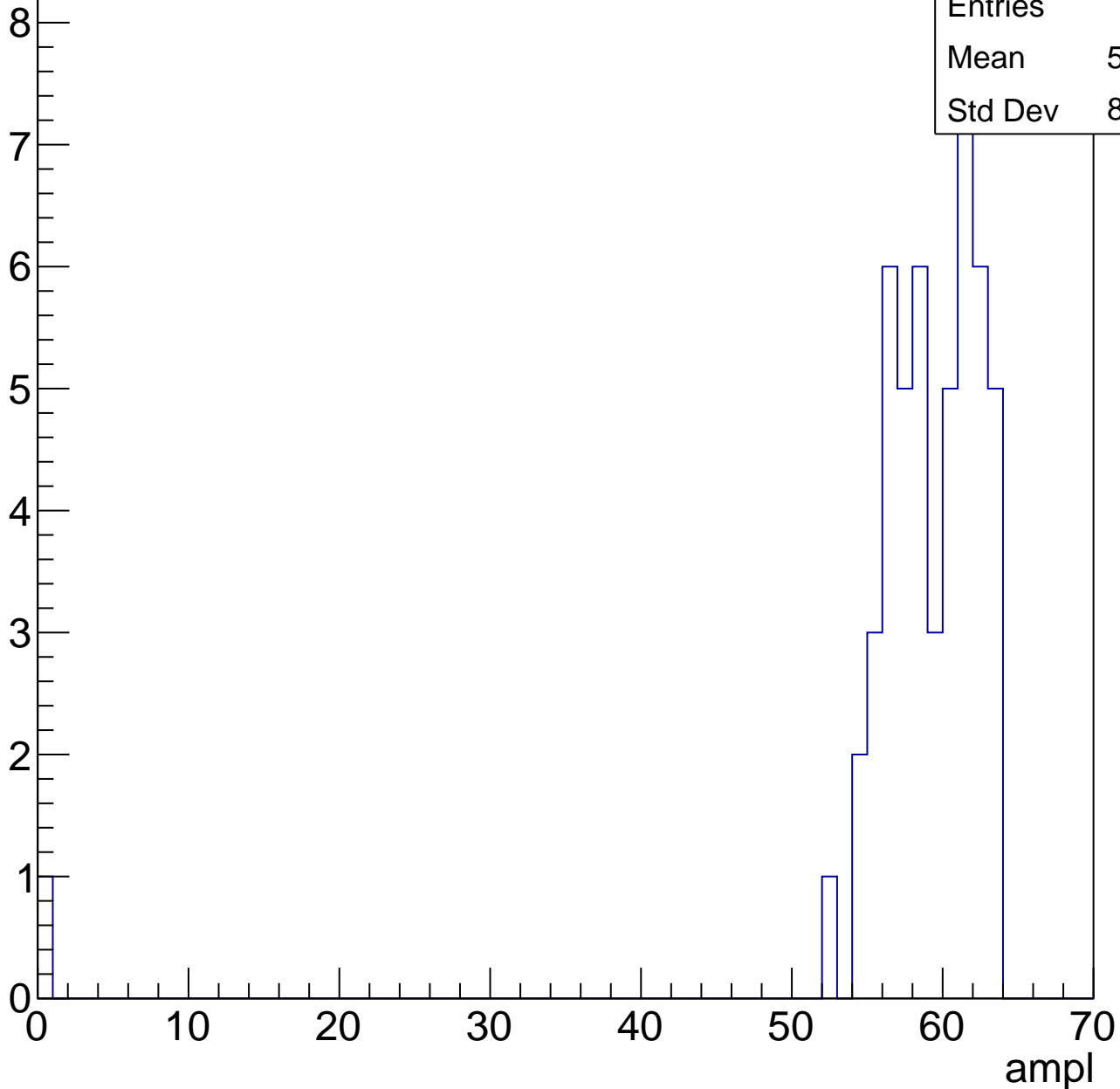


B1L102S, U6-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

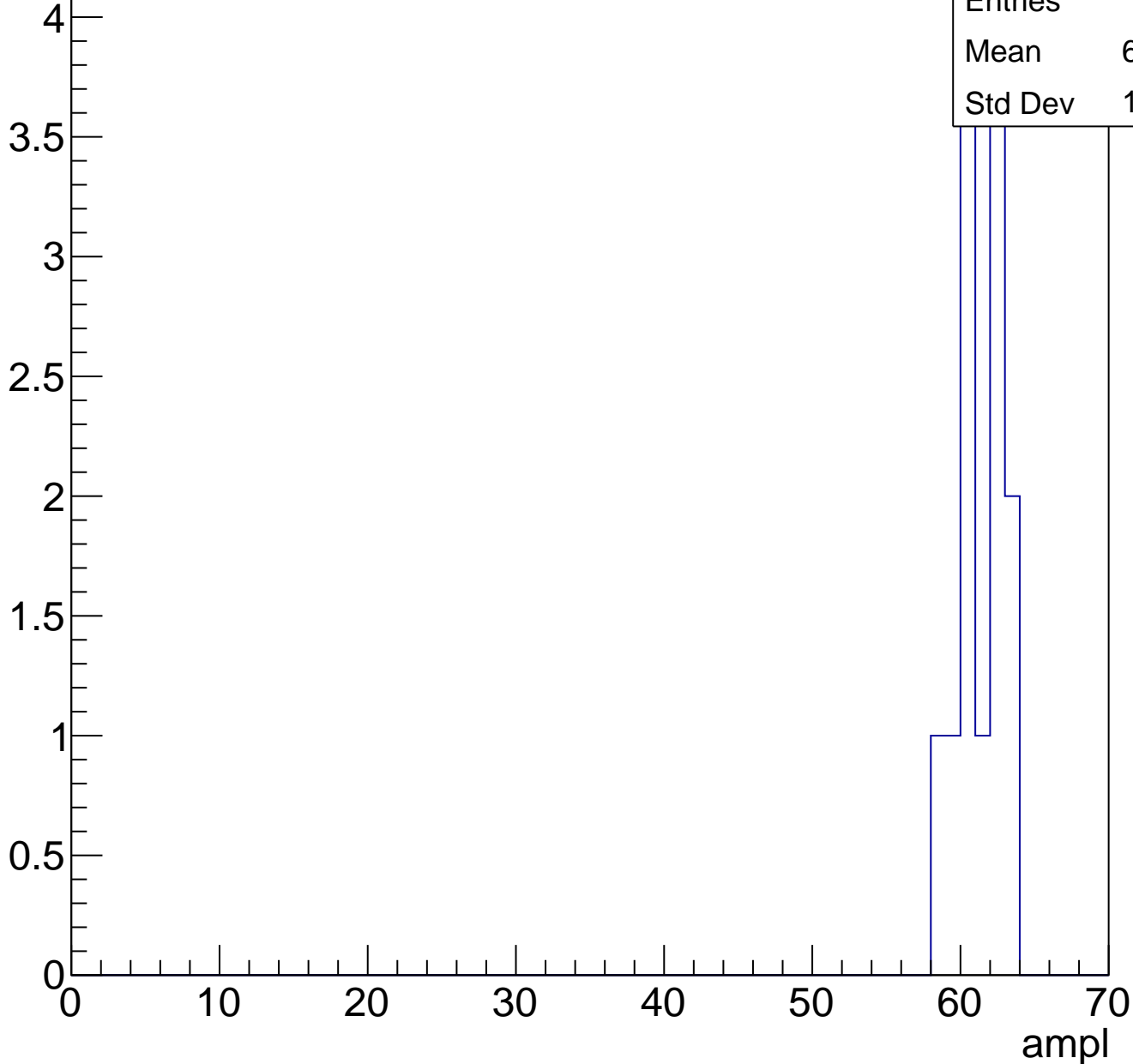
Entries	51
Mean	57.76
Std Dev	8.633



B1L102S, U6-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	63
Std Dev	0

B1L102S, U6-ch107, adc0

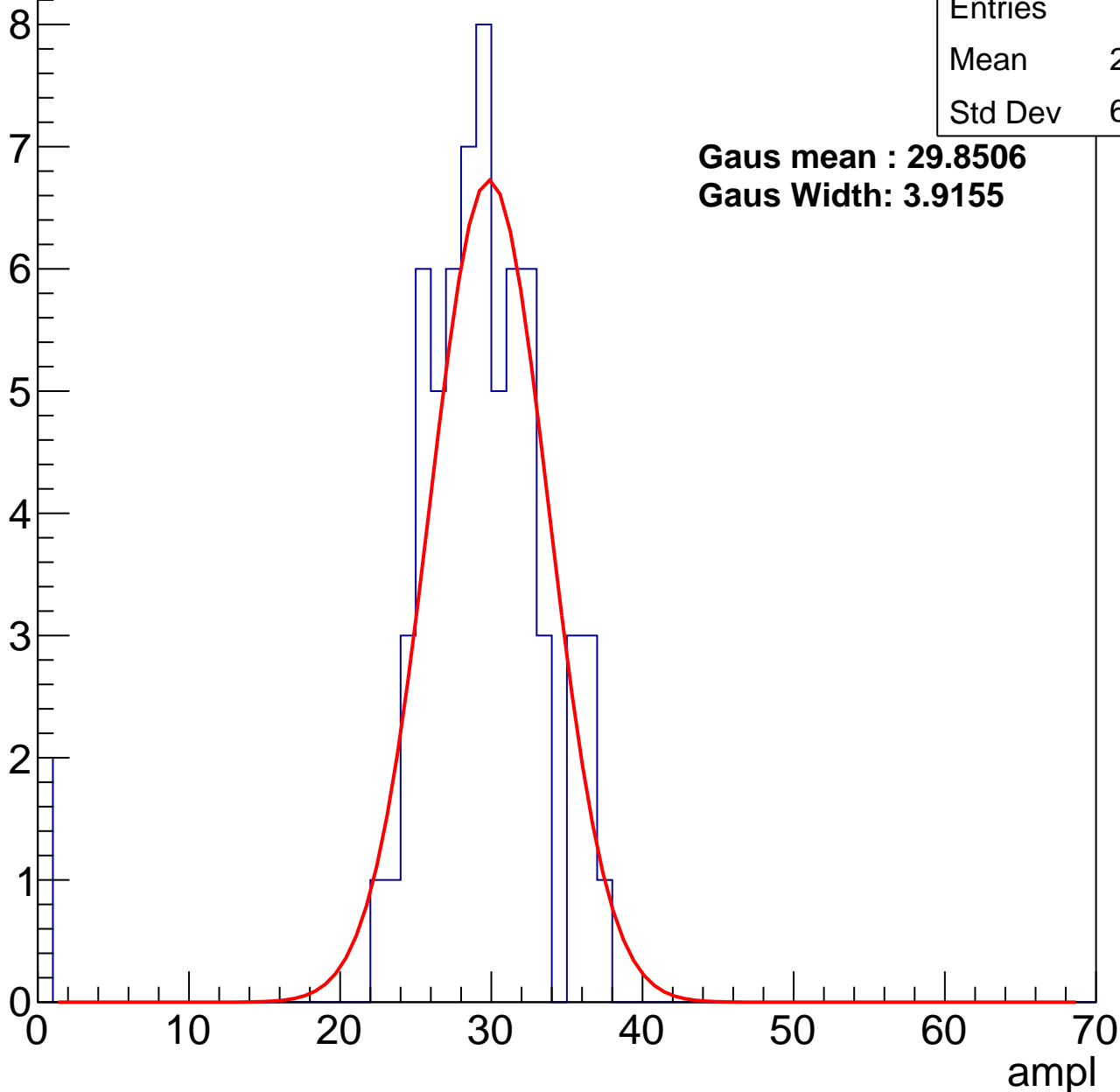
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.24
Std Dev	6.063

Gaus mean : 29.8506

Gaus Width: 3.9155



B1L102S, U6-ch107, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	35.43
Std Dev	3.666

Gaus mean : 36.3889

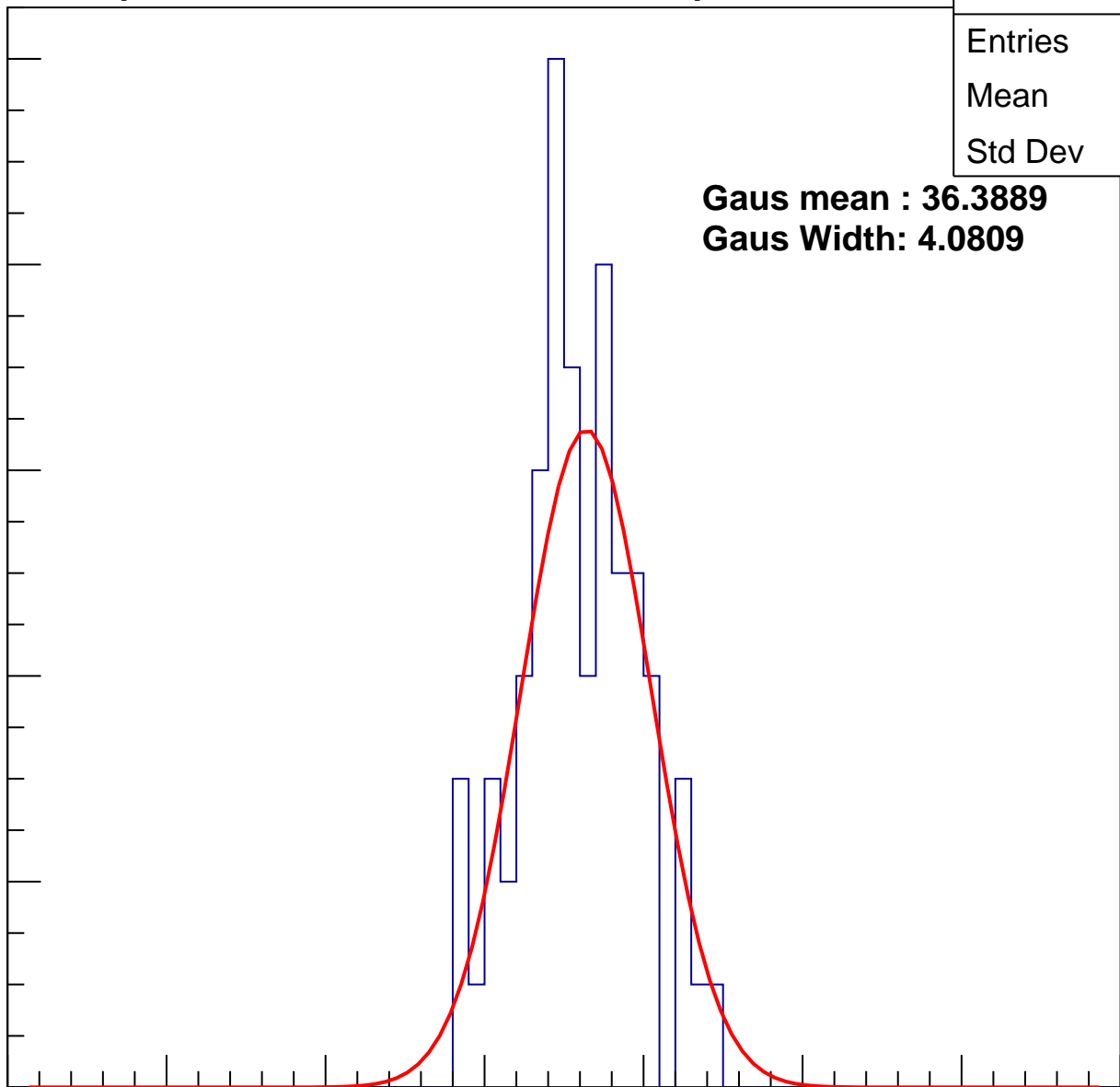
Gaus Width: 4.0809

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch107, adc2

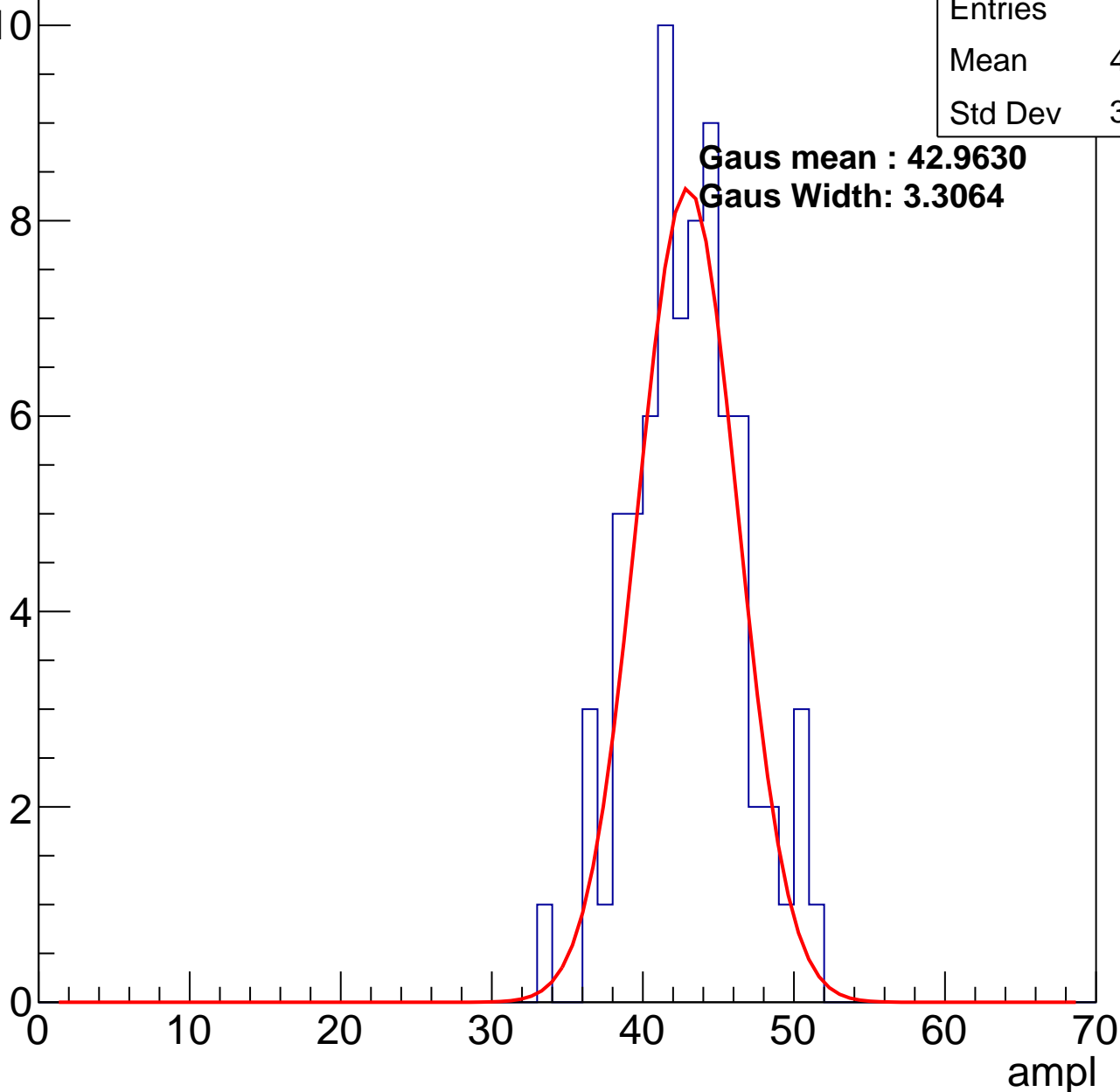
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	42.54
Std Dev	3.607

Gaus mean : 42.9630

Gaus Width: 3.3064

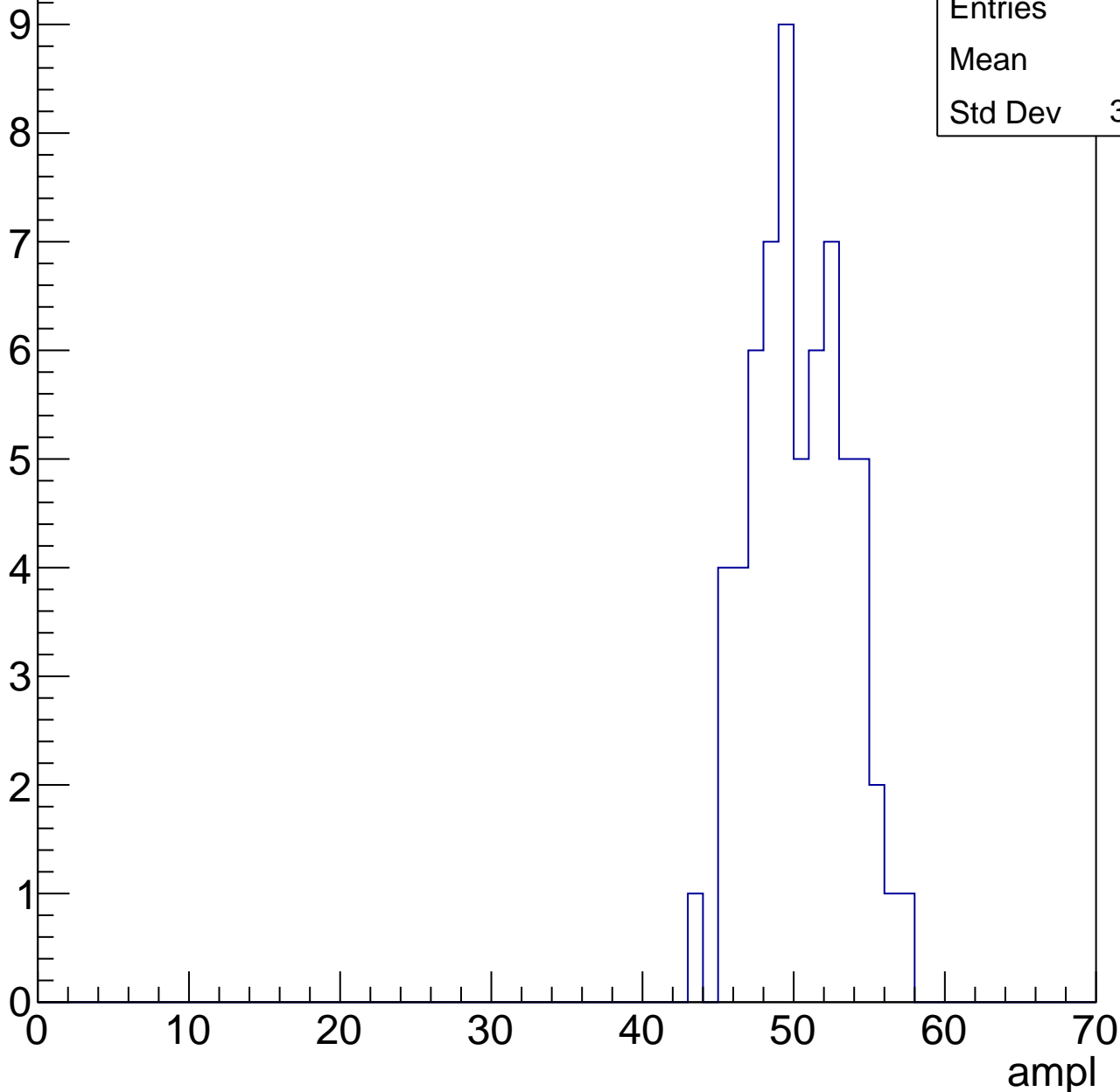


B1L102S, U6-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	49.9
Std Dev	3.079

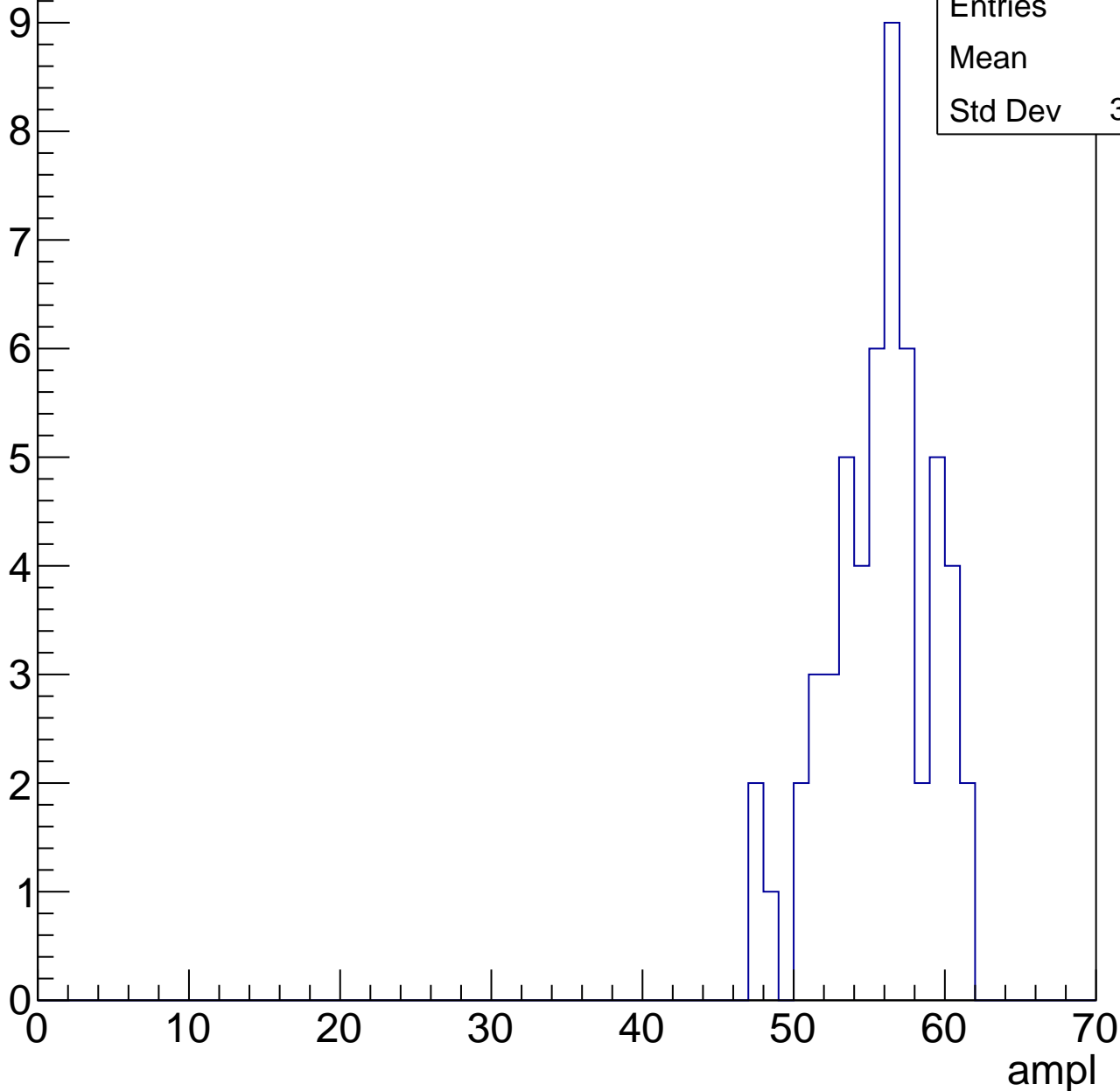


B1L102S, U6-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.2
Std Dev	3.407

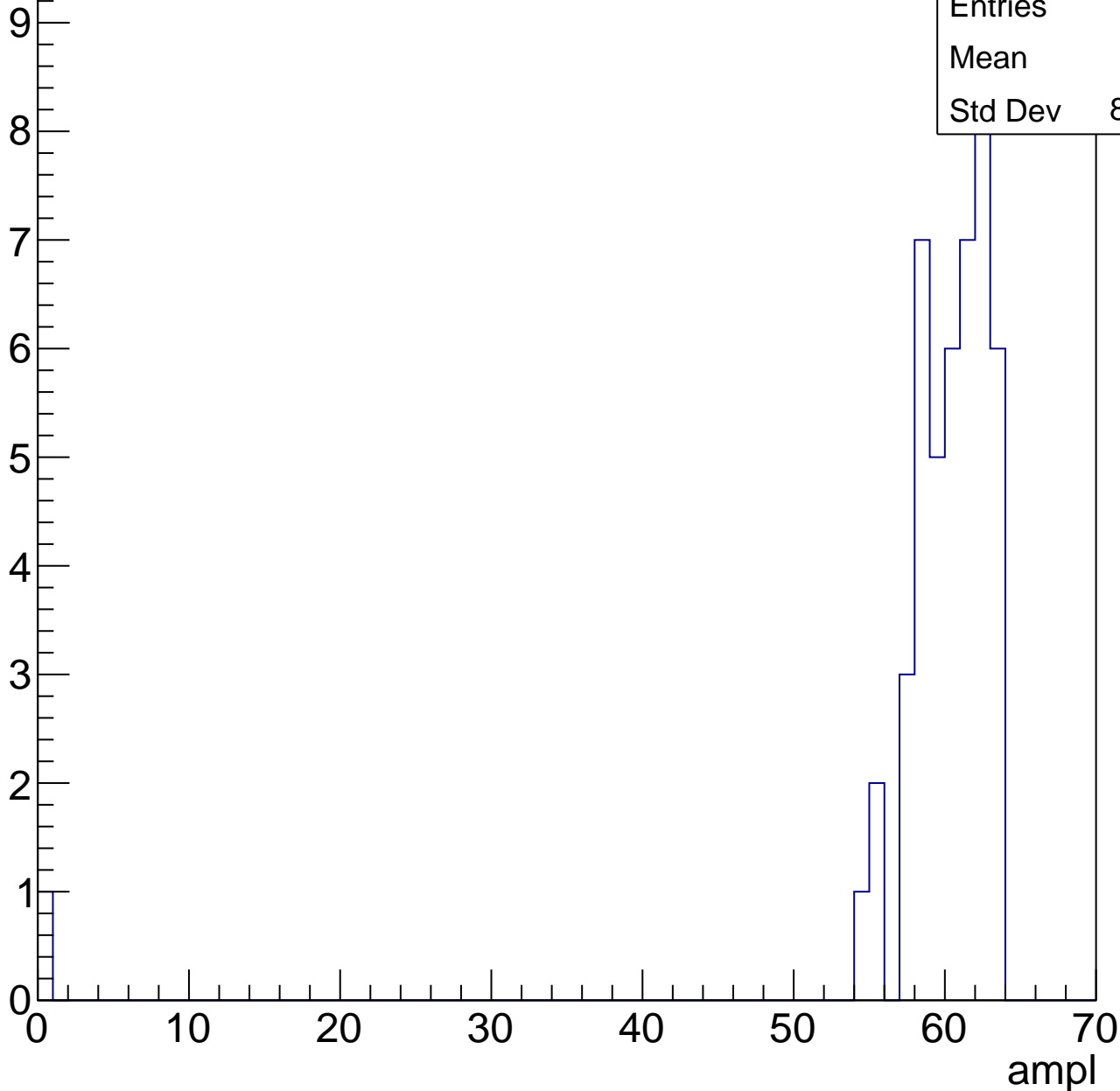


B1L102S, U6-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

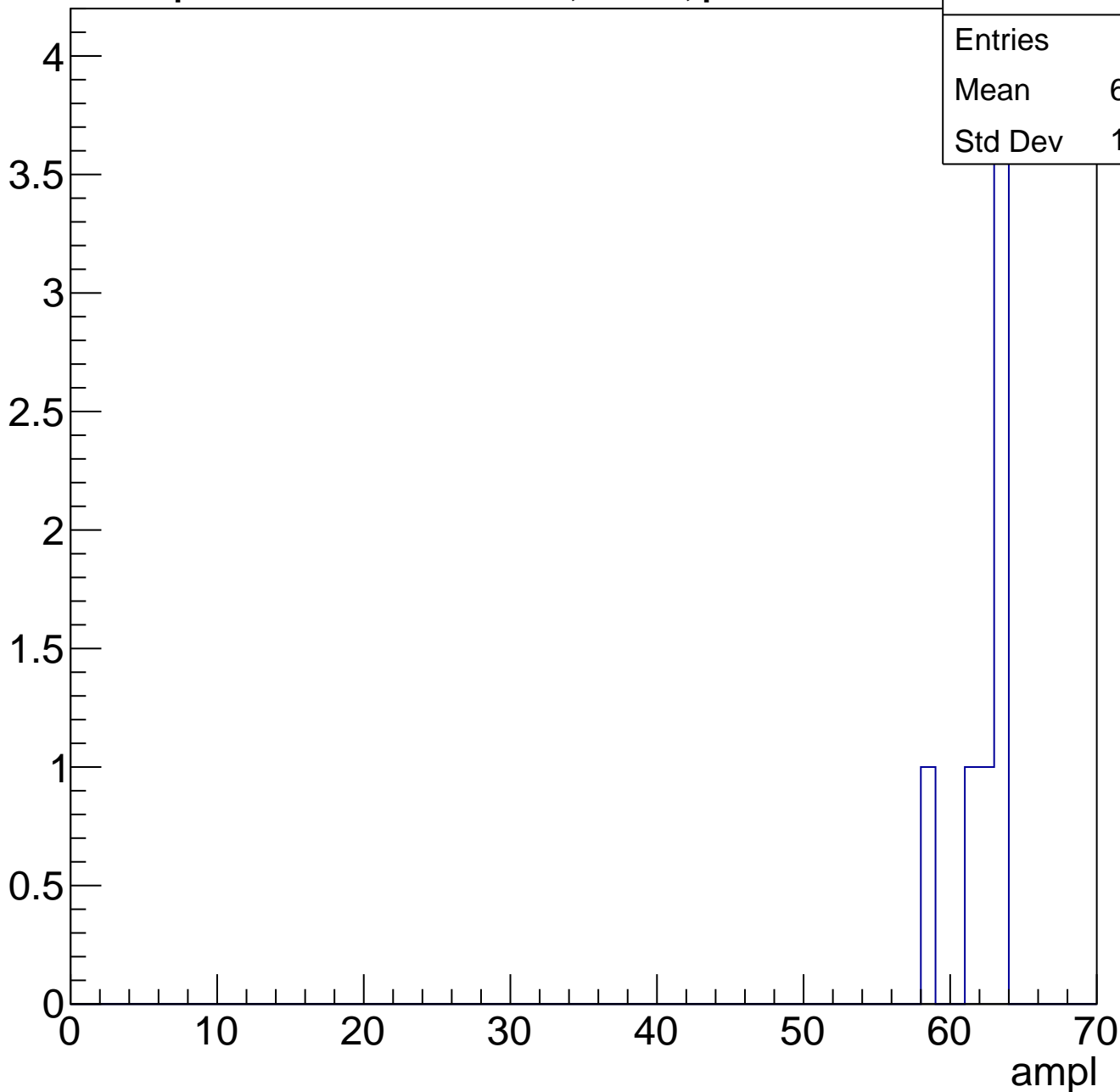
Entries	47
Mean	58.7
Std Dev	8.949



B1L102S, U6-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	61.86
Std Dev	1.726

B1L102S, U6-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch108, adc0

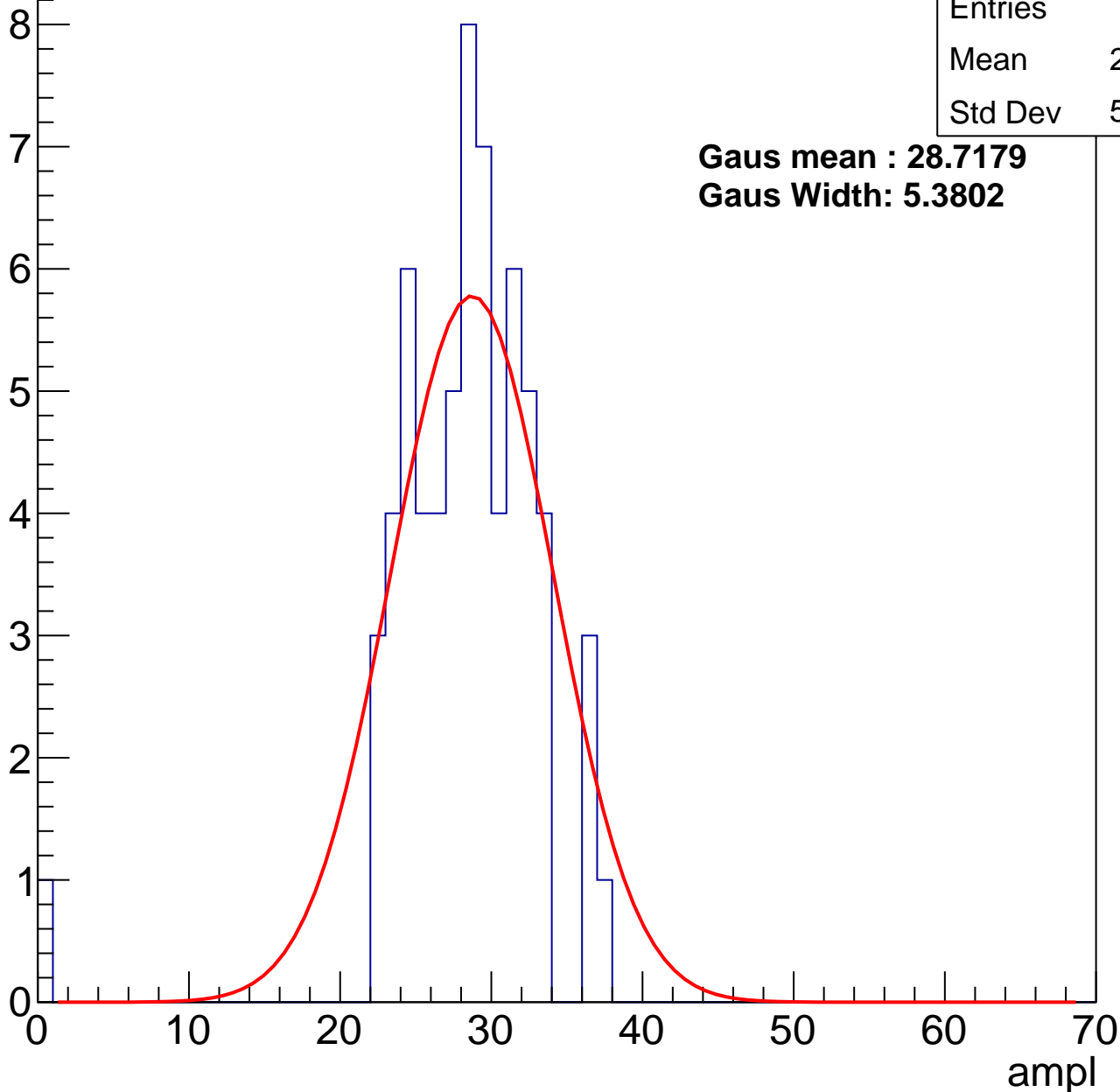
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	27.86
Std Dev	5.074

Gaus mean : 28.7179

Gaus Width: 5.3802



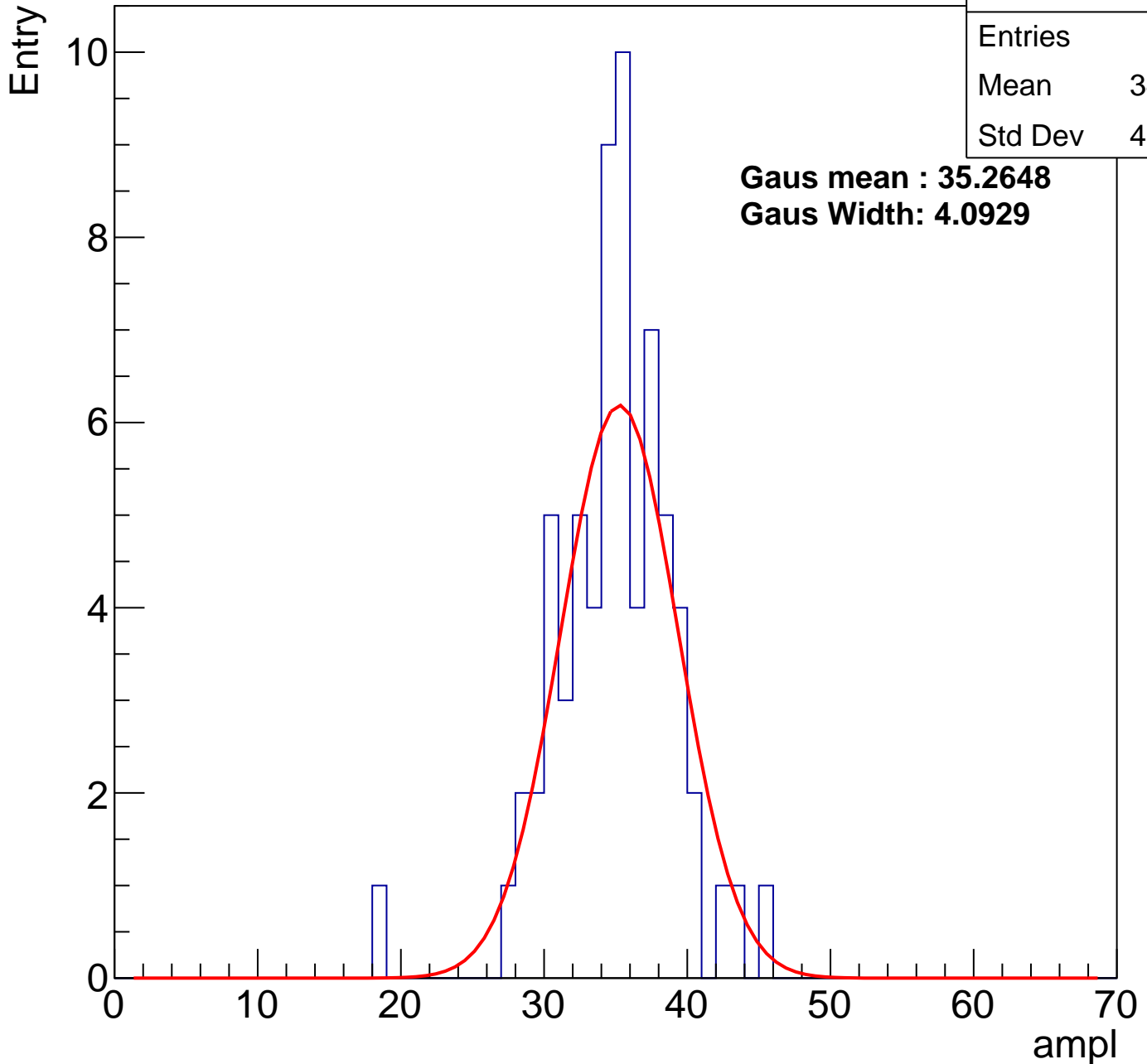
B1L102S, U6-ch108, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	34.46
Std Dev	4.144

Gaus mean : 35.2648

Gaus Width: 4.0929



B1L102S, U6-ch108, adc2

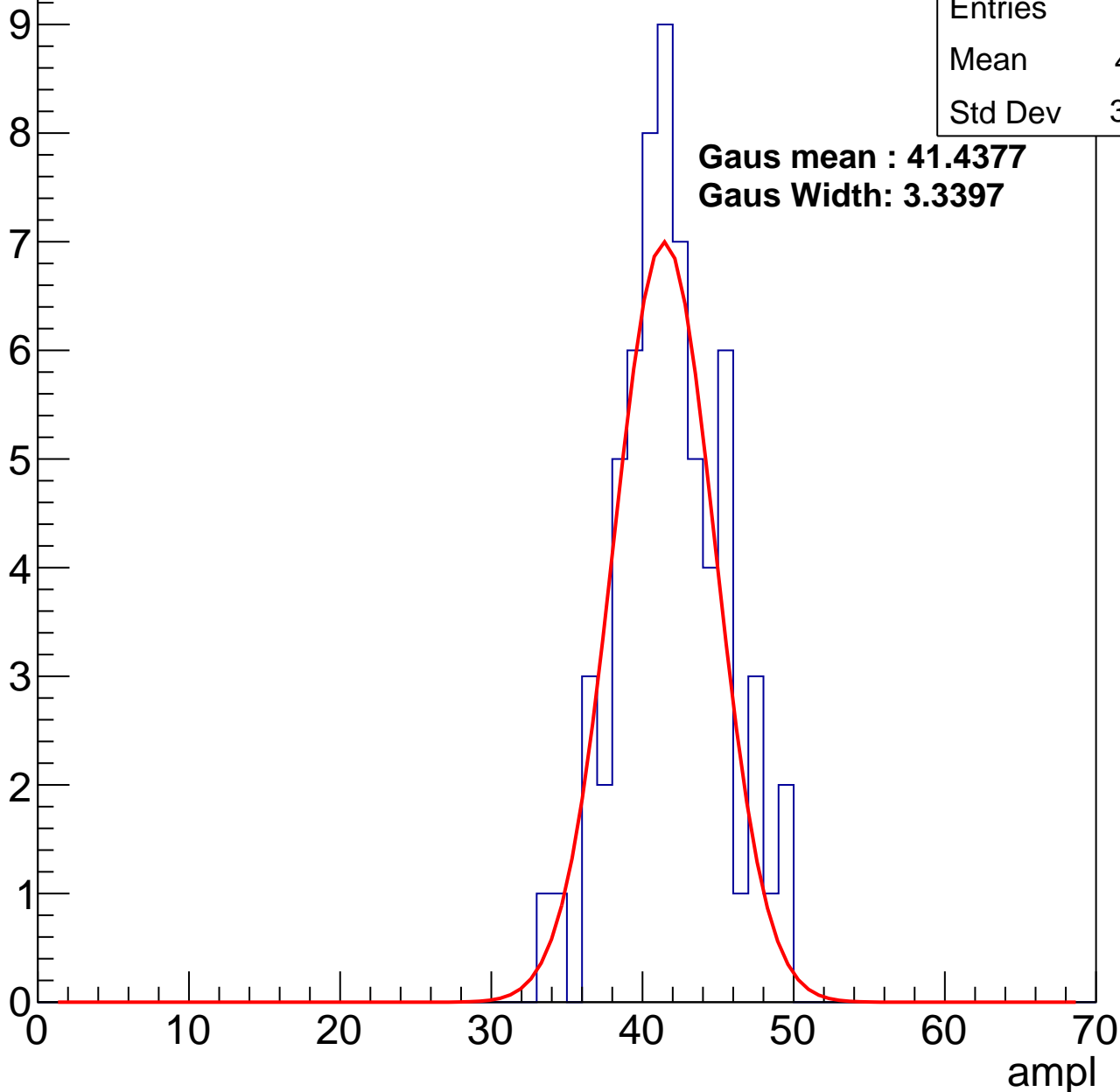
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.41
Std Dev	3.436

Gaus mean : 41.4377

Gaus Width: 3.3397

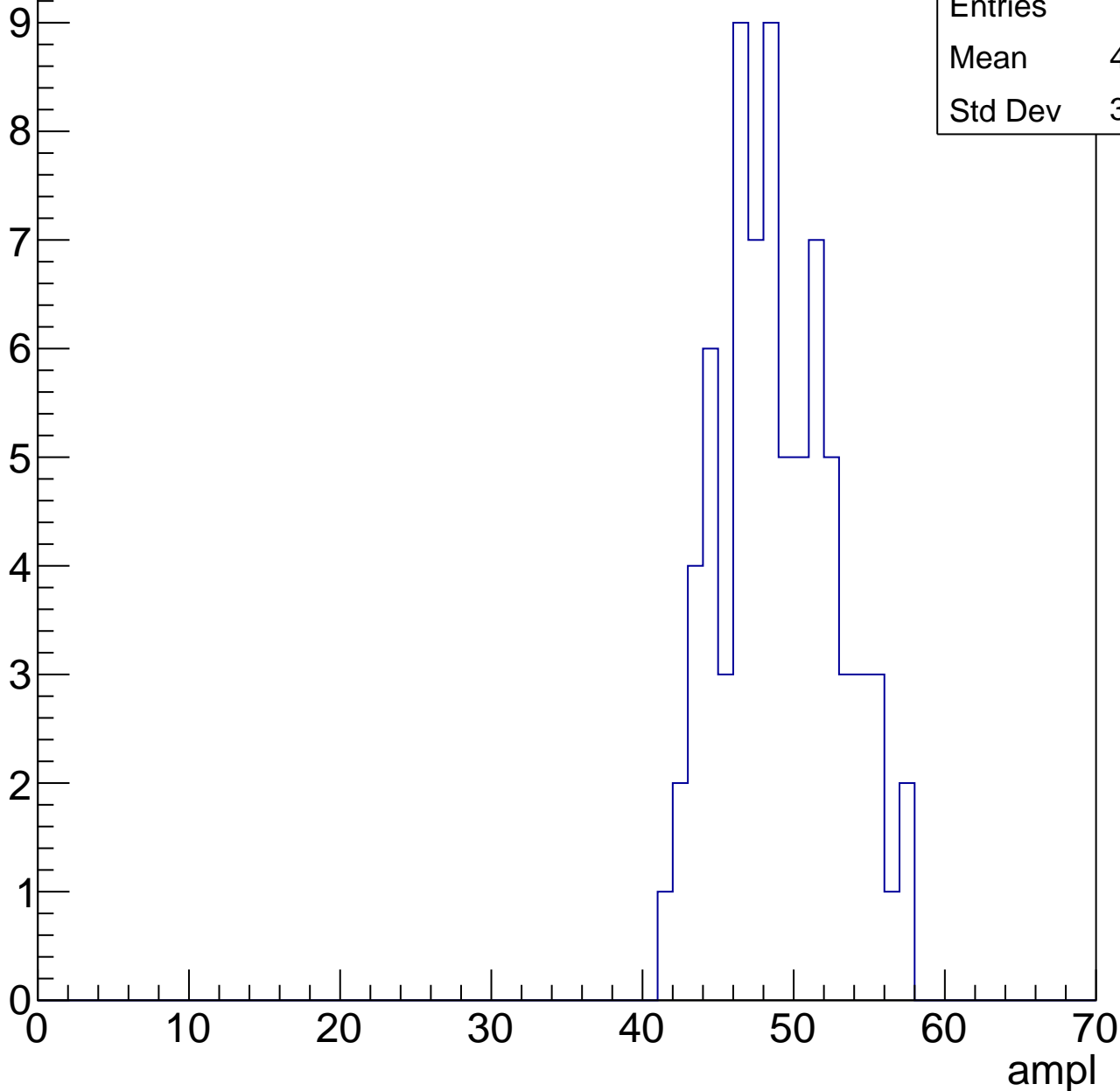


B1L102S, U6-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.52
Std Dev	3.814

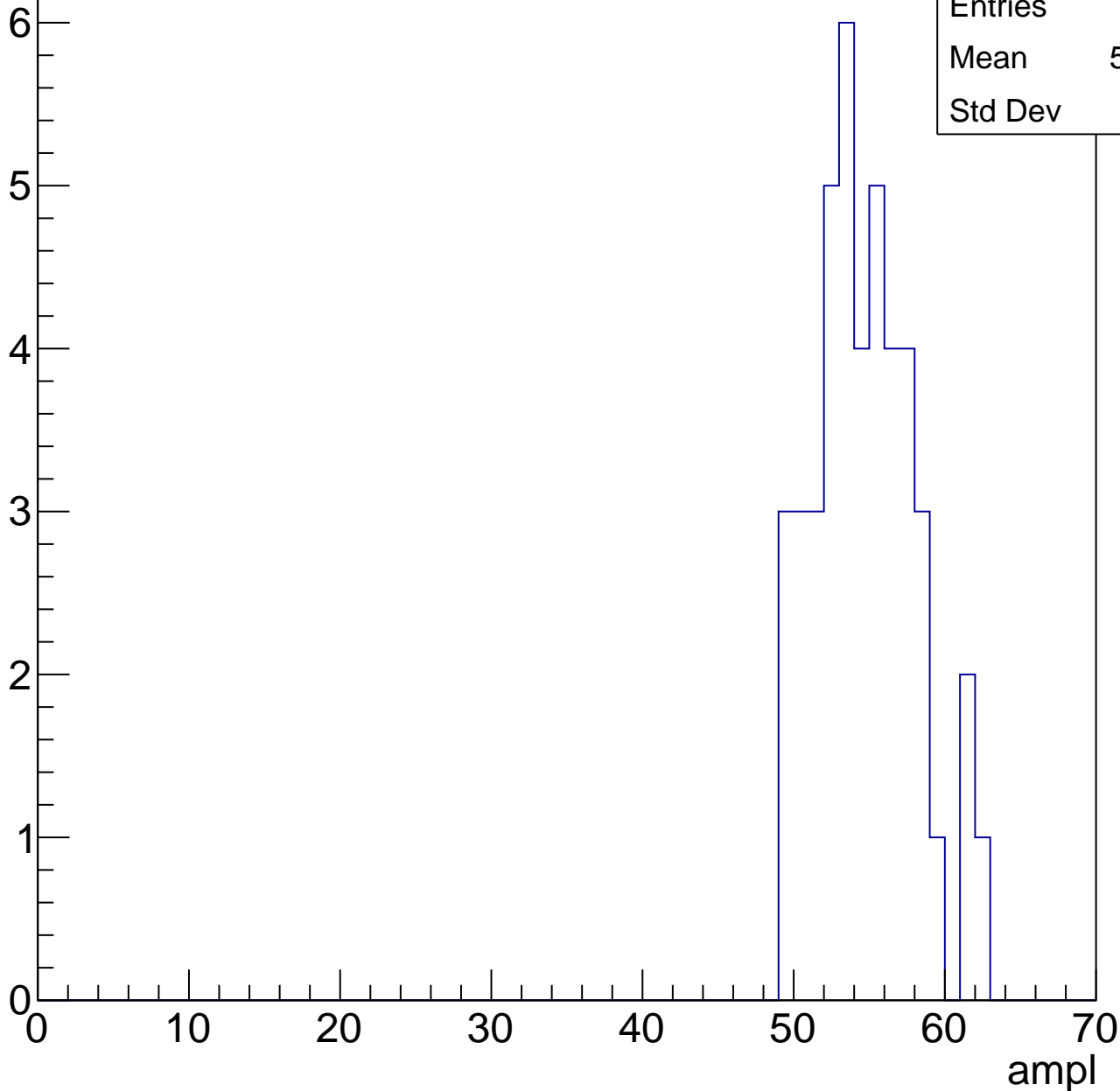


B1L102S, U6-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	54.27
Std Dev	3.25

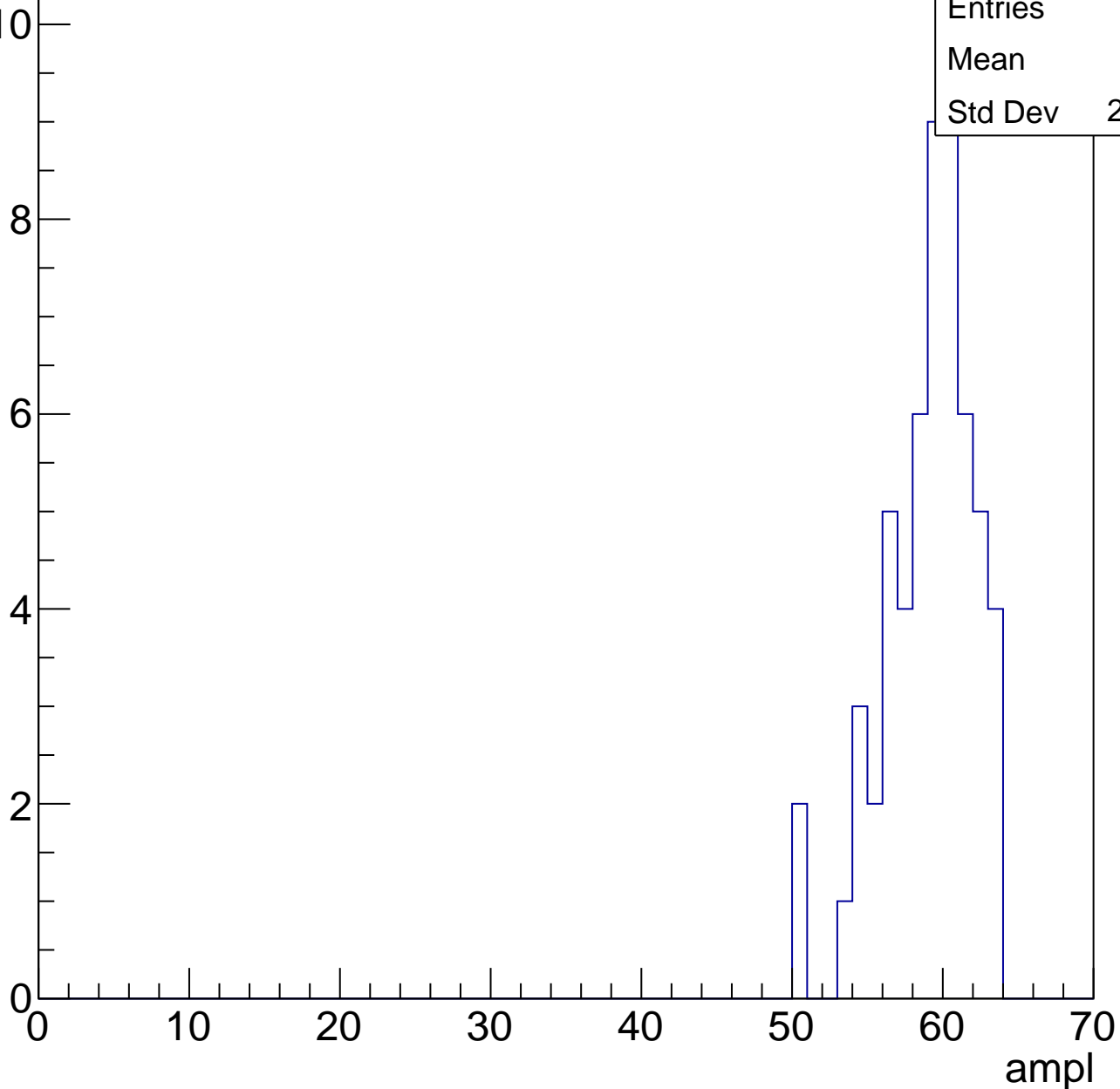


B1L102S, U6-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

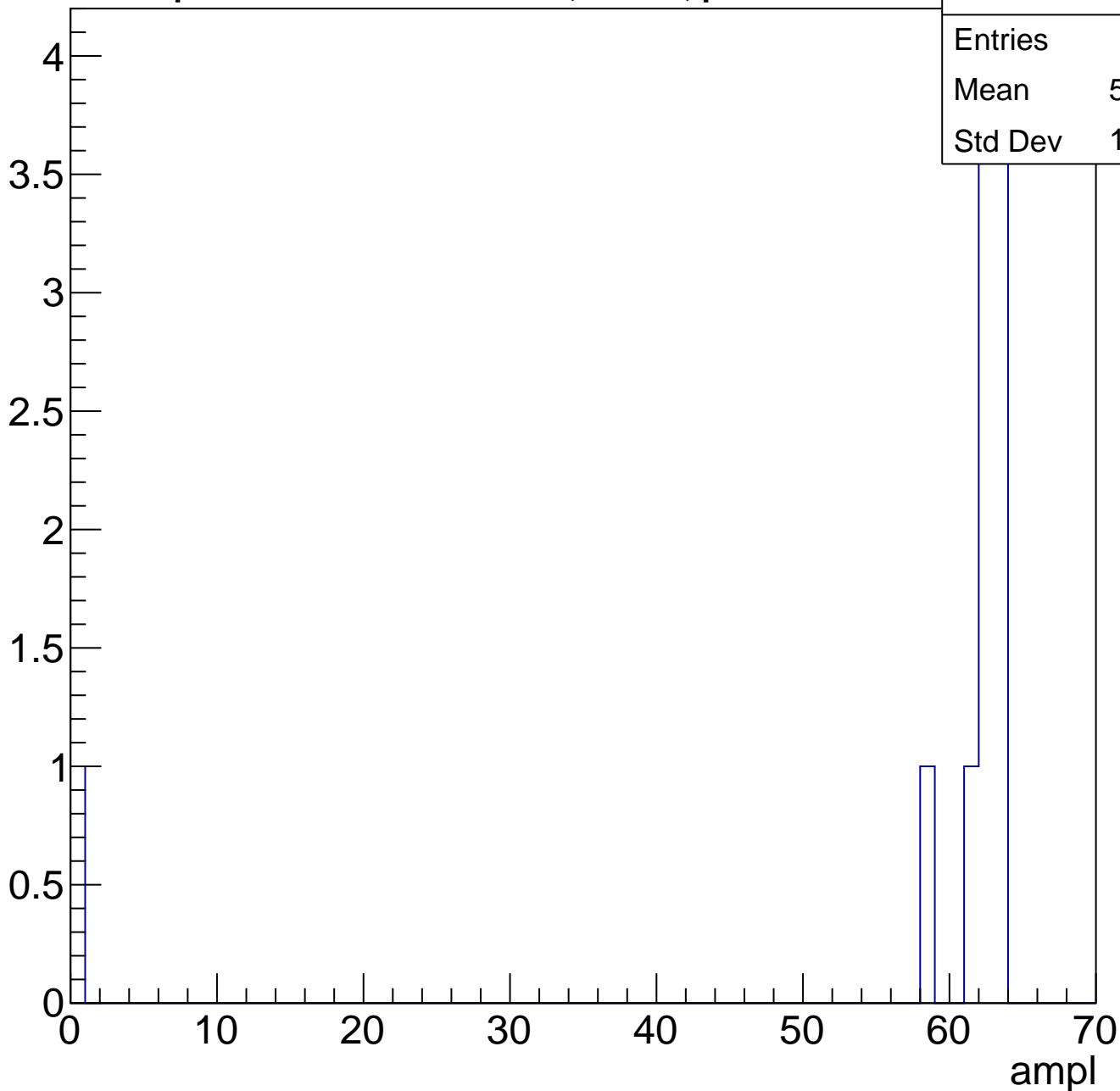
Entries	57
Mean	58.6
Std Dev	2.979



B1L102S, U6-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch109, adc0

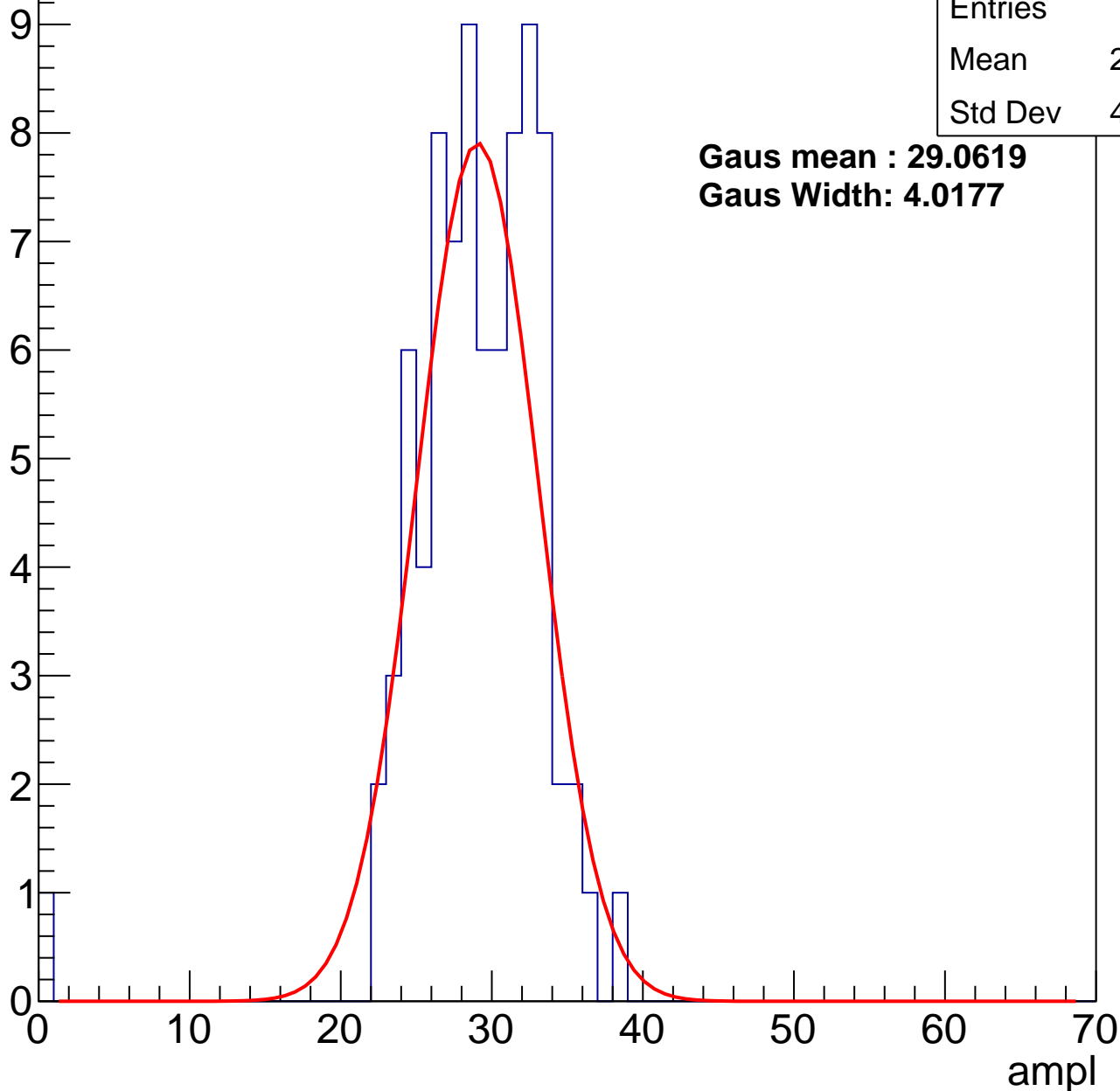
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.58
Std Dev	4.727

Gaus mean : 29.0619

Gaus Width: 4.0177

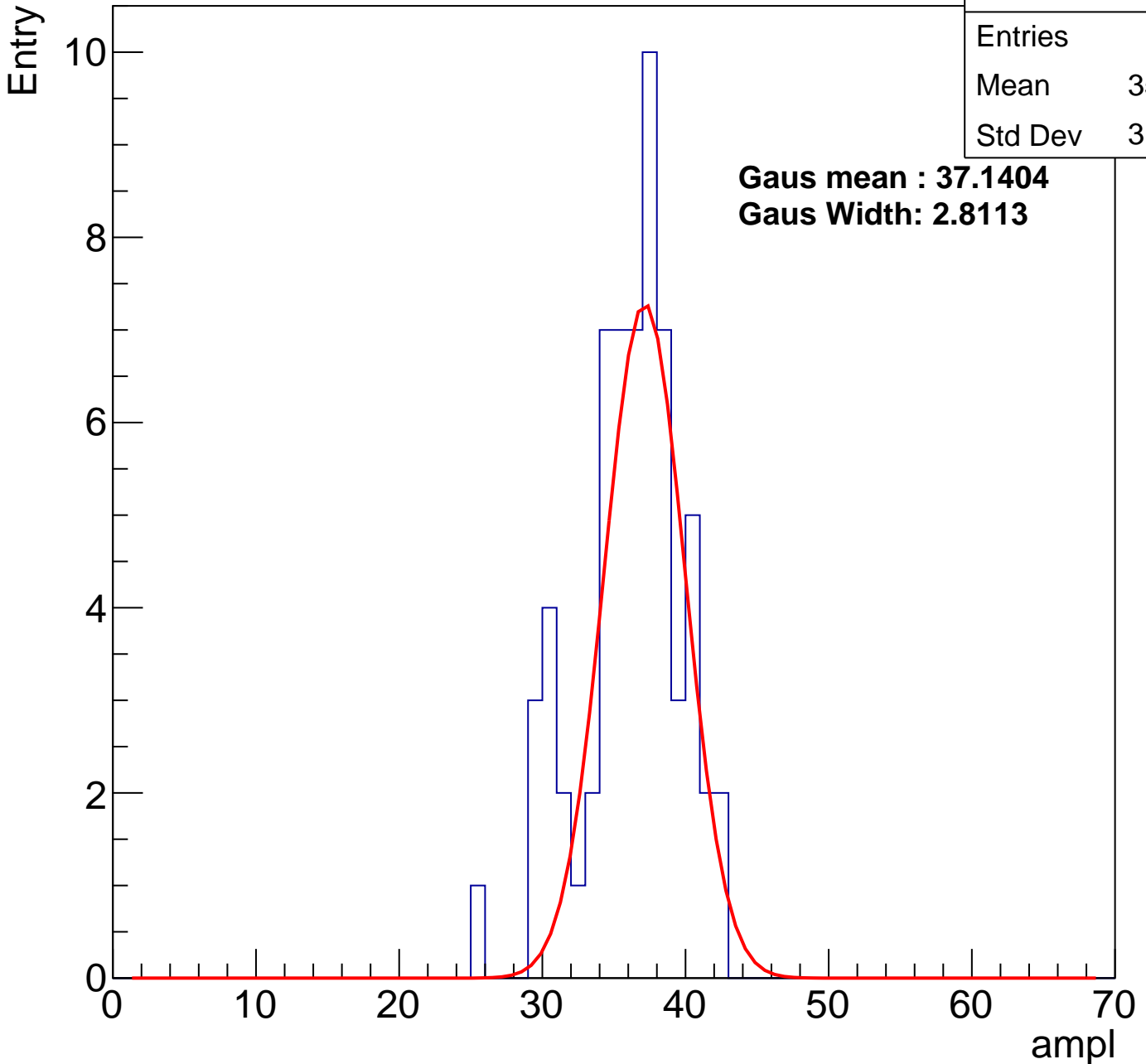


B1L102S, U6-ch109, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	63
Mean	35.65
Std Dev	3.542

Gaus mean : 37.1404
Gaus Width: 2.8113



B1L102S, U6-ch109, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.78
Std Dev	3.465

Gaus mean : 43.0692

Gaus Width: 3.5375

10

8

6

4

2

0

0

10

20

30

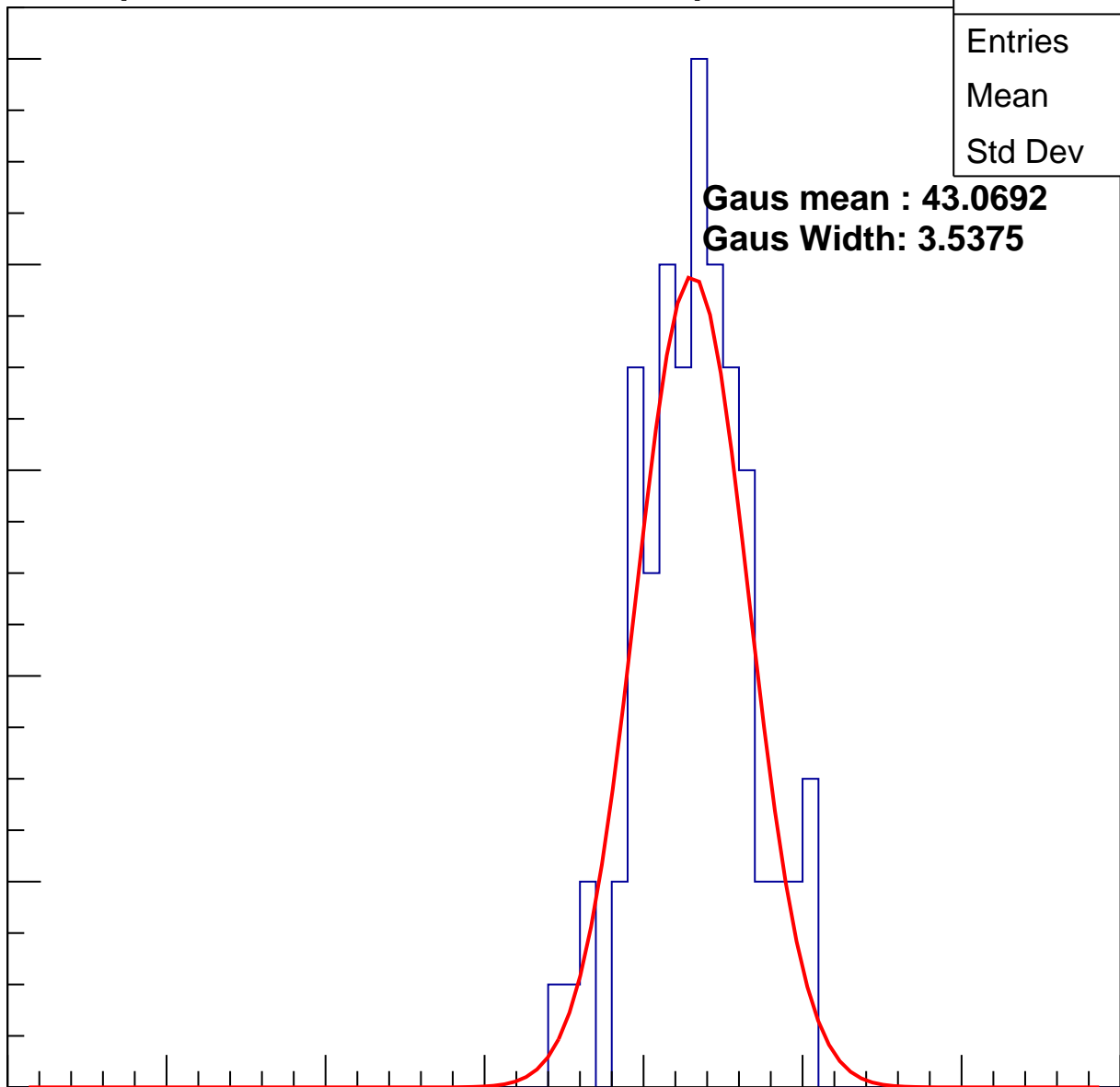
40

50

60

70

ampl

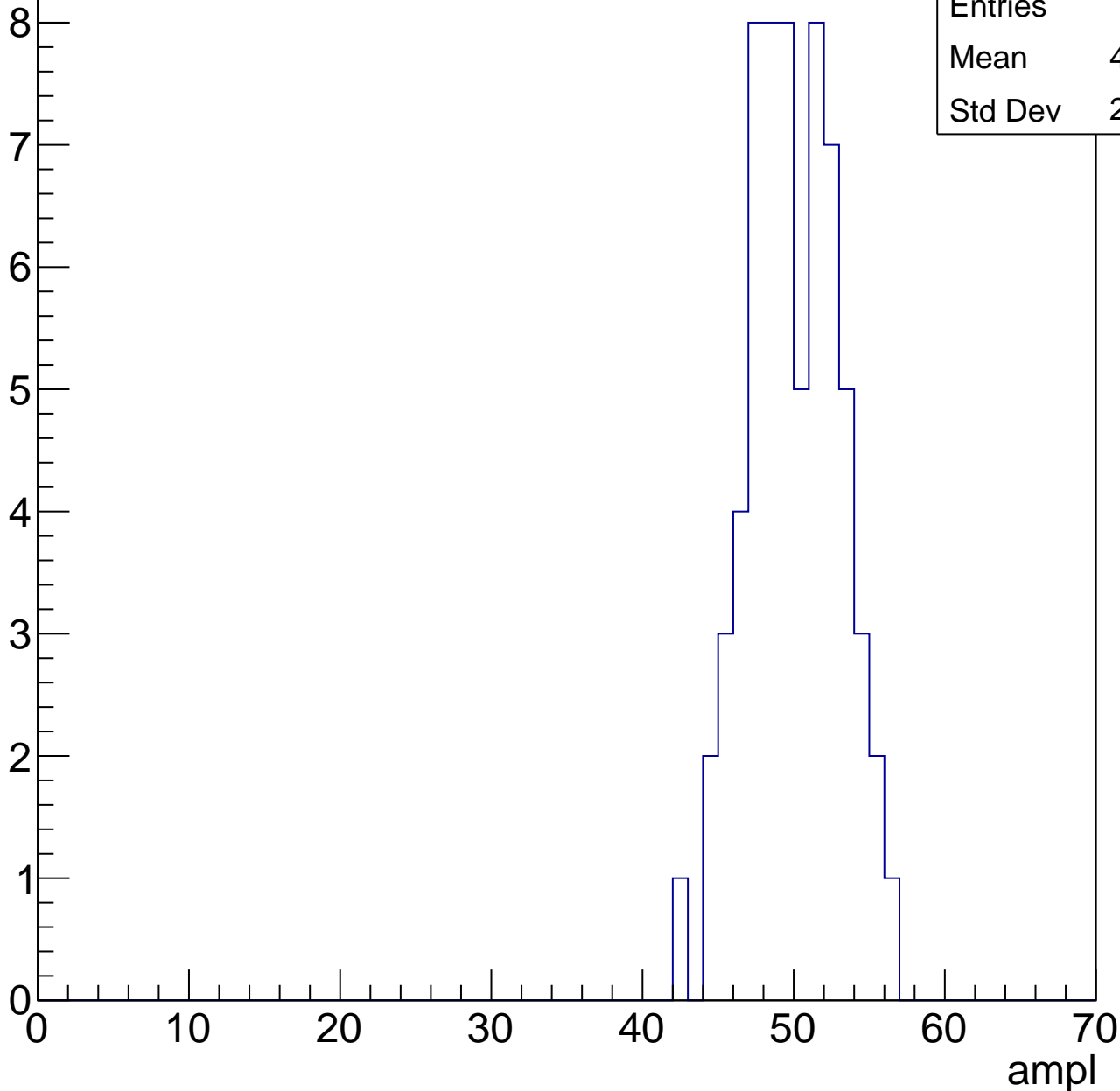


B1L102S, U6-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

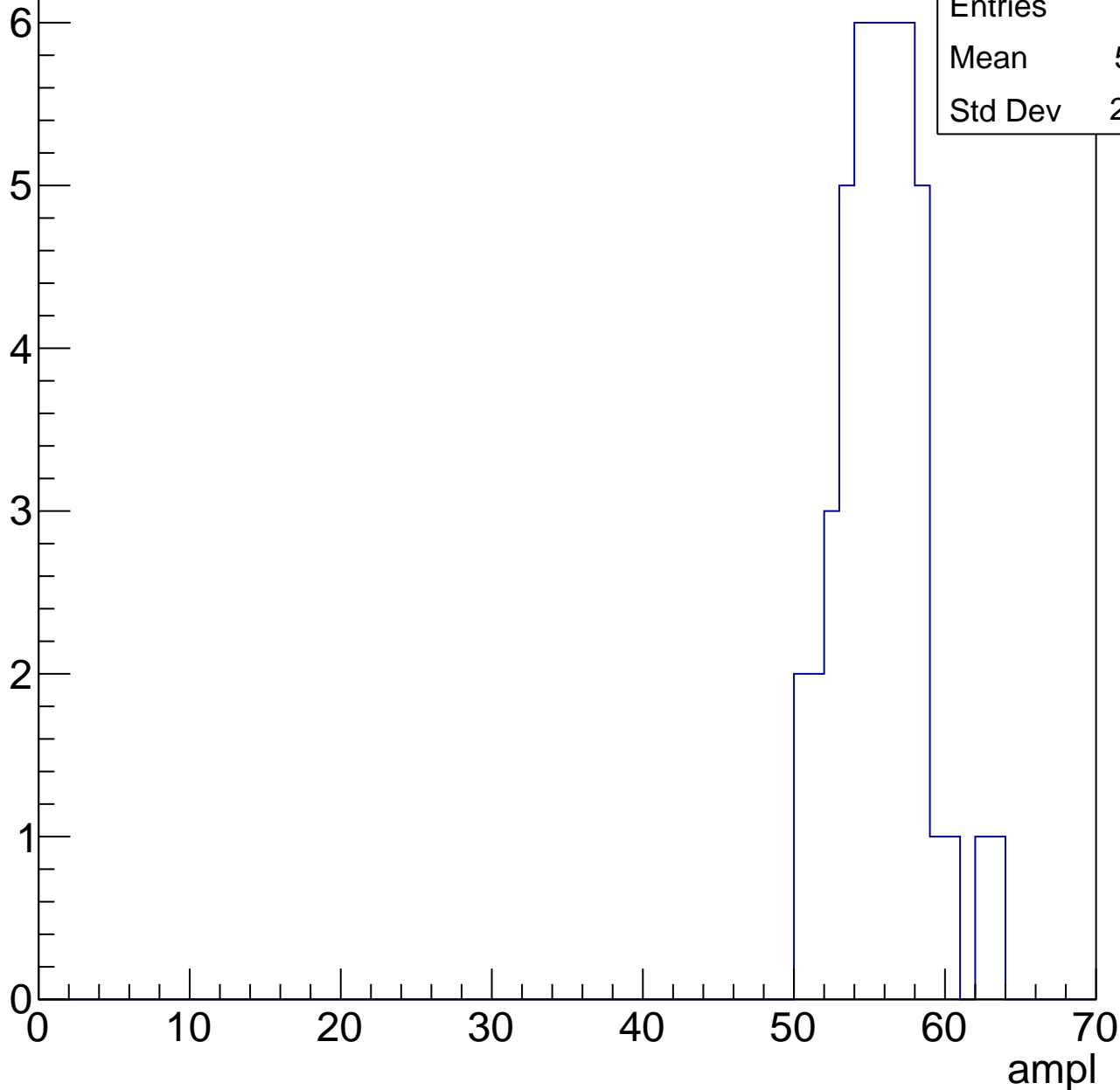
Entries	65
Mean	49.48
Std Dev	2.998



B1L102S, U6-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

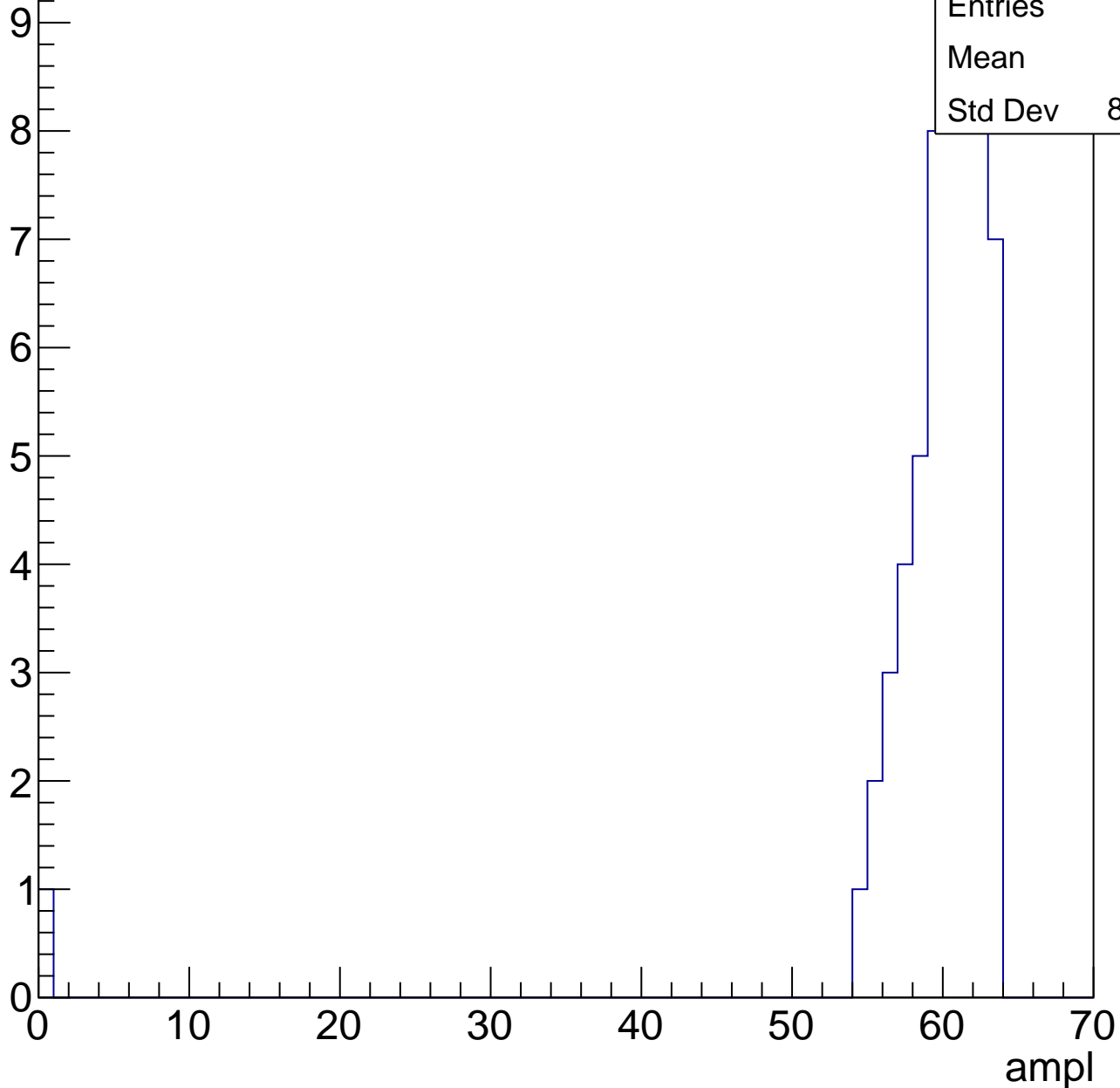


Entries	45
Mean	55.31
Std Dev	2.827

B1L102S, U6-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

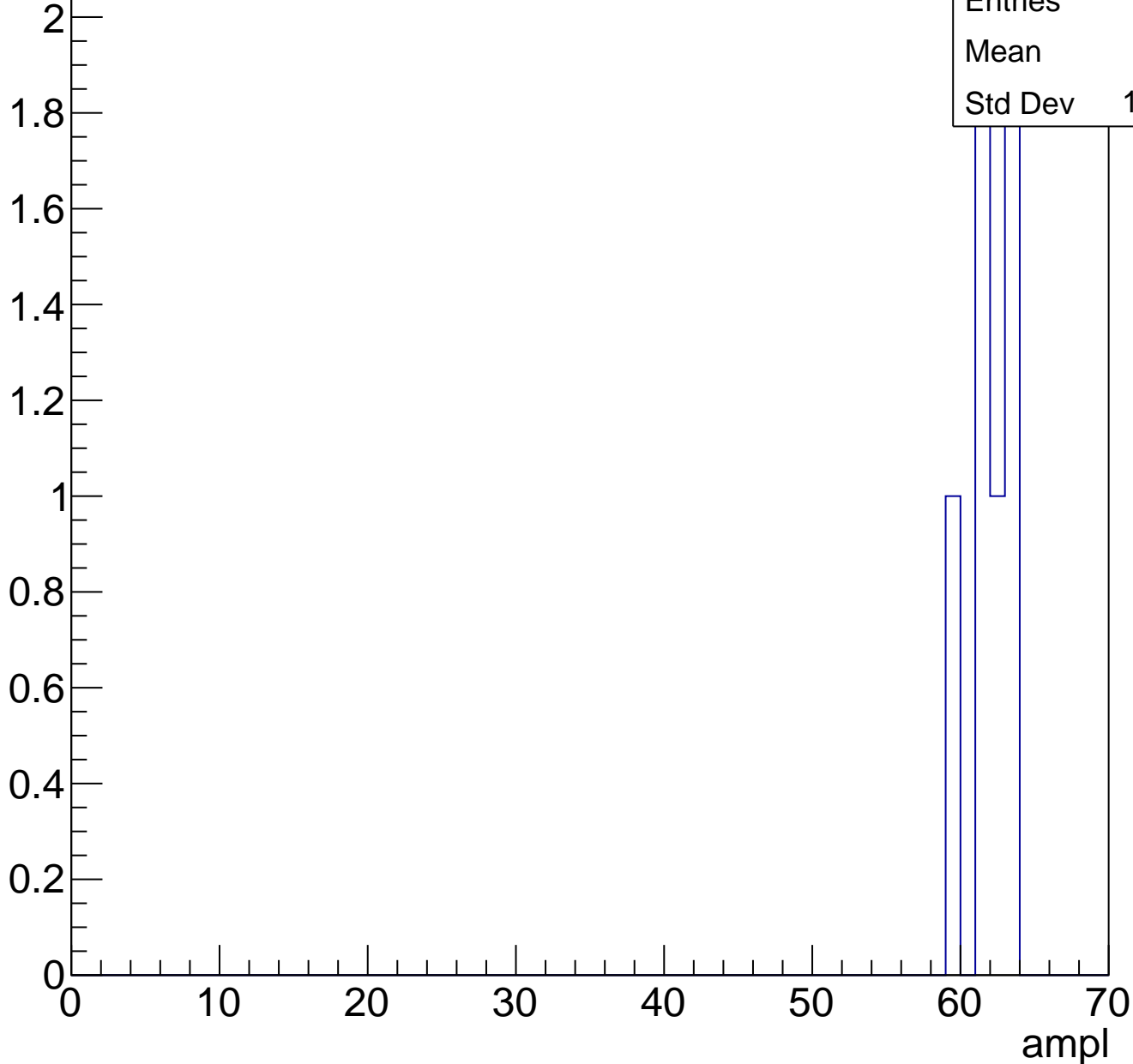
Entry



B1L102S, U6-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch110, adc0

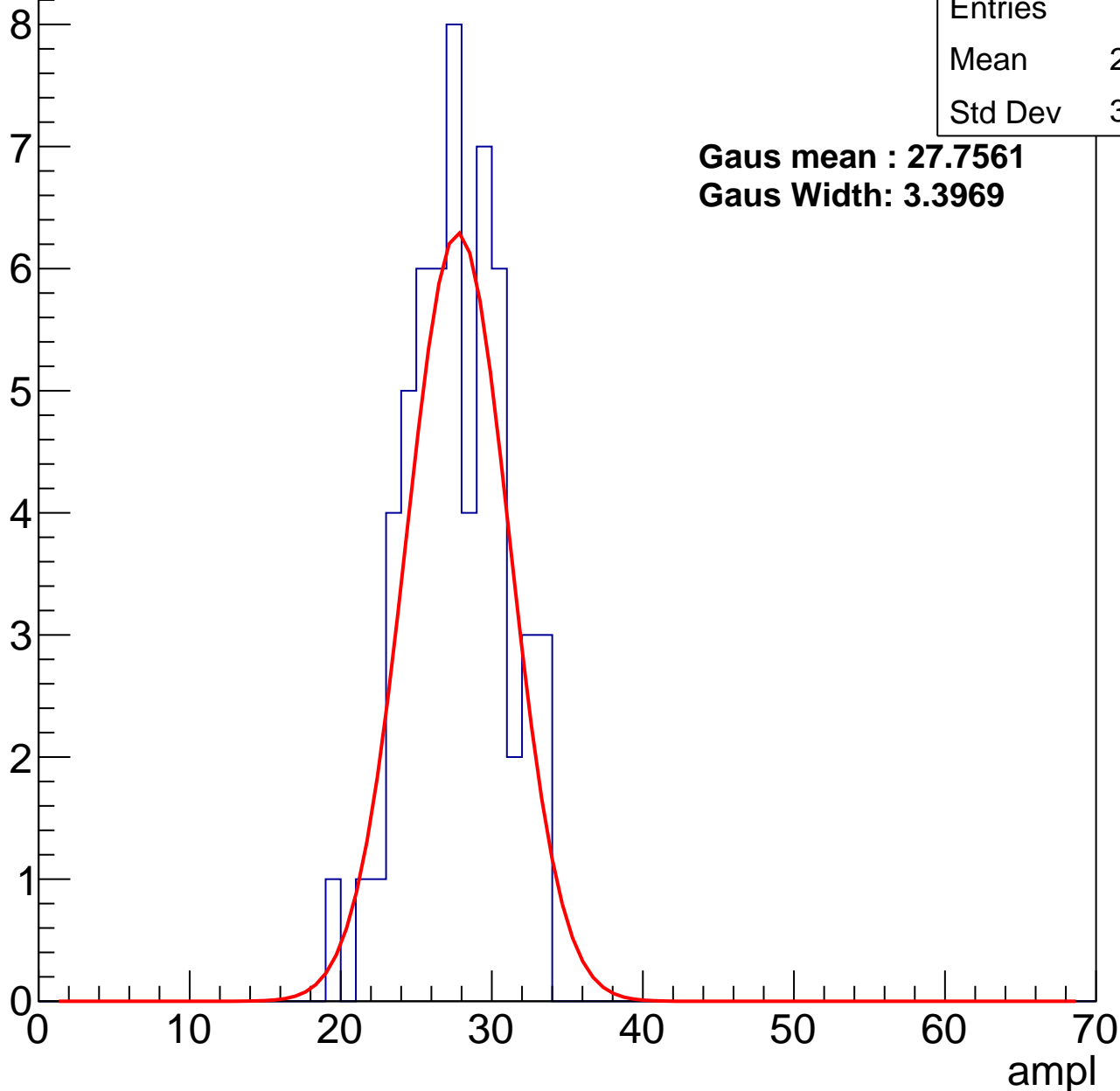
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.16
Std Dev	3.144

Gaus mean : 27.7561

Gaus Width: 3.3969



B1L102S, U6-ch110, adc1

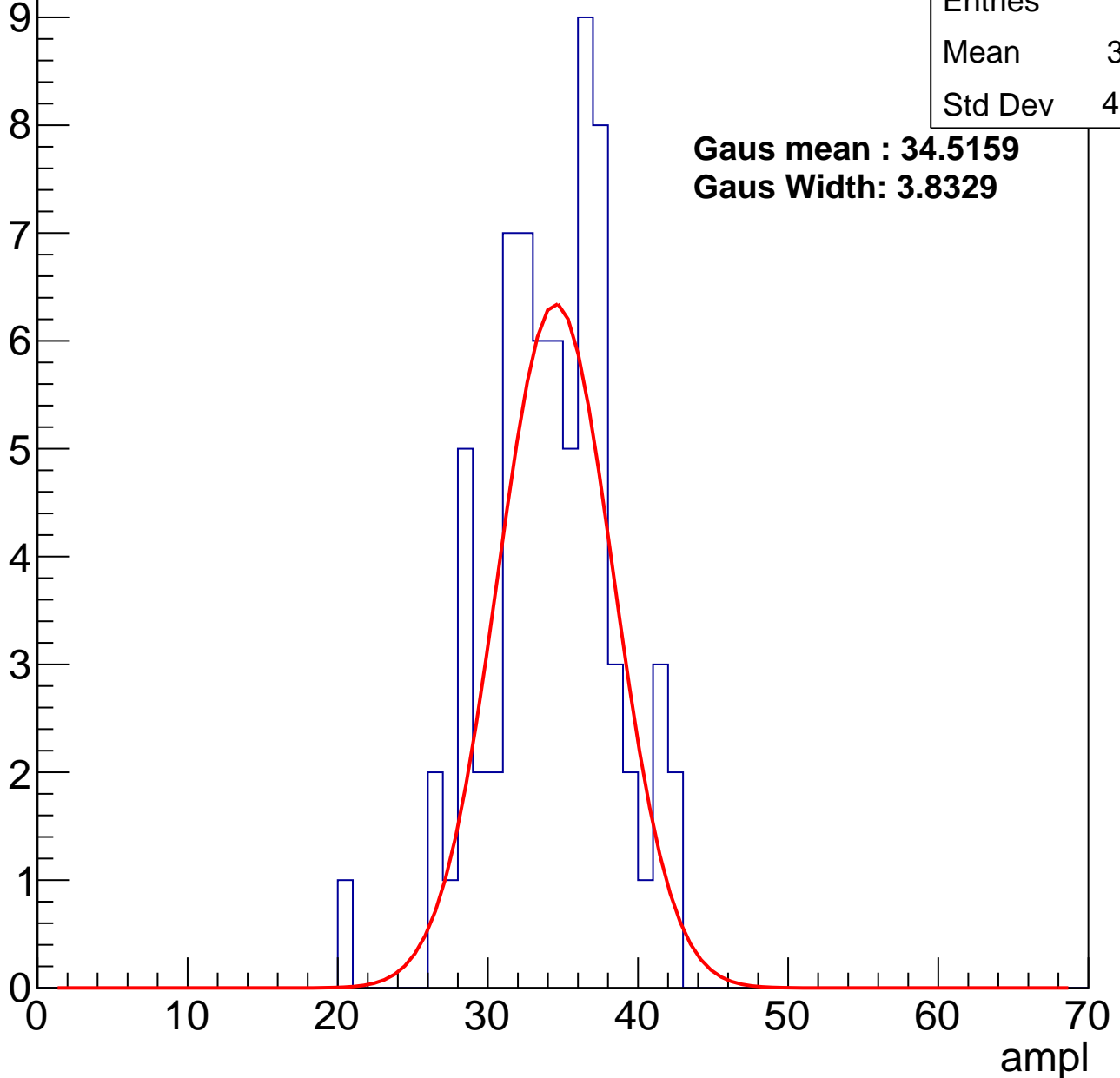
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	33.81
Std Dev	4.169

Gaus mean : 34.5159

Gaus Width: 3.8329



B1L102S, U6-ch110, adc2

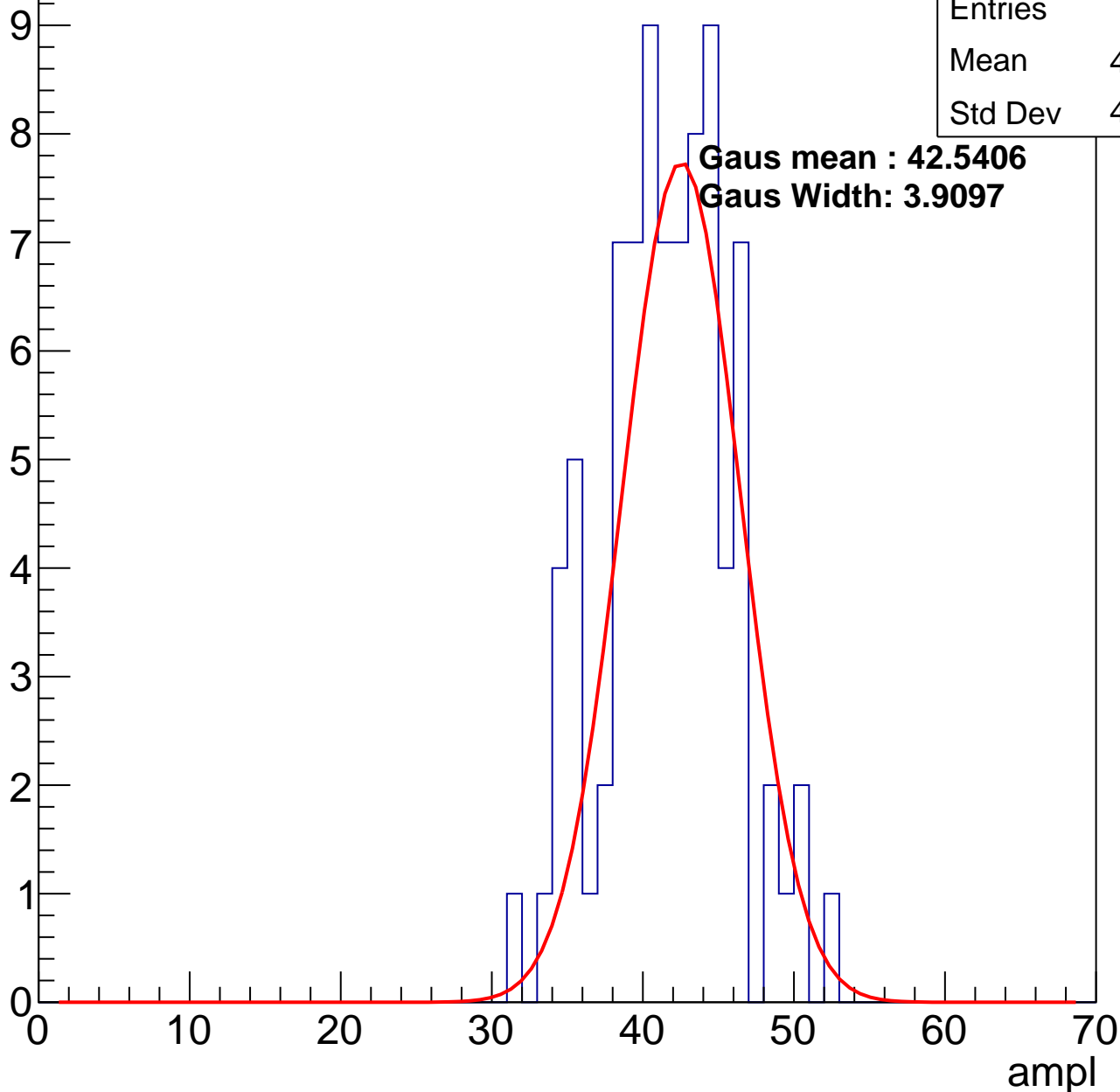
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	41.22
Std Dev	4.196

Gaus mean : 42.5406

Gaus Width: 3.9097

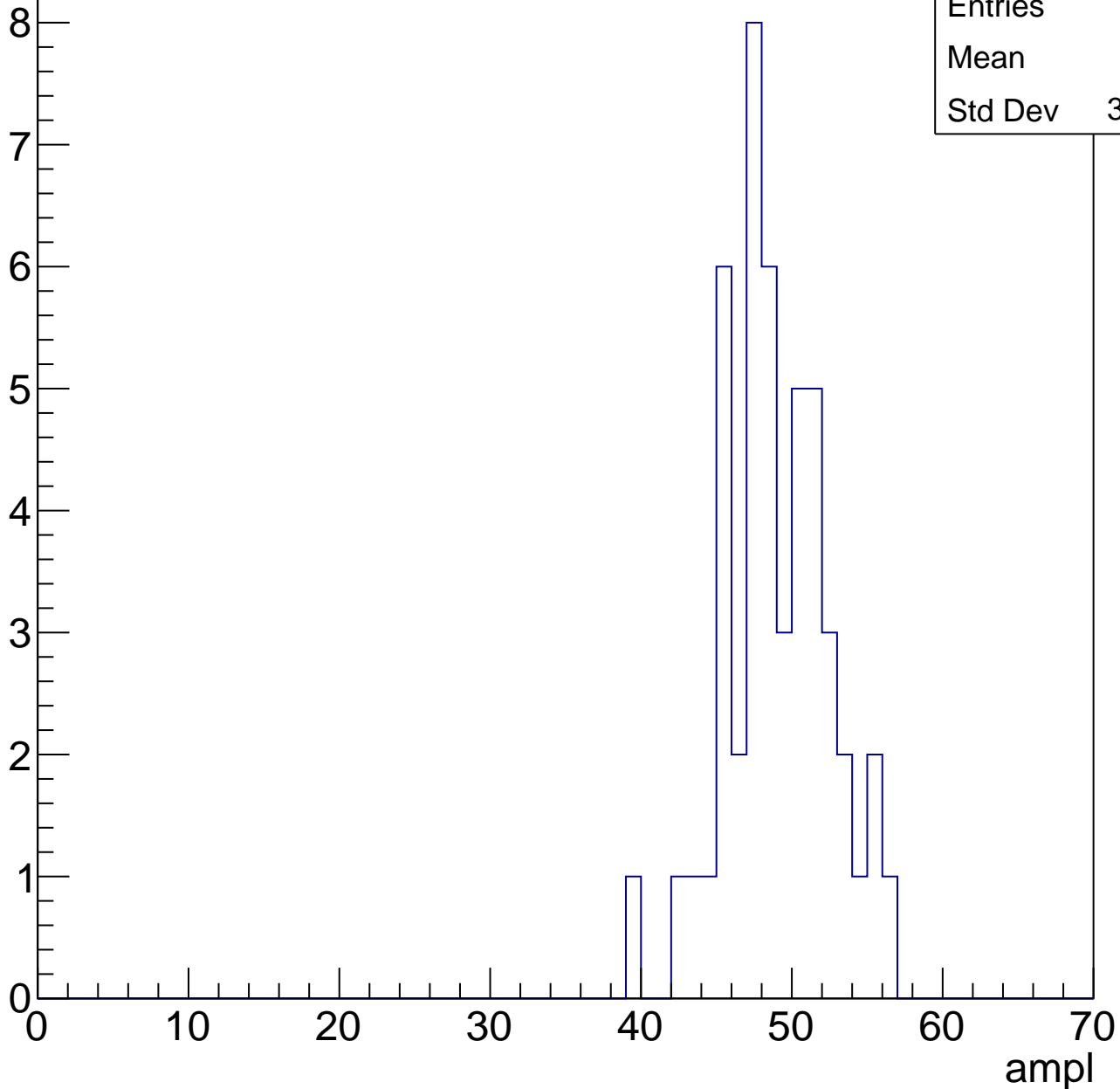


B1L102S, U6-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	48.5
Std Dev	3.476

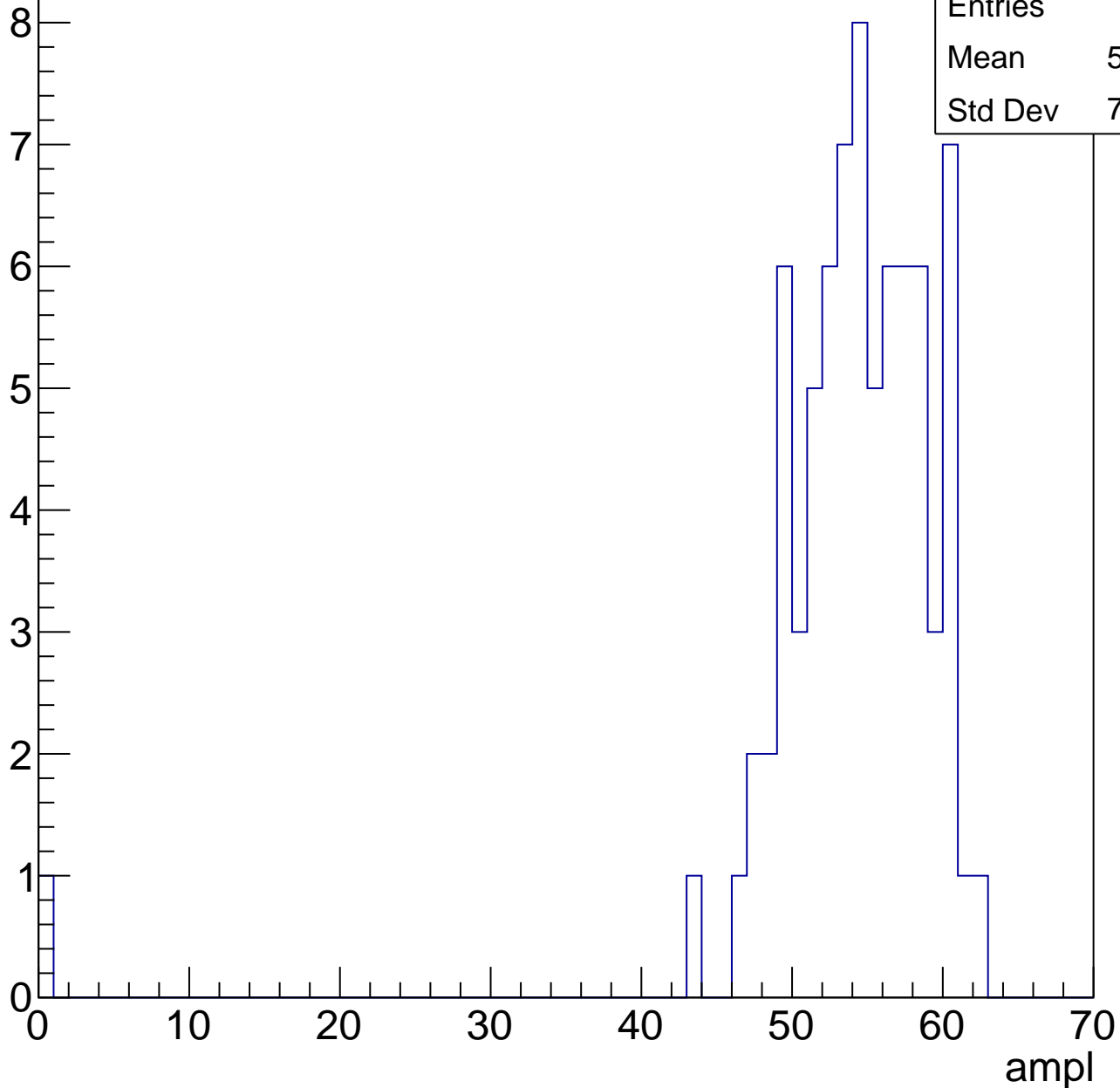


B1L102S, U6-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	53.43
Std Dev	7.332

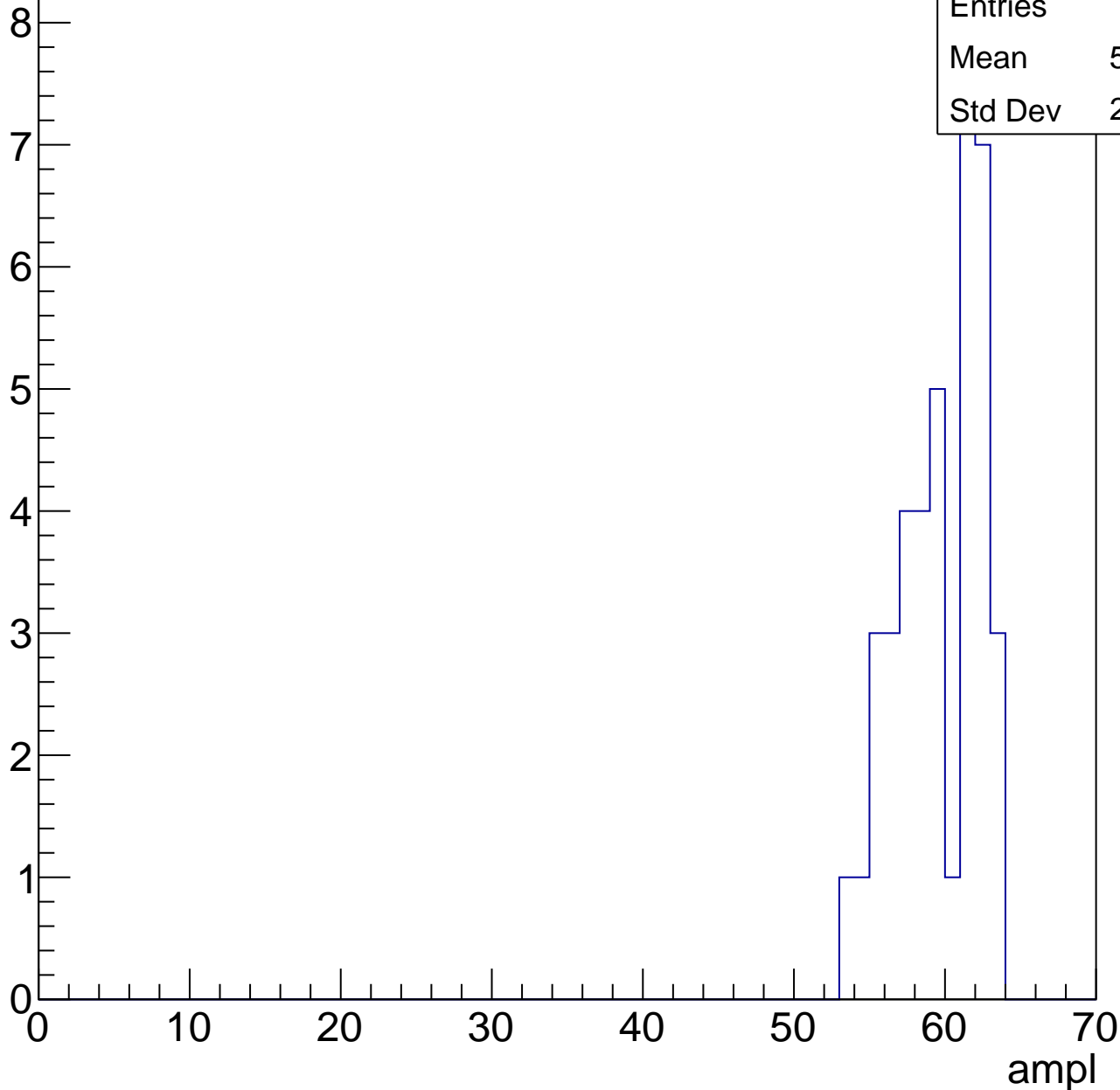


B1L102S, U6-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	59.15
Std Dev	2.735

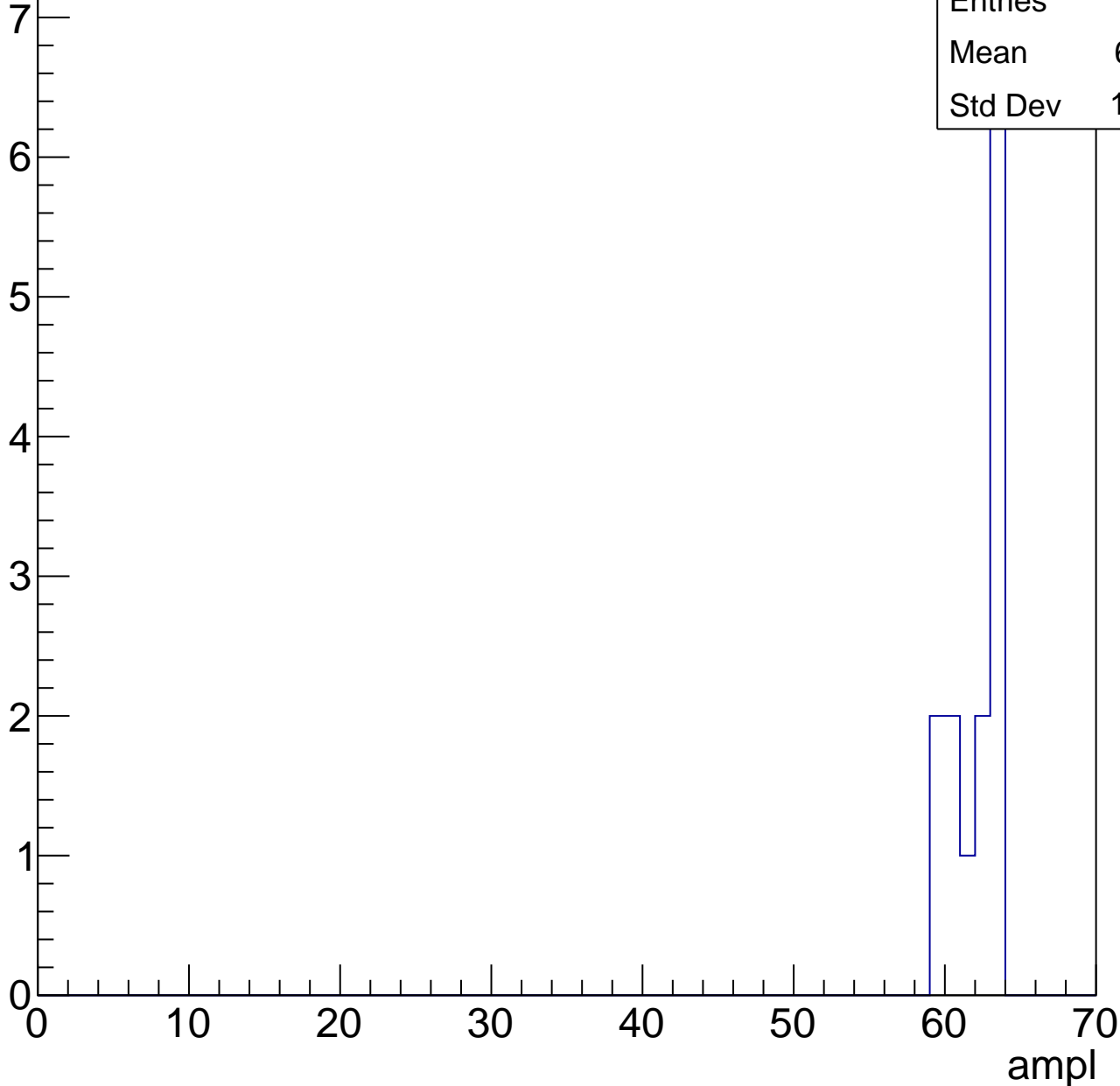


B1L102S, U6-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.71
Std Dev	1.532



B1L102S, U6-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U6-ch111, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	86
Mean	28.4
Std Dev	4.821

Gaus mean : 29.6045

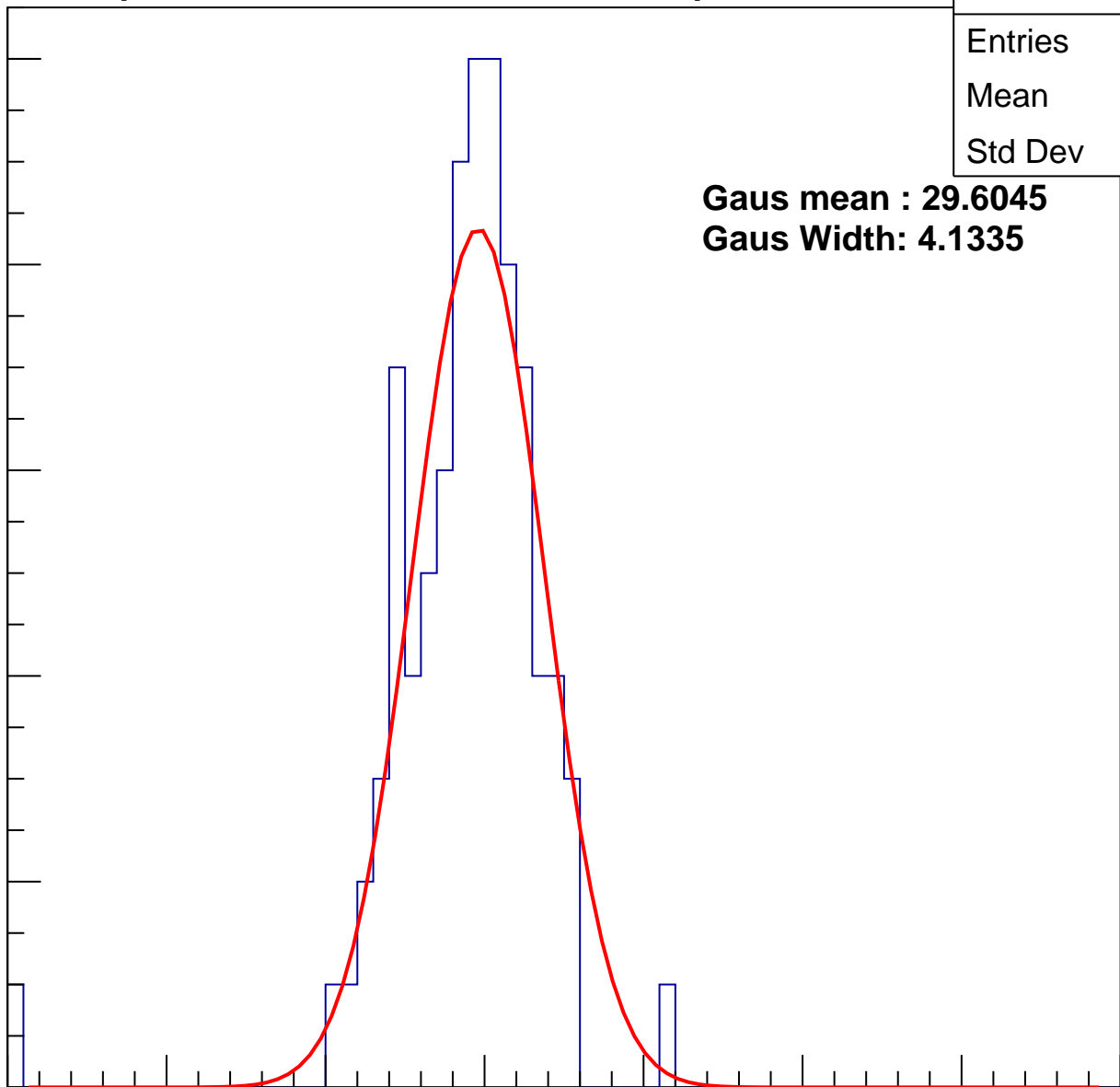
Gaus Width: 4.1335

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



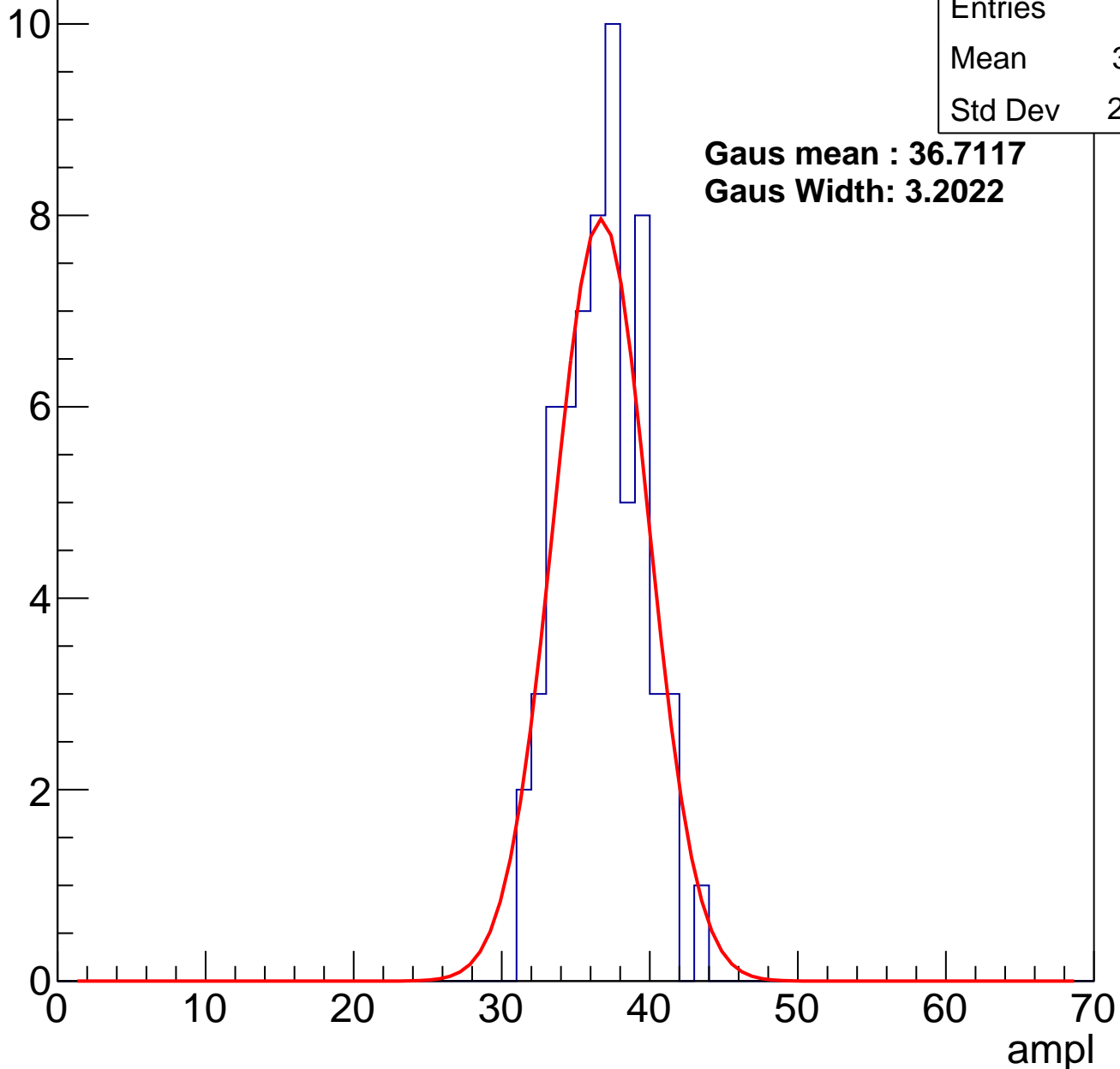
B1L102S, U6-ch111, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	62
Mean	36.31
Std Dev	2.698

Gaus mean : 36.7117
Gaus Width: 3.2022

Entry



B1L102S, U6-ch111, adc2

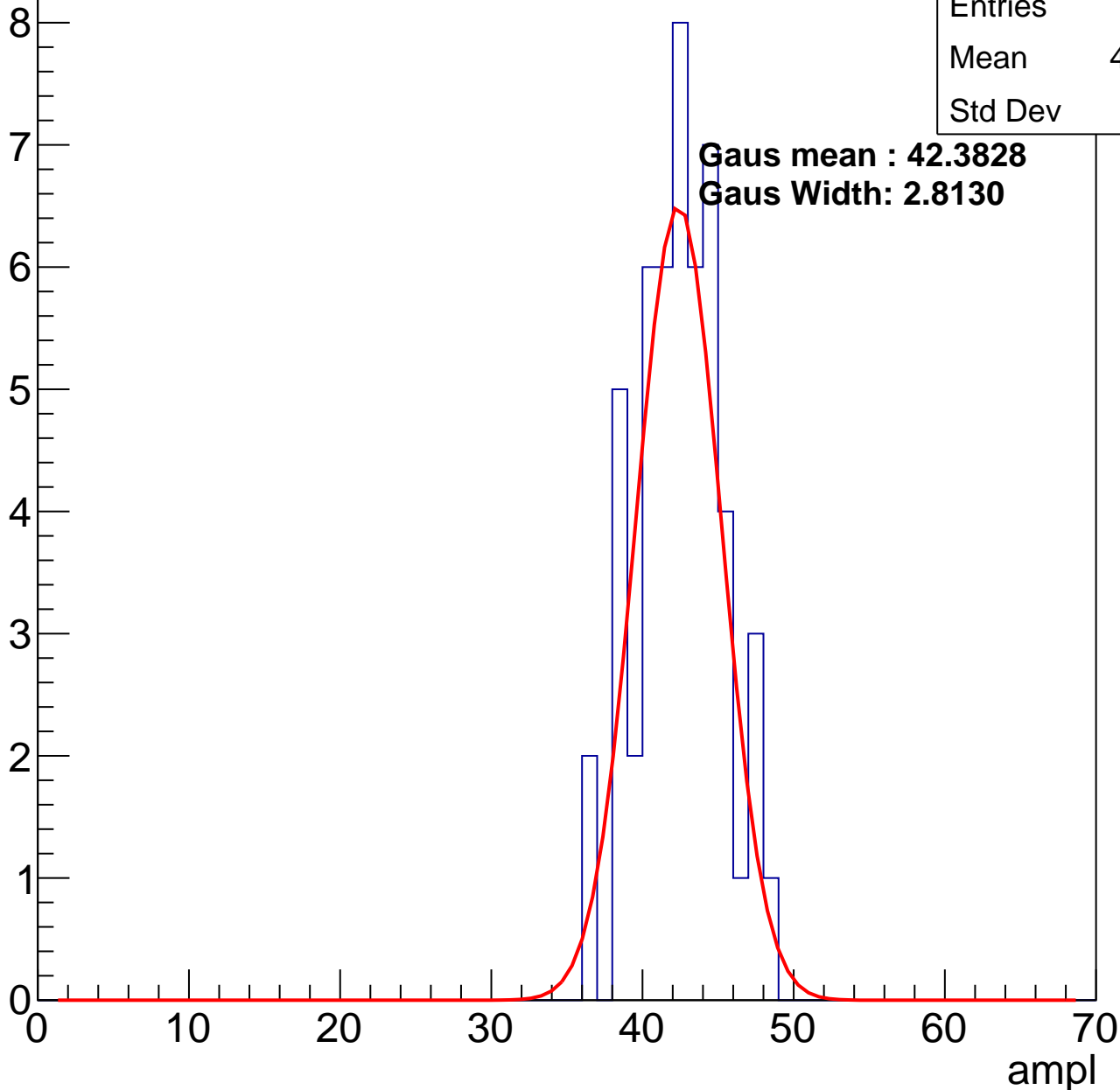
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	42.02
Std Dev	2.79

Gaus mean : 42.3828

Gaus Width: 2.8130



B1L102S, U6-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

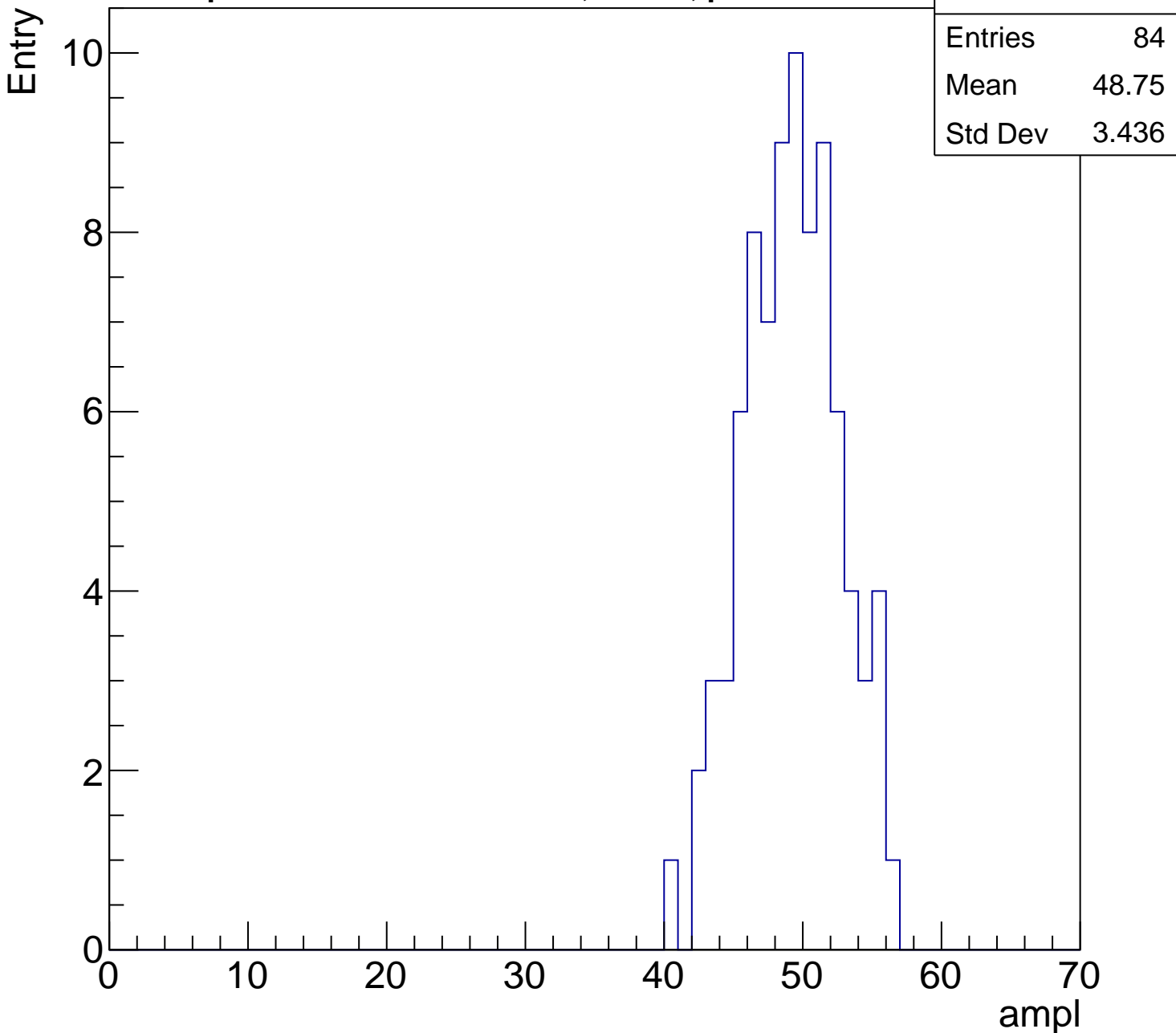
Entries	84
Mean	48.75
Std Dev	3.436

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

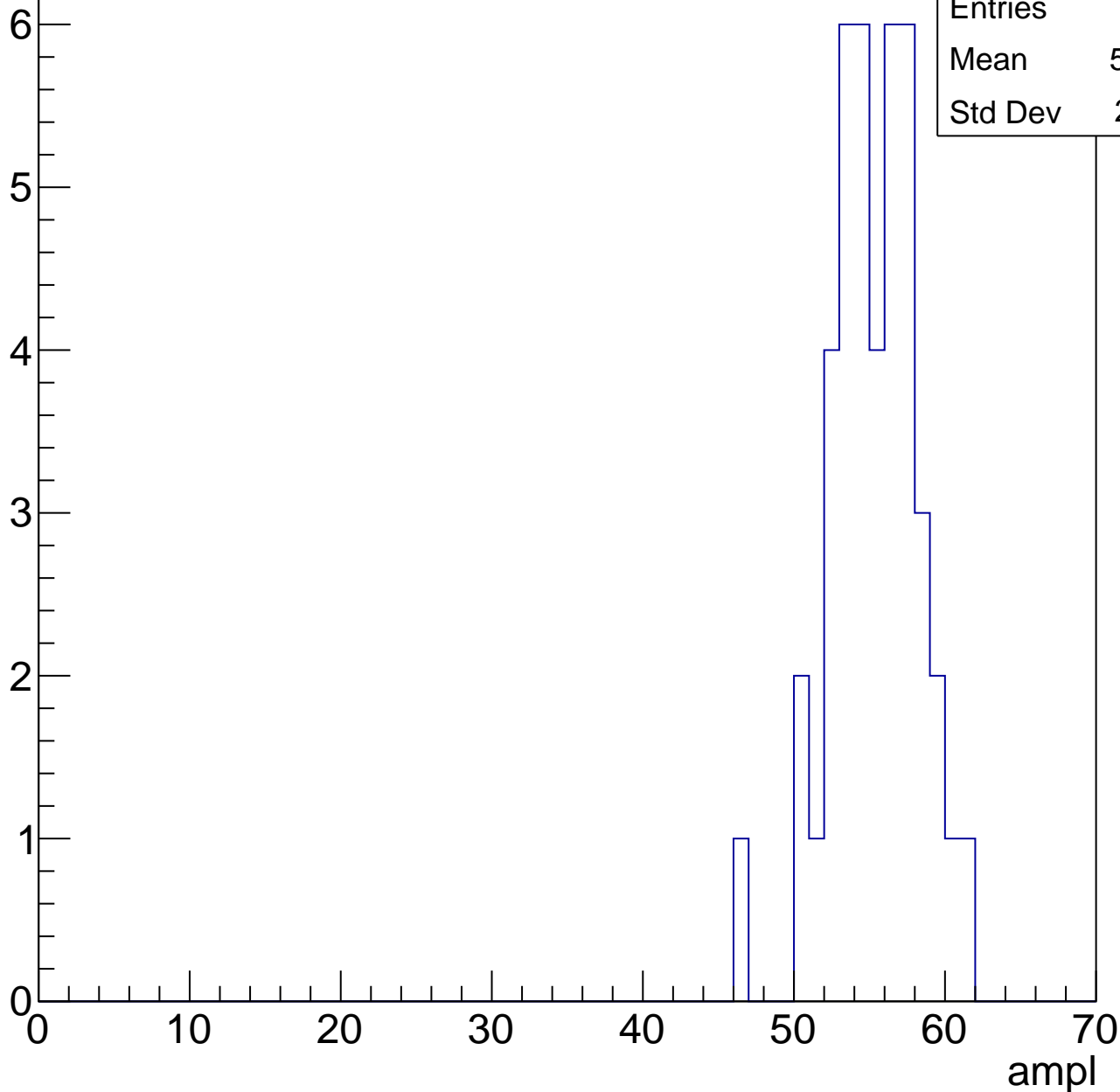


B1L102S, U6-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	54.84
Std Dev	2.901



B1L102S, U6-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	58.87
Std Dev	7.948

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

9

B1L102S, U6-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch112, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	28.89
Std Dev	5.913

Gaus mean : 30.0975

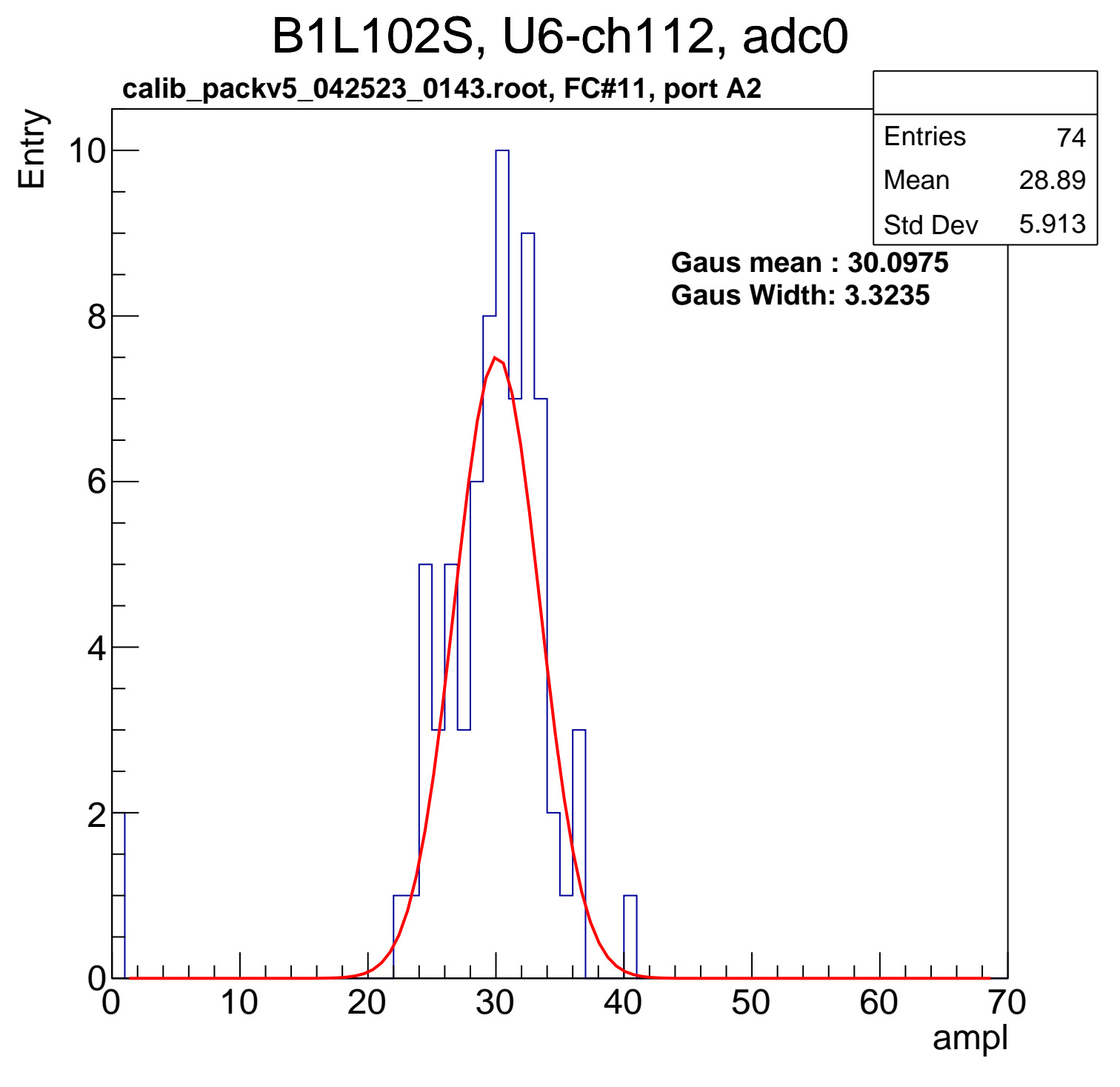
Gaus Width: 3.3235

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch112, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	37.09
Std Dev	3.138

Gaus mean : 37.5933

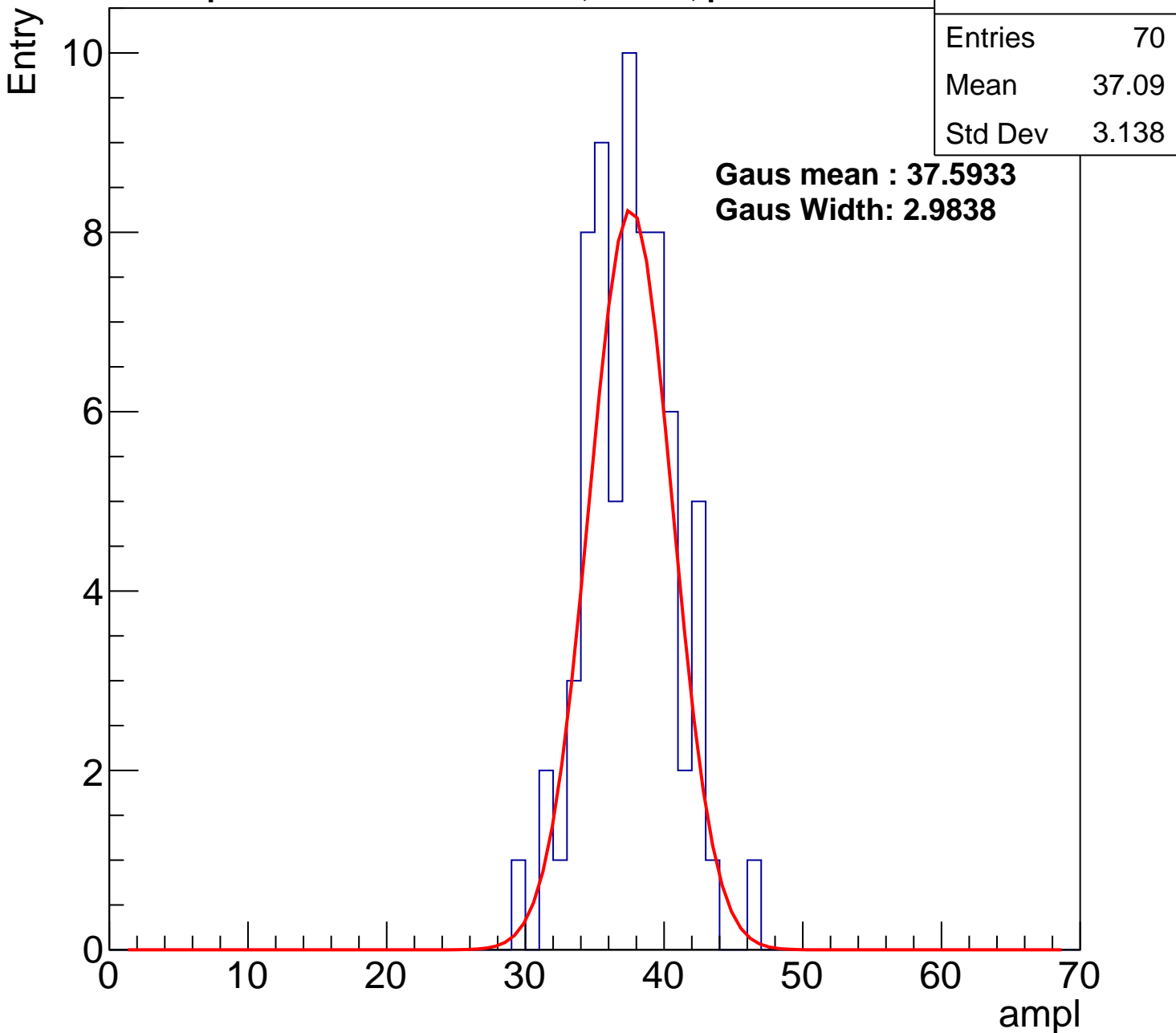
Gaus Width: 2.9838

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch112, adc2

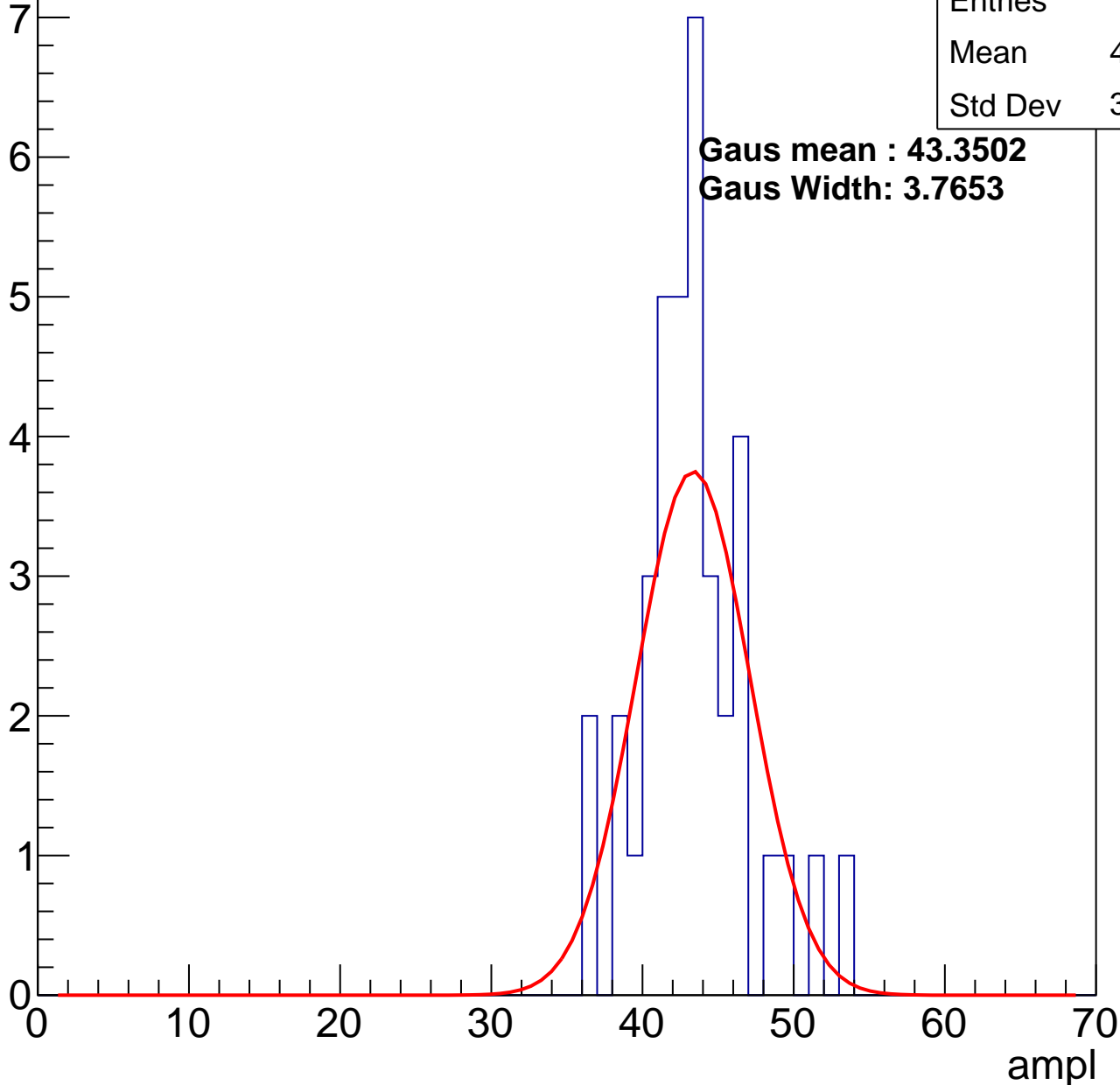
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	42.89
Std Dev	3.582

Gaus mean : 43.3502

Gaus Width: 3.7653



B1L102S, U6-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

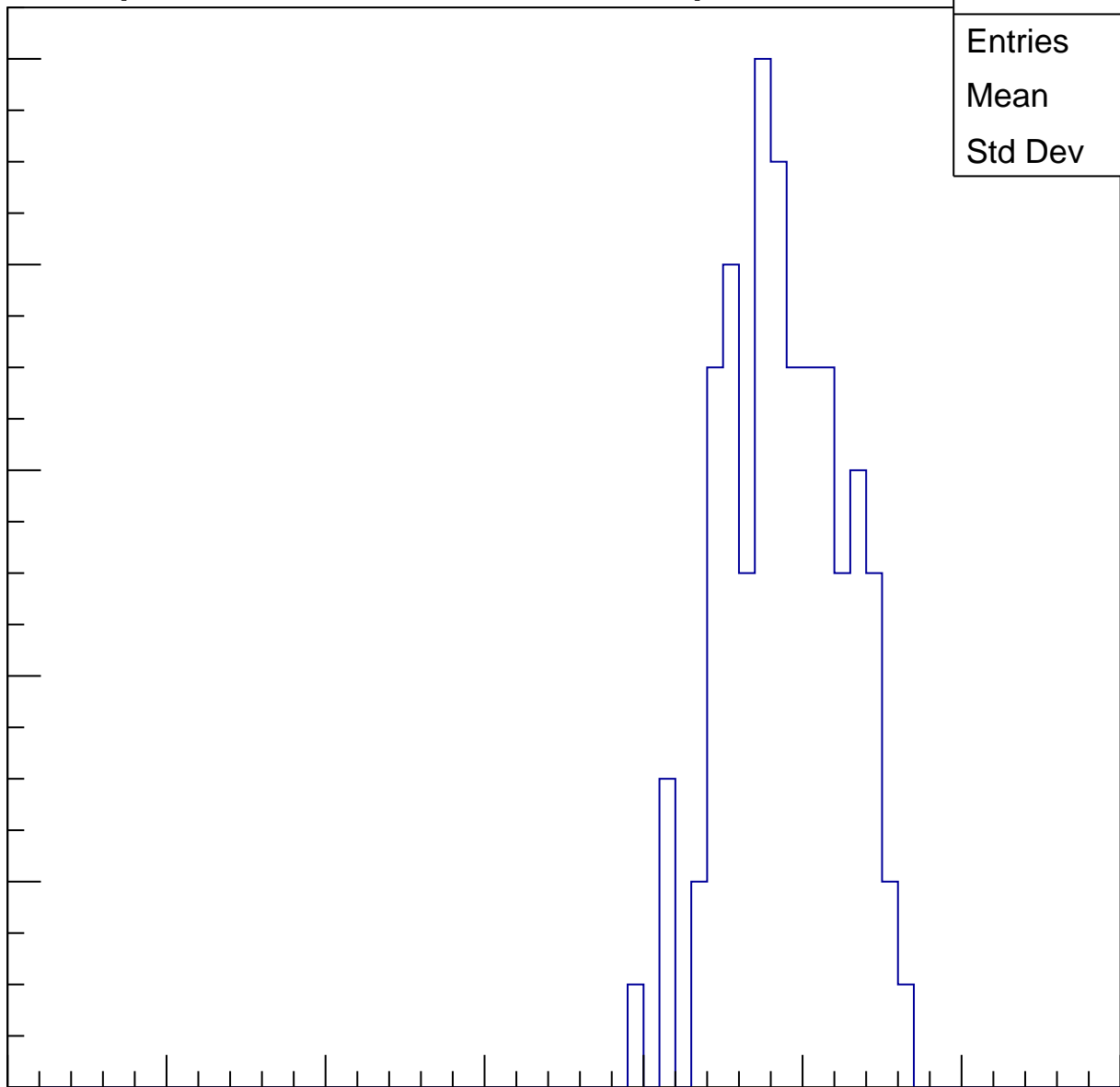
Entries	85
Mean	48.38
Std Dev	3.678

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

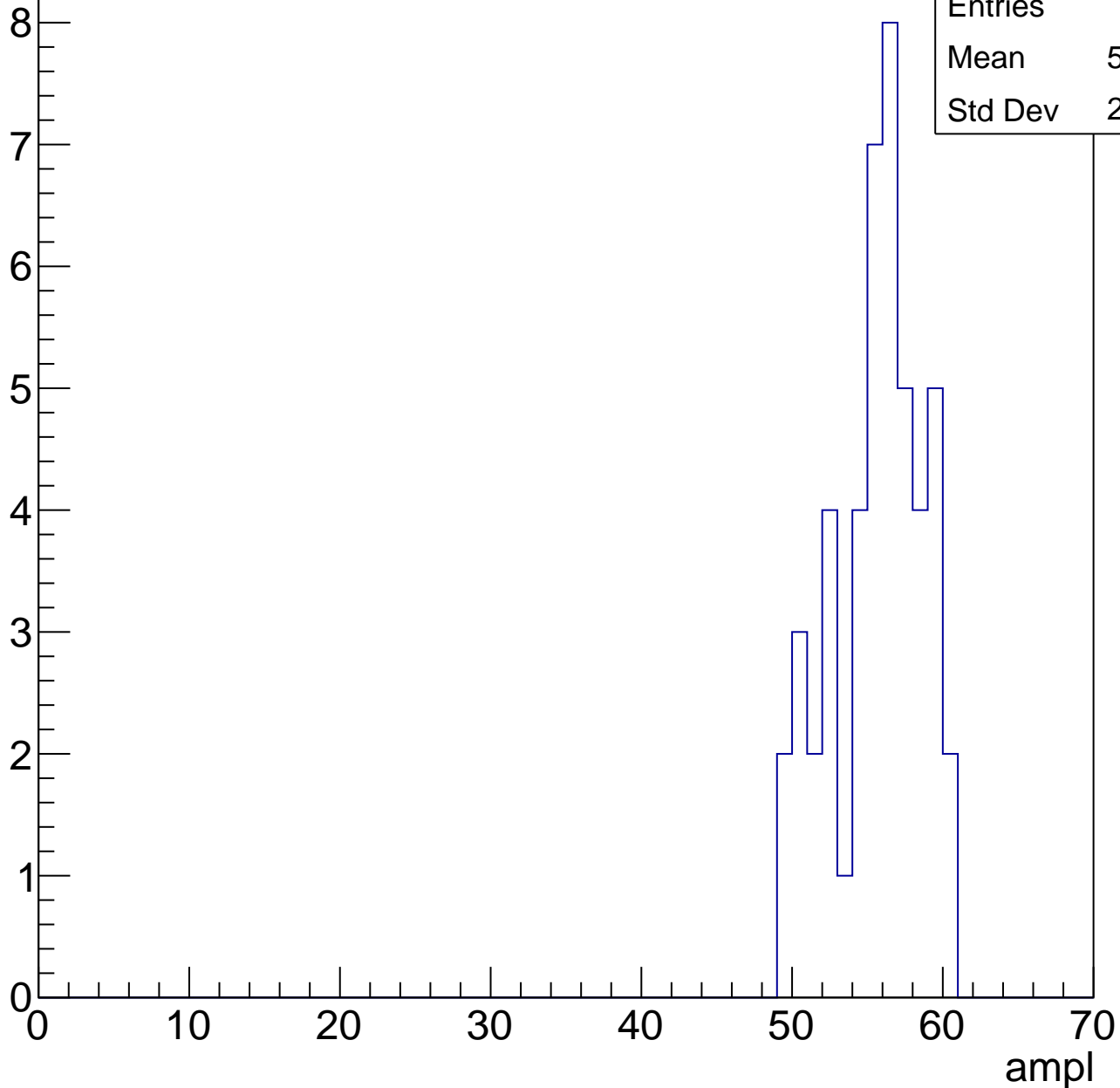
ampl



B1L102S, U6-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



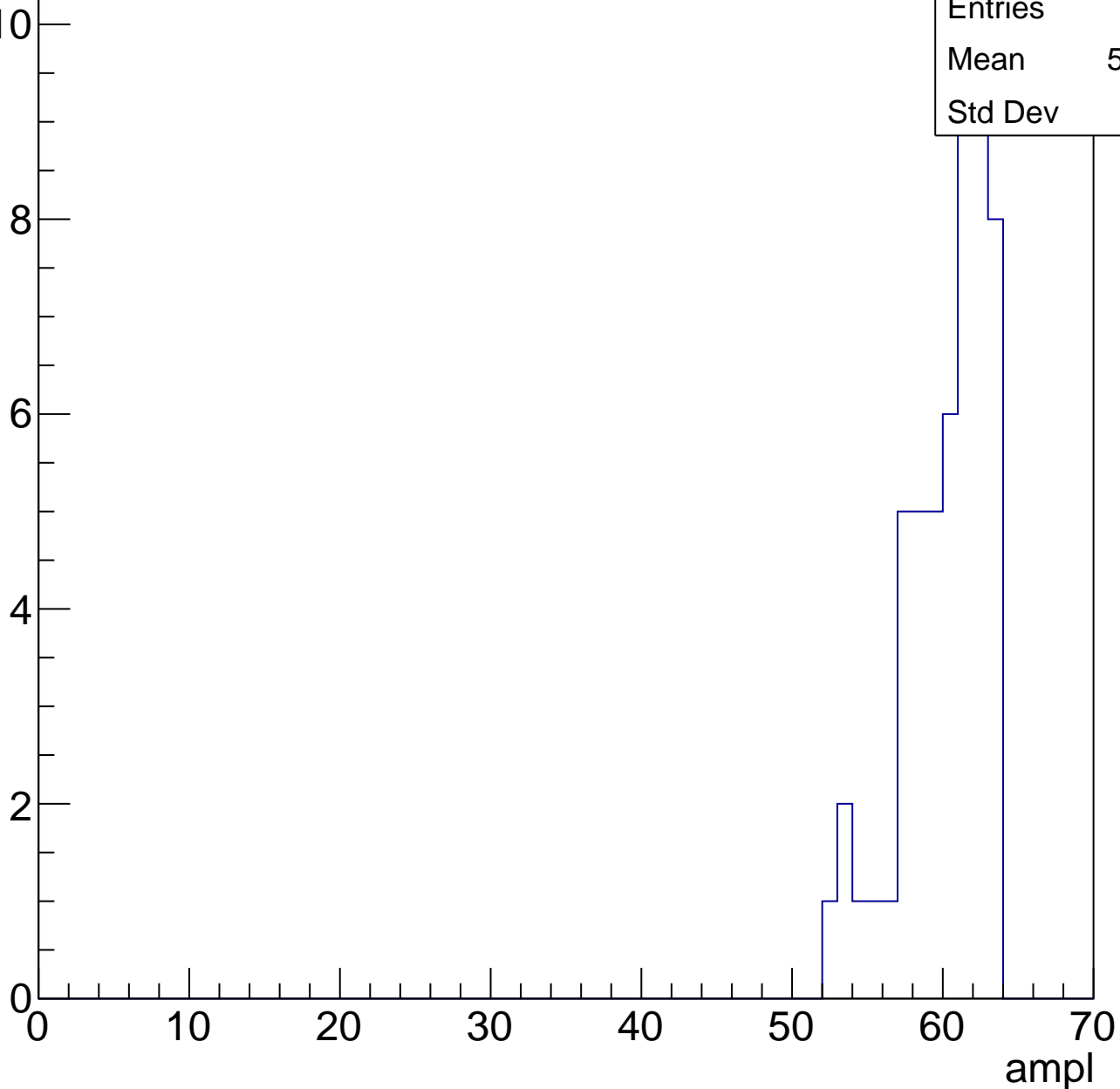
Entries	47
Mean	55.15
Std Dev	2.975

B1L102S, U6-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	59.74
Std Dev	2.81



B1L102S, U6-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

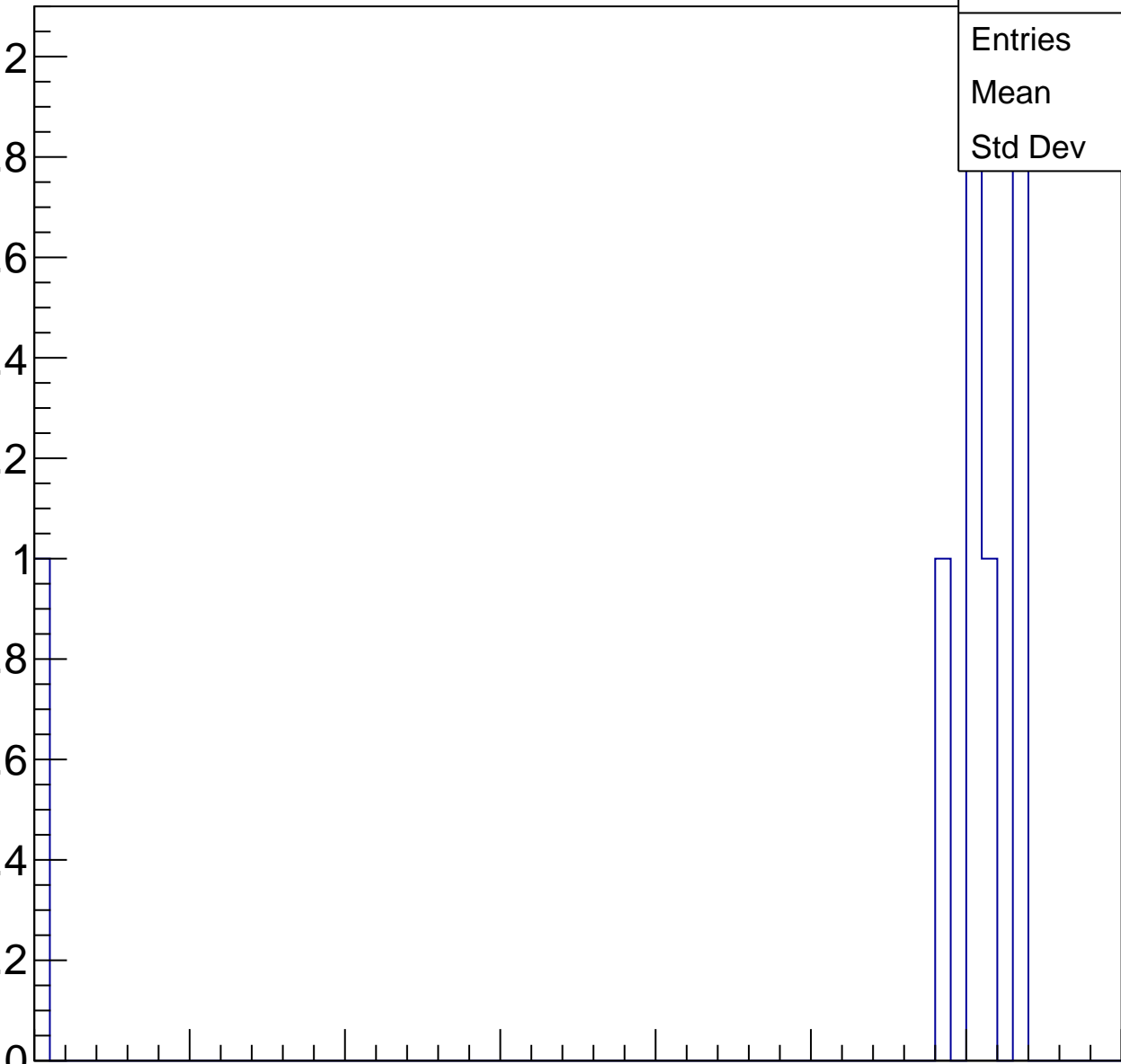
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.14
Std Dev	21.35

0 10 20 30 40 50 60 70

ampl



B1L102S, U6-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch113, adc0

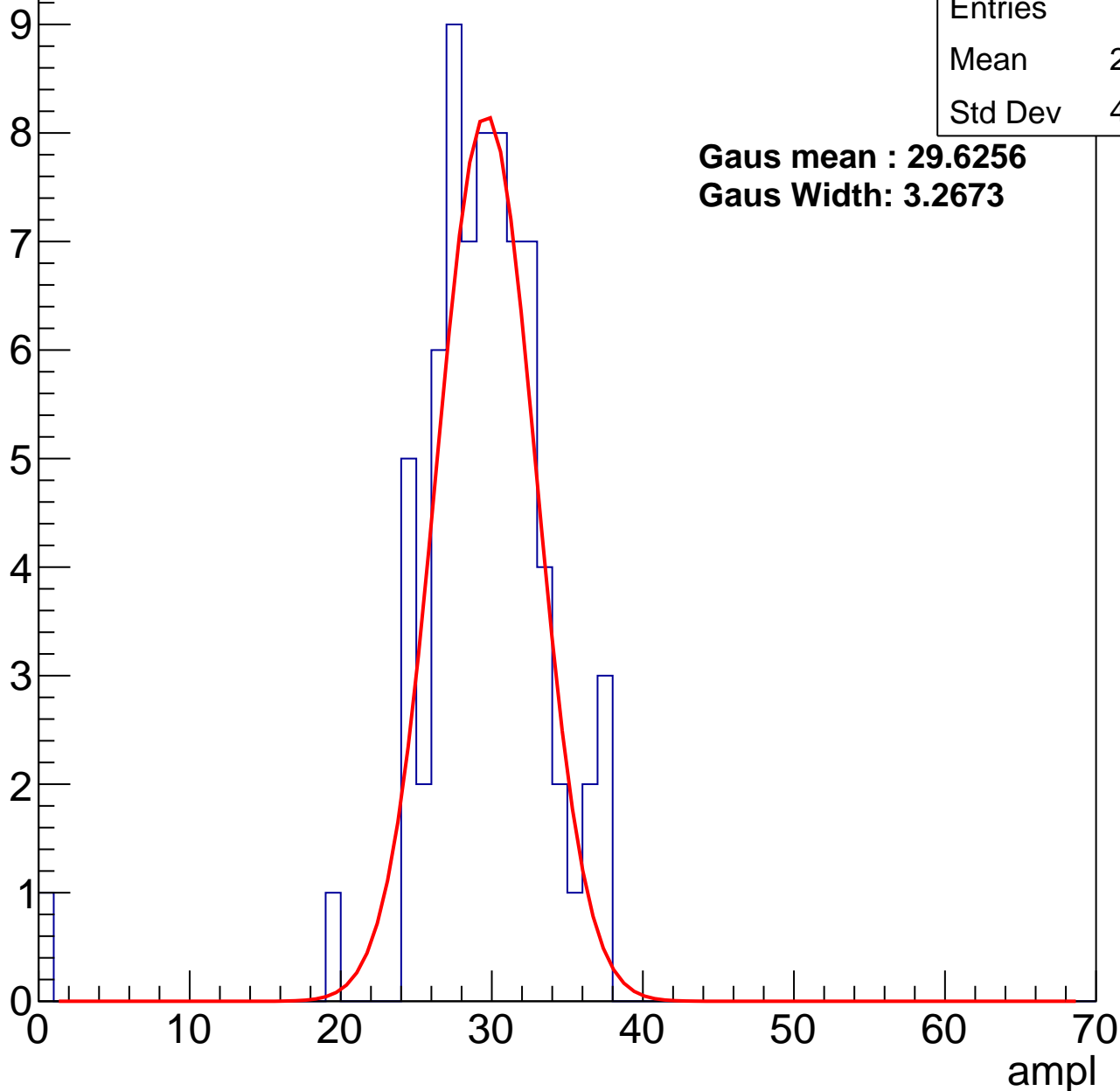
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.97
Std Dev	4.874

Gaus mean : 29.6256

Gaus Width: 3.2673



B1L102S, U6-ch113, adc1

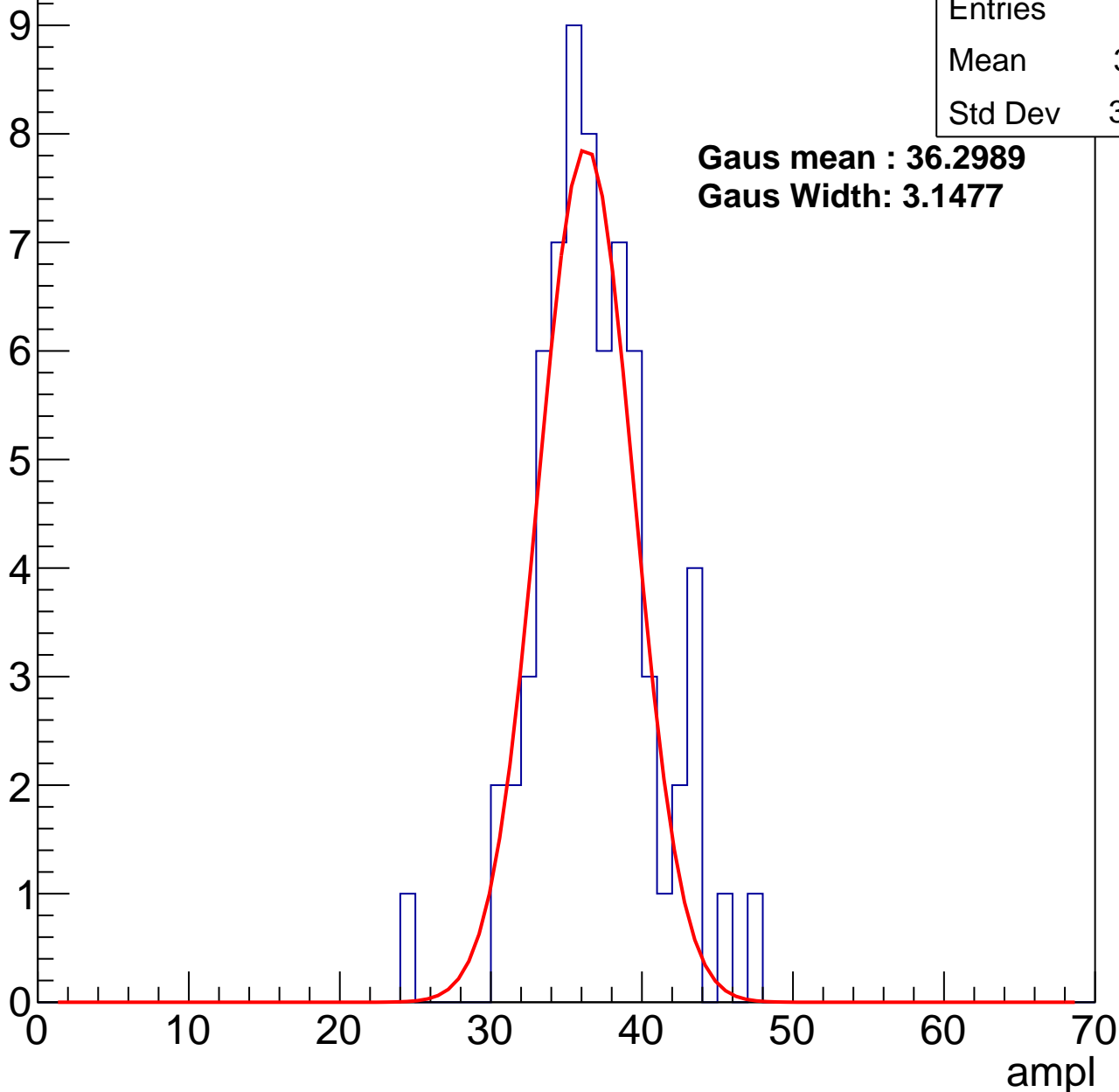
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.41
Std Dev	3.854

Gaus mean : 36.2989

Gaus Width: 3.1477



B1L102S, U6-ch113, adc2

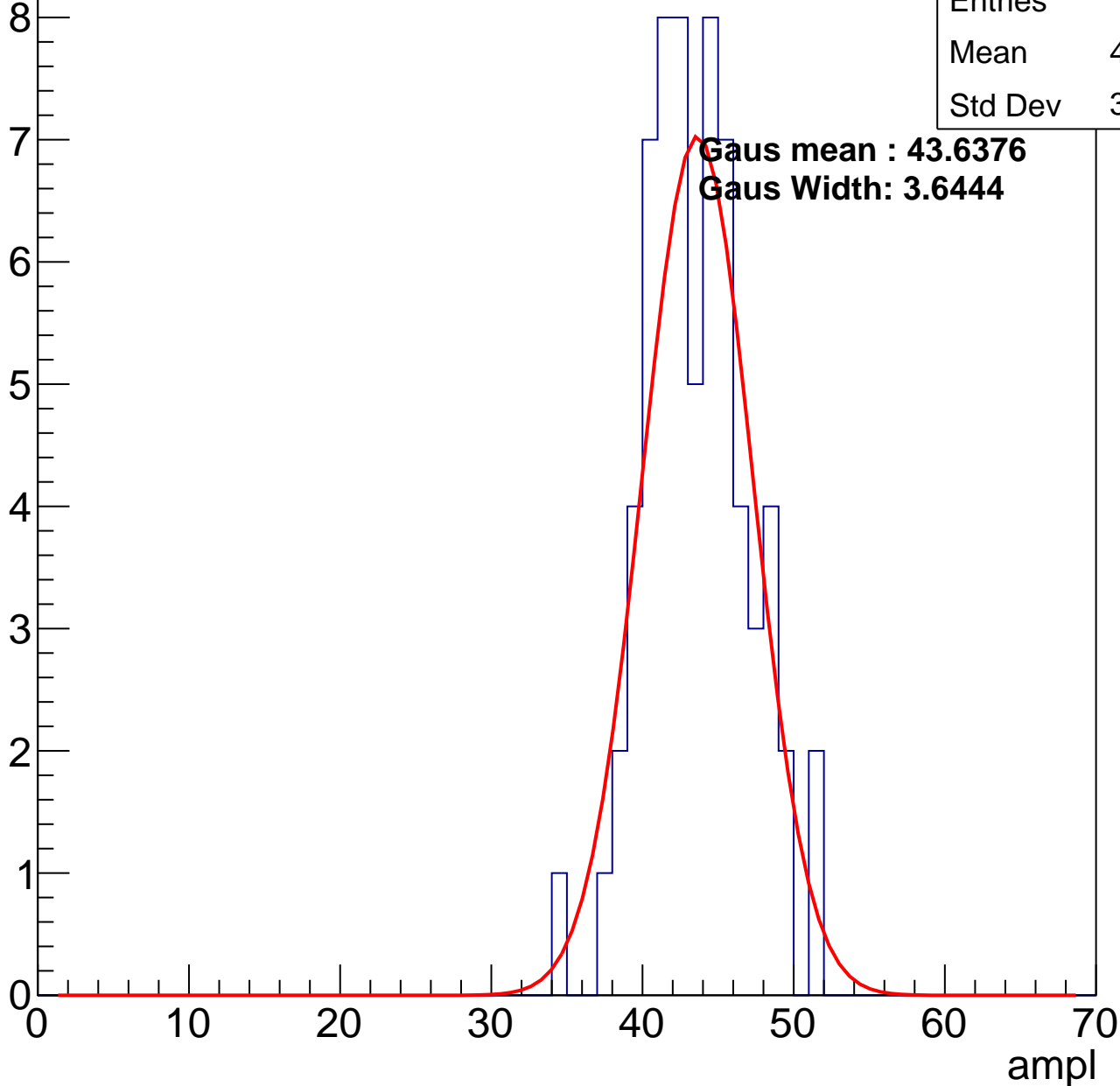
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.12
Std Dev	3.378

Gaus mean : 43.6376

Gaus Width: 3.6444

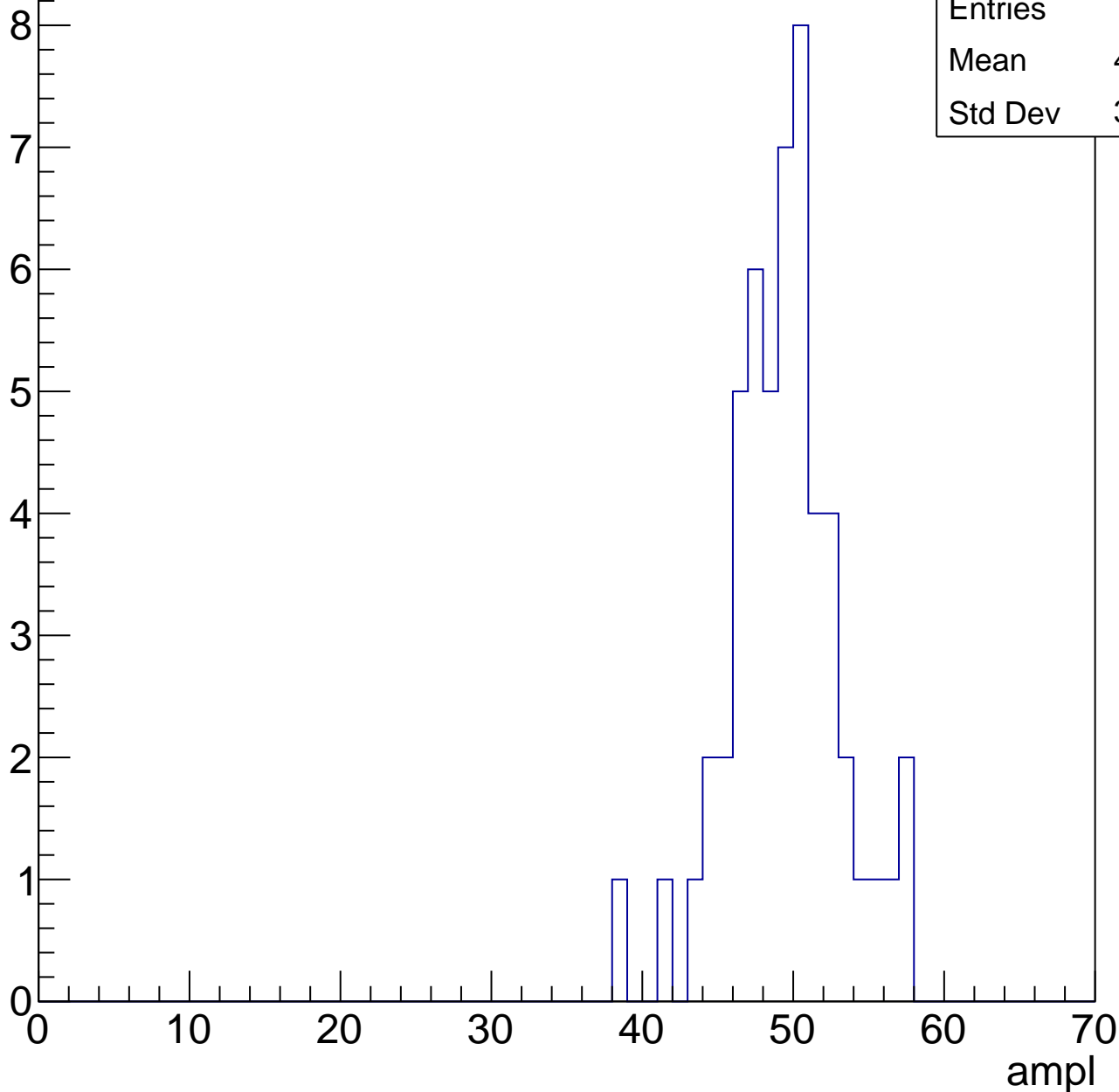


B1L102S, U6-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.91
Std Dev	3.651

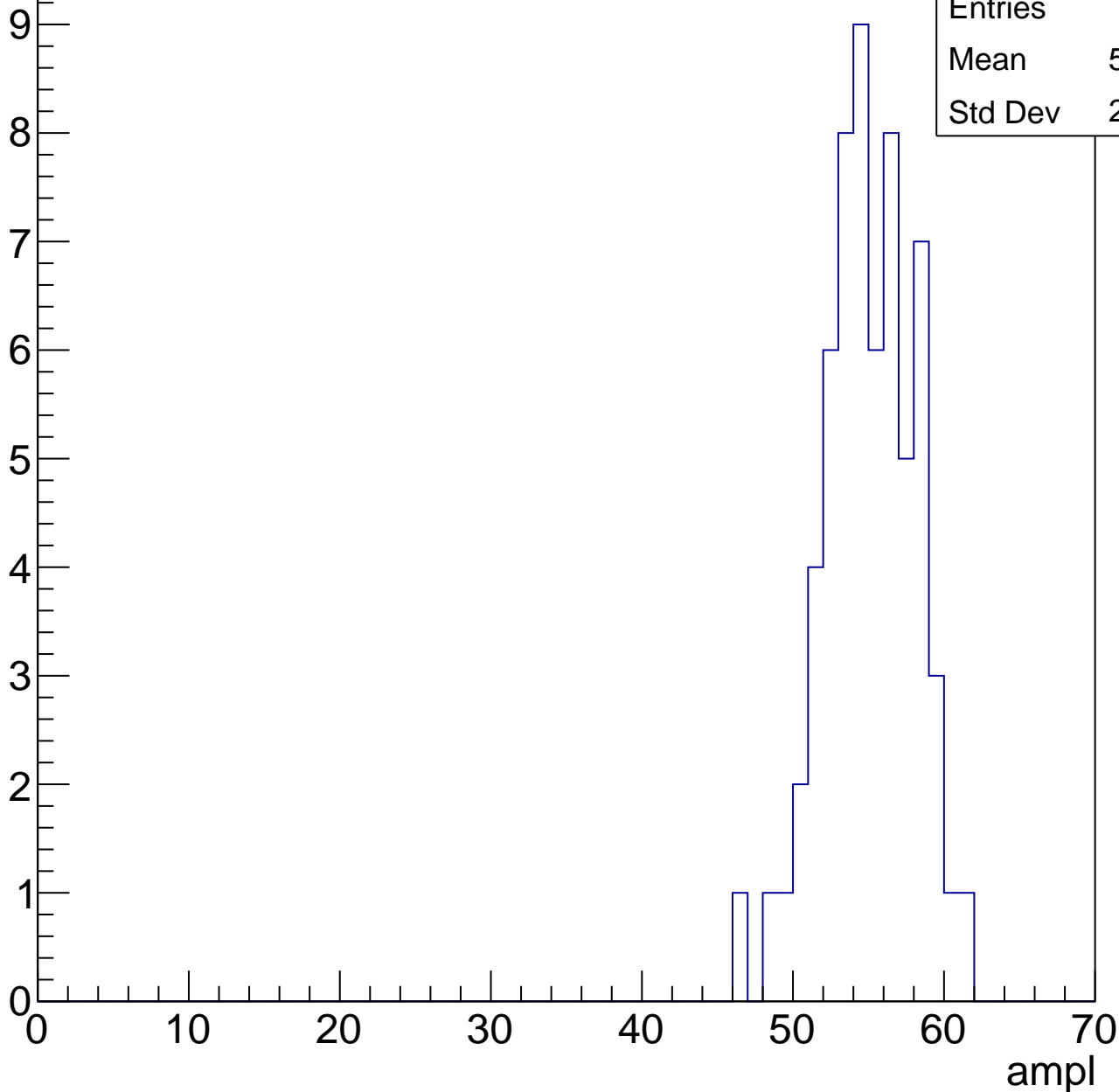


B1L102S, U6-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.54
Std Dev	2.986



B1L102S, U6-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

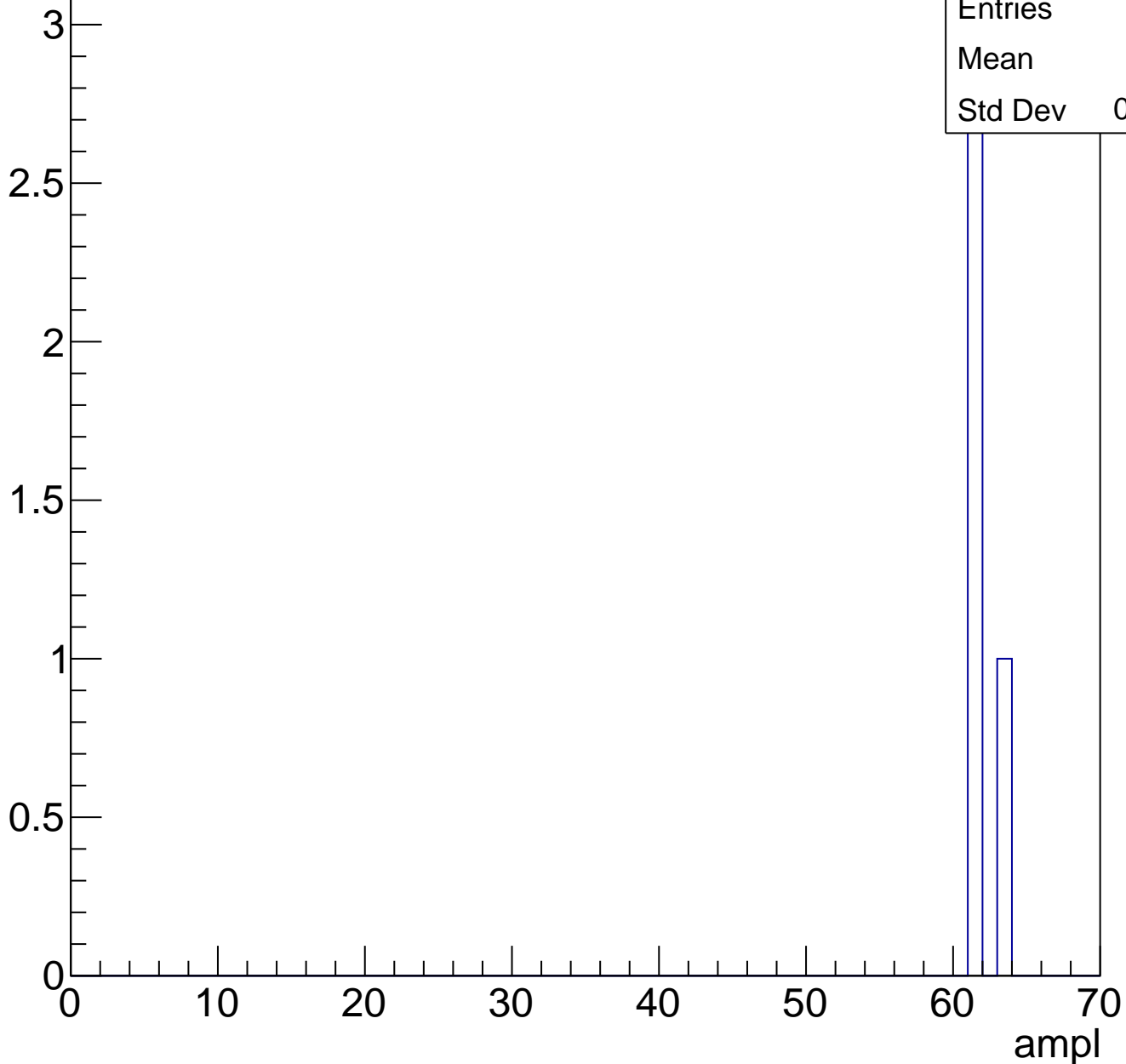
ampl

Entries	52
Mean	59.08
Std Dev	8.571

B1L102S, U6-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch114, adc0

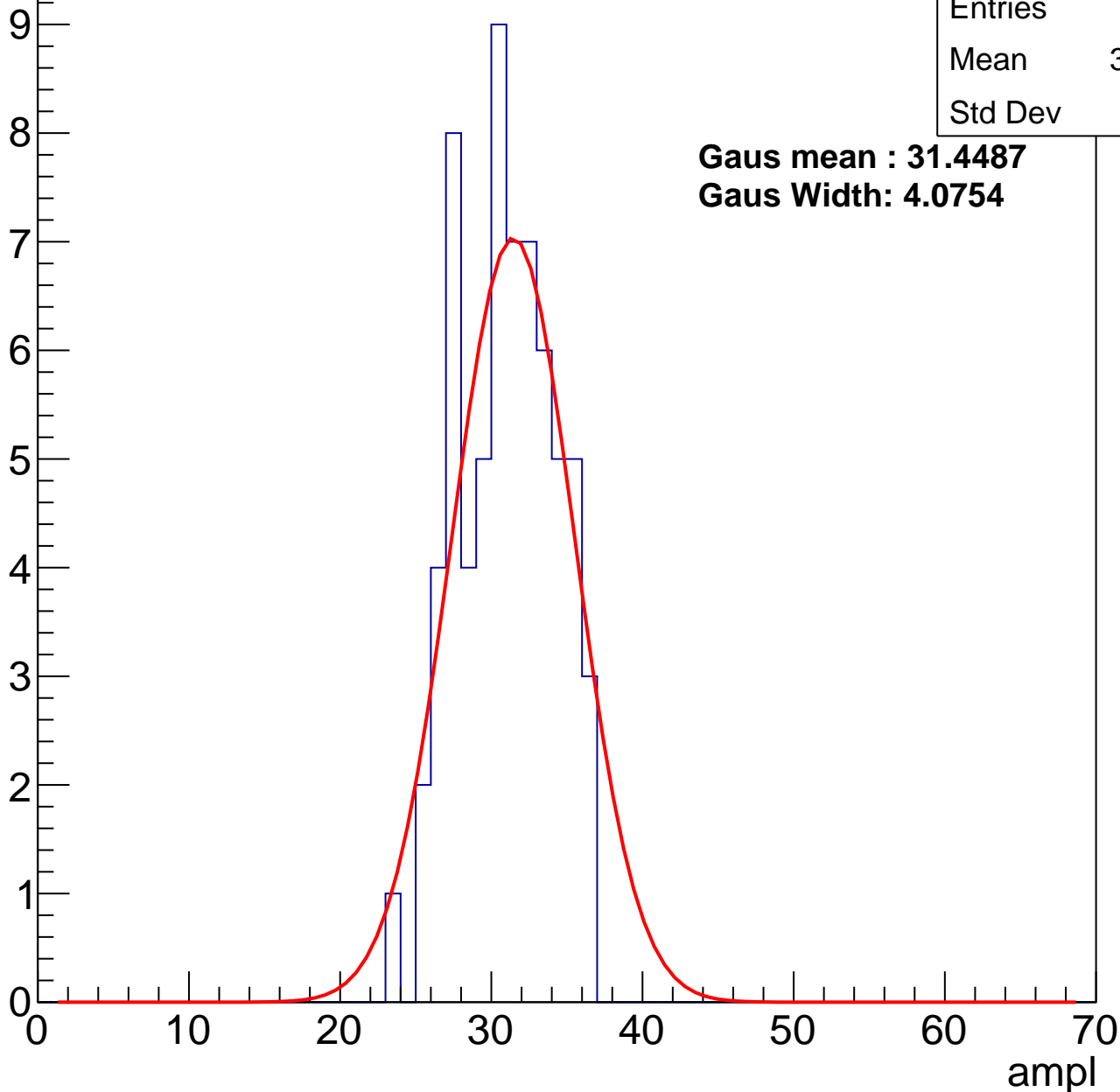
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	30.48
Std Dev	3.12

Gaus mean : 31.4487

Gaus Width: 4.0754



B1L102S, U6-ch114, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	38.1
Std Dev	3.73

Gaus mean : 39.0870

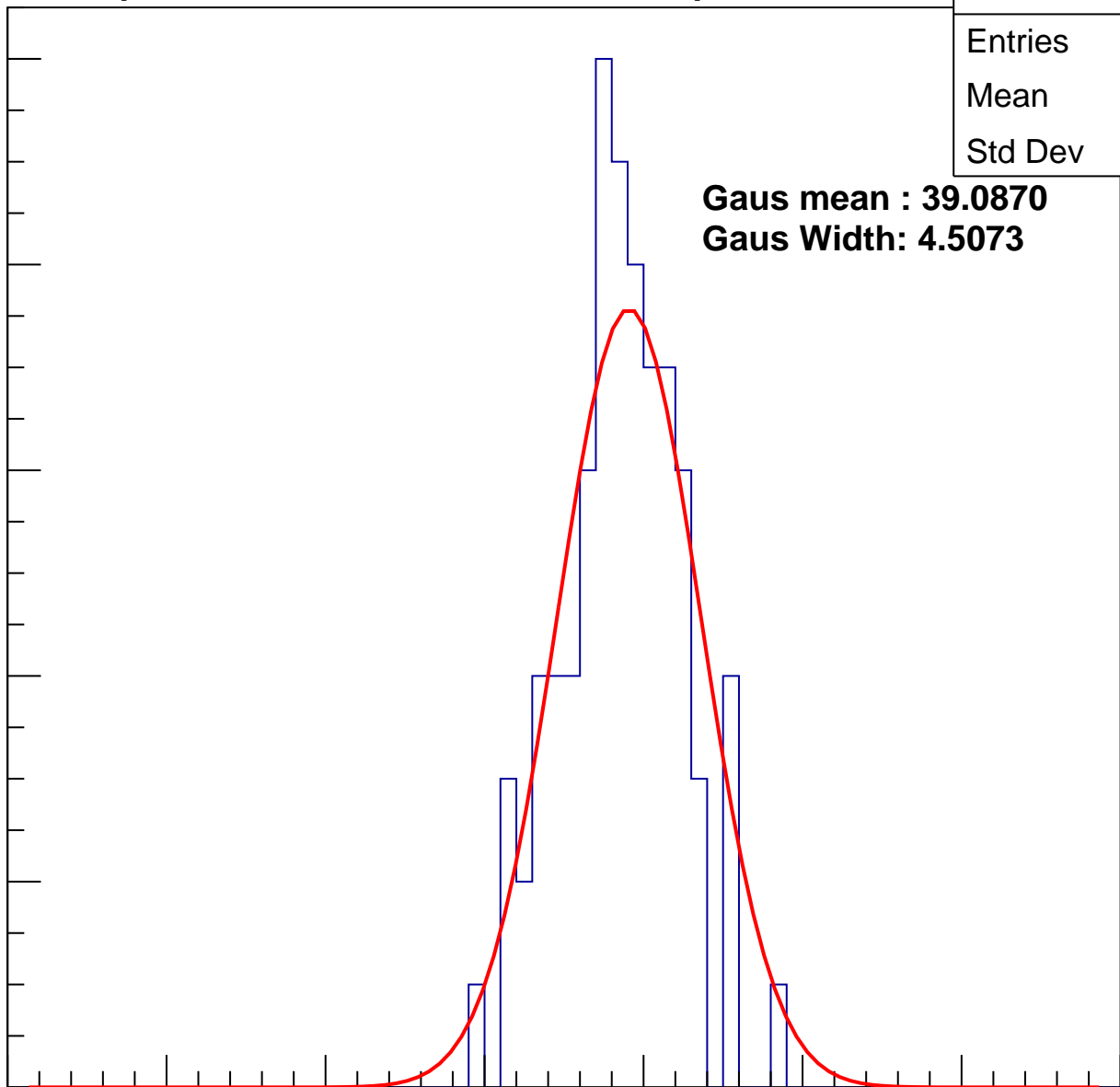
Gaus Width: 4.5073

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U6-ch114, adc2

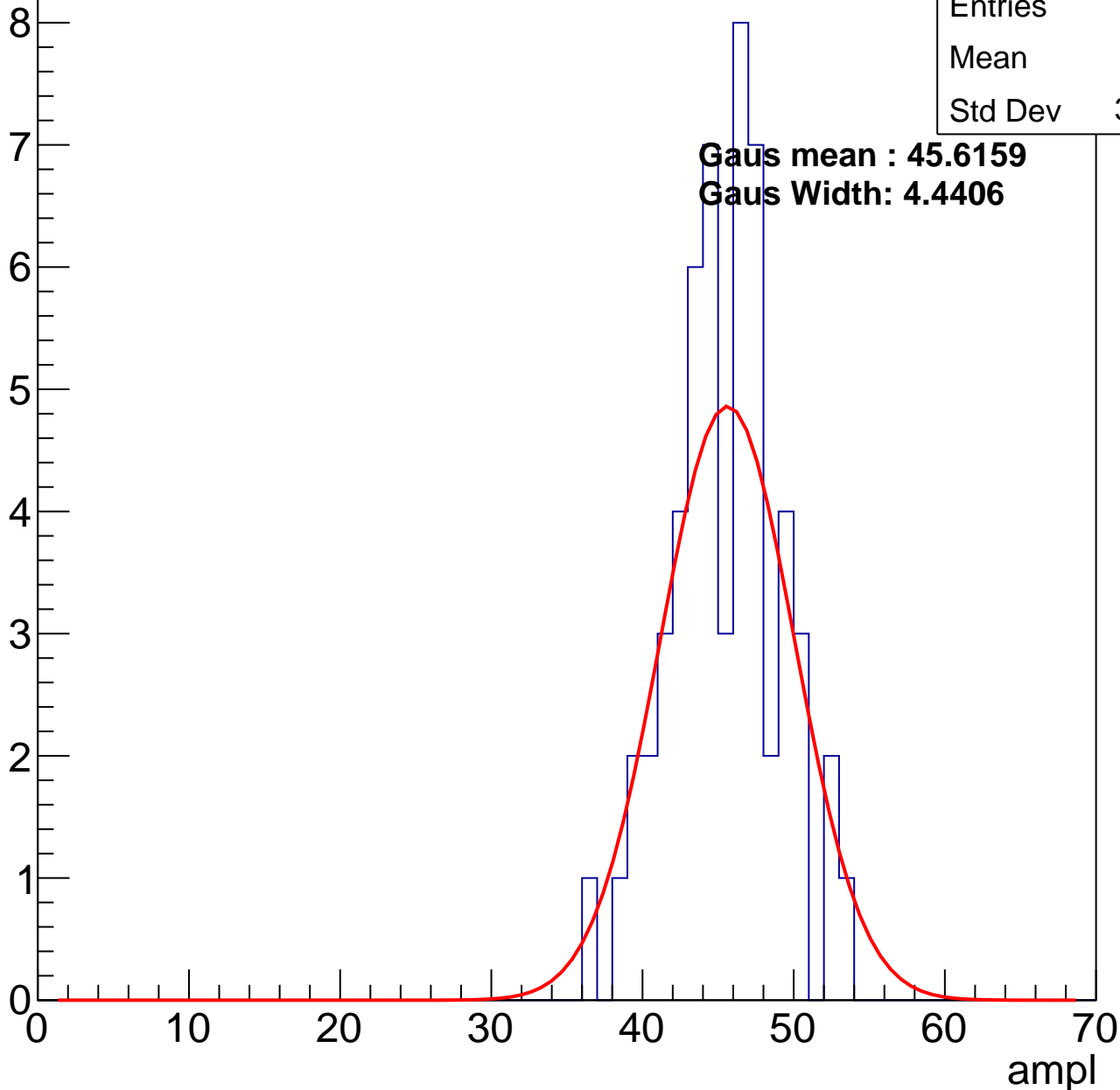
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	45
Std Dev	3.591

Gaus mean : 45.6159

Gaus Width: 4.4406

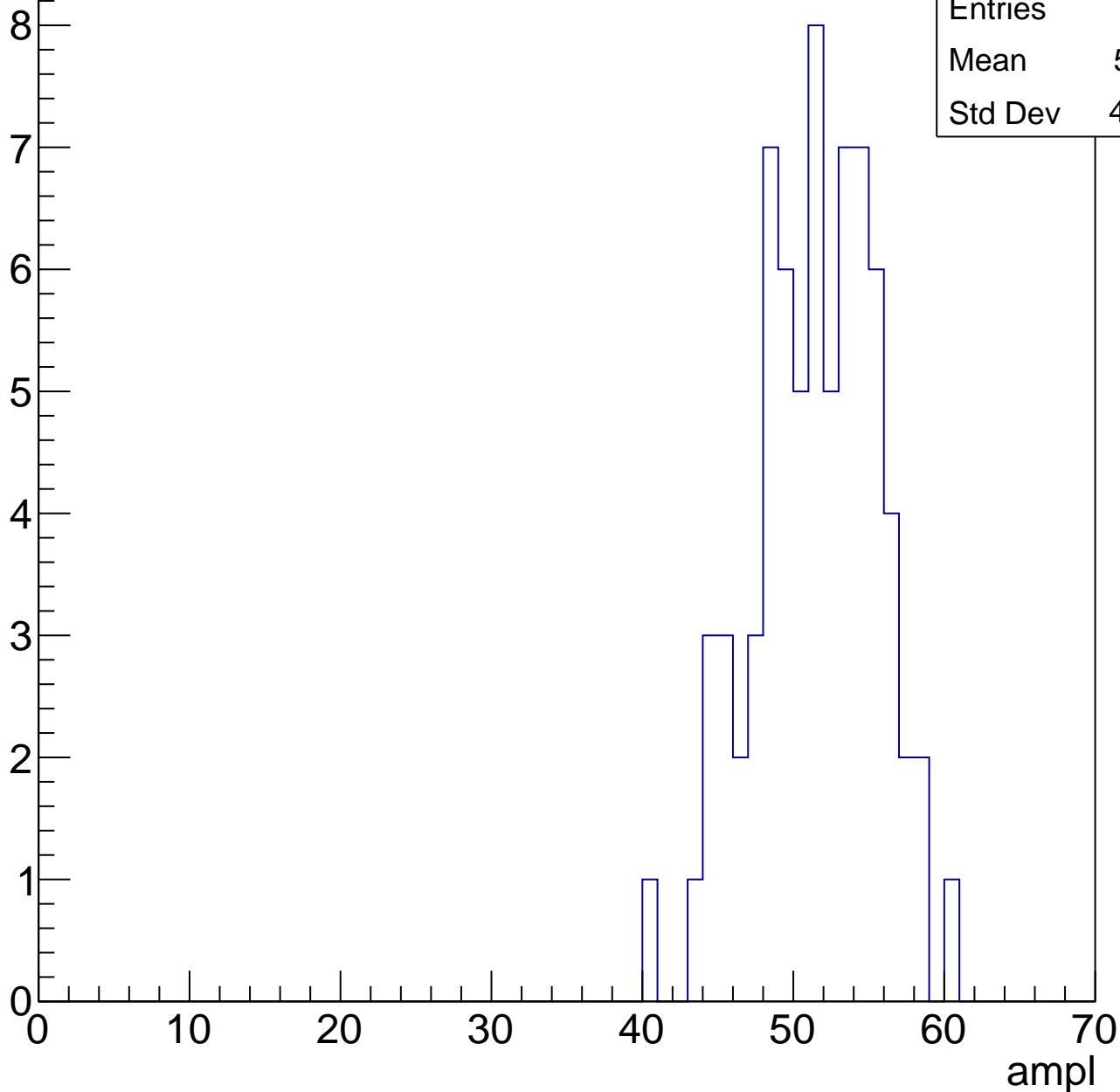


B1L102S, U6-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	51.01
Std Dev	4.009

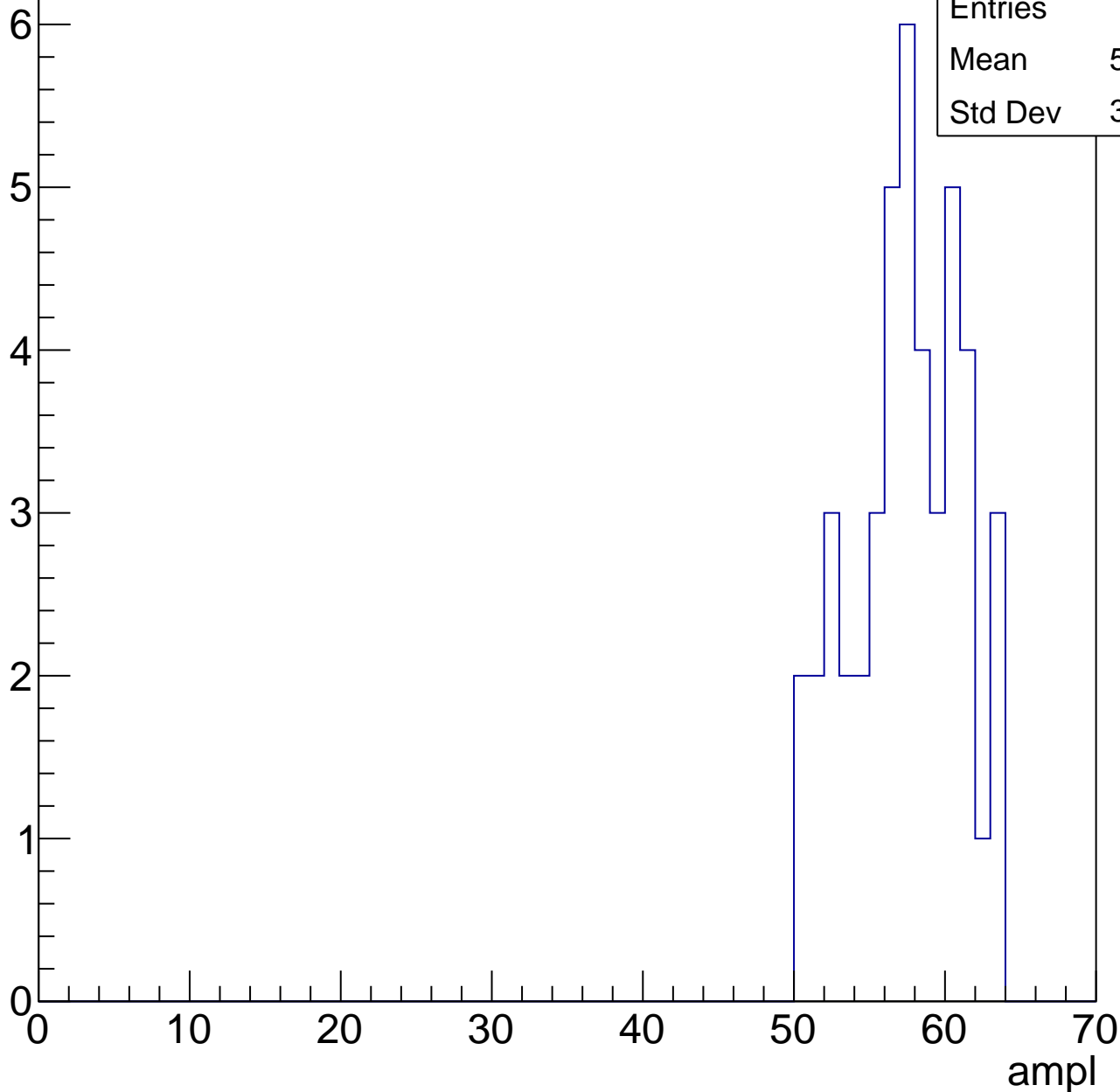


B1L102S, U6-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	56.96
Std Dev	3.559

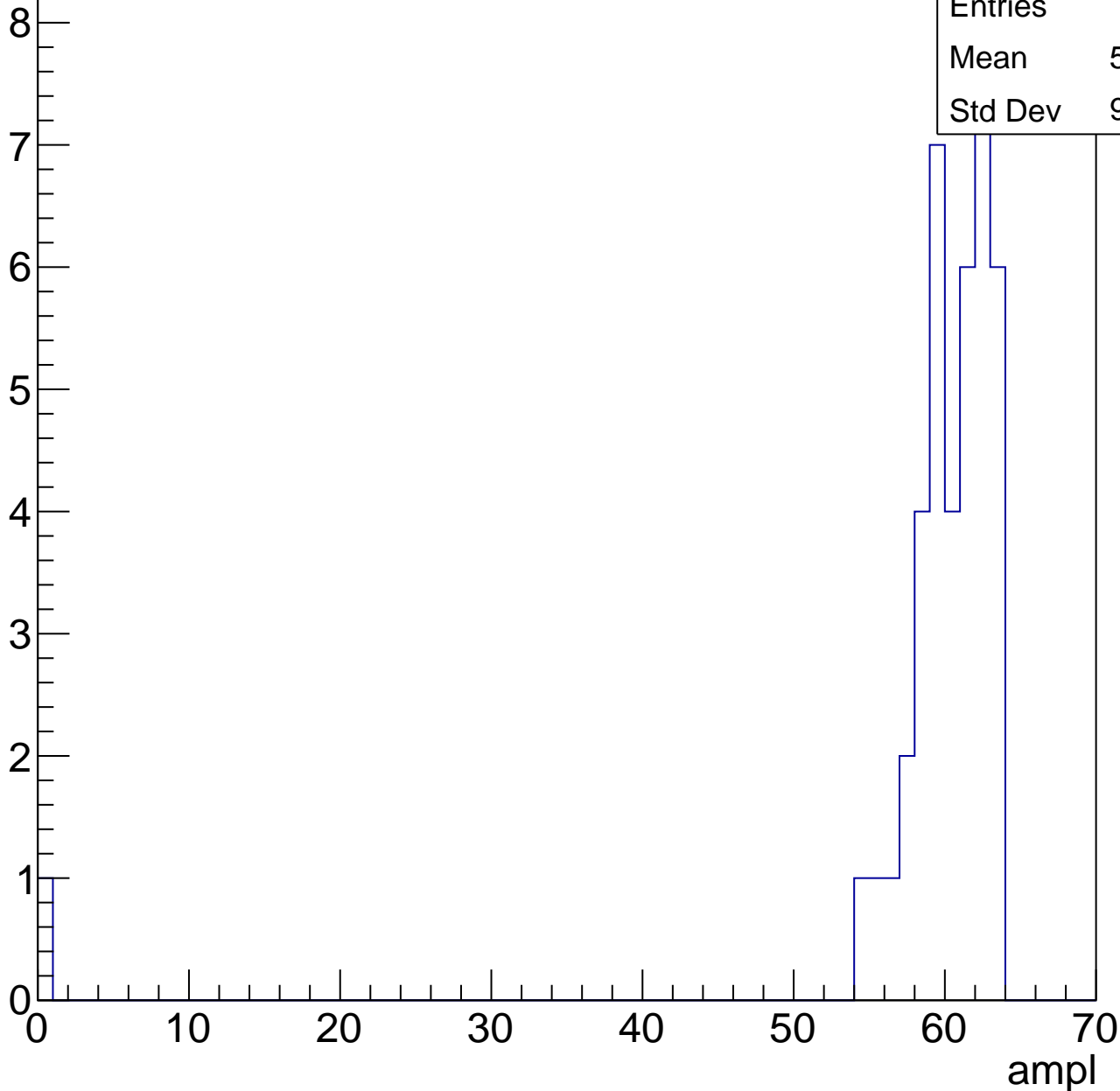


B1L102S, U6-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.63
Std Dev	9.543



B1L102S, U6-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch115, adc0

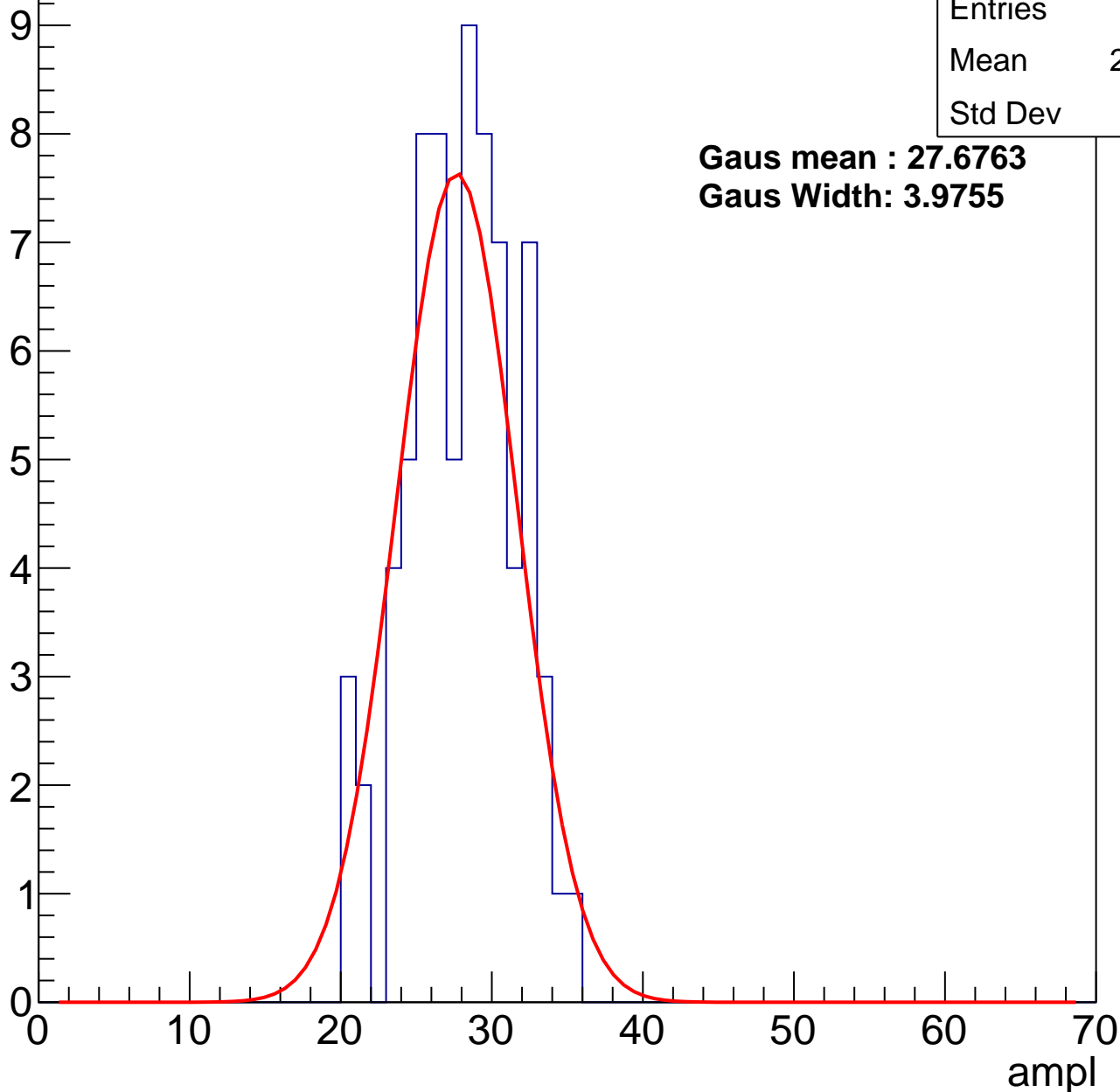
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27.56
Std Dev	3.48

Gaus mean : 27.6763

Gaus Width: 3.9755



B1L102S, U6-ch115, adc1

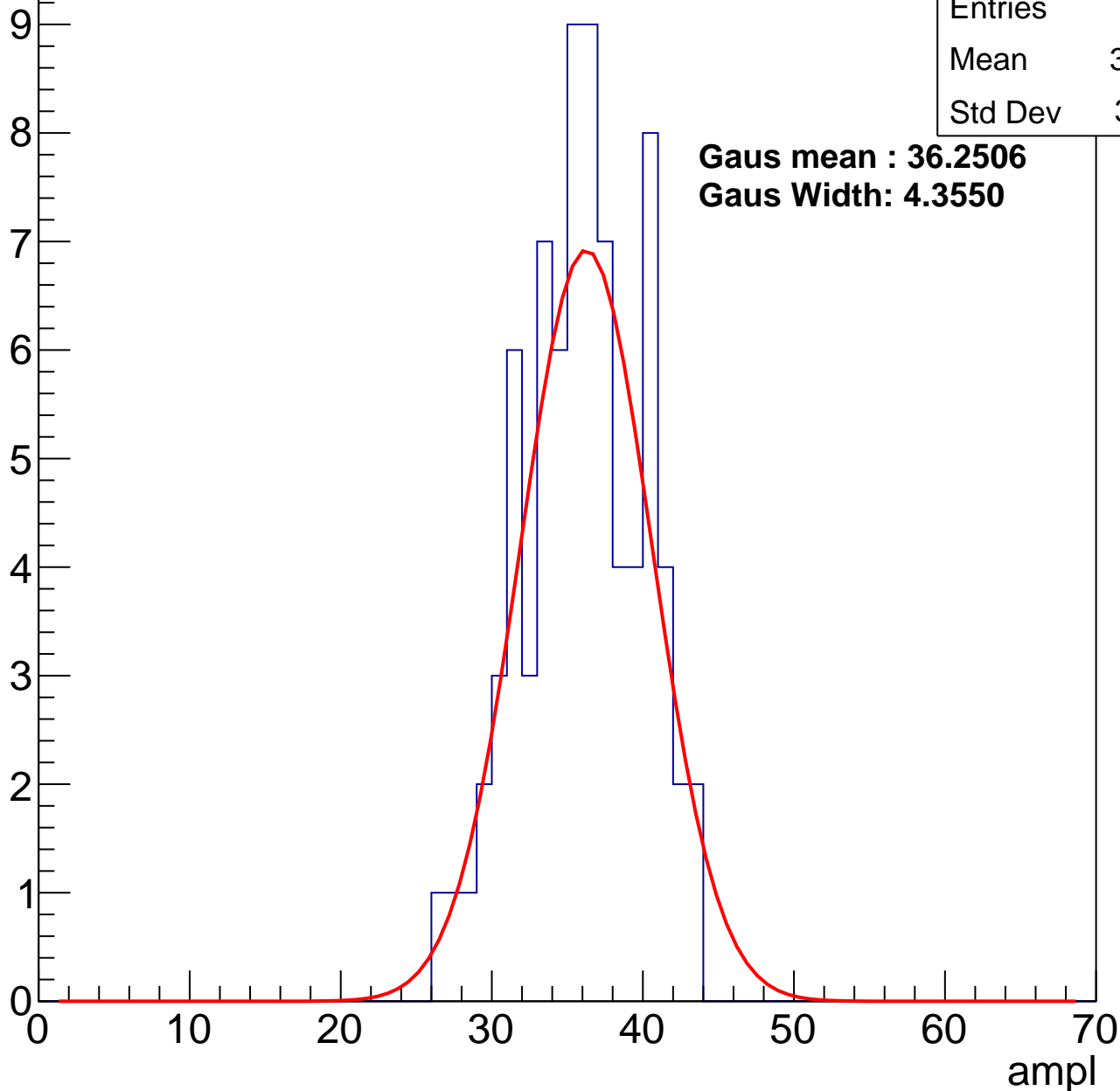
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.52
Std Dev	3.871

Gaus mean : 36.2506

Gaus Width: 4.3550



B1L102S, U6-ch115, adc2

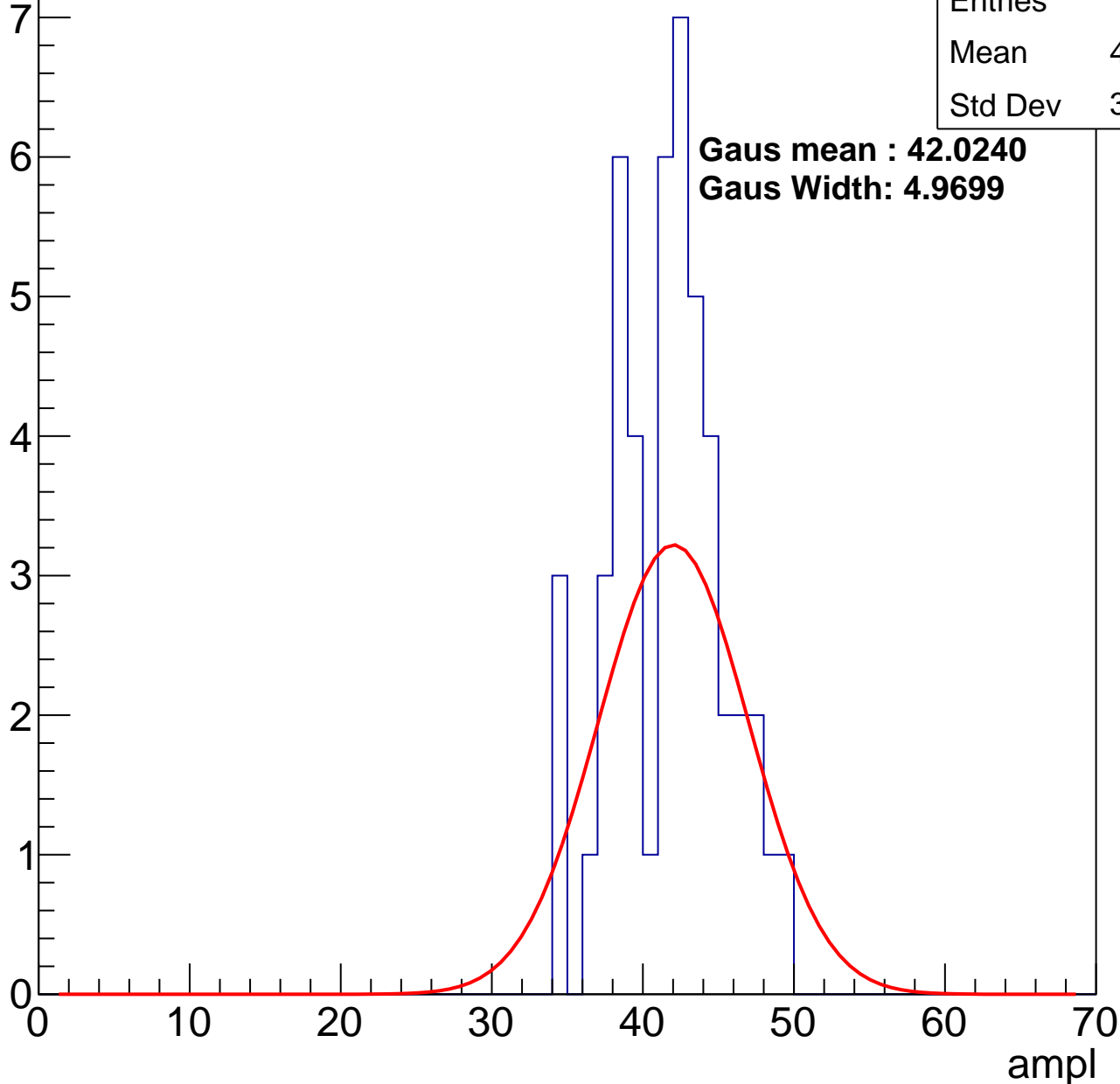
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	41.19
Std Dev	3.592

Gaus mean : 42.0240

Gaus Width: 4.9699



B1L102S, U6-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

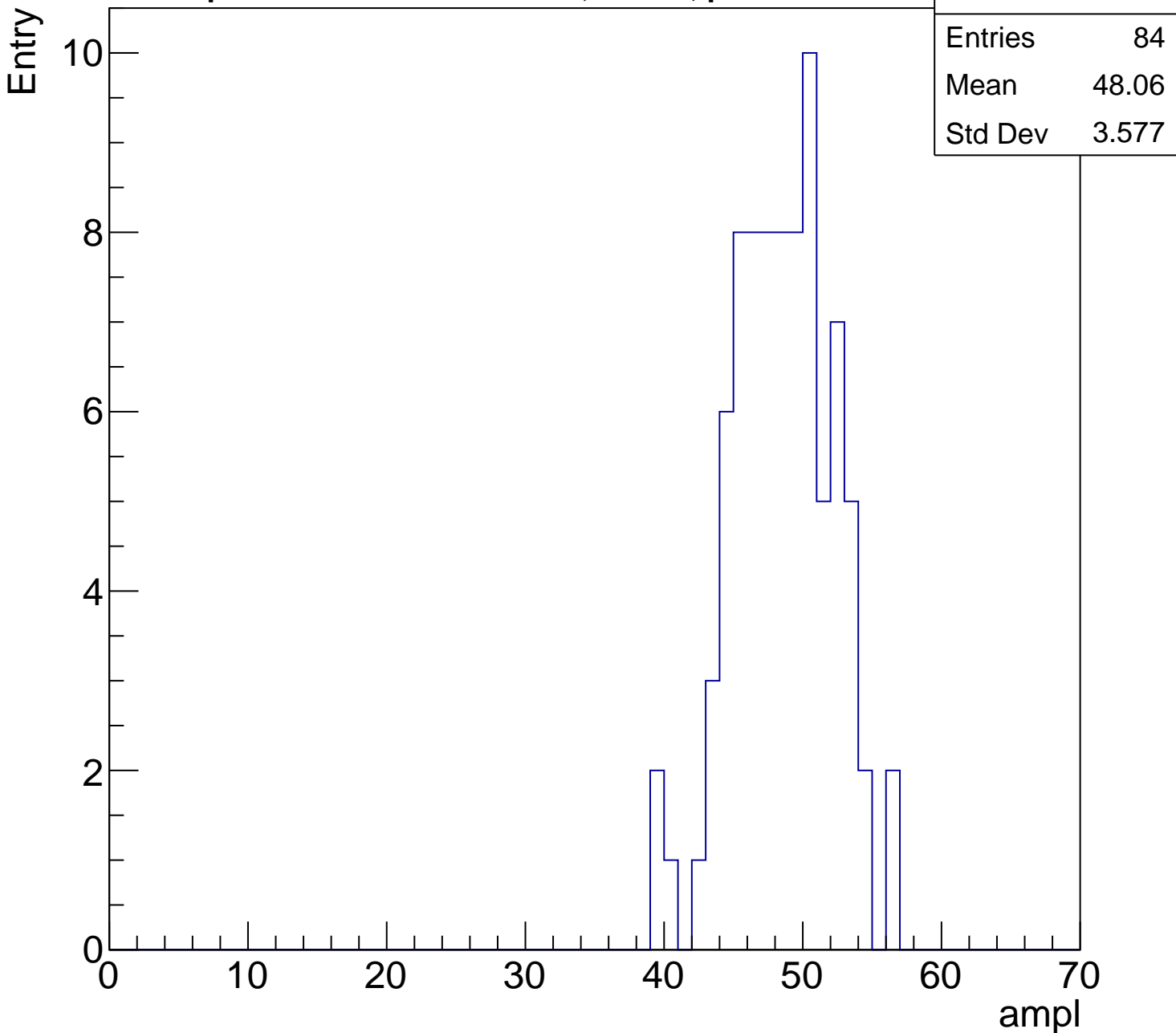
Entries	84
Mean	48.06
Std Dev	3.577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

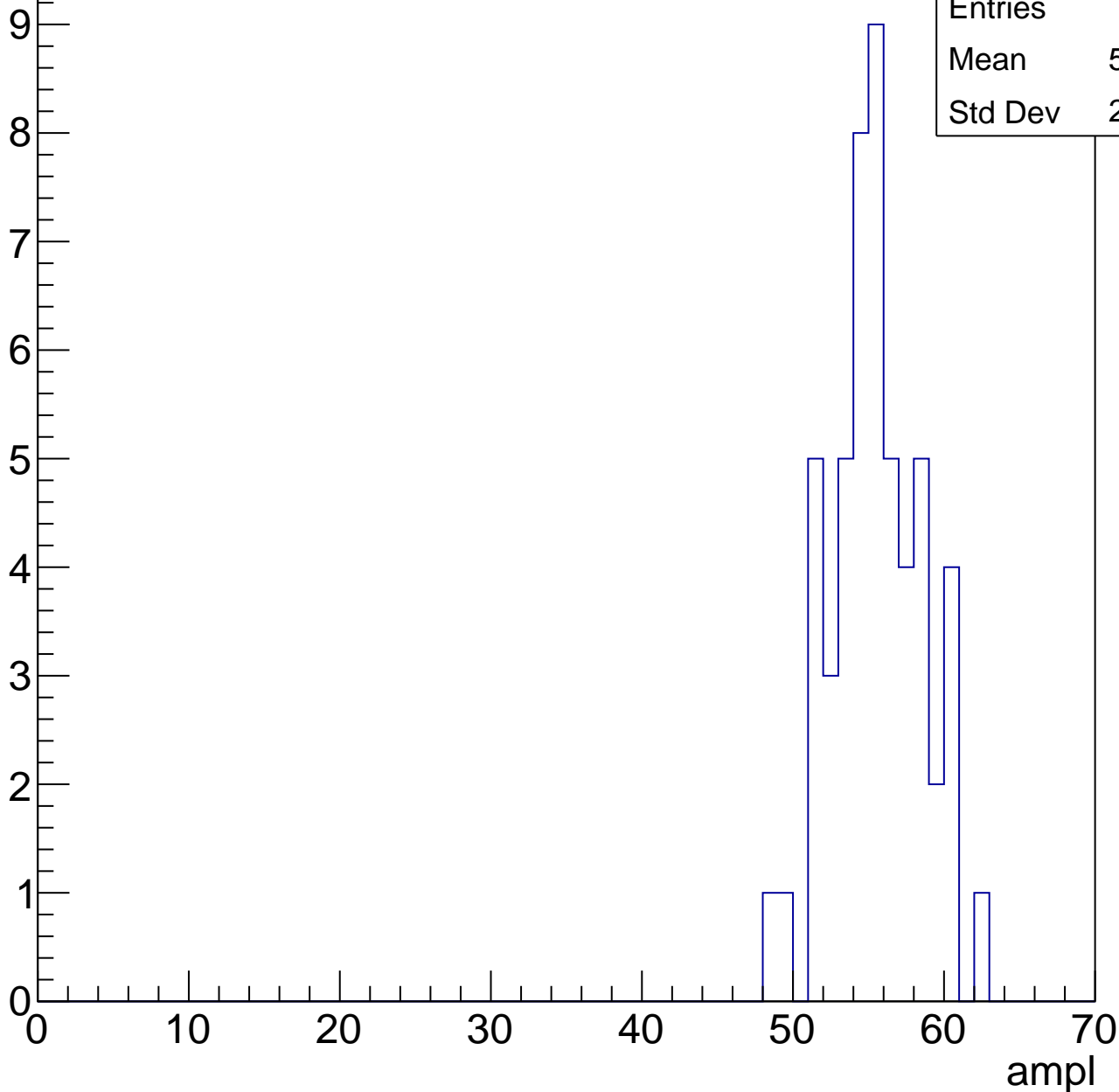


B1L102S, U6-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.06
Std Dev	2.968

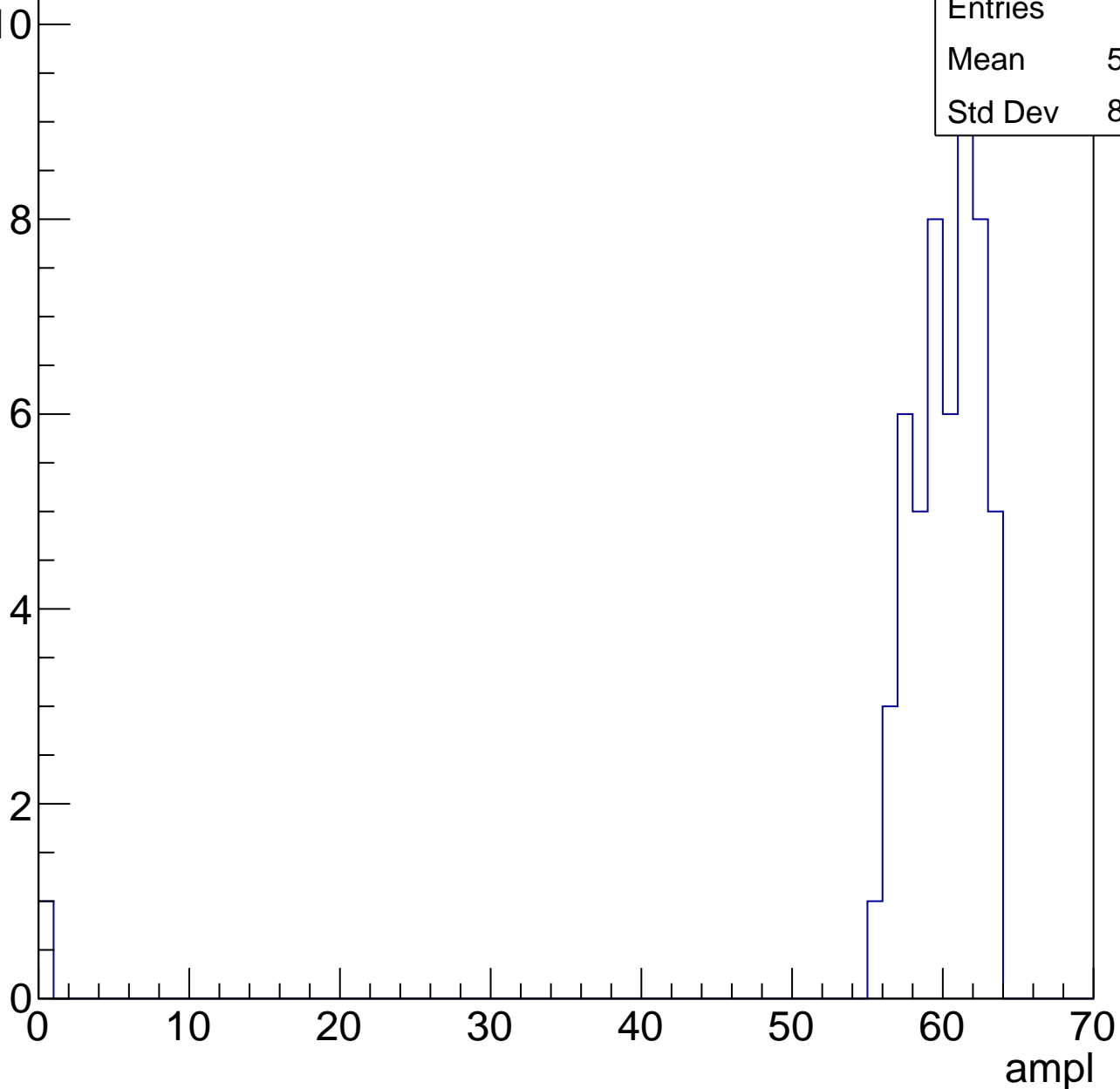


B1L102S, U6-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.64
Std Dev	8.405

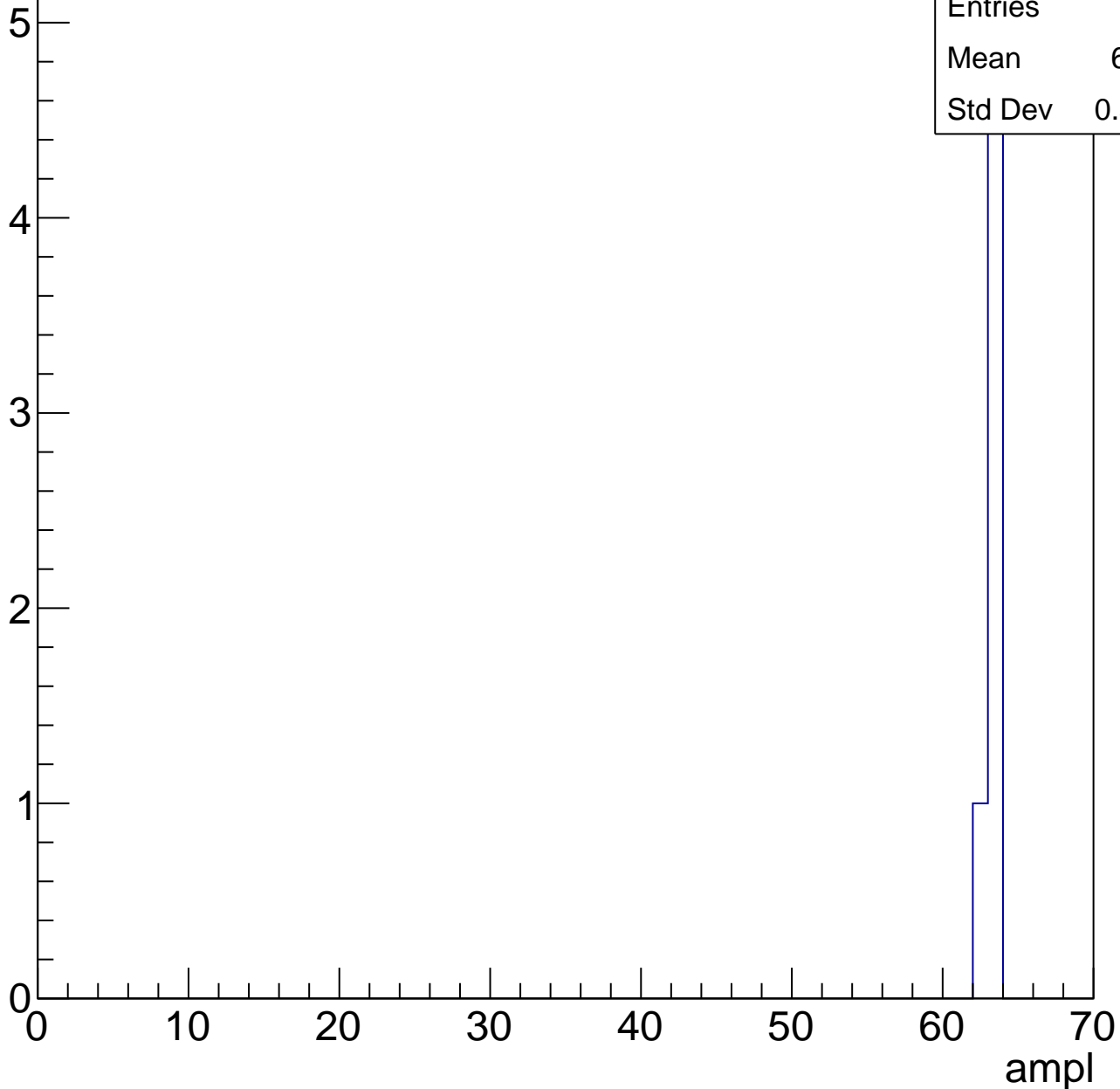


B1L102S, U6-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B1L102S, U6-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch116, adc0

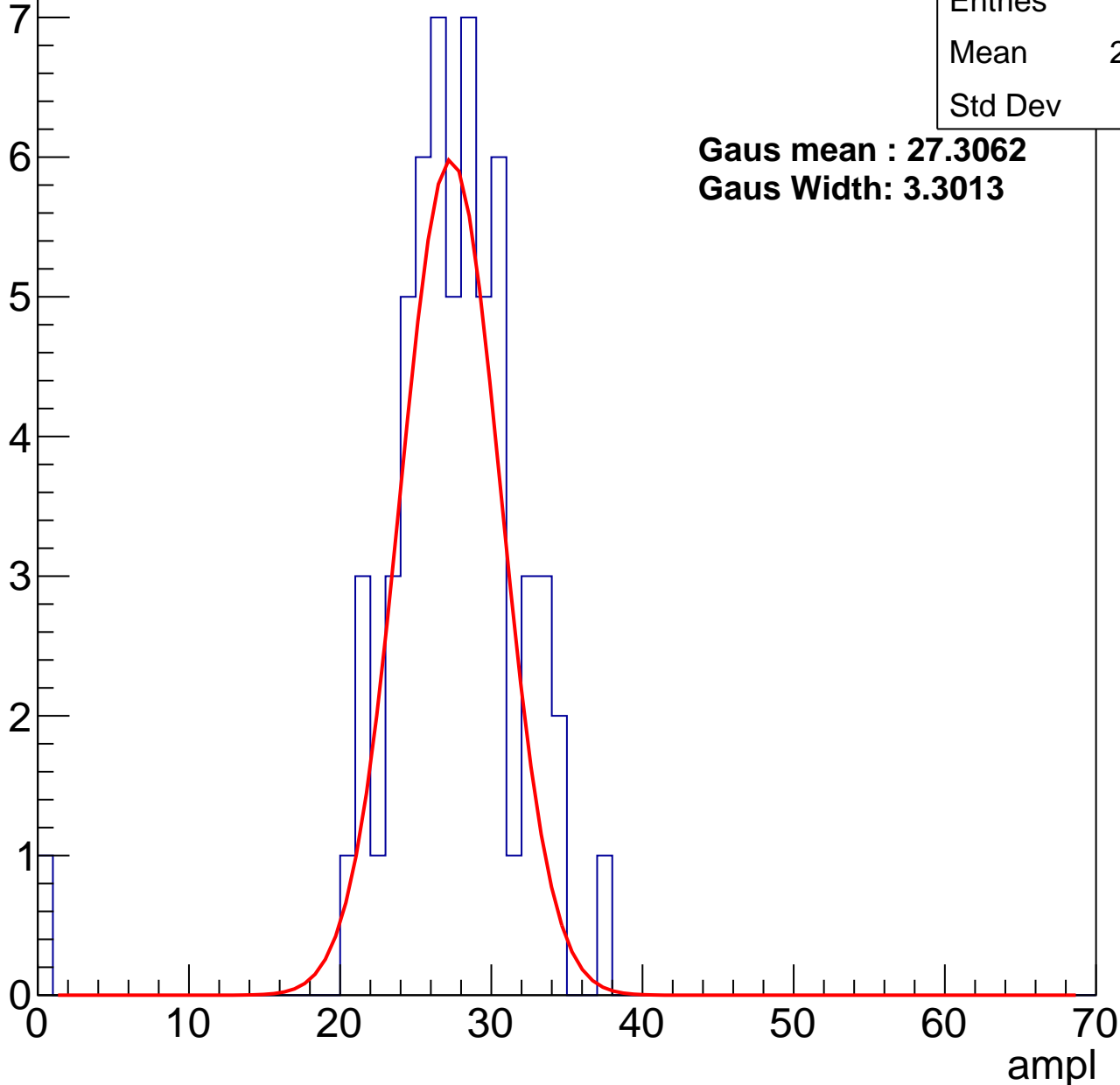
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	26.88
Std Dev	5.02

Gaus mean : 27.3062

Gaus Width: 3.3013



B1L102S, U6-ch116, adc1

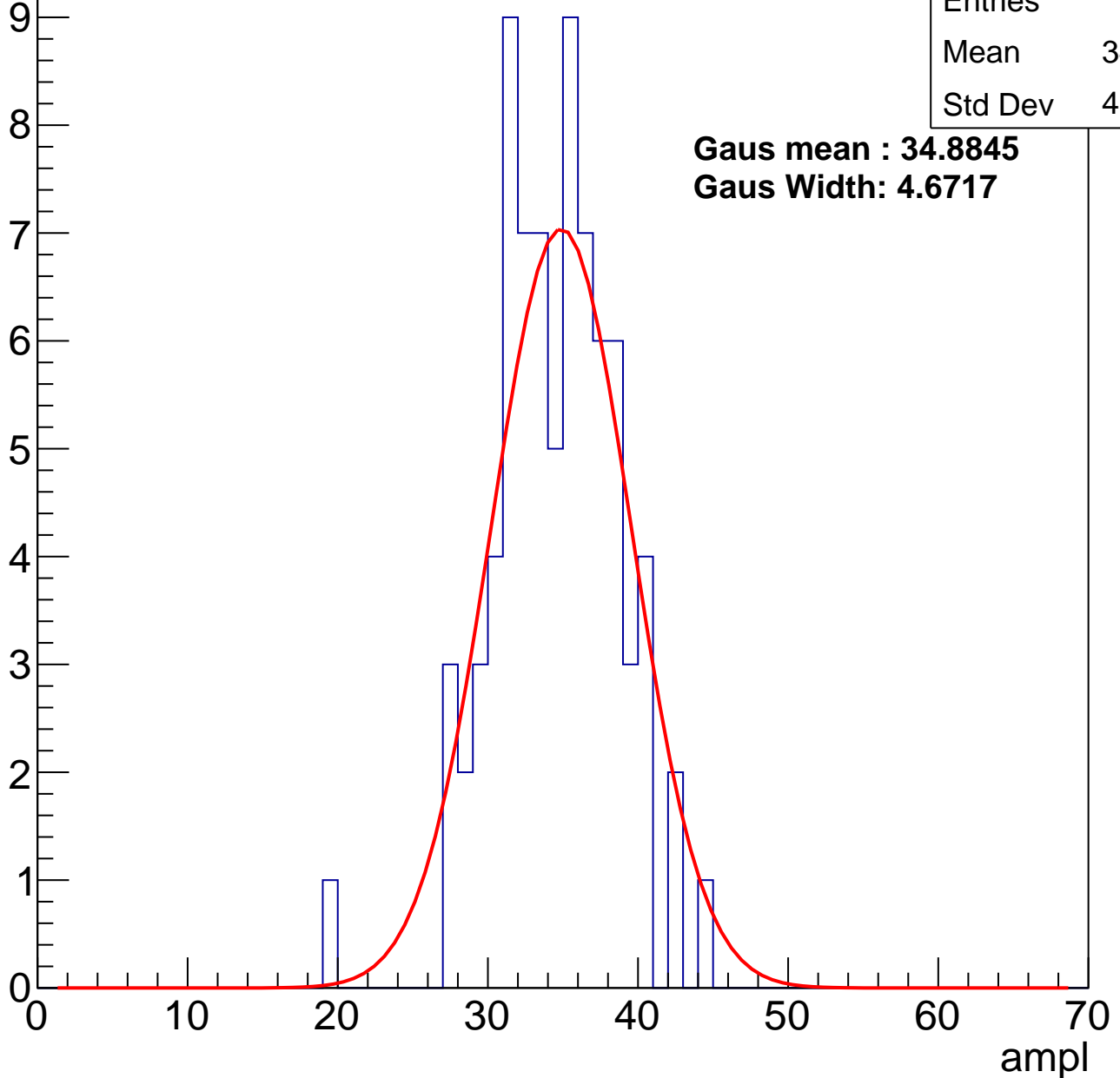
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	34.04
Std Dev	4.108

Gaus mean : 34.8845

Gaus Width: 4.6717



B1L102S, U6-ch116, adc2

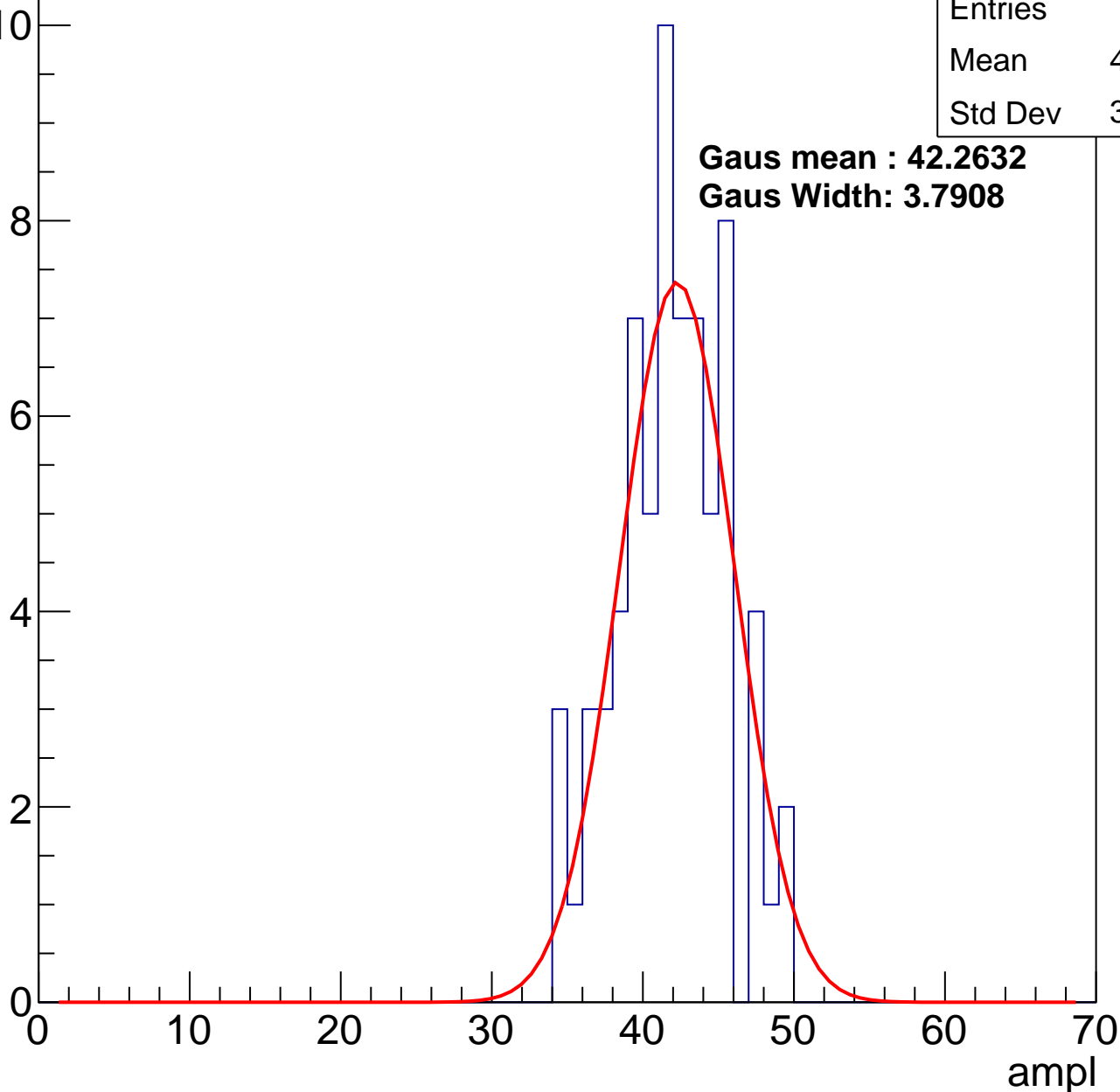
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.43
Std Dev	3.568

Gaus mean : 42.2632

Gaus Width: 3.7908

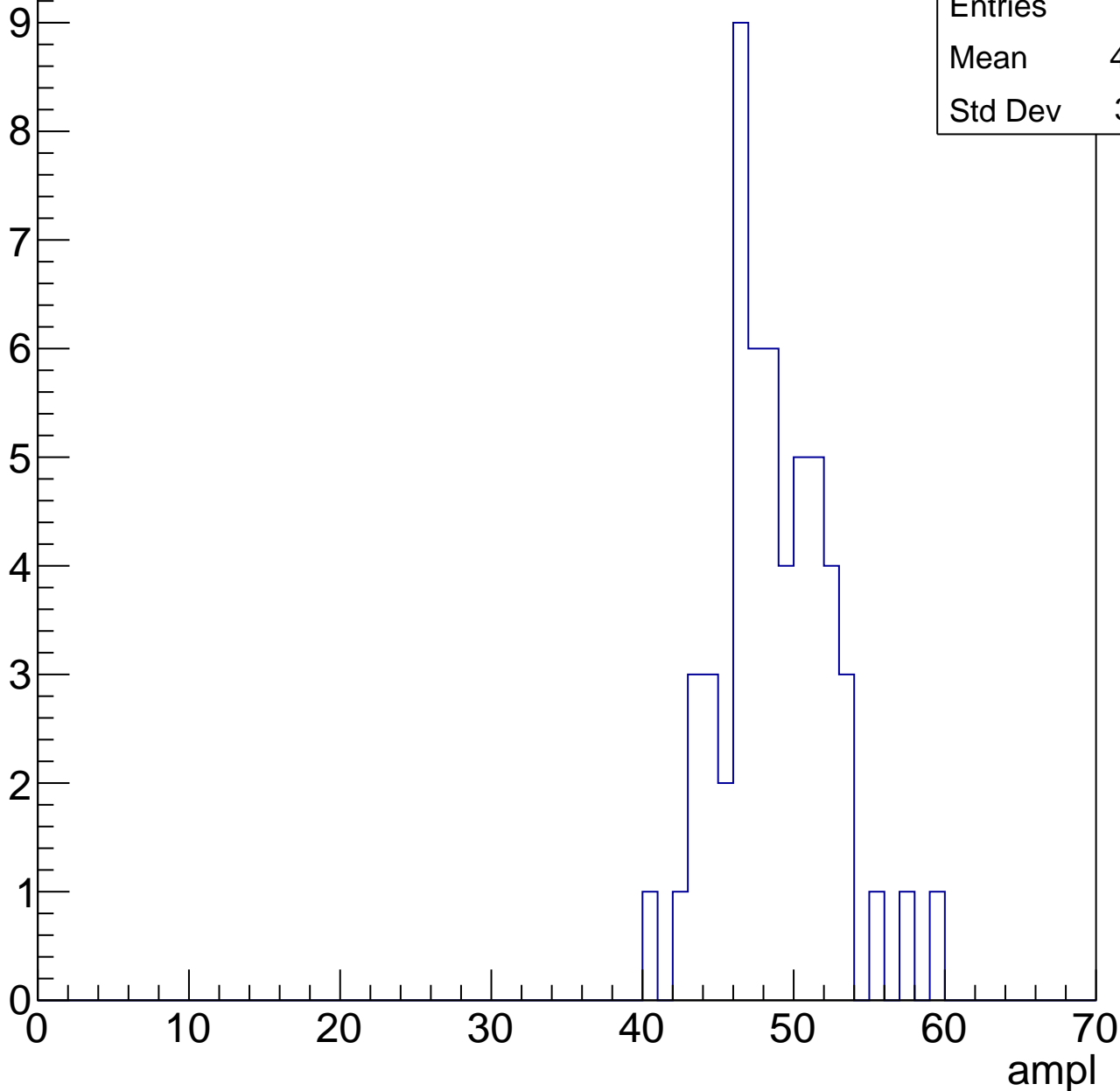


B1L102S, U6-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.29
Std Dev	3.661

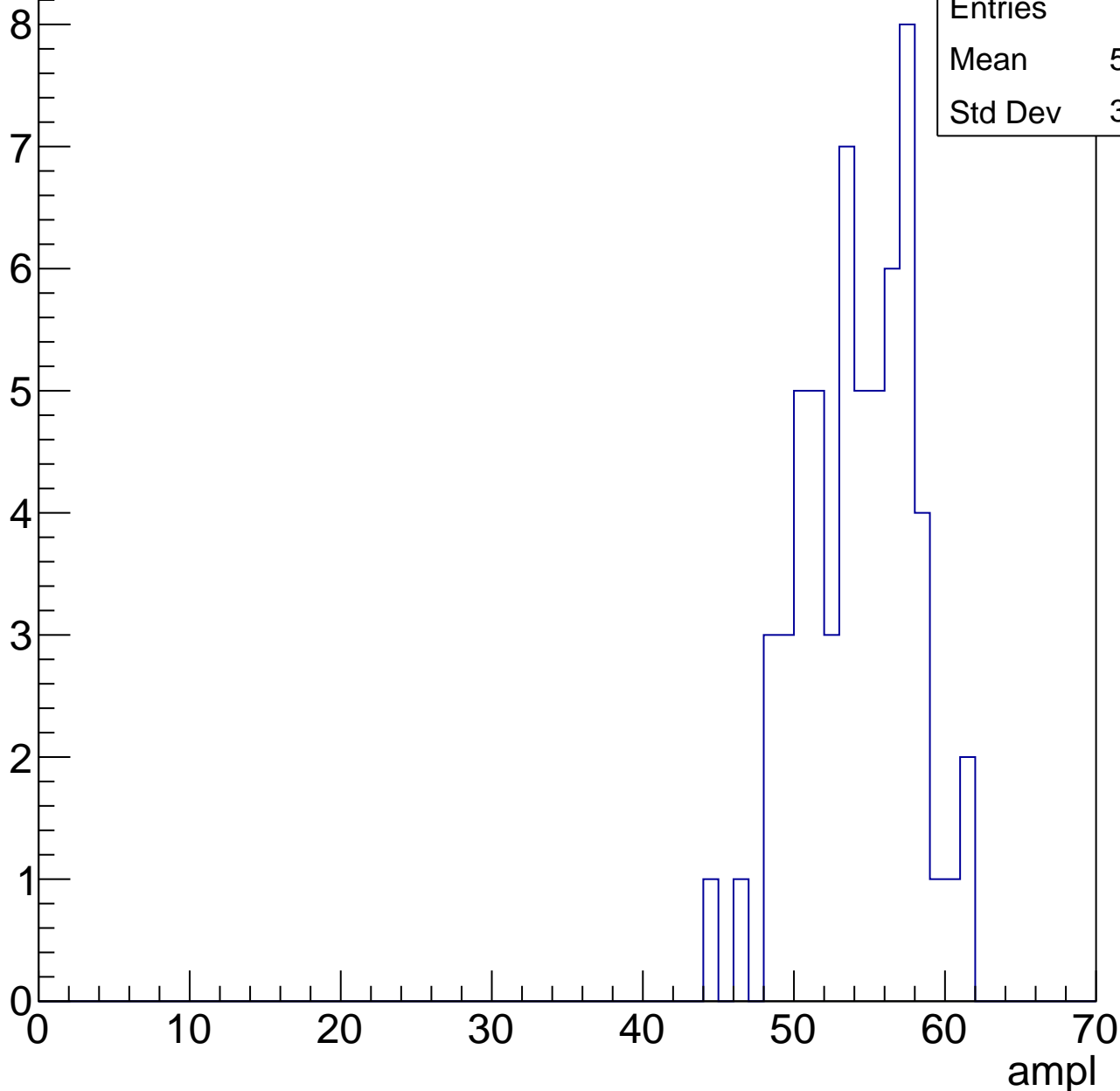


B1L102S, U6-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.72
Std Dev	3.675

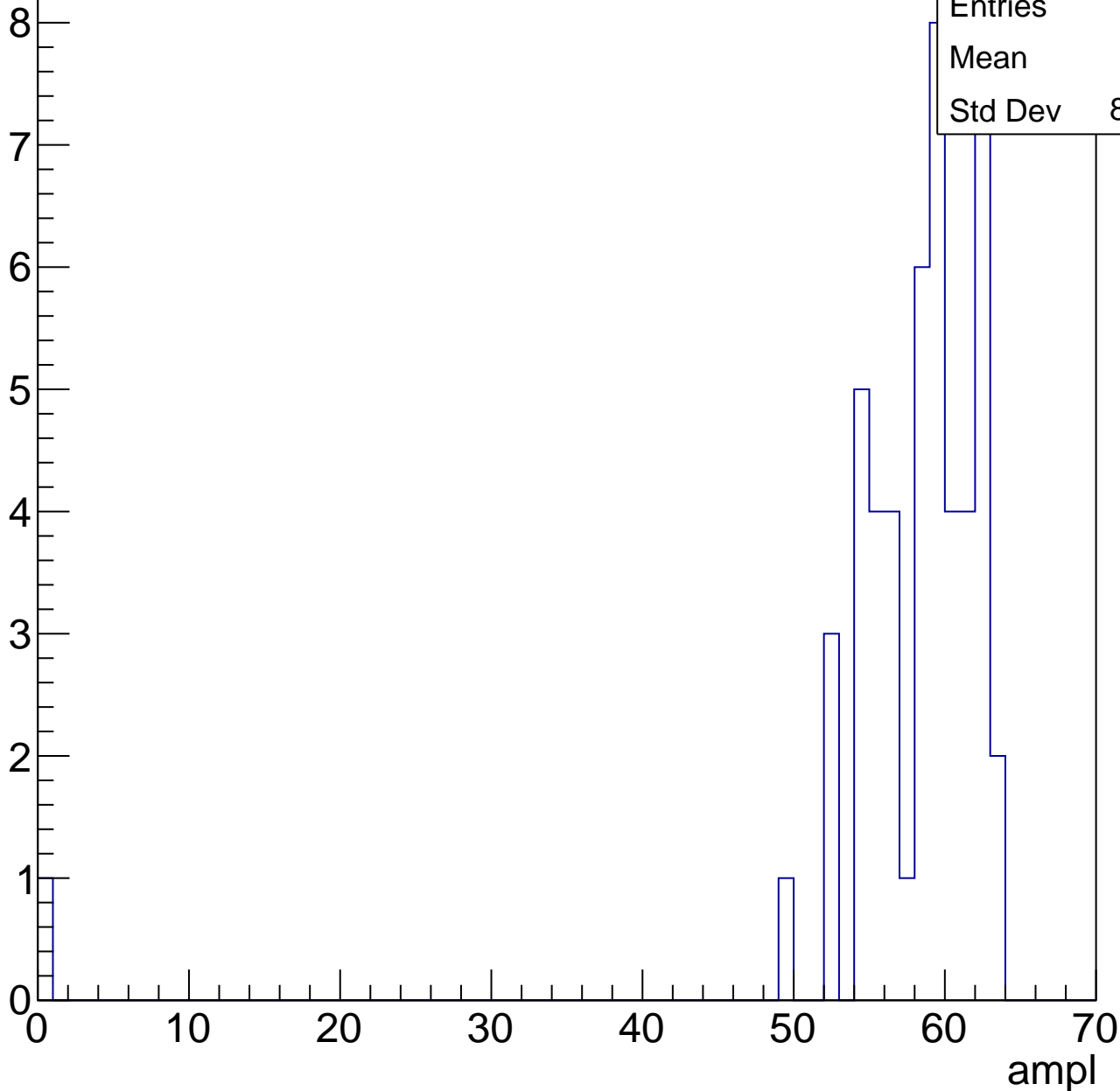


B1L102S, U6-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.9
Std Dev	8.703

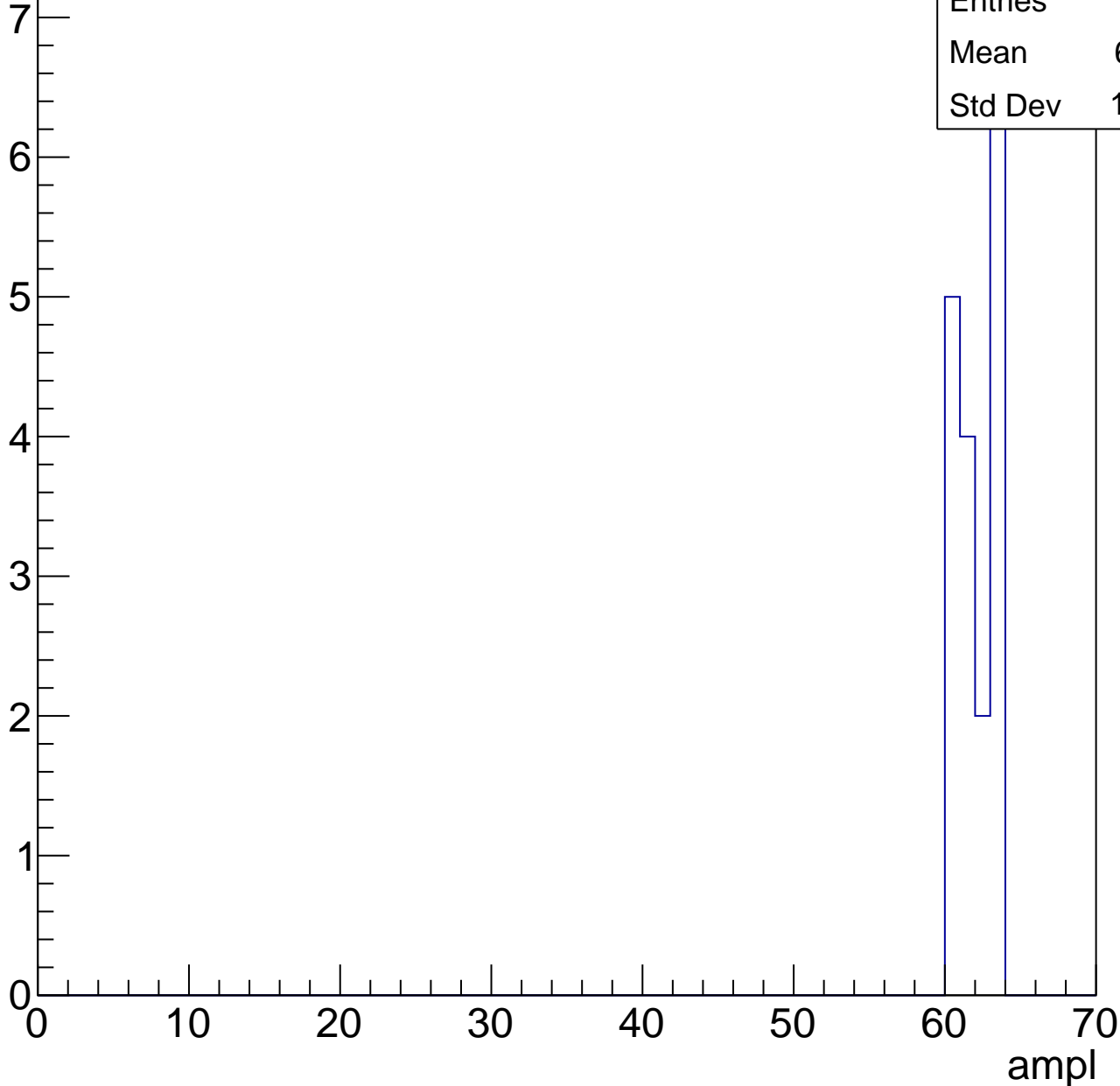


B1L102S, U6-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

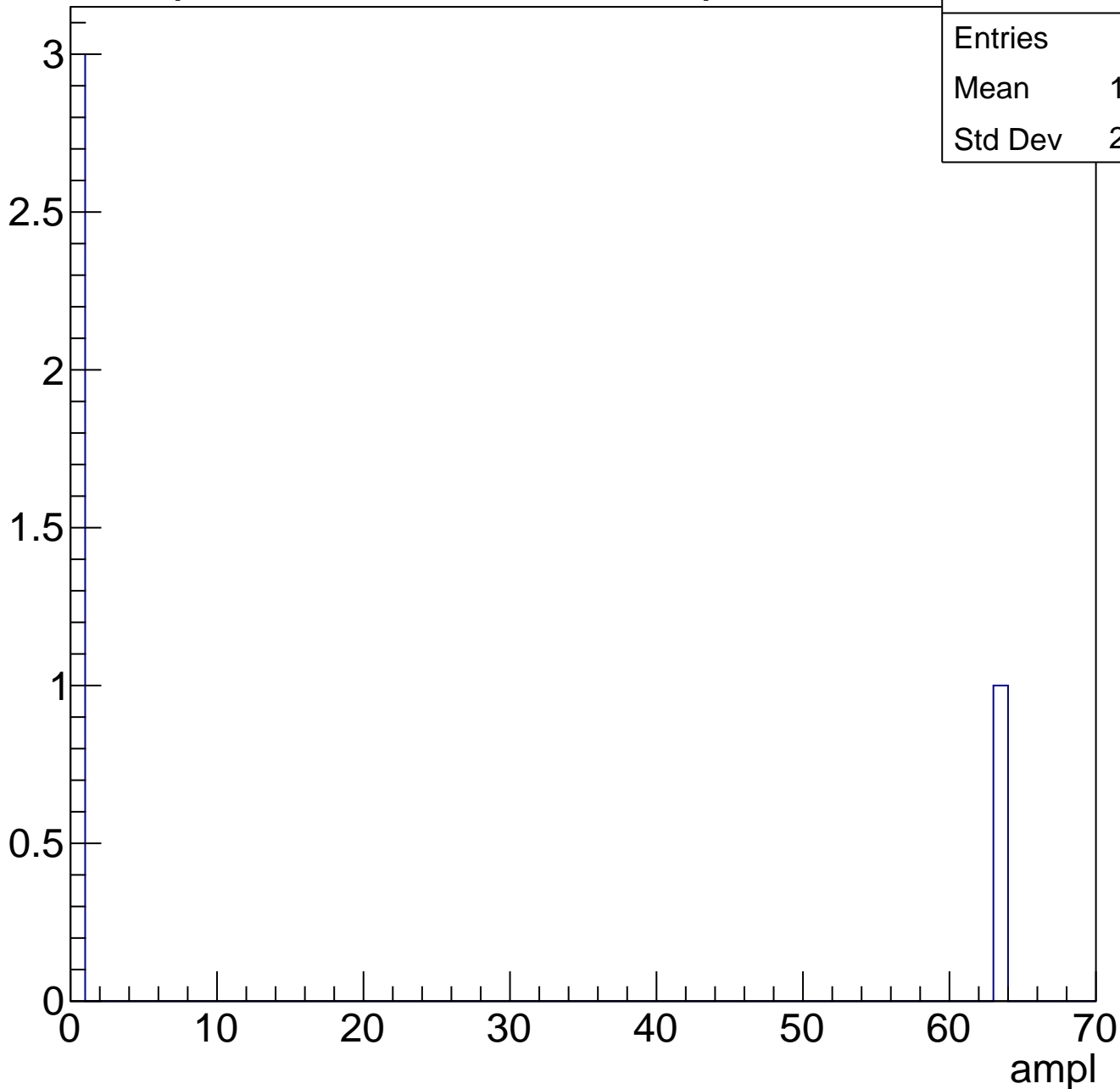
Entries	18
Mean	61.61
Std Dev	1.253



B1L102S, U6-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch117, adc0

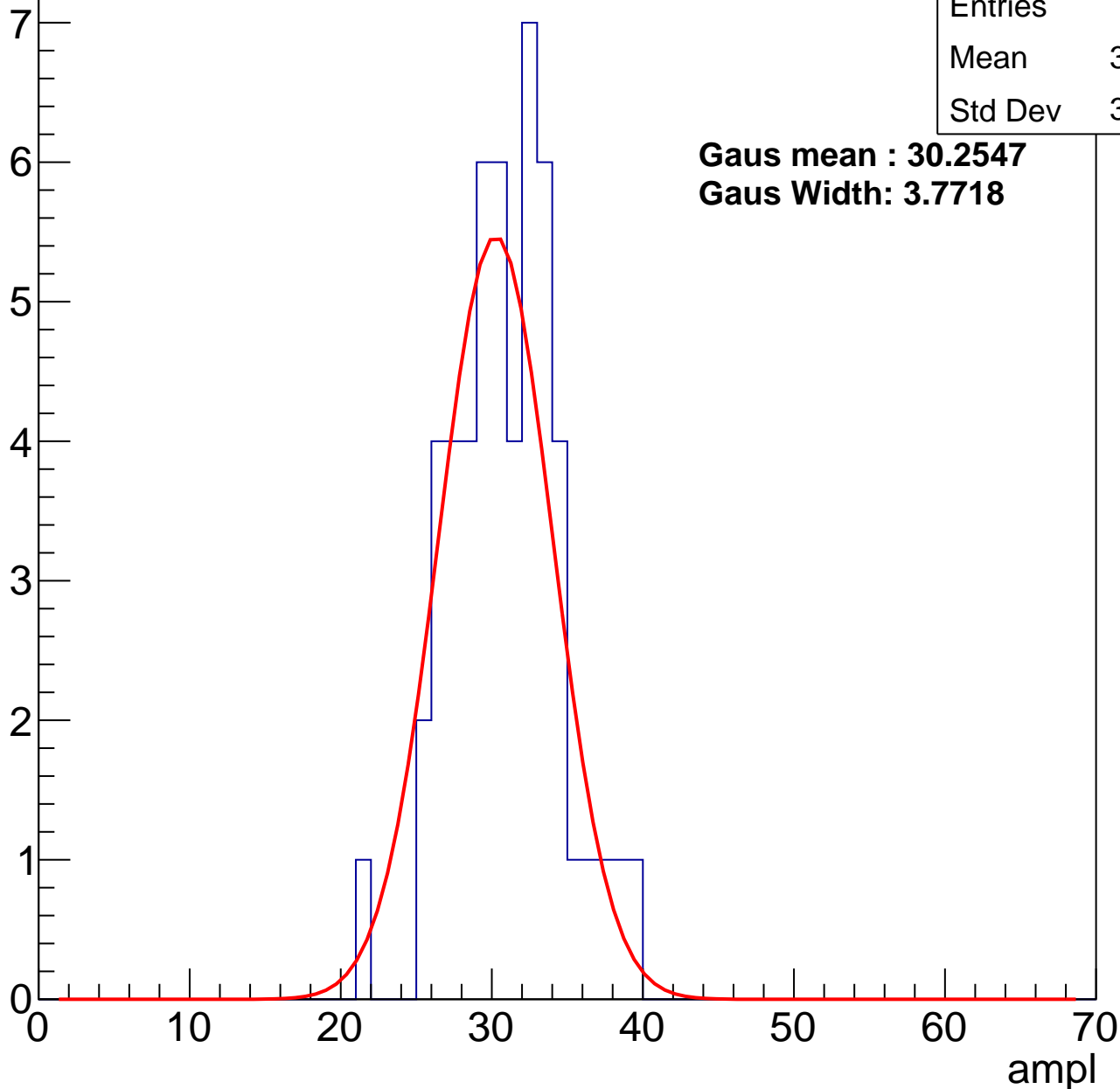
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	30.49
Std Dev	3.489

Gaus mean : 30.2547

Gaus Width: 3.7718



B1L102S, U6-ch117, adc1

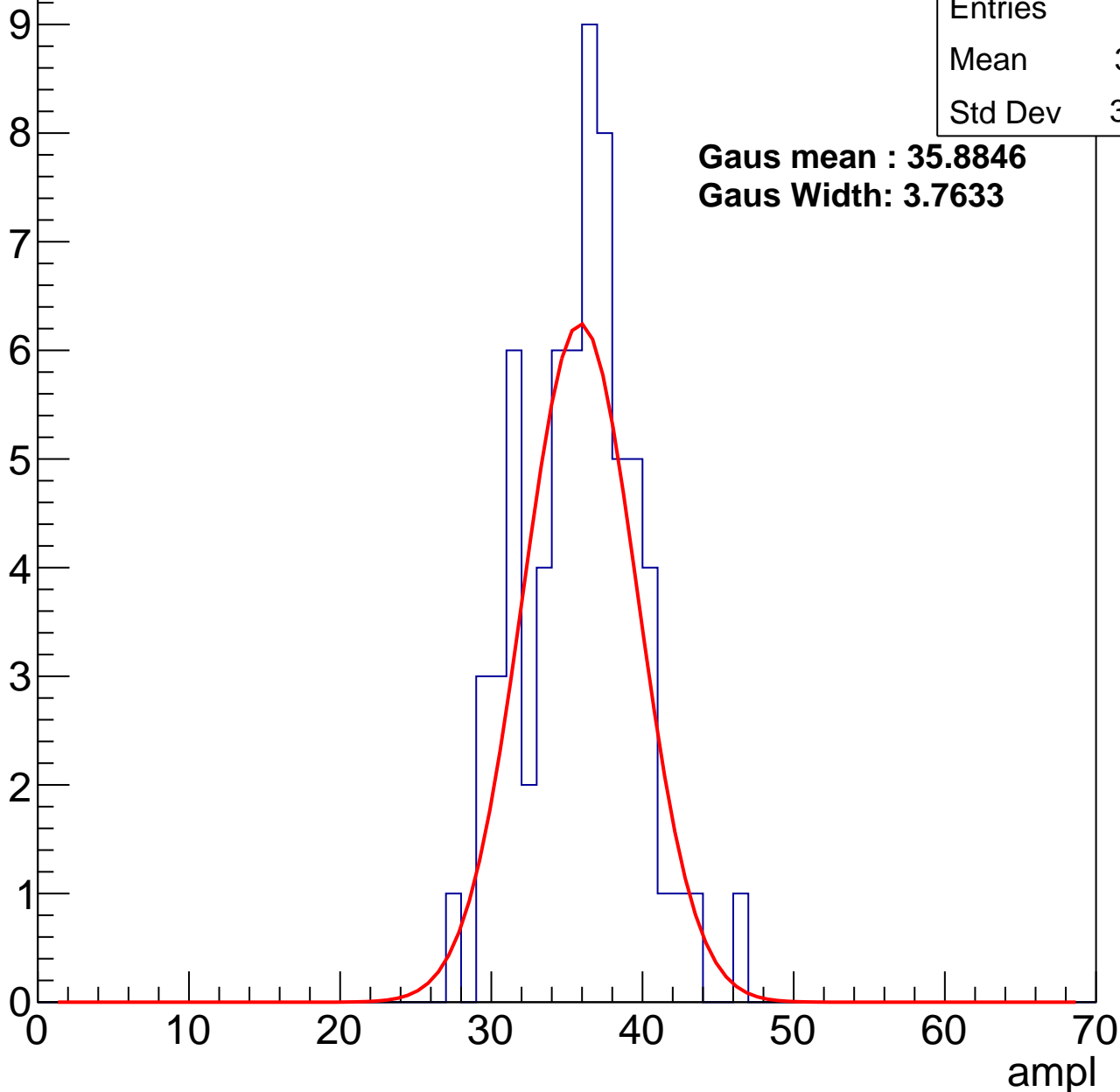
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.41
Std Dev	3.709

Gaus mean : 35.8846

Gaus Width: 3.7633



B1L102S, U6-ch117, adc2

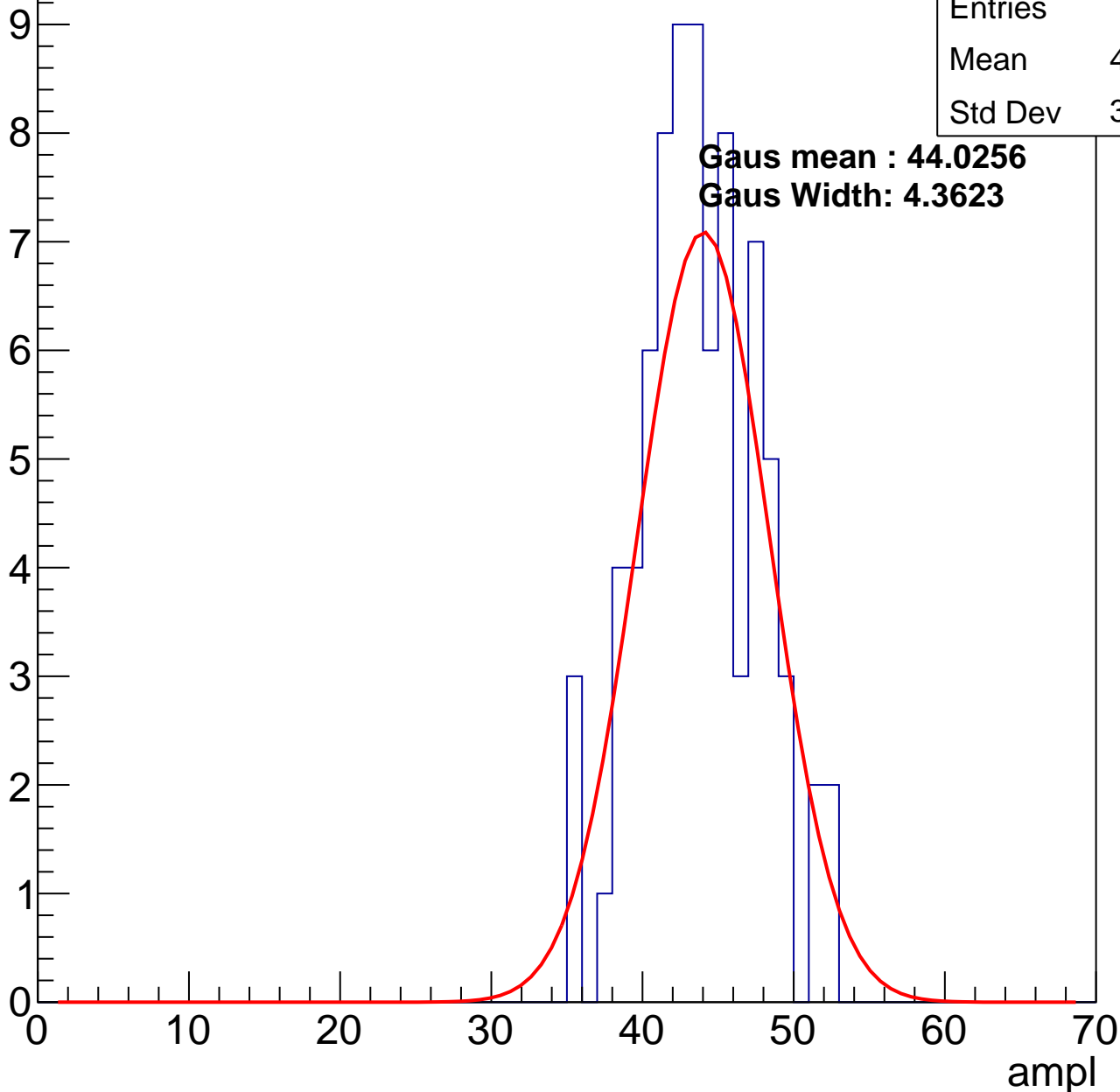
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.34
Std Dev	3.837

Gaus mean : 44.0256

Gaus Width: 4.3623



B1L102S, U6-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

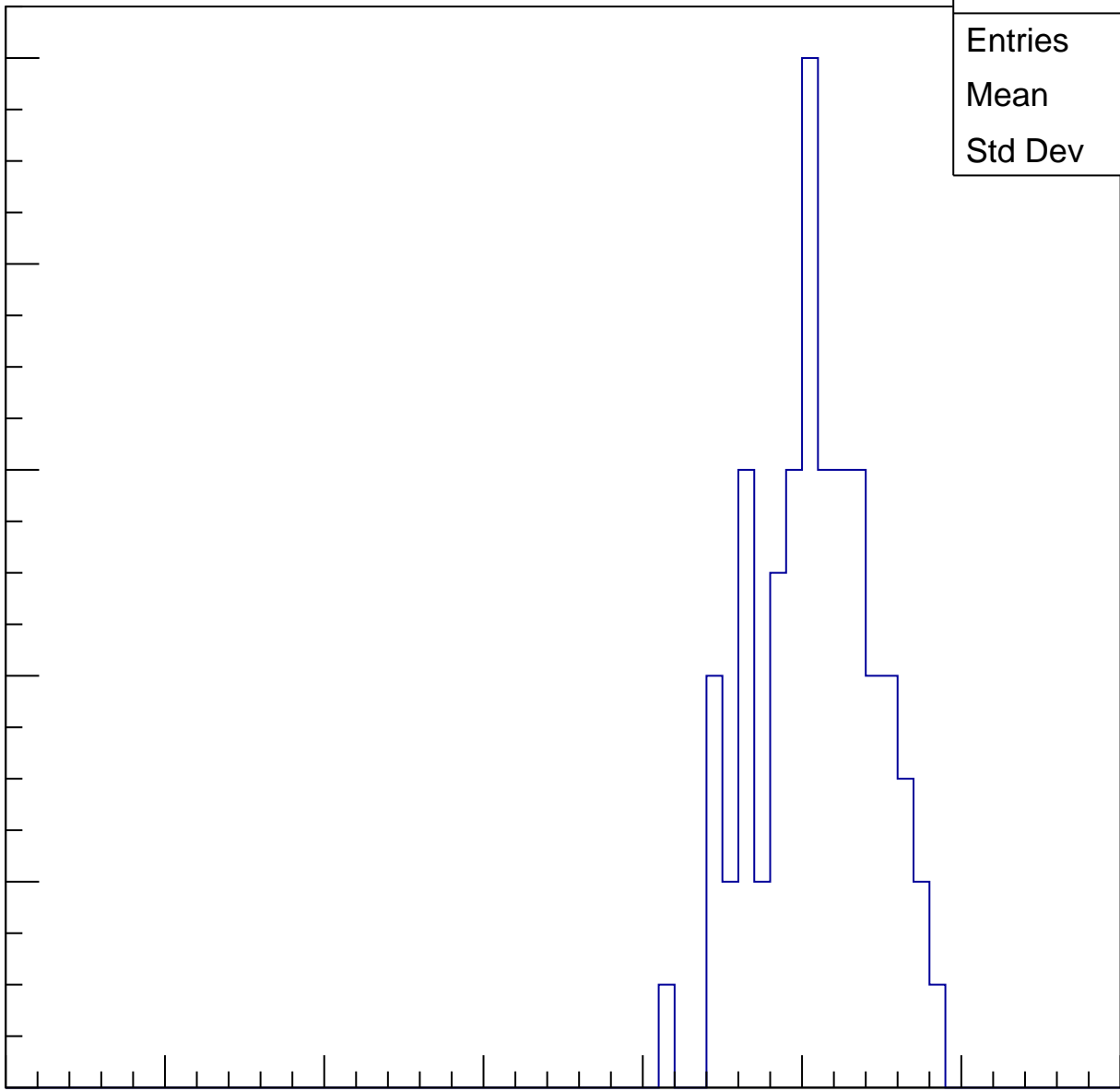
Entries	68
Mean	50.34
Std Dev	3.689

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

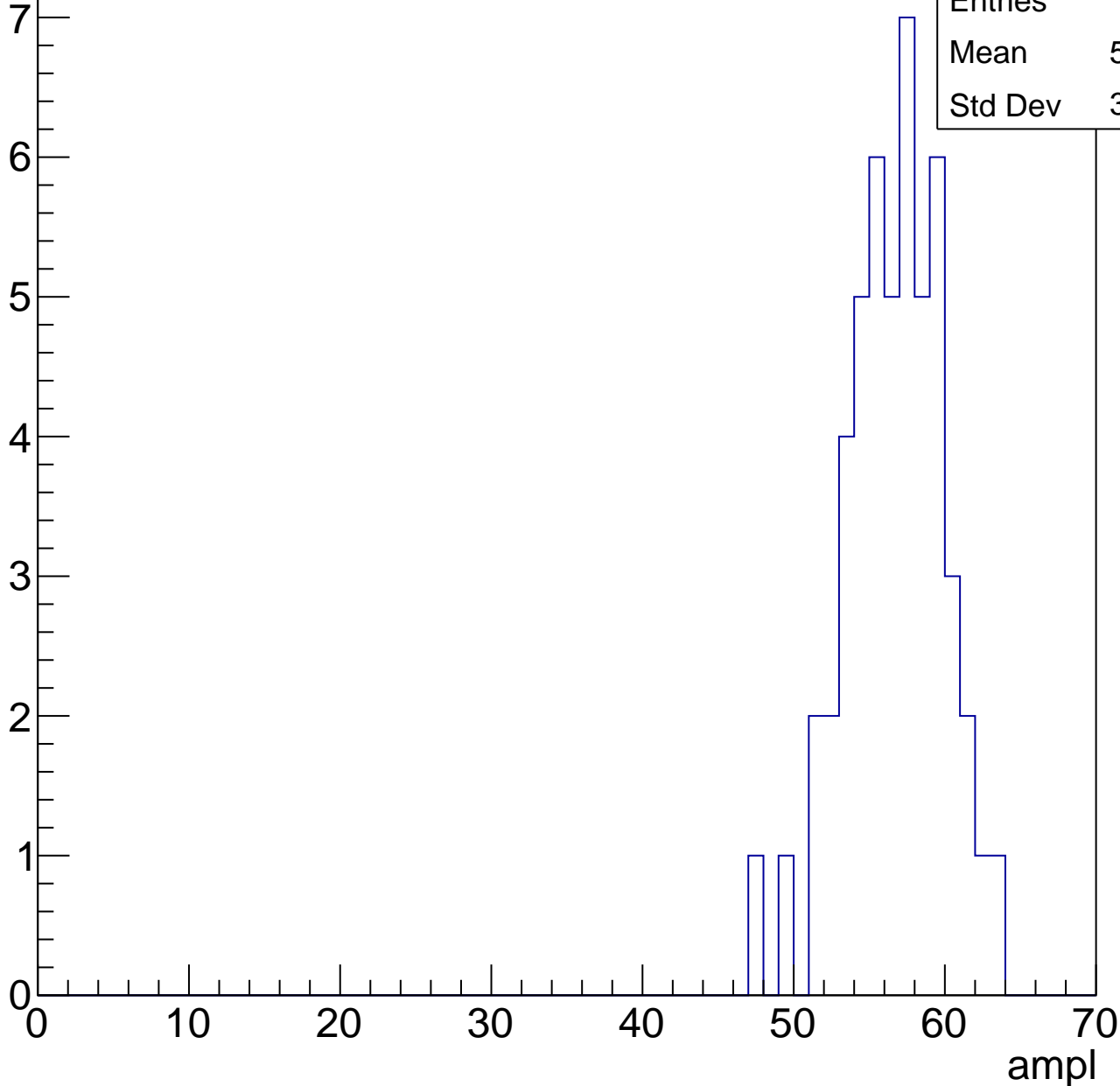


B1L102S, U6-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	56.16
Std Dev	3.244

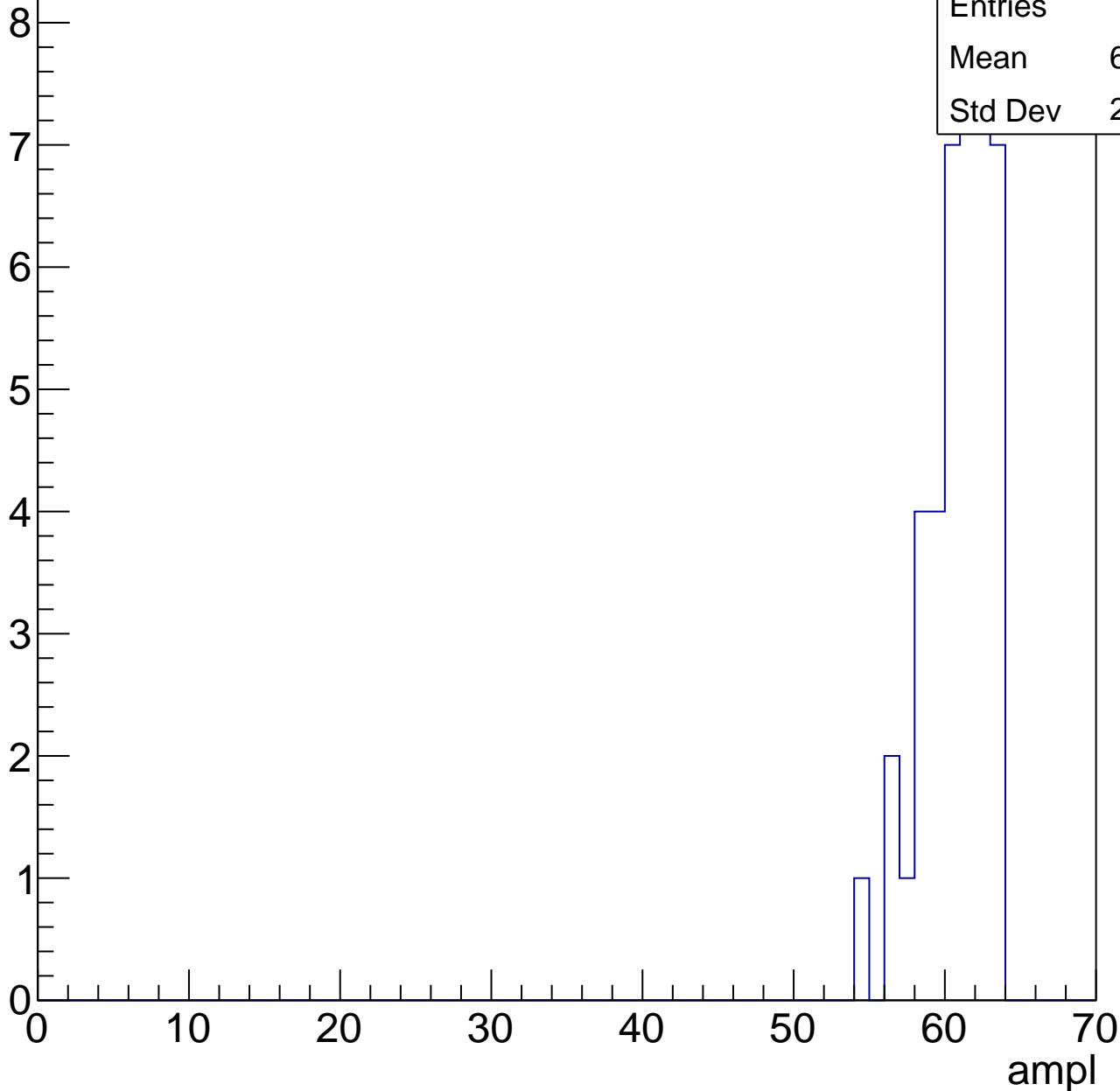


B1L102S, U6-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	60.38
Std Dev	2.149



B1L102S, U6-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	47
Std Dev	27.14

ampl

B1L102S, U6-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L102S, U6-ch118, adc0

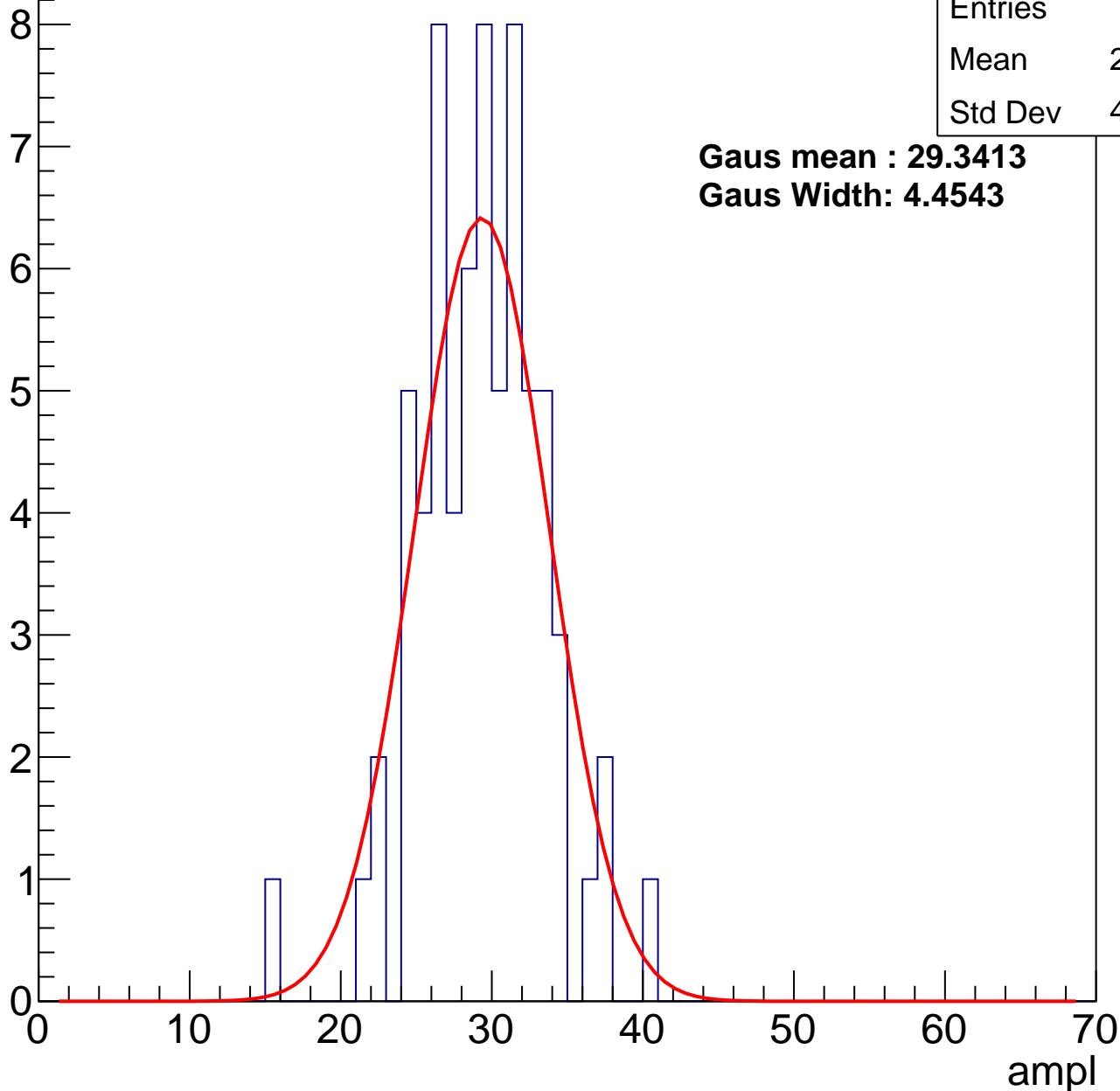
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.86
Std Dev	4.126

Gaus mean : 29.3413

Gaus Width: 4.4543



B1L102S, U6-ch118, adc1

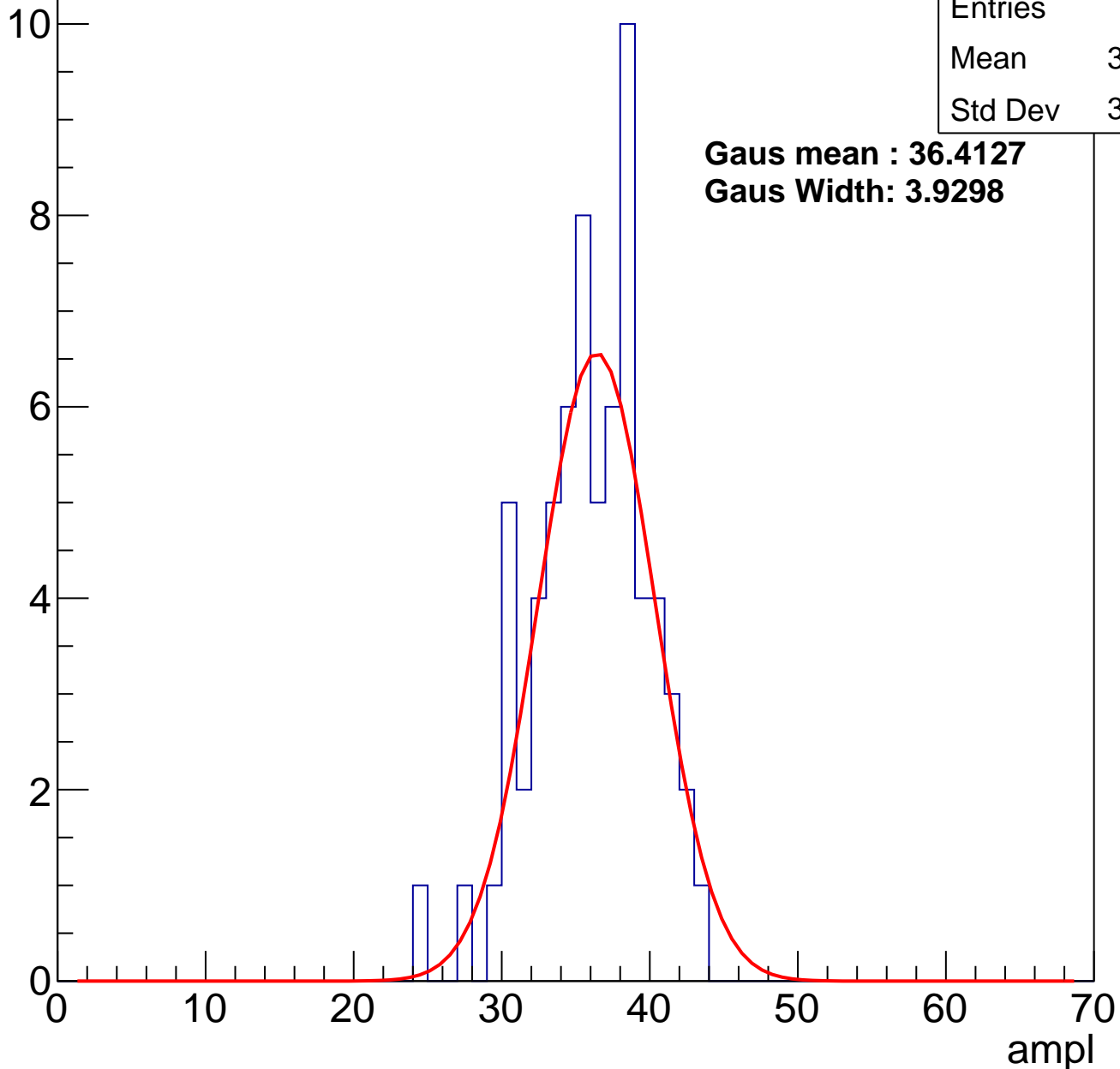
calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	35.54
Std Dev	3.782

Gaus mean : 36.4127

Gaus Width: 3.9298

Entry



B1L102S, U6-ch118, adc2

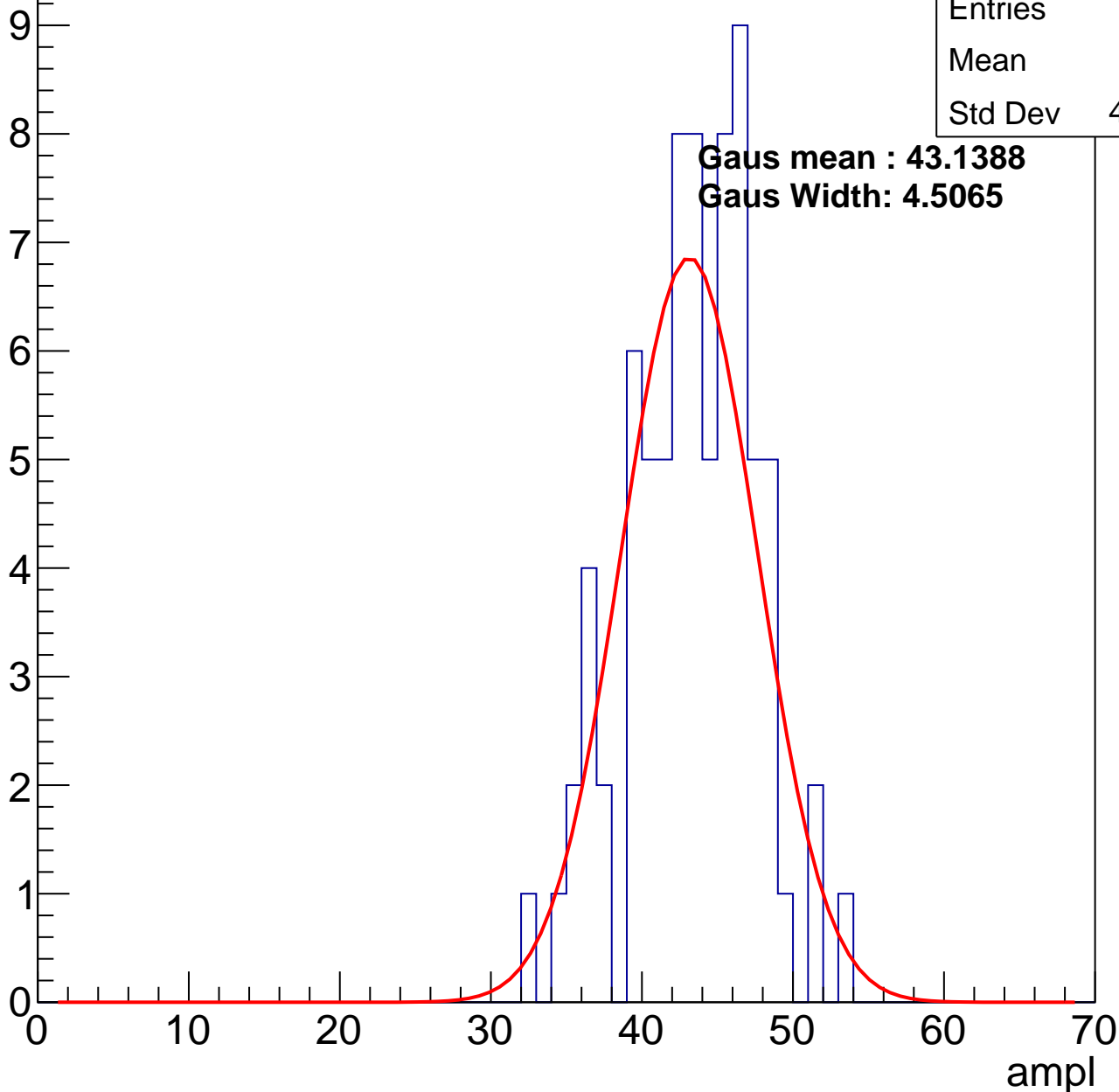
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	42.9
Std Dev	4.165

Gaus mean : 43.1388

Gaus Width: 4.5065

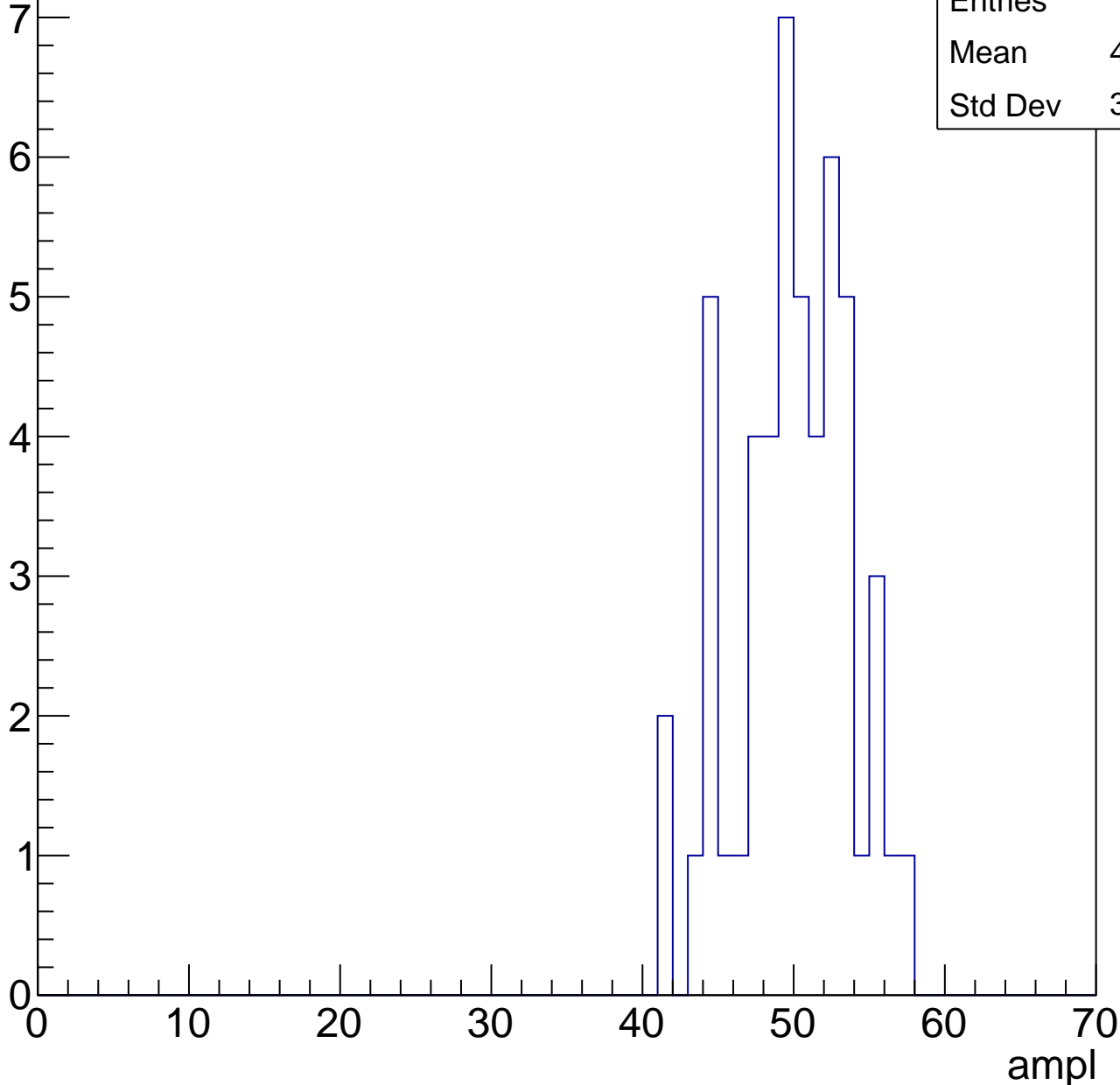


B1L102S, U6-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.45
Std Dev	3.769

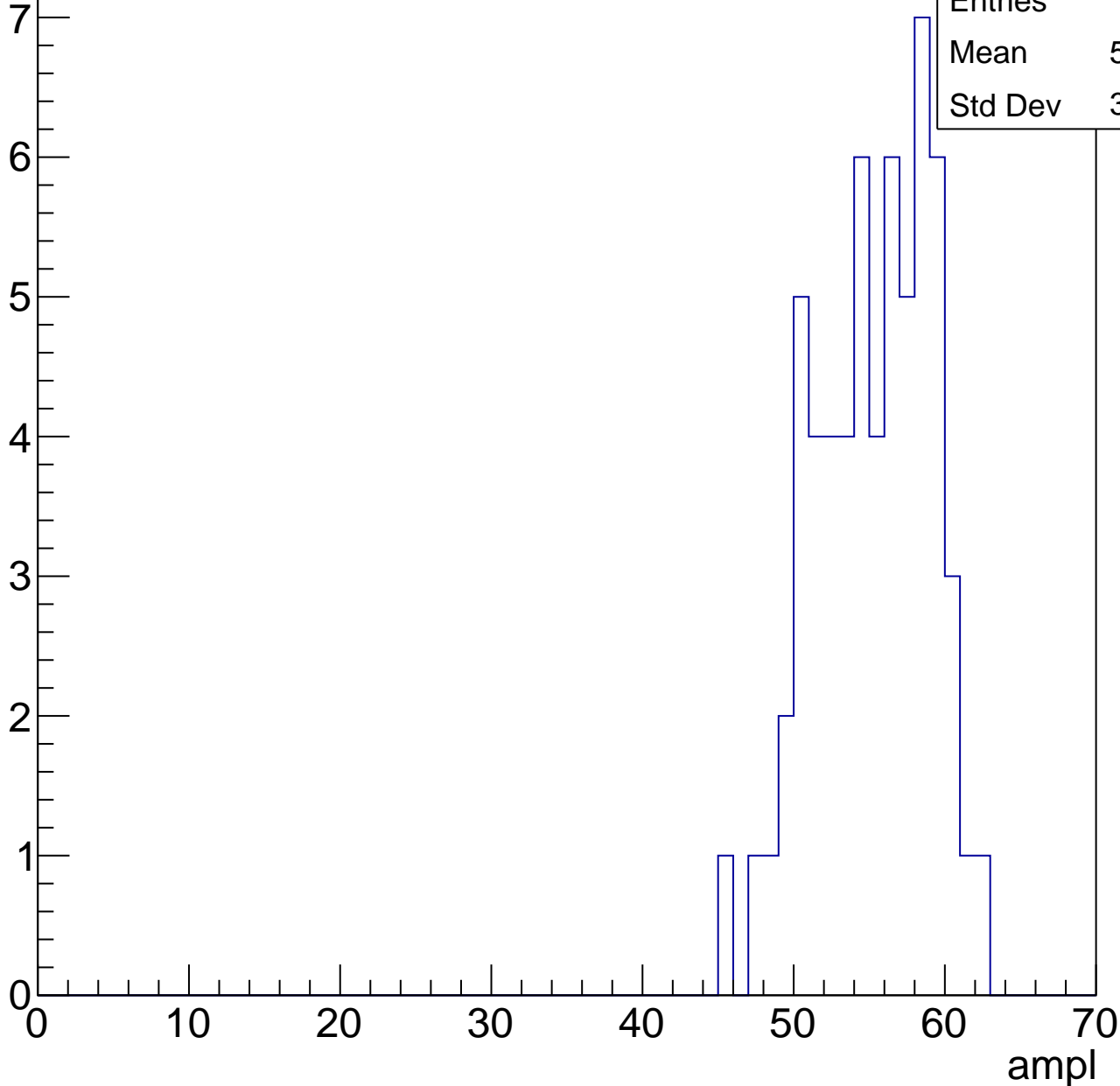


B1L102S, U6-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.75
Std Dev	3.784

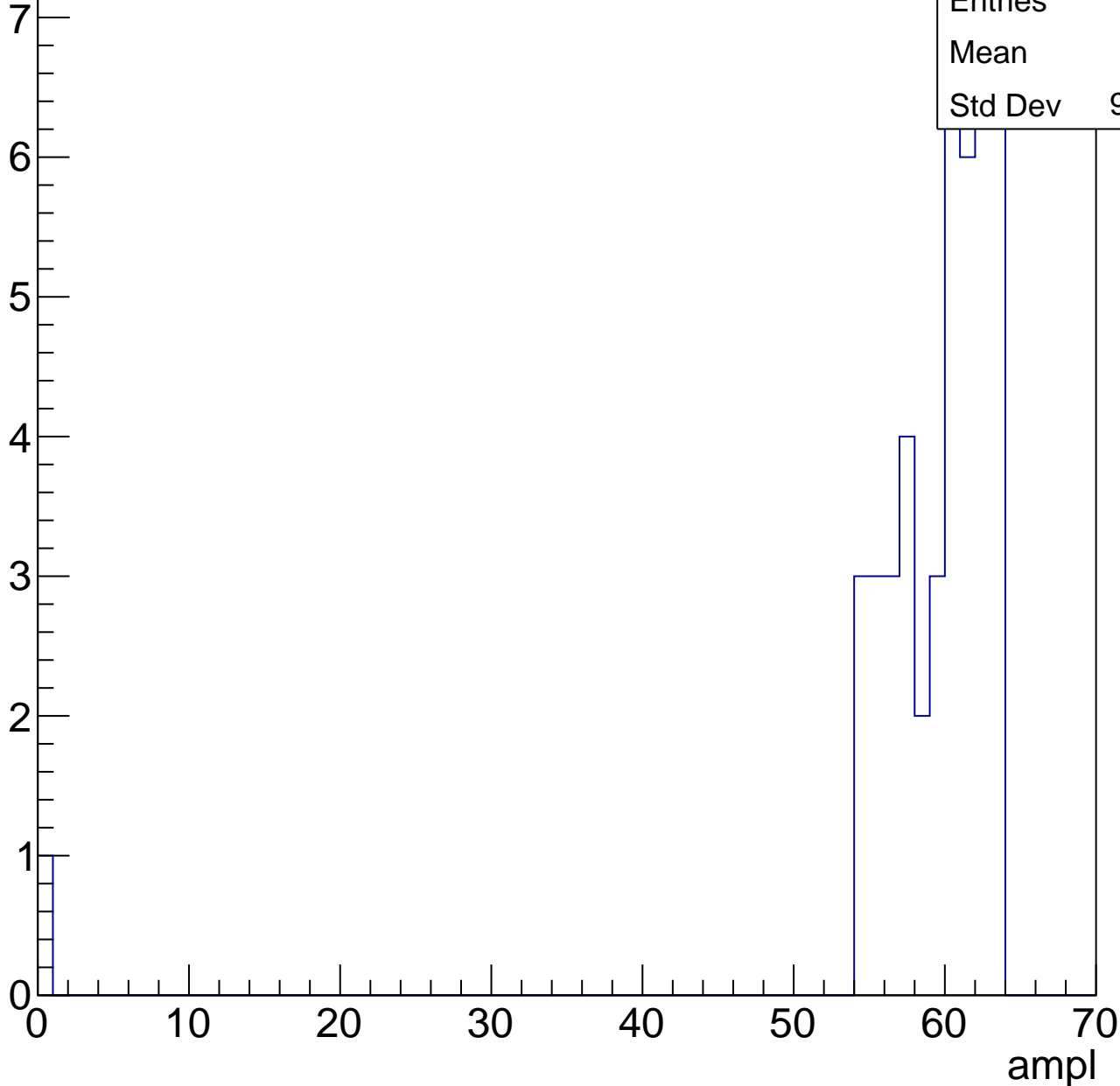


B1L102S, U6-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

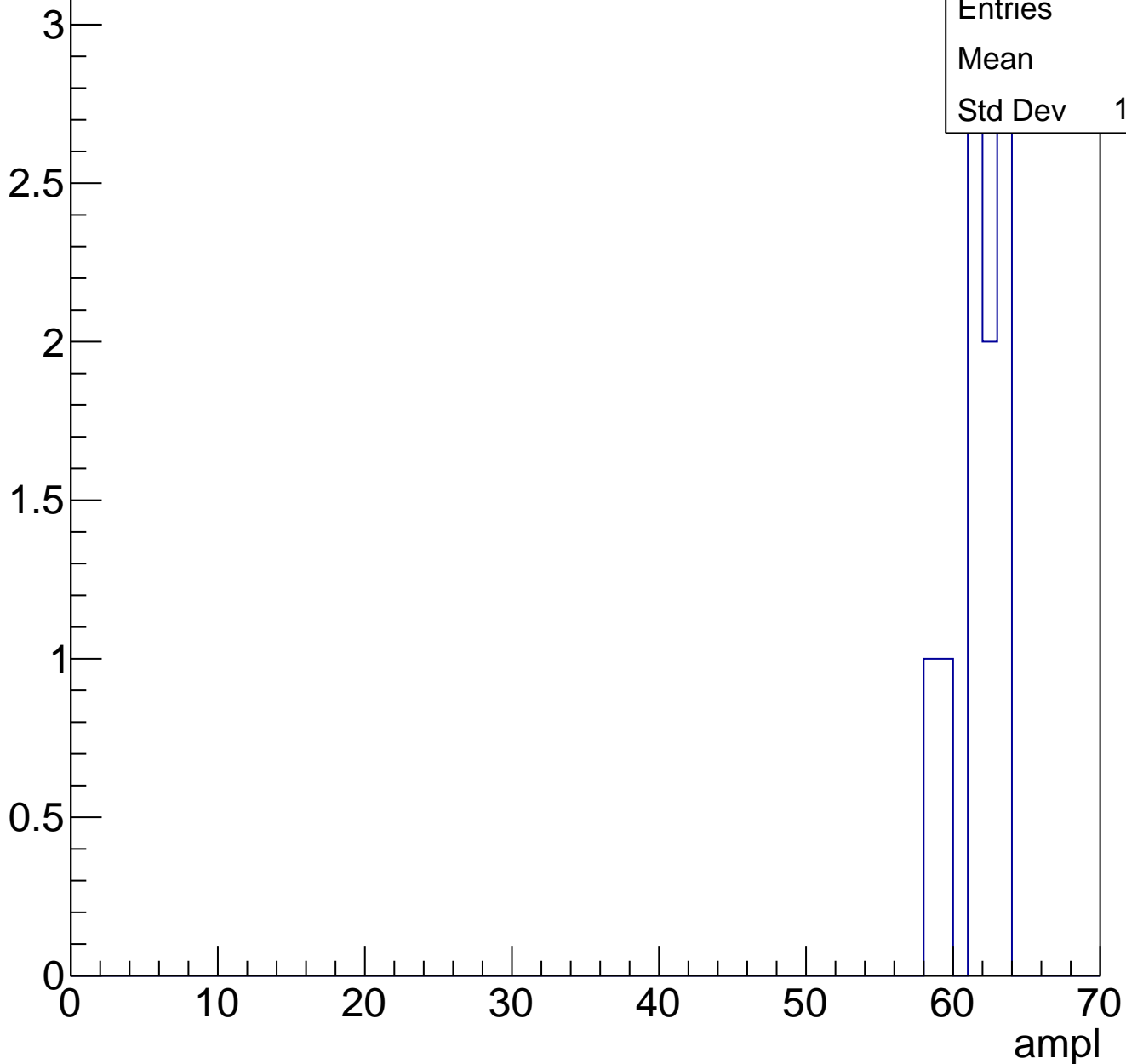
Entries	46
Mean	58.2
Std Dev	9.119



B1L102S, U6-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U6-ch119, adc0

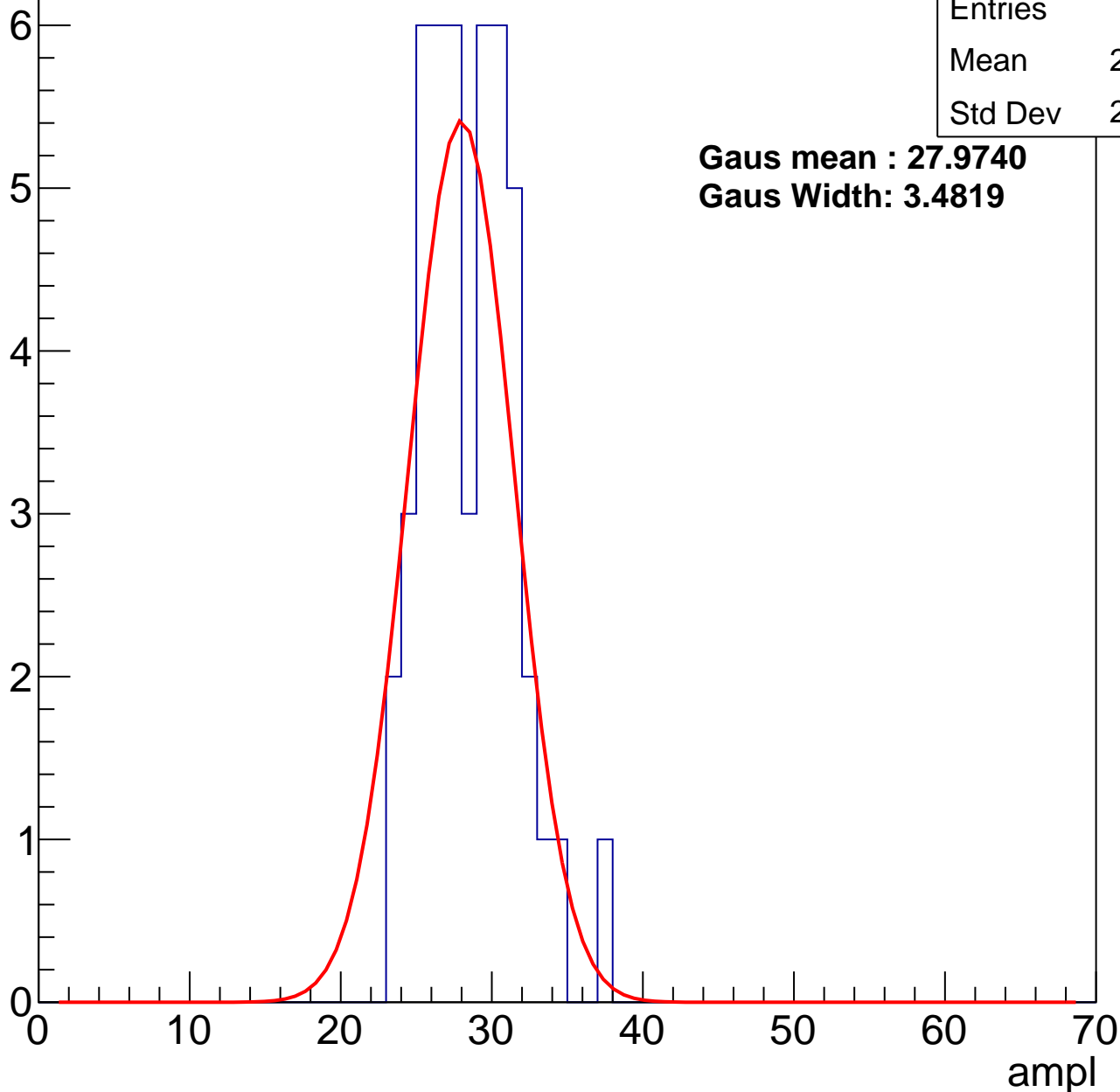
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	28.06
Std Dev	2.996

Gaus mean : 27.9740

Gaus Width: 3.4819

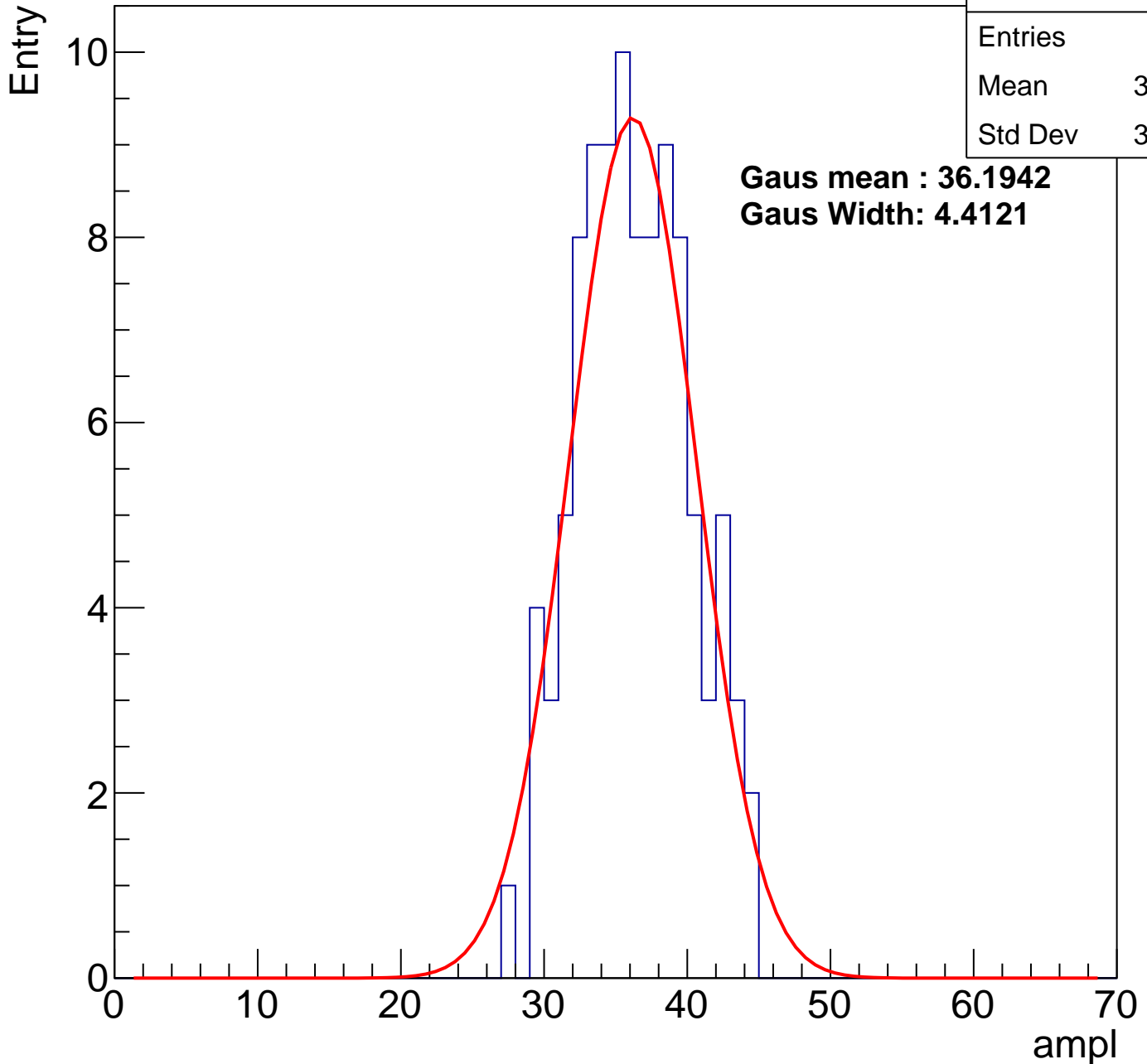


B1L102S, U6-ch119, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	100
Mean	35.85
Std Dev	3.858

Gaus mean : 36.1942
Gaus Width: 4.4121



B1L102S, U6-ch119, adc2

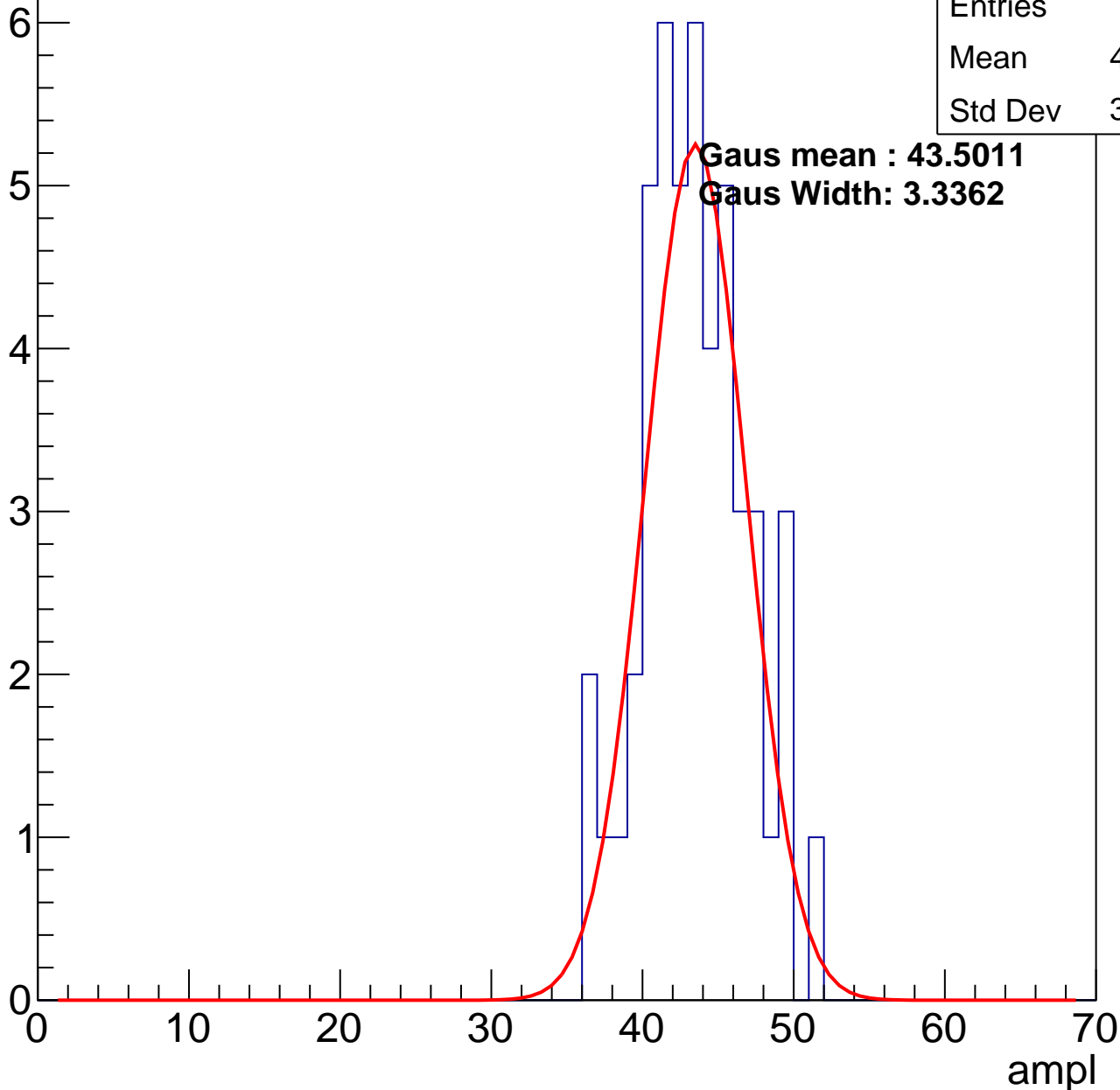
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	43.02
Std Dev	3.419

Gaus mean : 43.5011

Gaus Width: 3.3362

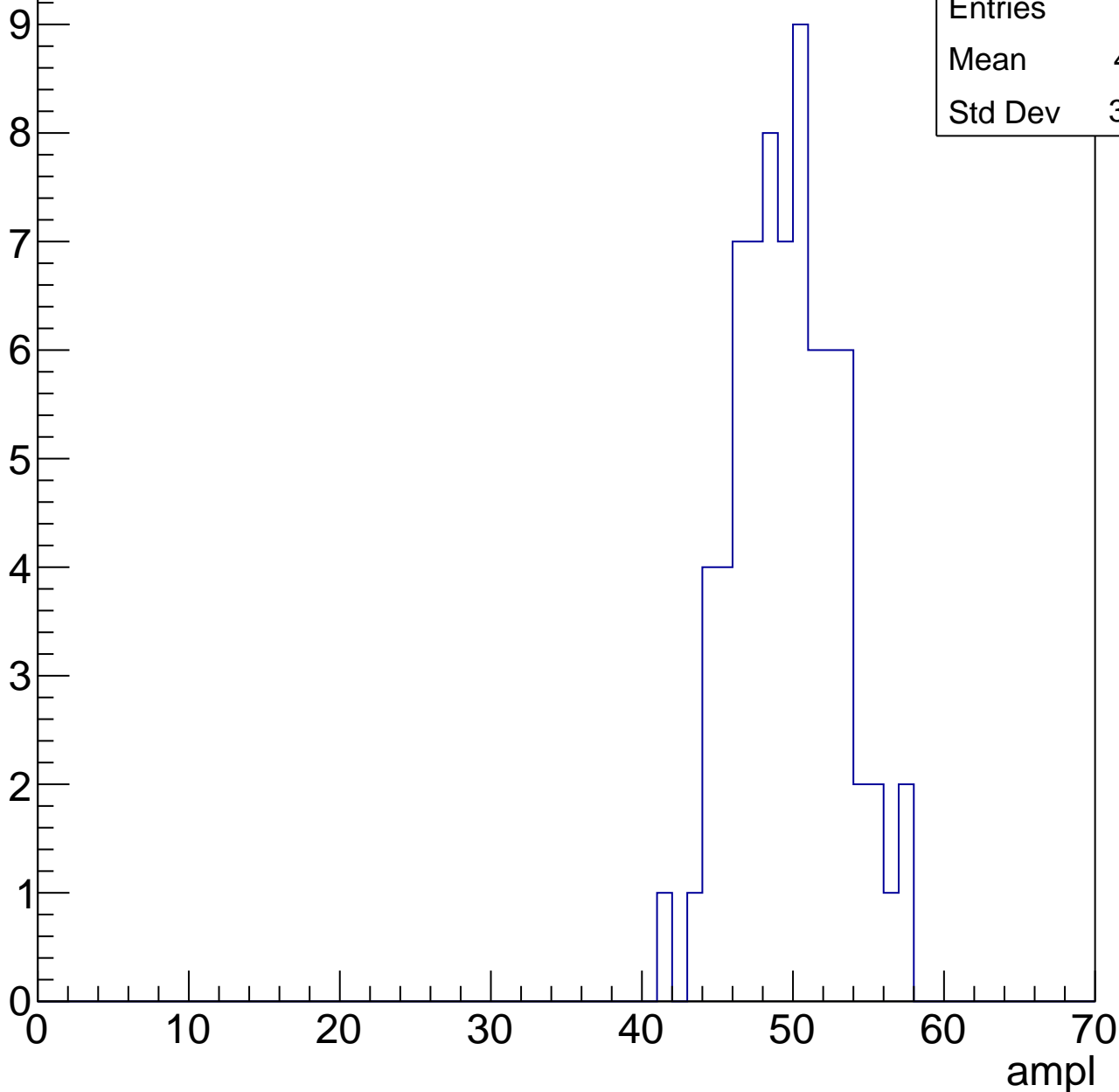


B1L102S, U6-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	49.21
Std Dev	3.396

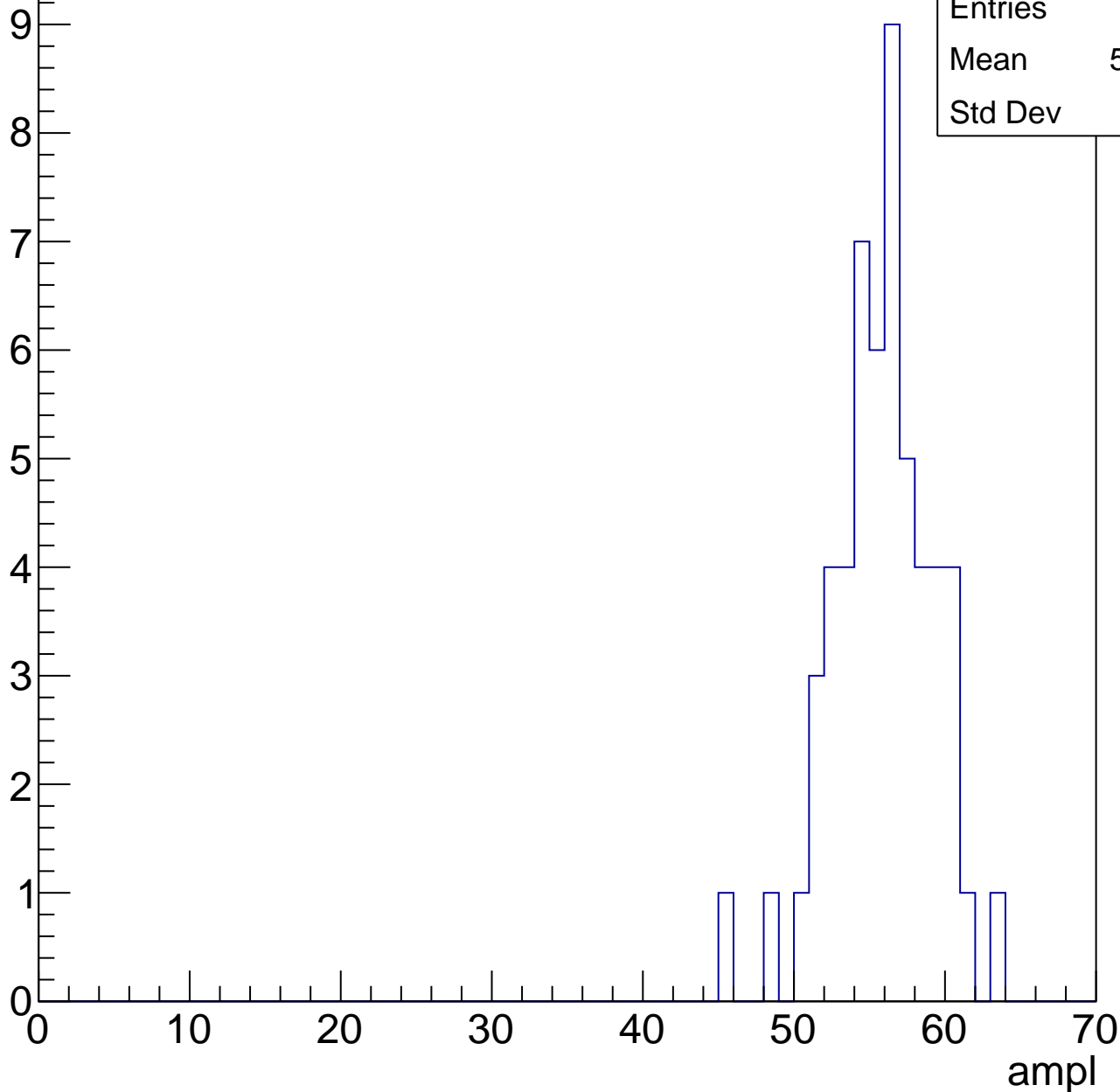


B1L102S, U6-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.36
Std Dev	3.31

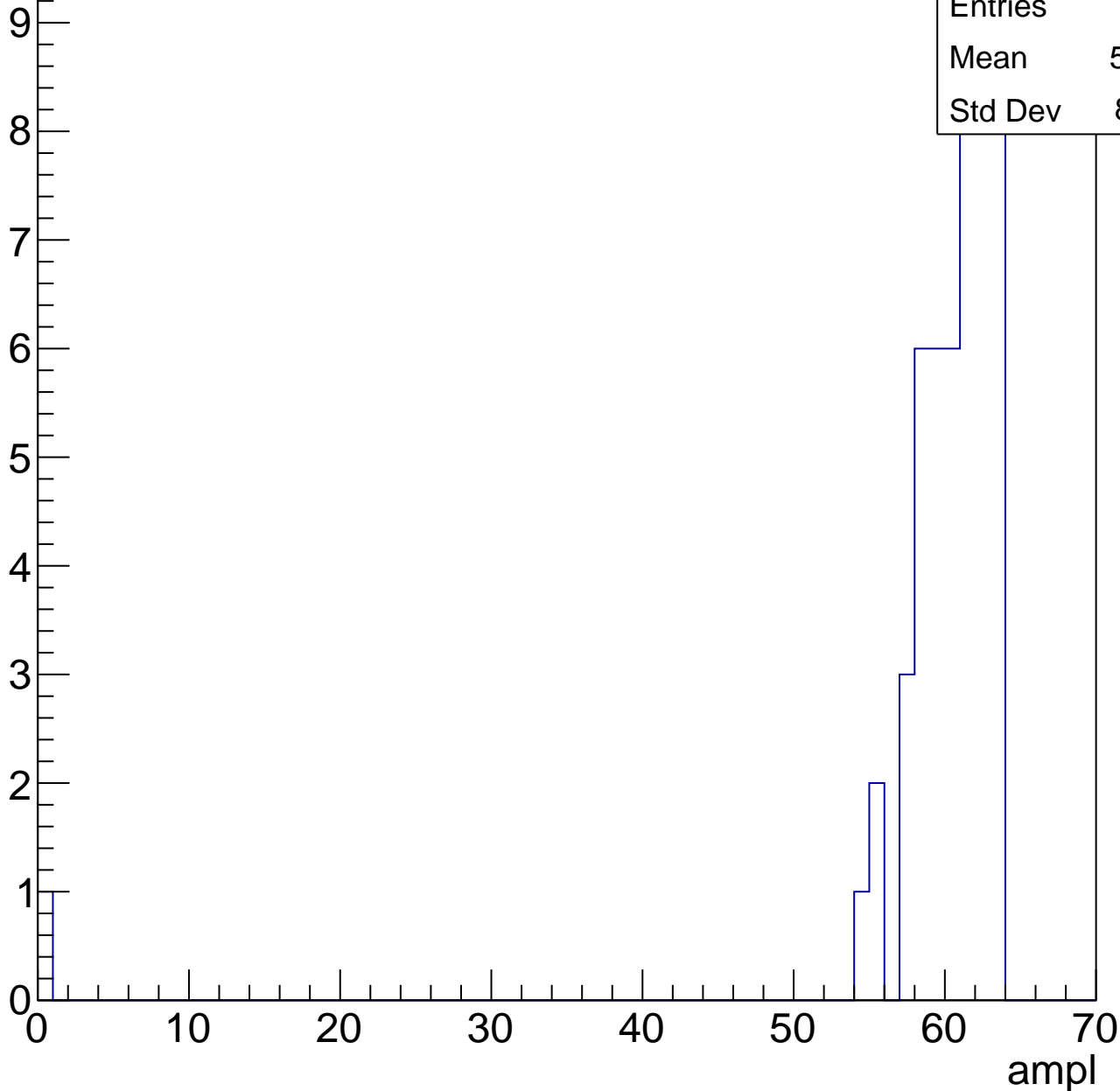


B1L102S, U6-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	58.94
Std Dev	8.721



B1L102S, U6-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U6-ch120, adc0

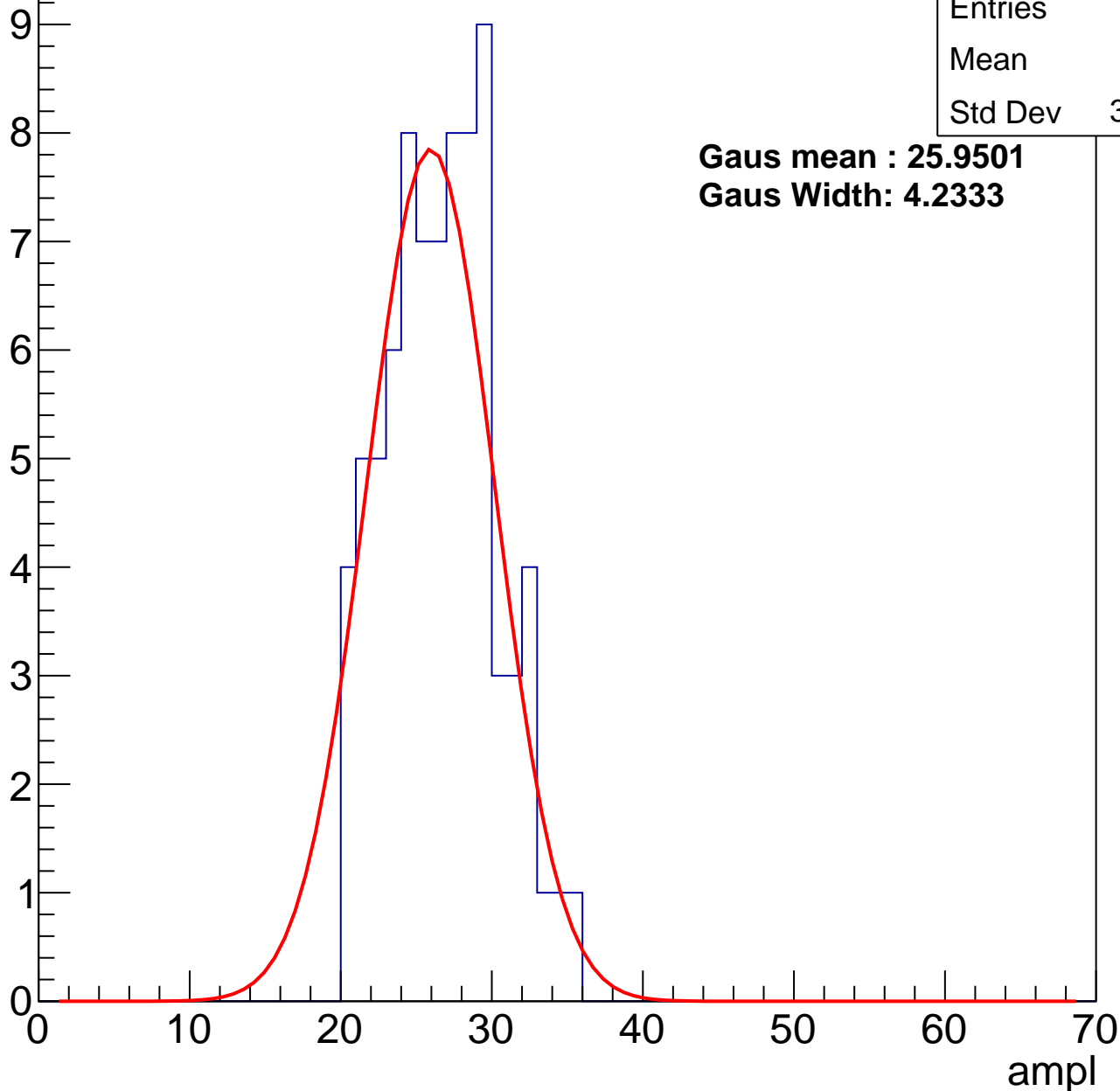
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	26.2
Std Dev	3.572

Gaus mean : 25.9501

Gaus Width: 4.2333



B1L102S, U6-ch120, adc1

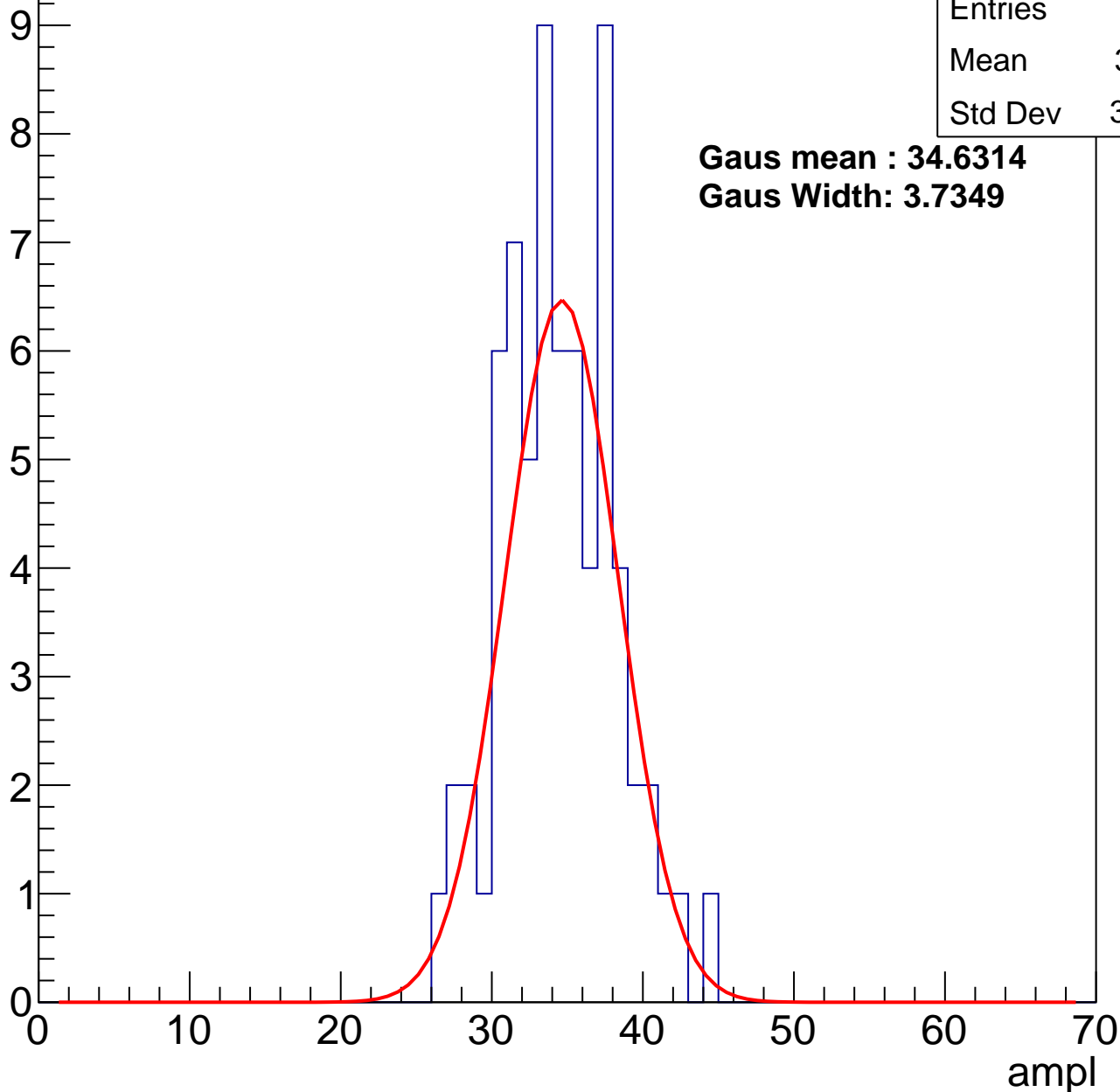
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.01
Std Dev	3.709

Gaus mean : 34.6314

Gaus Width: 3.7349



B1L102S, U6-ch120, adc2

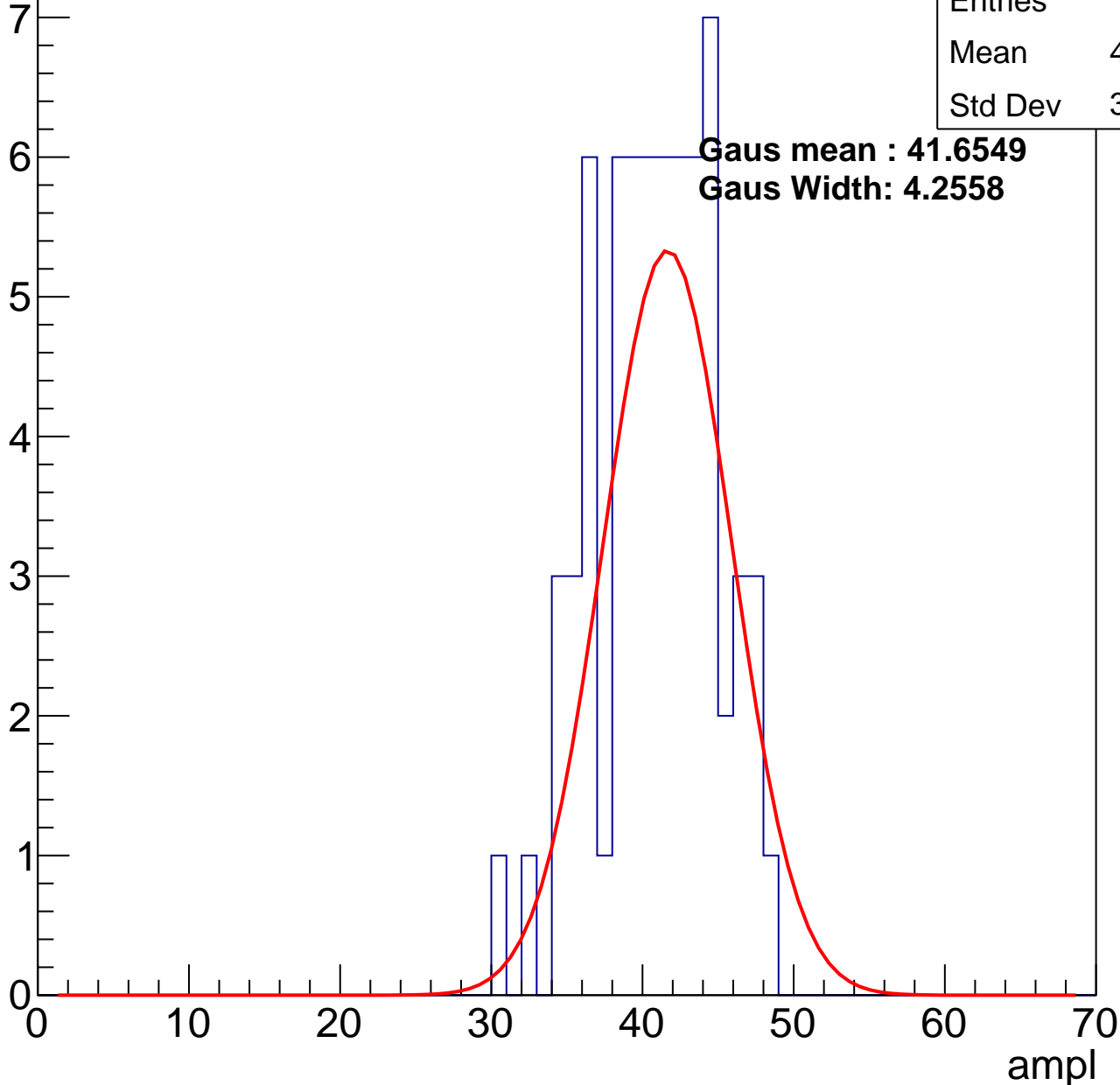
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.37
Std Dev	3.935

Gaus mean : 41.6549

Gaus Width: 4.2558

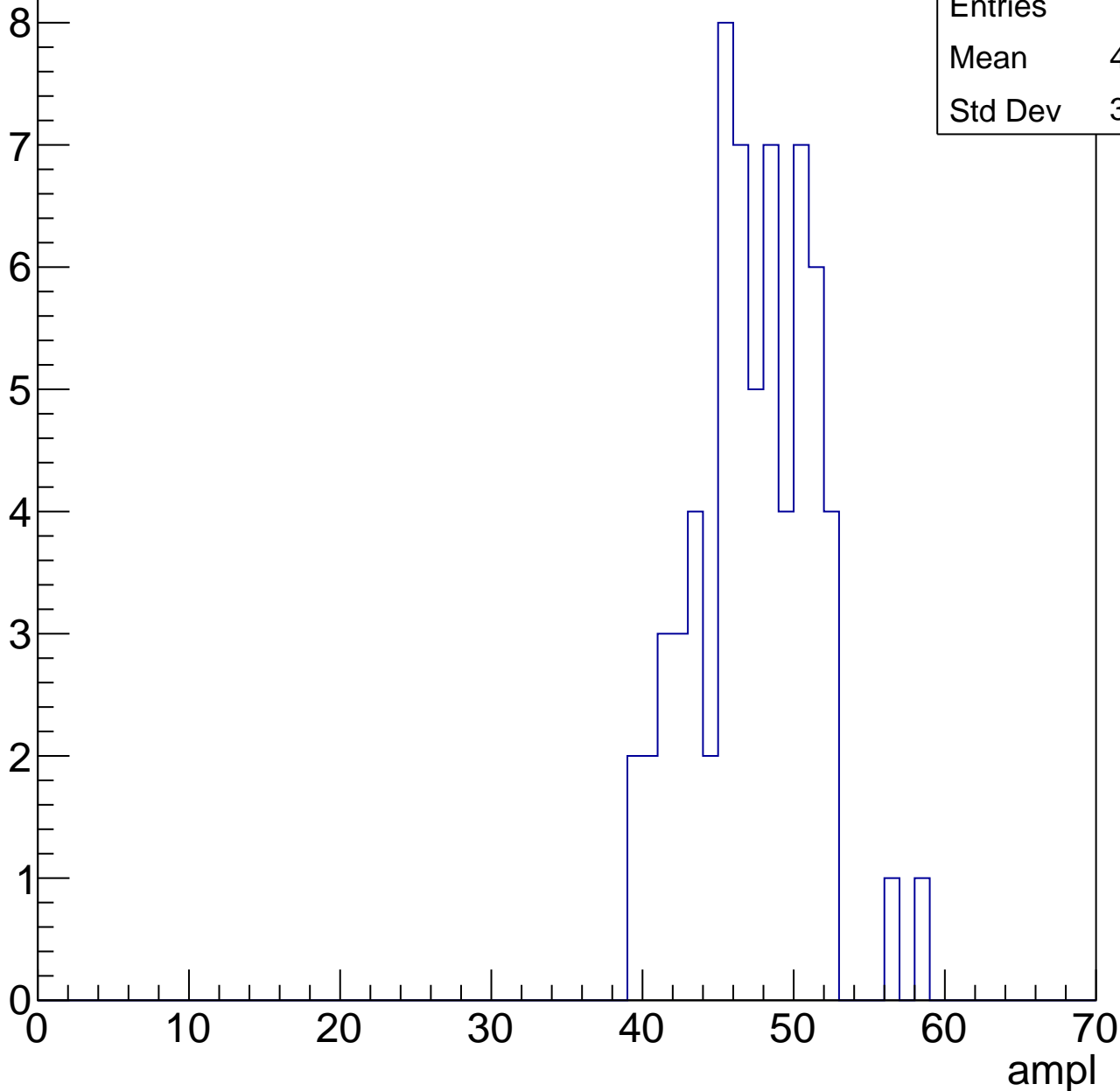


B1L102S, U6-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.88
Std Dev	3.918

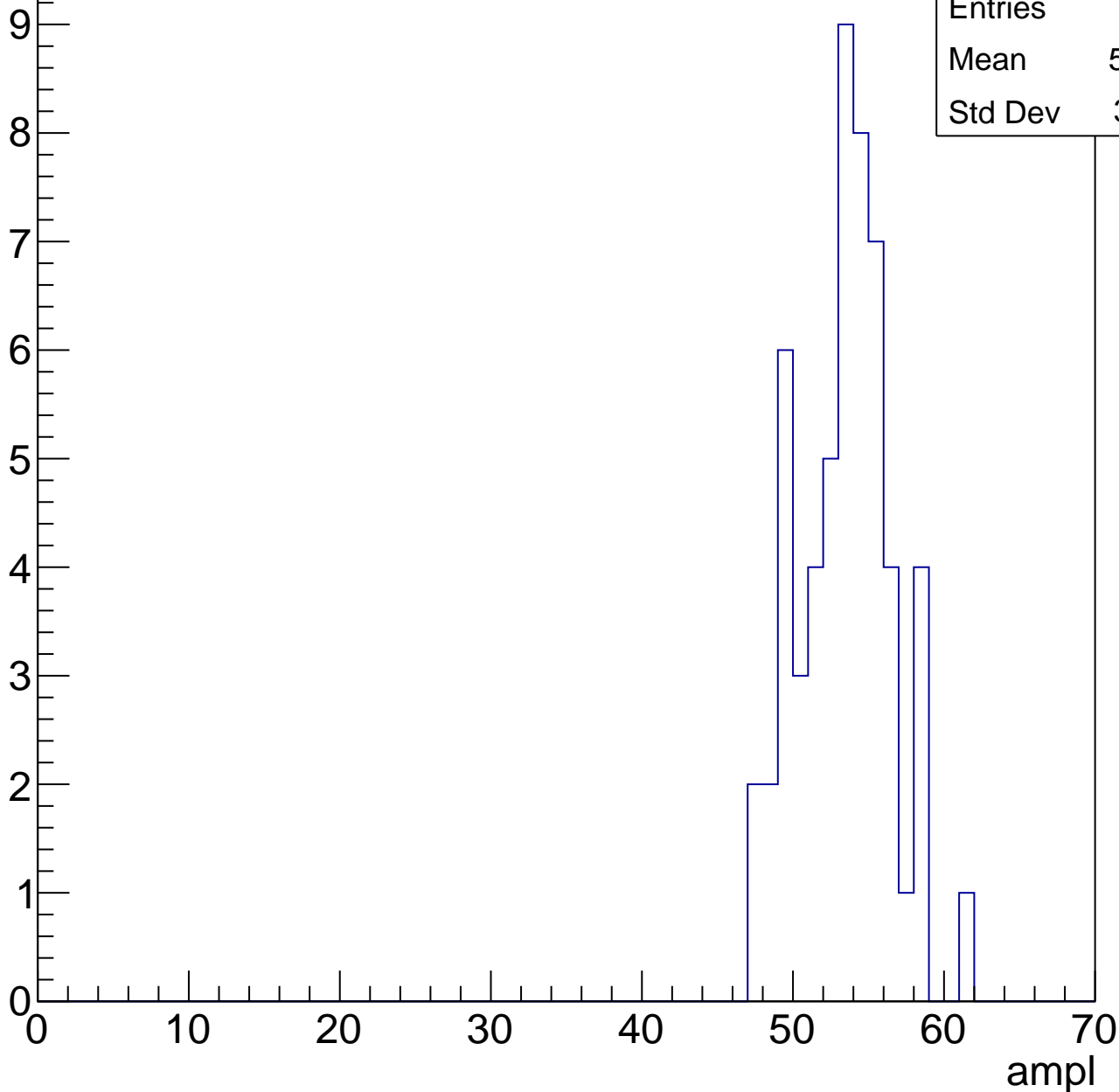


B1L102S, U6-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

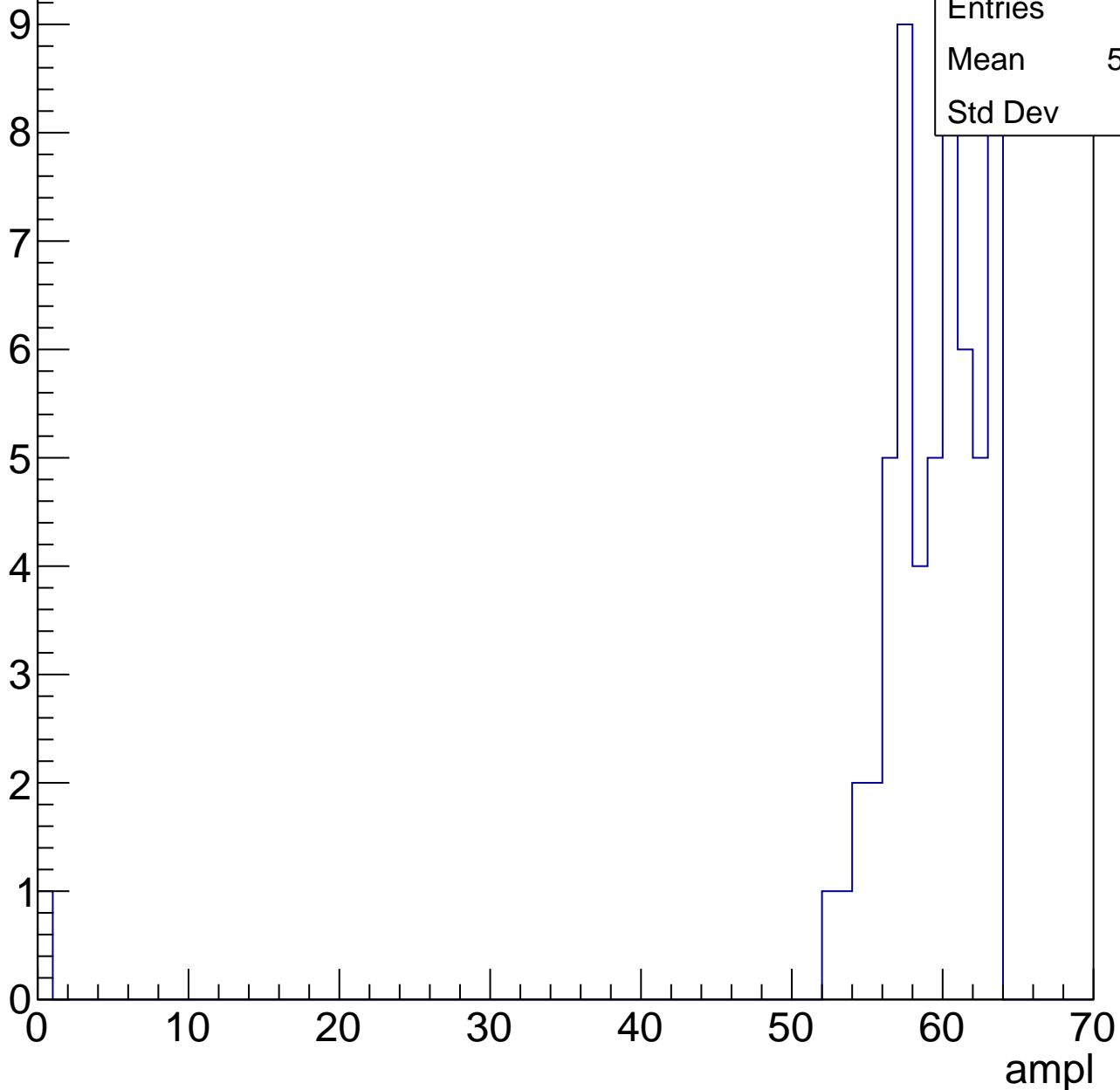
Entries	56
Mean	52.96
Std Dev	3.041



B1L102S, U6-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



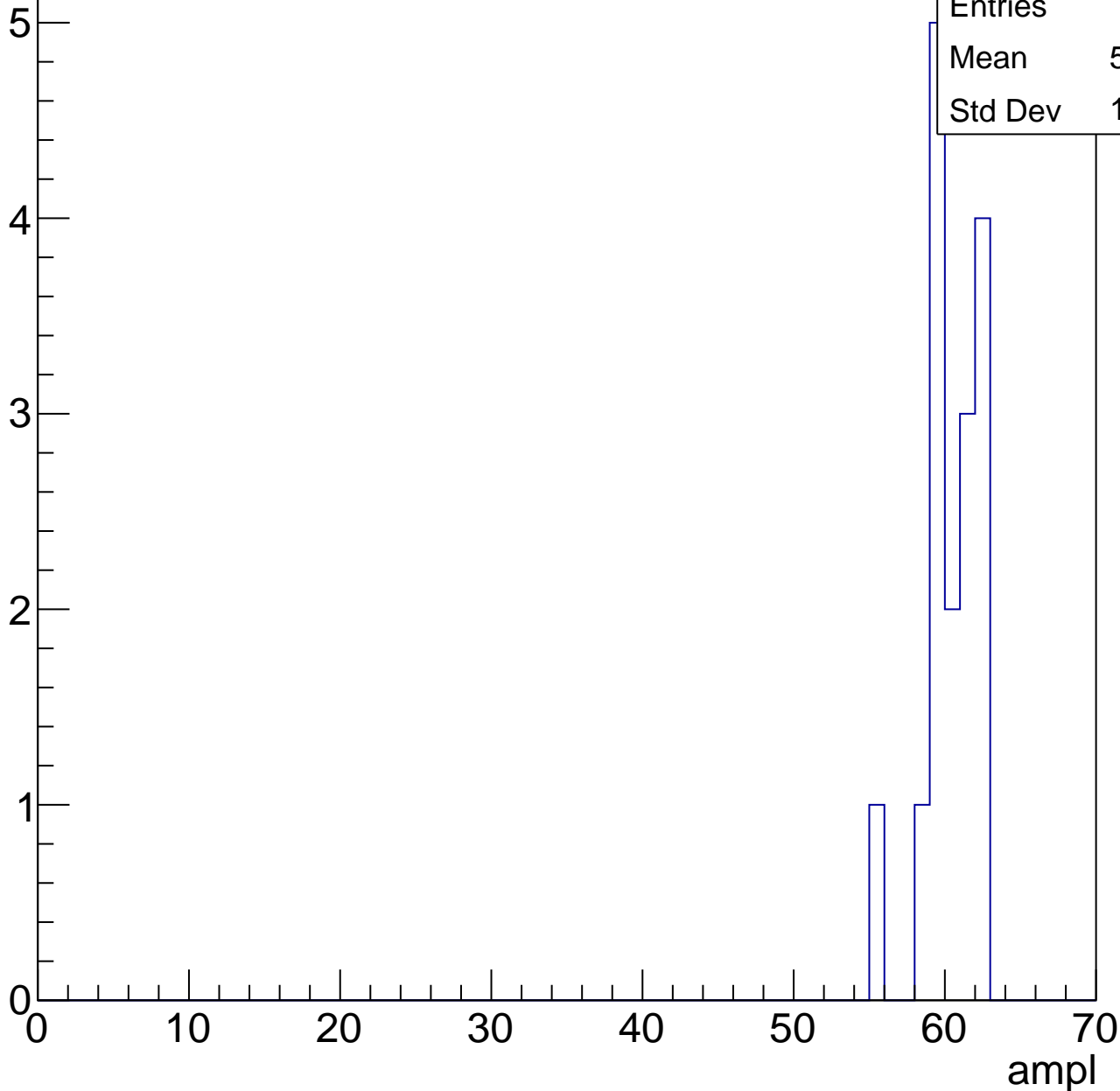
Entries	58
Mean	58.03
Std Dev	8.2

B1L102S, U6-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	59.94
Std Dev	1.819



B1L102S, U6-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch121, adc0

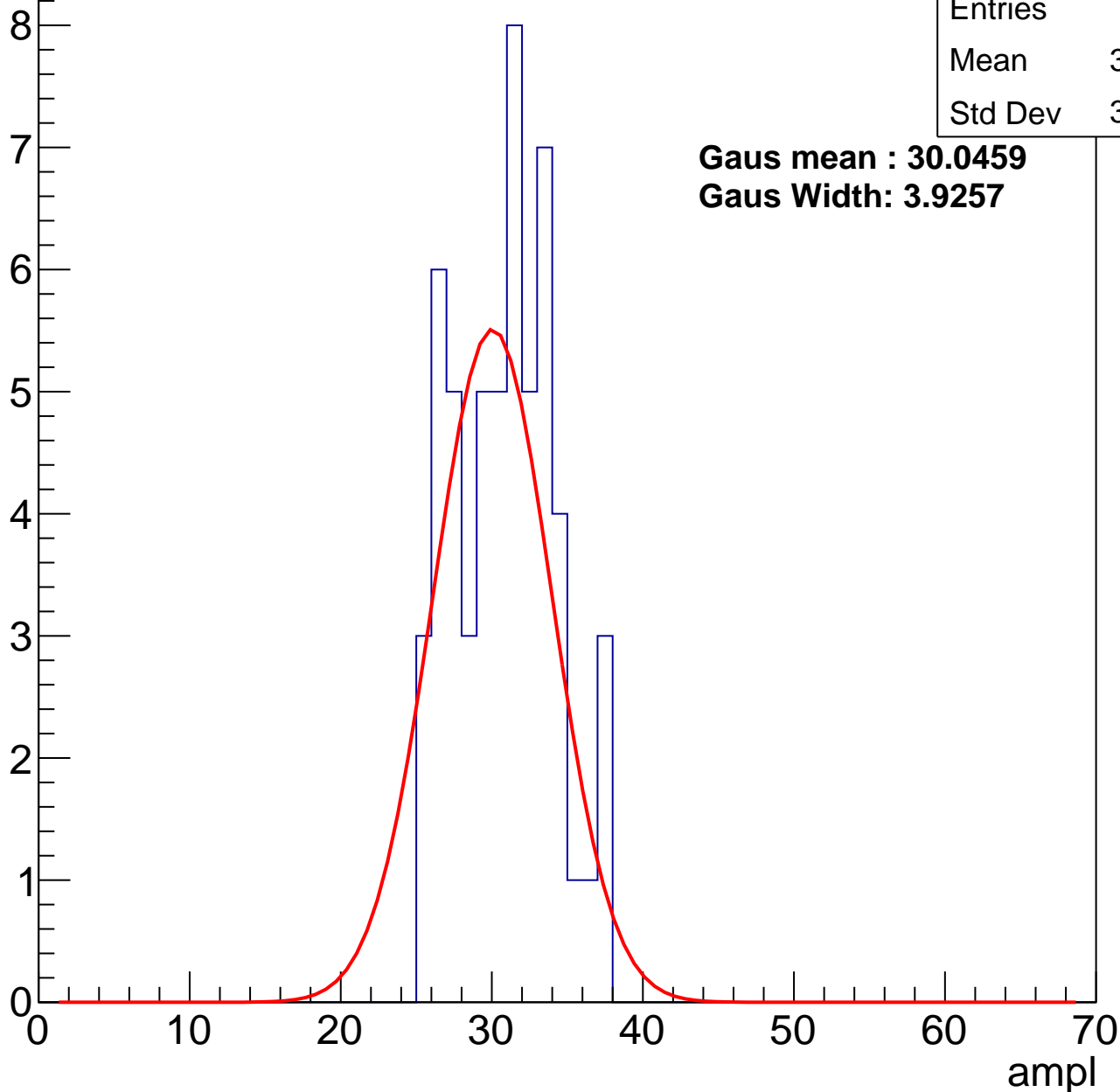
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	30.39
Std Dev	3.239

Gaus mean : 30.0459

Gaus Width: 3.9257



B1L102S, U6-ch121, adc1

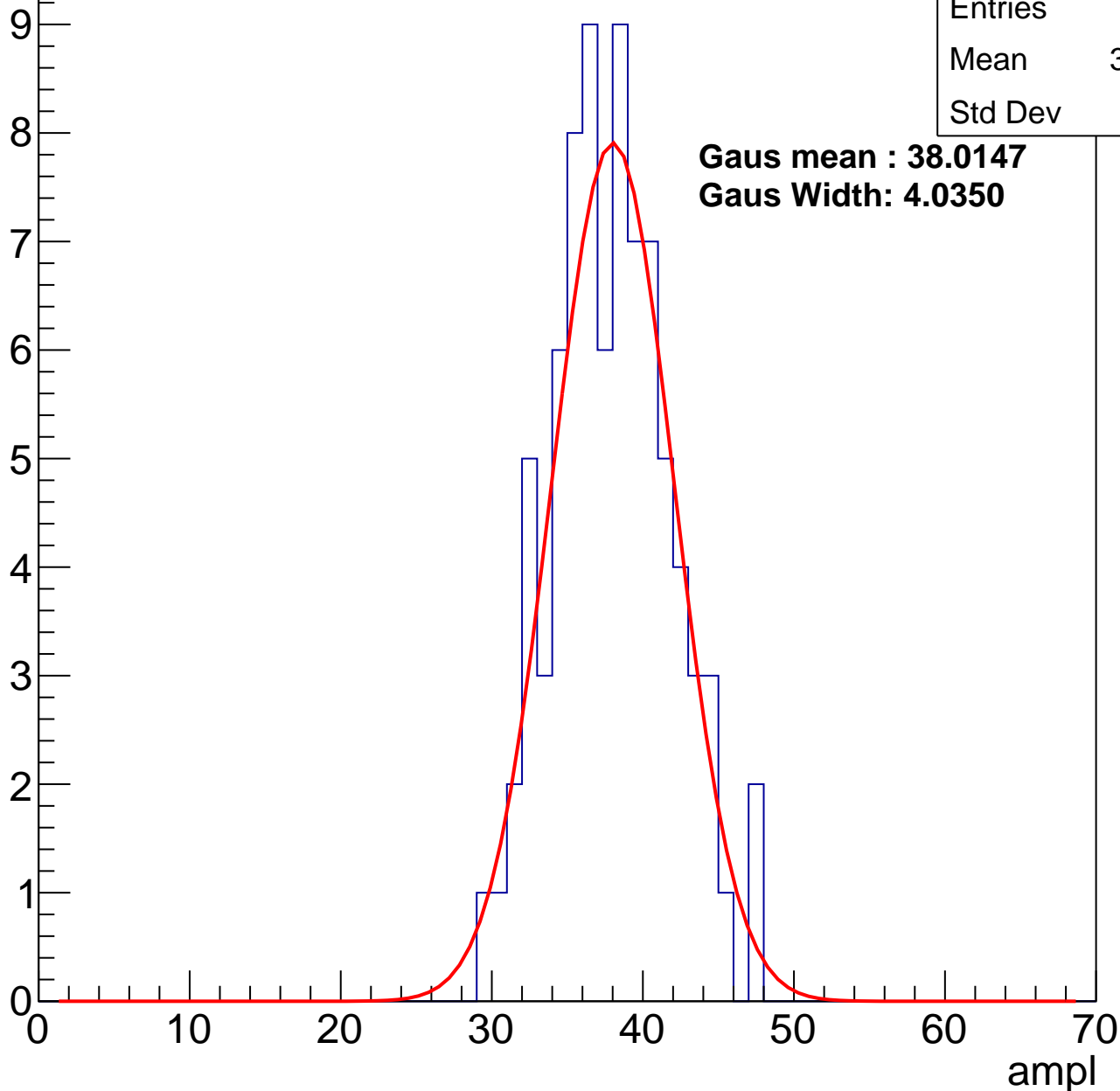
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	37.54
Std Dev	3.87

Gaus mean : 38.0147

Gaus Width: 4.0350



B1L102S, U6-ch121, adc2

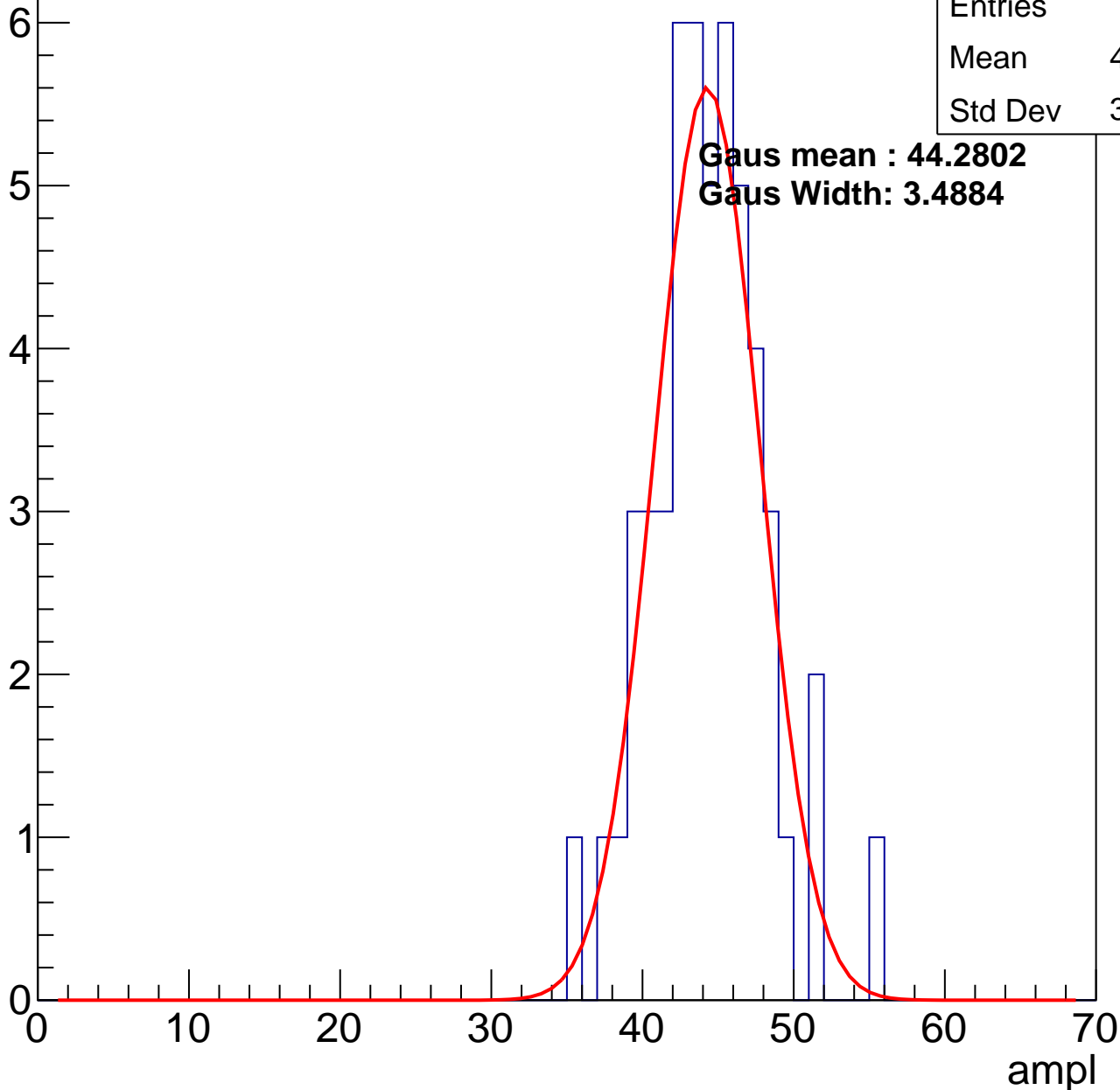
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	43.88
Std Dev	3.703

Gaus mean : 44.2802

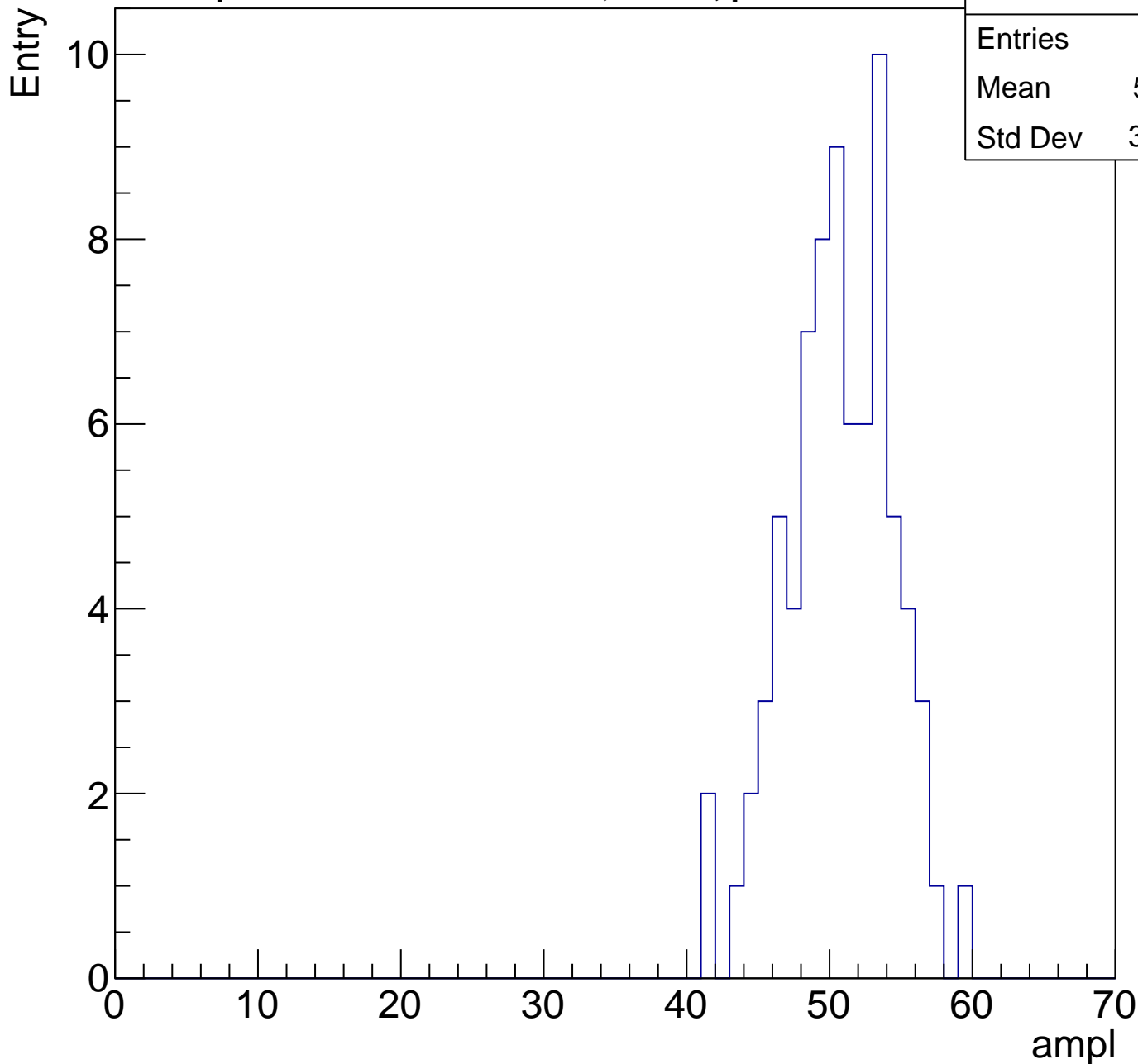
Gaus Width: 3.4884



B1L102S, U6-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	50.21
Std Dev	3.683

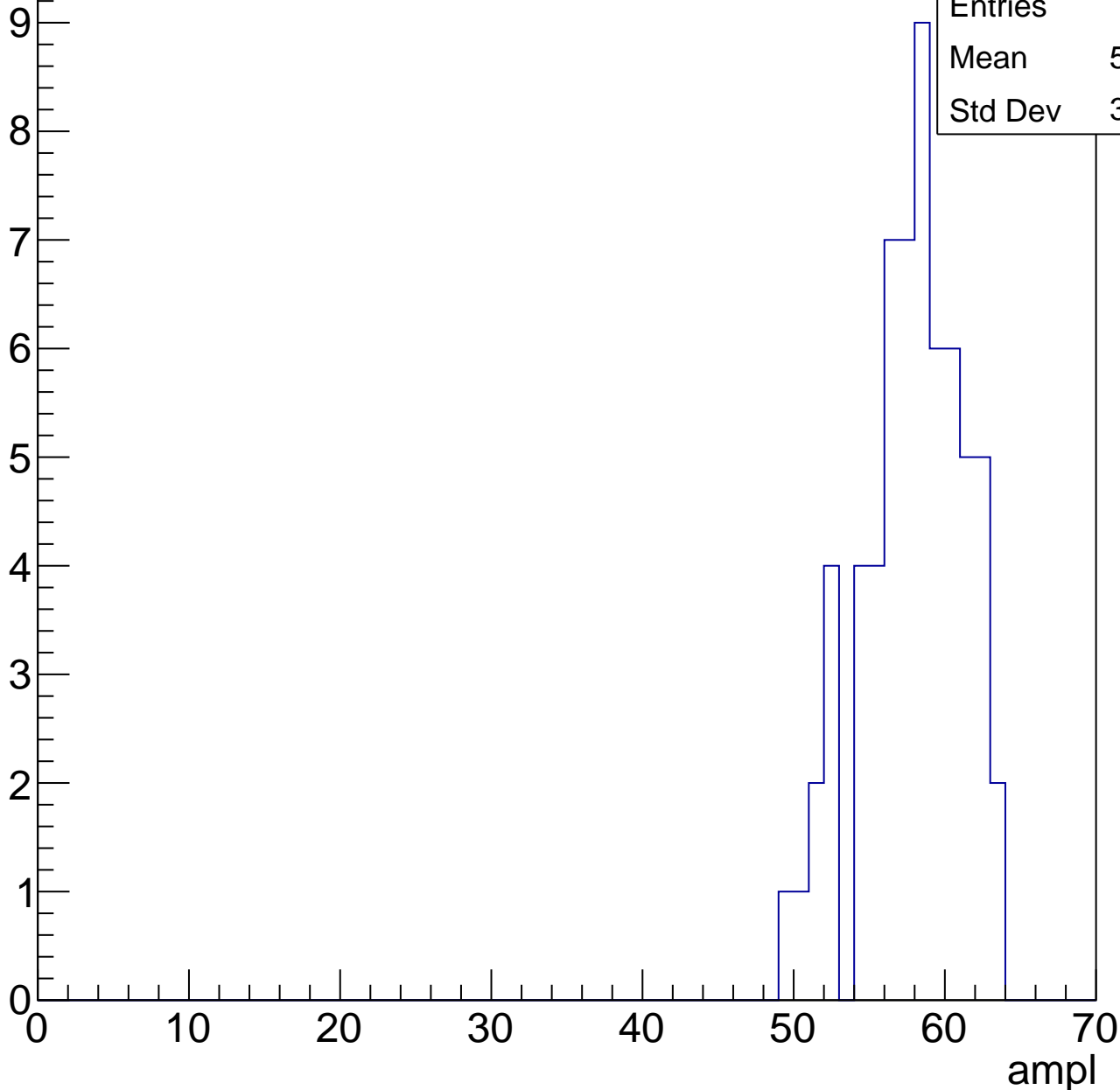


B1L102S, U6-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	57.35
Std Dev	3.334

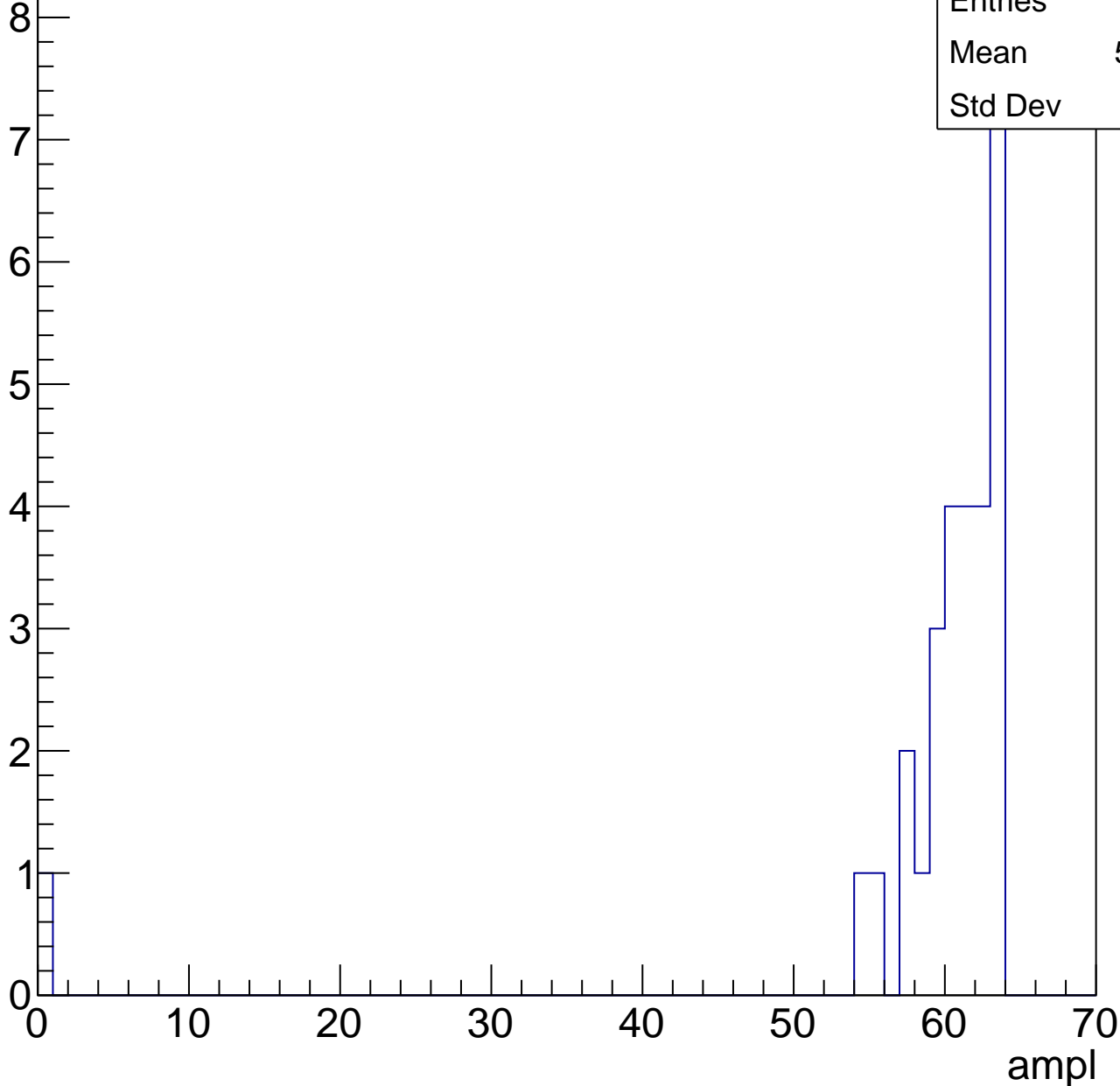


B1L102S, U6-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	29
Mean	58.41
Std Dev	11.3



B1L102S, U6-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch122, adc0

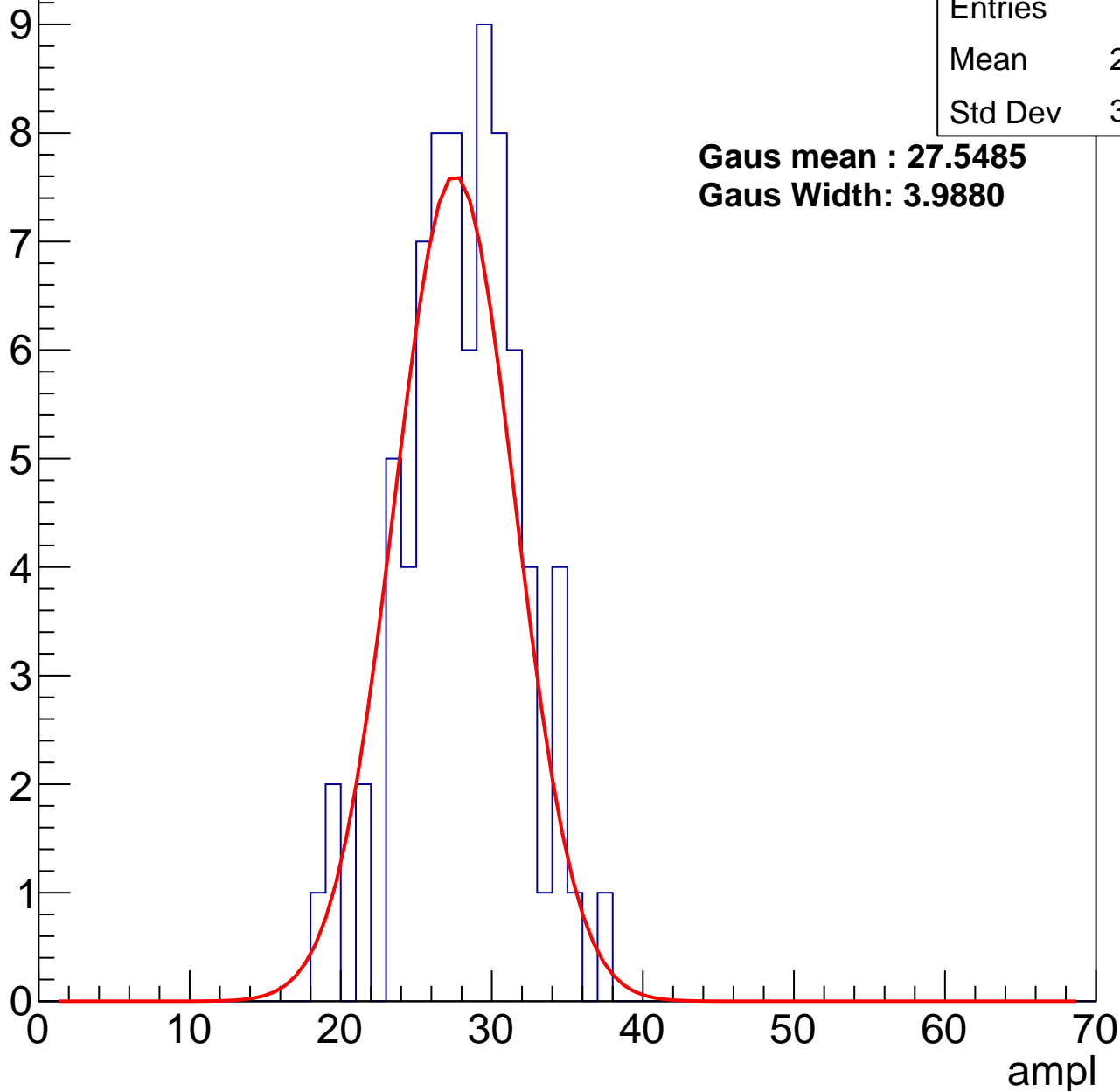
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	27.69
Std Dev	3.784

Gaus mean : 27.5485

Gaus Width: 3.9880



B1L102S, U6-ch122, adc1

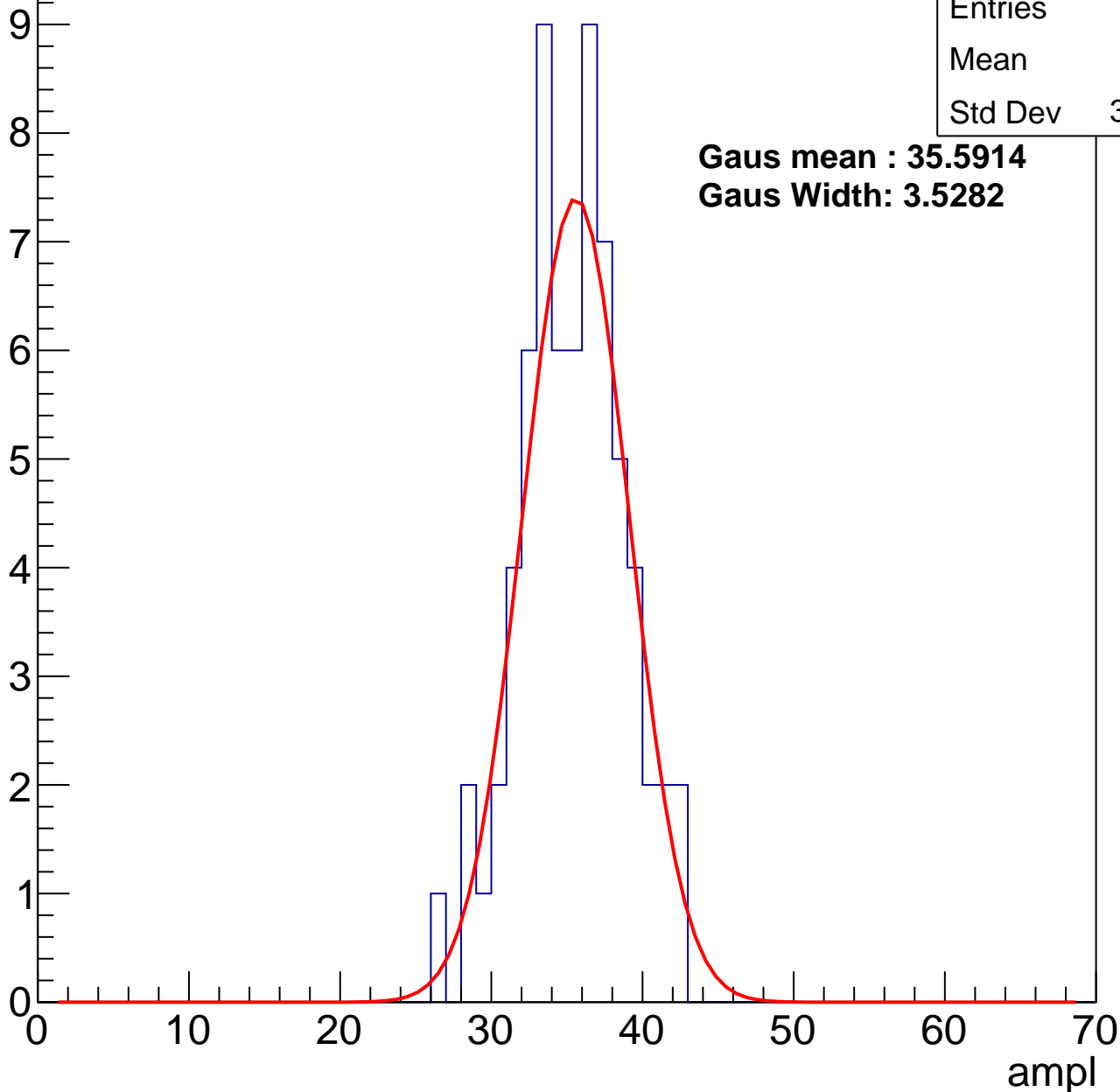
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	34.9
Std Dev	3.409

Gaus mean : 35.5914

Gaus Width: 3.5282



B1L102S, U6-ch122, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.09
Std Dev	3.575

Gaus mean : 42.7478

Gaus Width: 4.2061

10

8

6

4

2

0

0

10

20

30

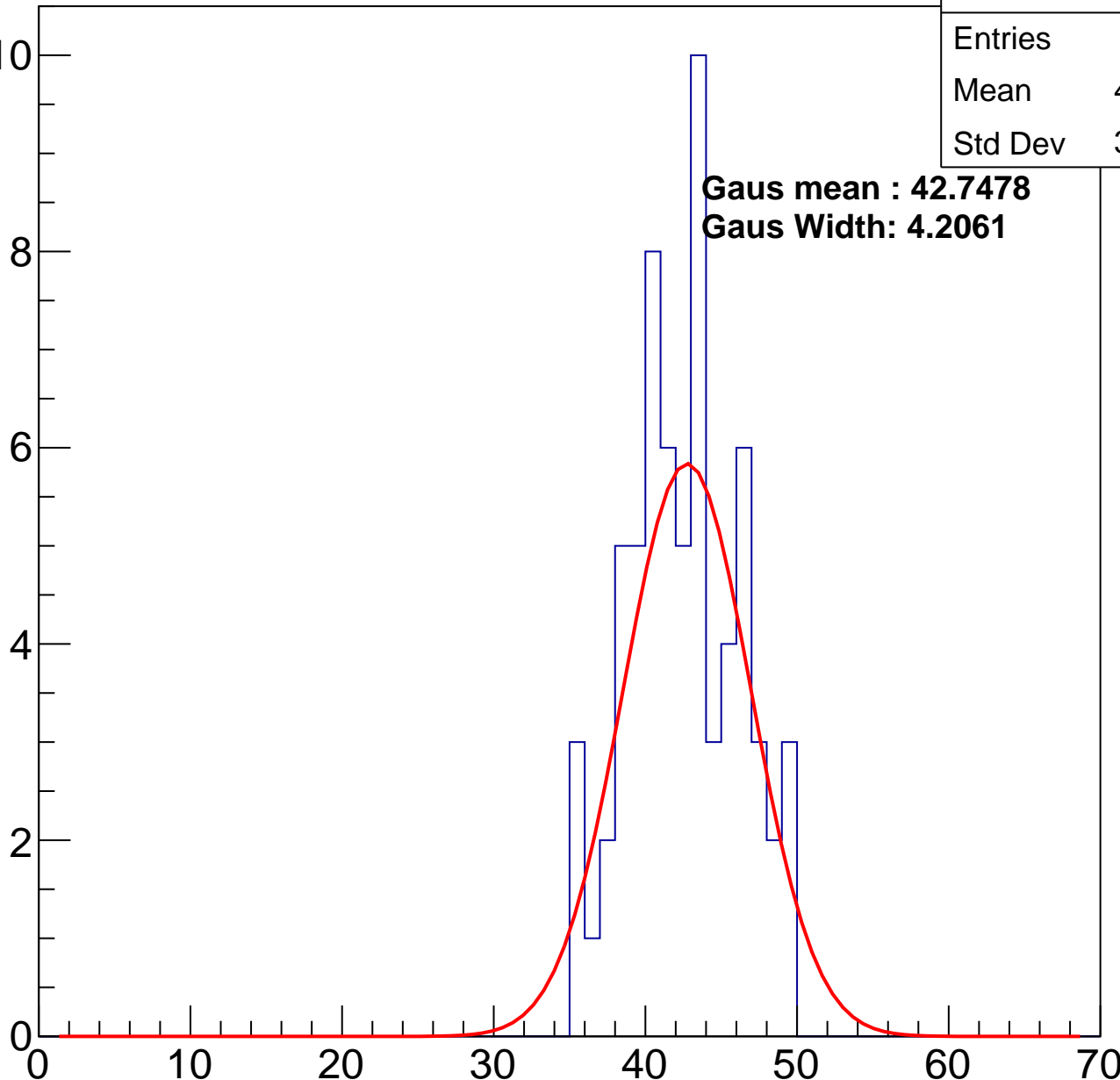
40

50

60

70

ampl

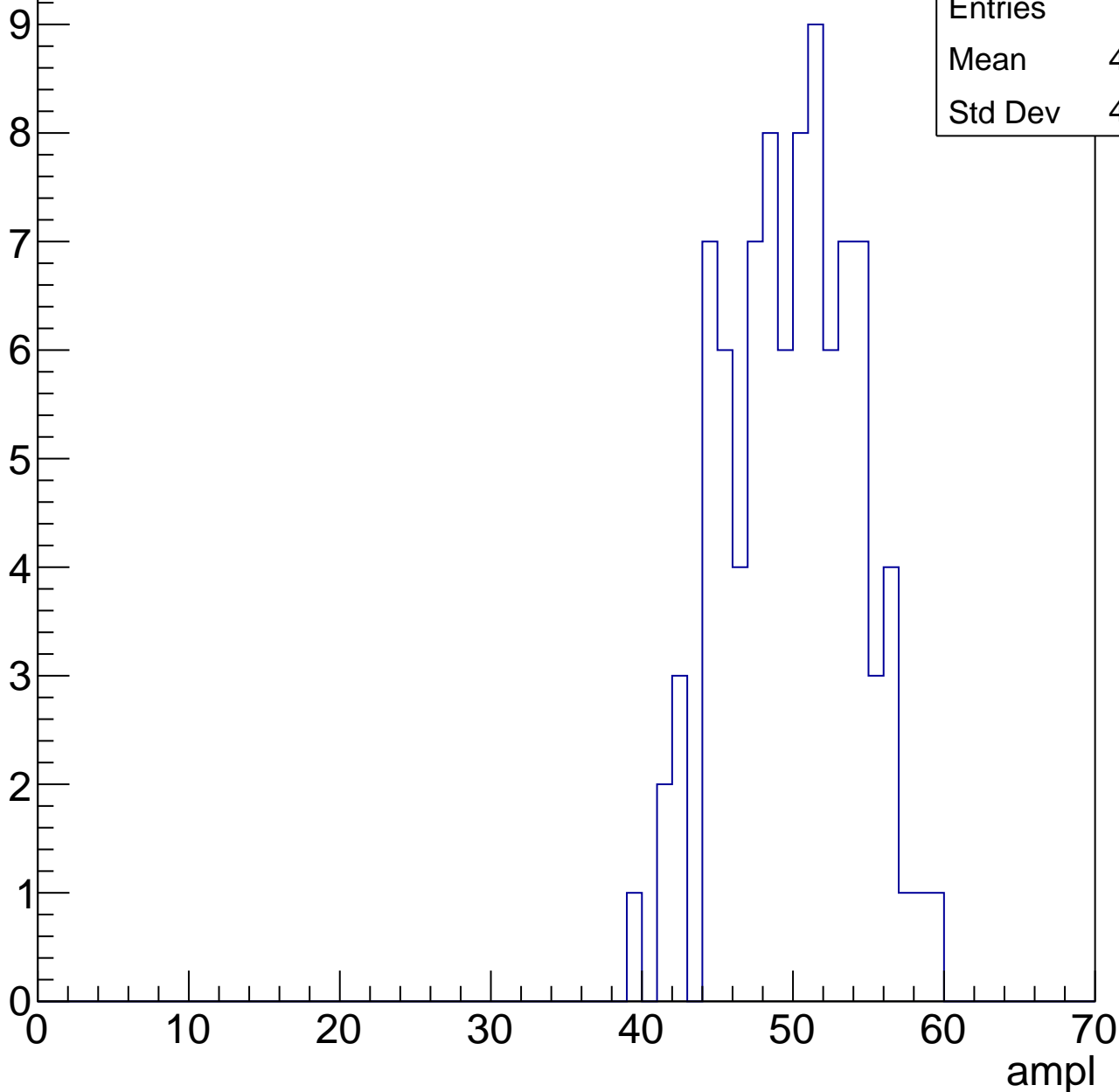


B1L102S, U6-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	91
Mean	49.44
Std Dev	4.238

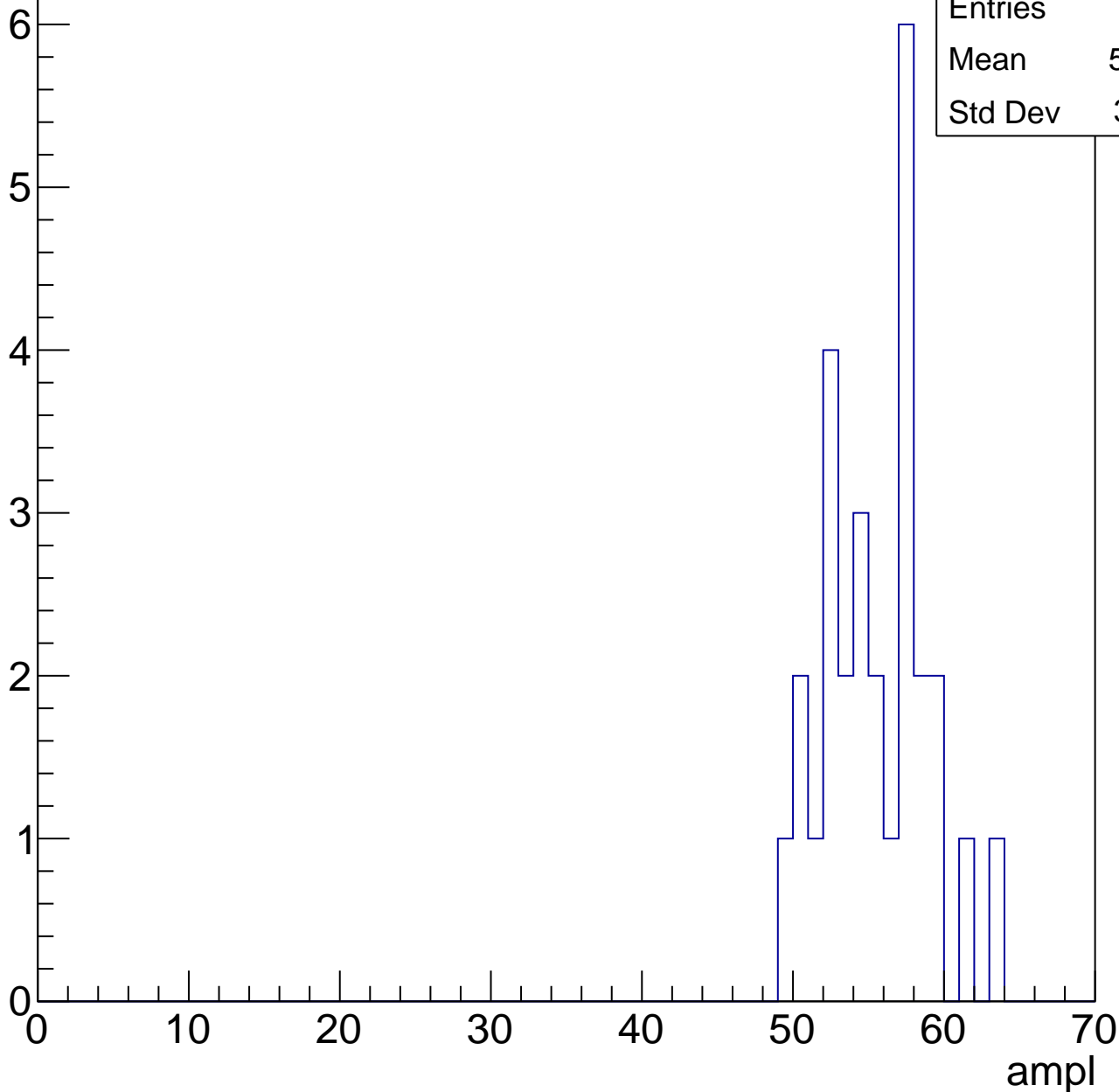


B1L102S, U6-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	28
Mean	55.07
Std Dev	3.401

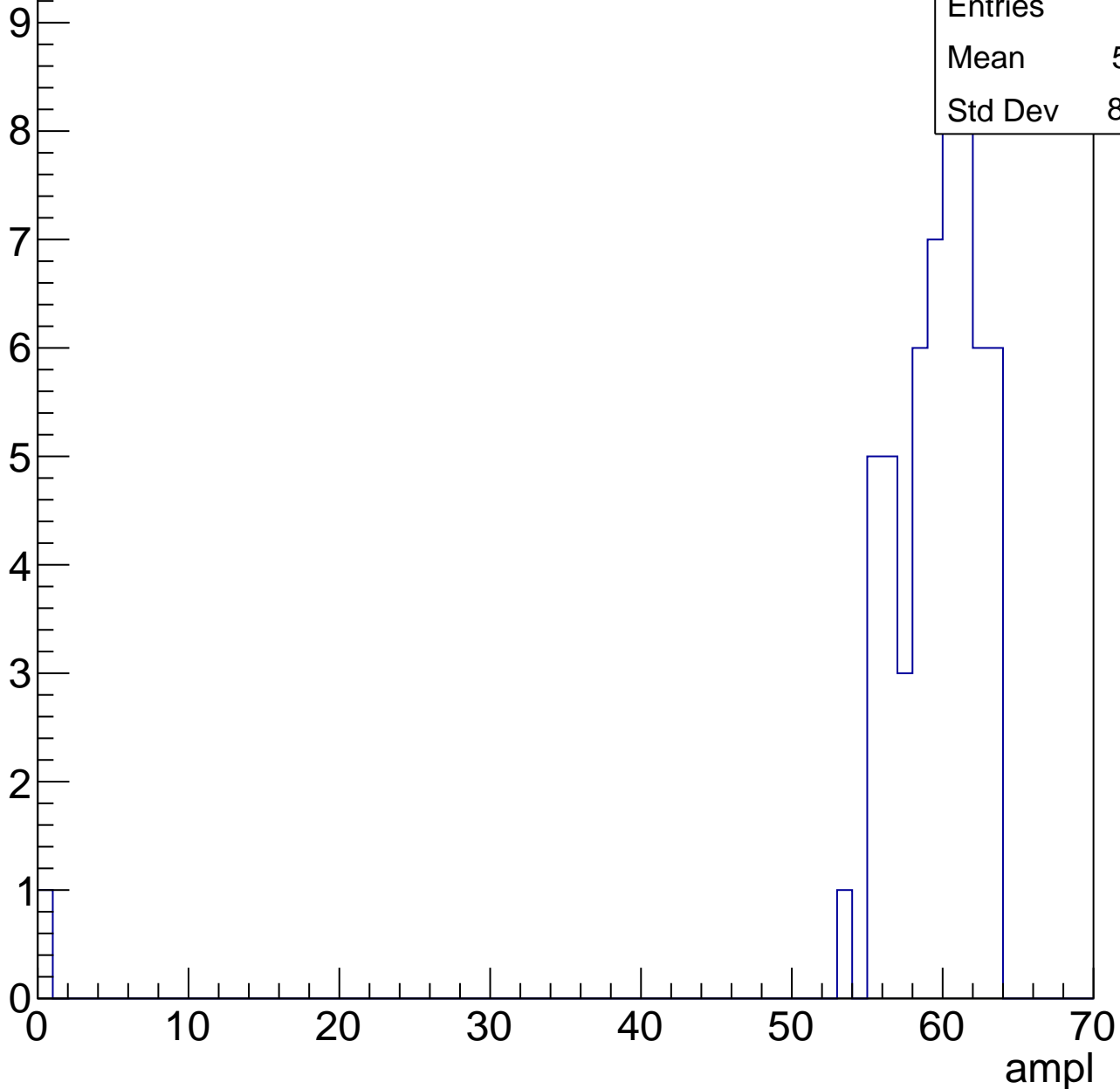


B1L102S, U6-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

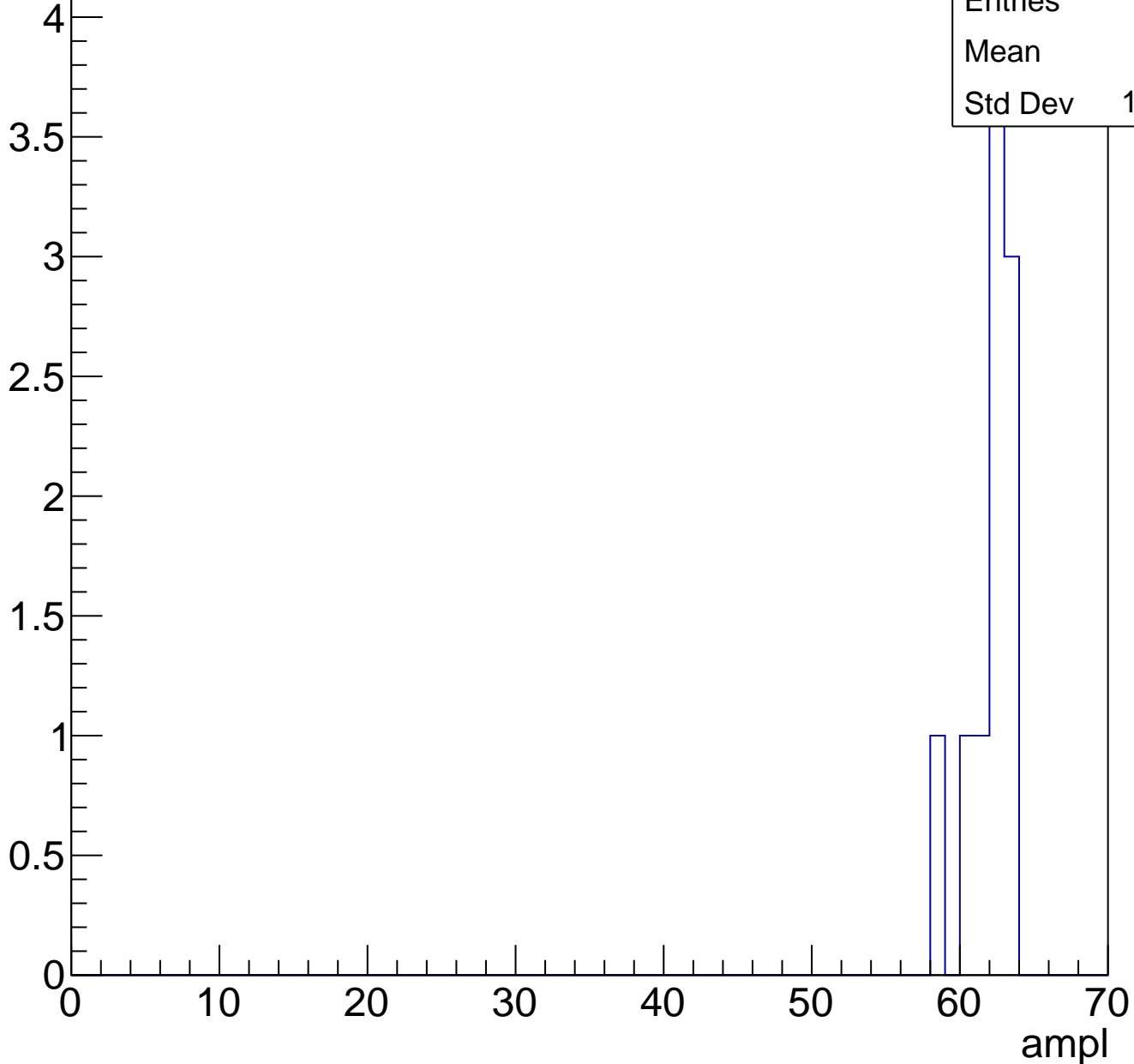
Entries	57
Mean	58.21
Std Dev	8.182



B1L102S, U6-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.6
Std Dev	1.497

B1L102S, U6-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch123, adc0

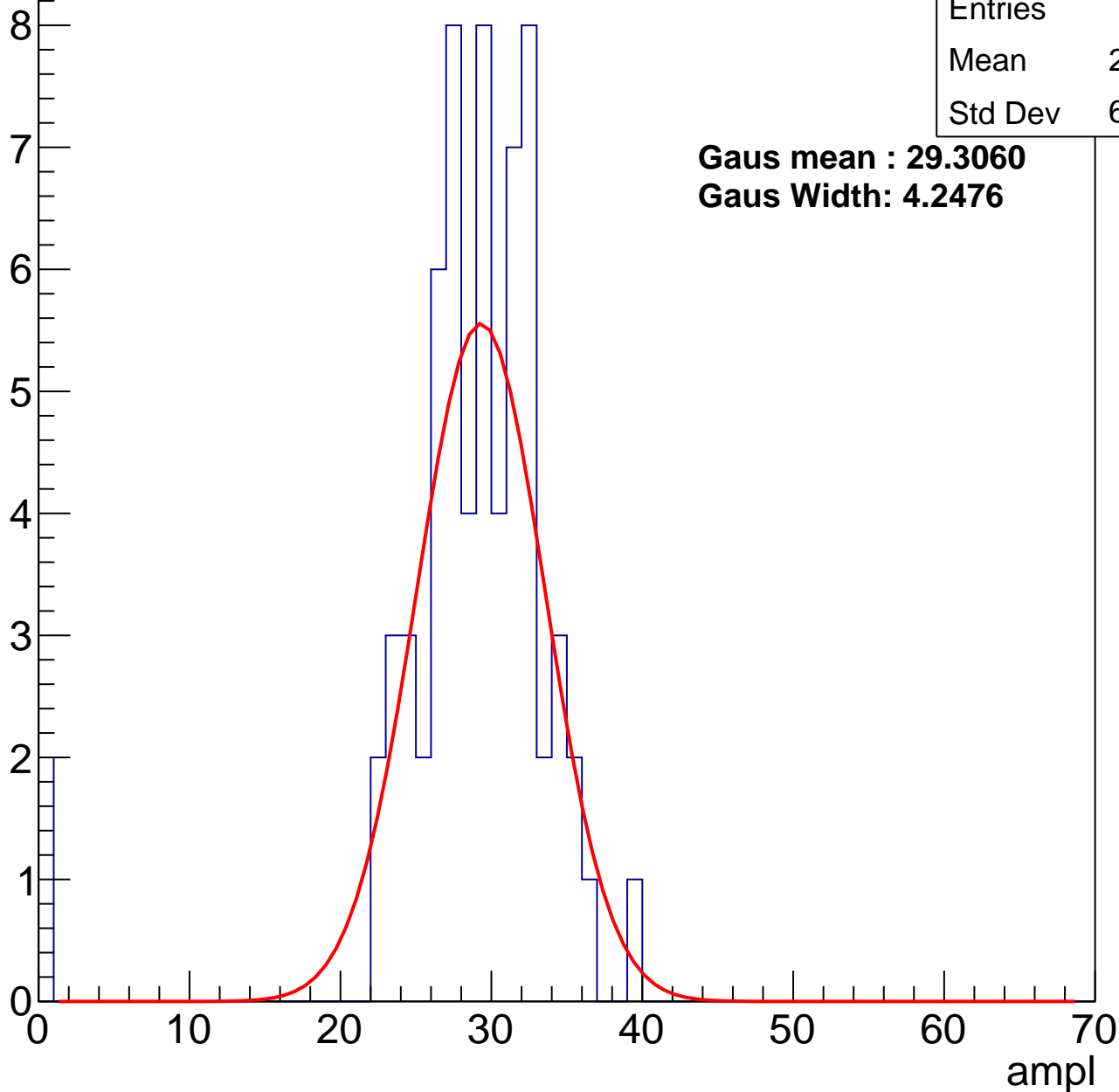
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	28.14
Std Dev	6.115

Gaus mean : 29.3060

Gaus Width: 4.2476



B1L102S, U6-ch123, adc1

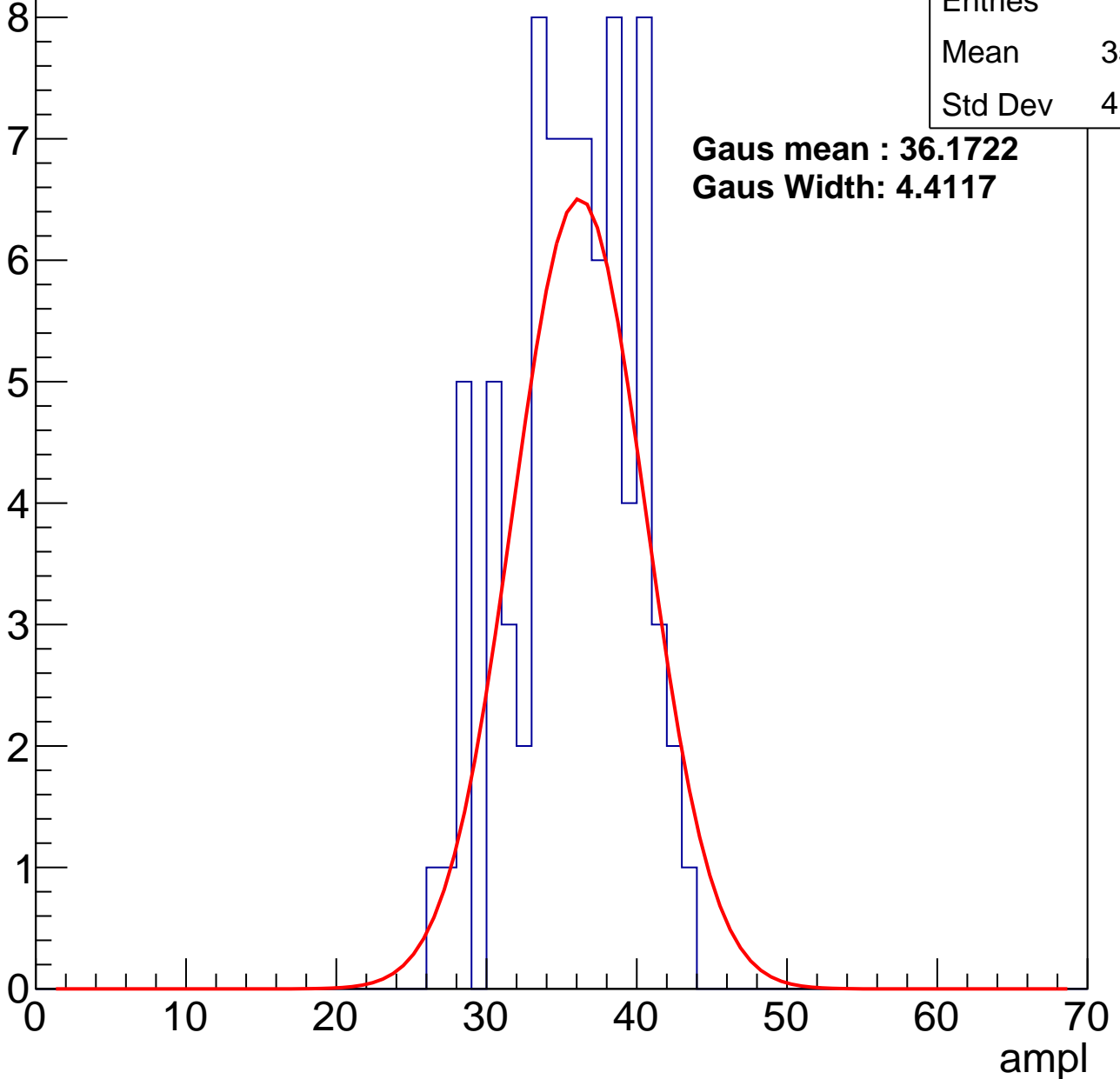
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.27
Std Dev	4.005

Gaus mean : 36.1722

Gaus Width: 4.4117



B1L102S, U6-ch123, adc2

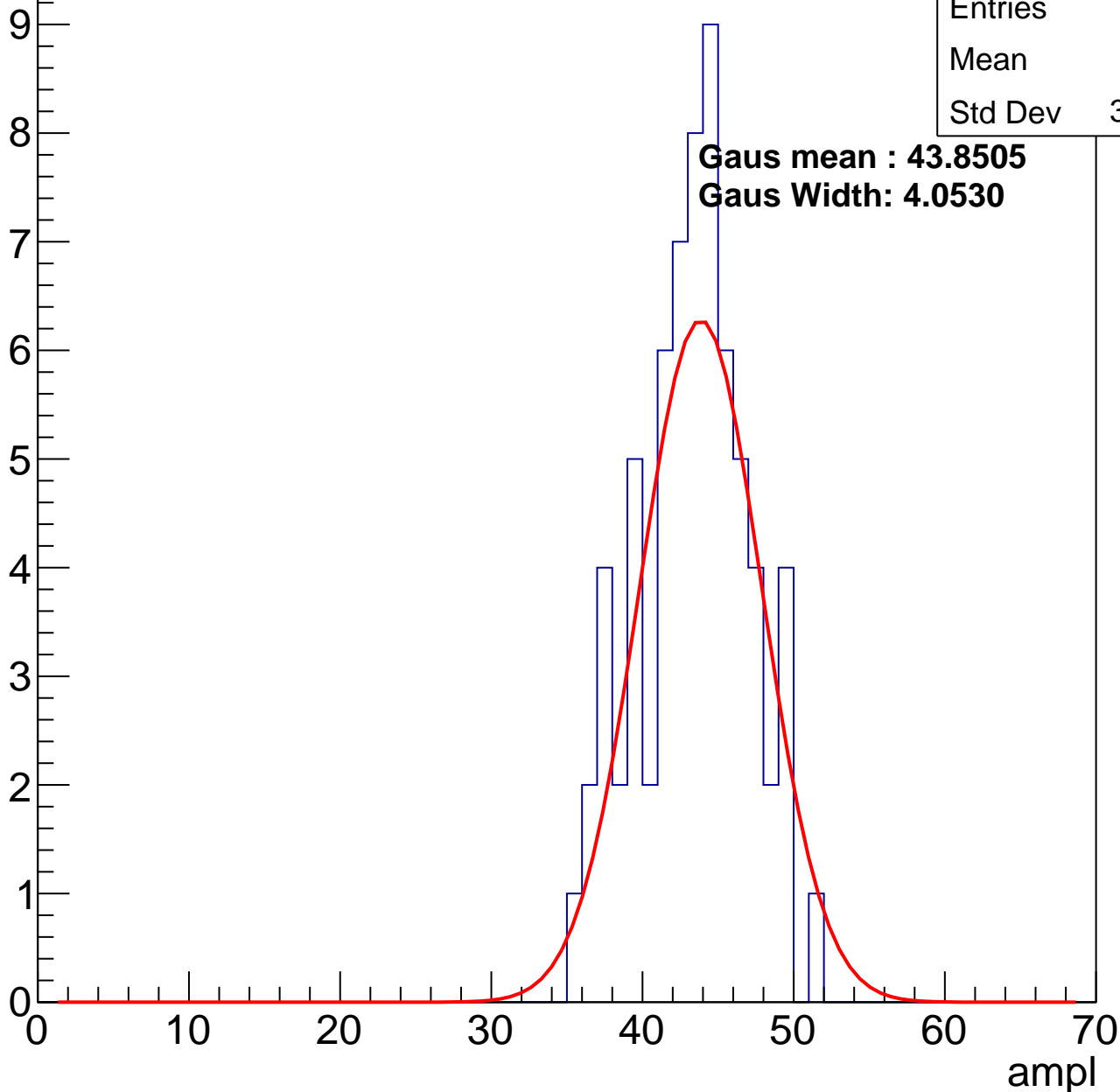
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	42.9
Std Dev	3.606

Gaus mean : 43.8505

Gaus Width: 4.0530

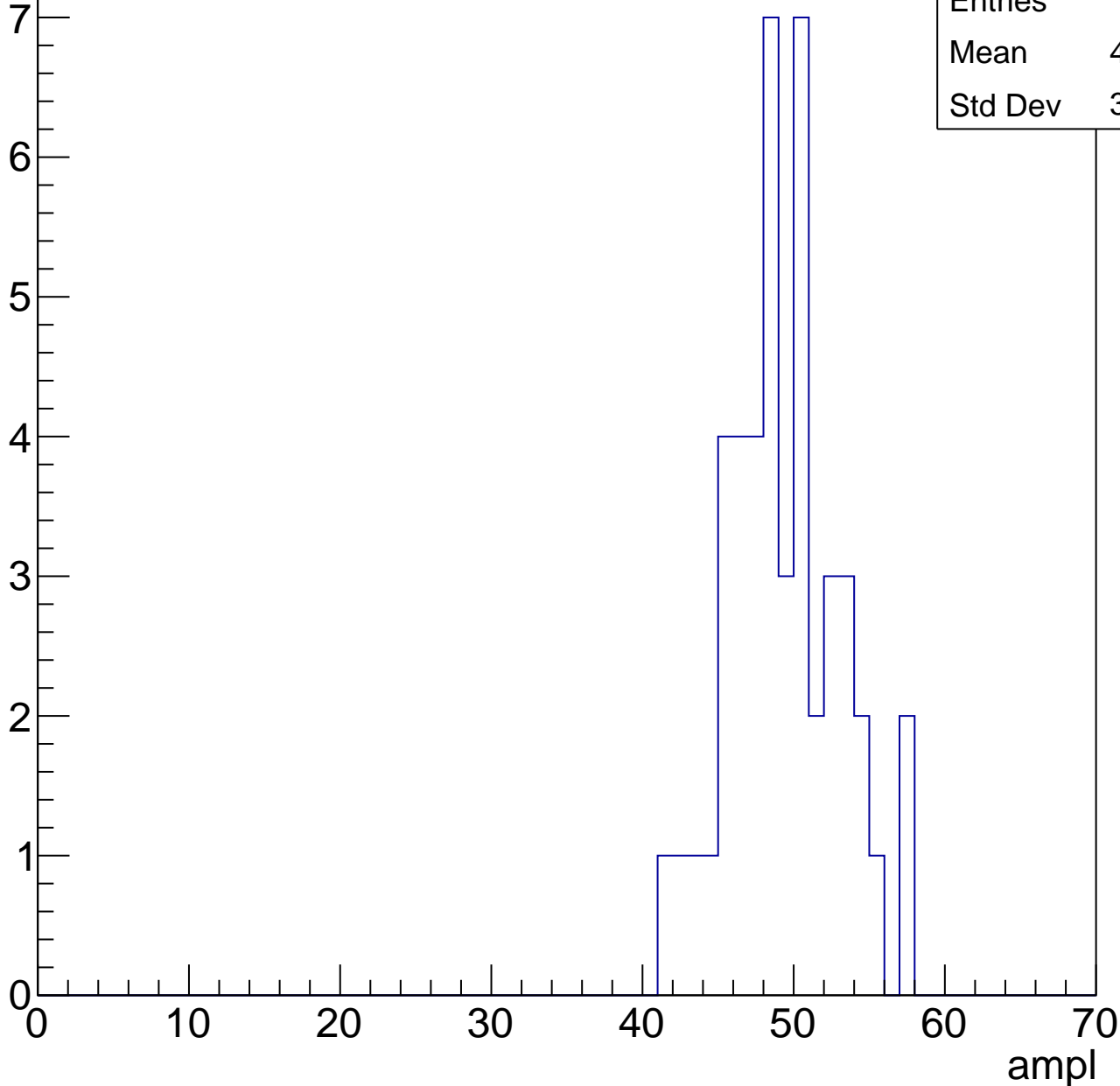


B1L102S, U6-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	48.89
Std Dev	3.613

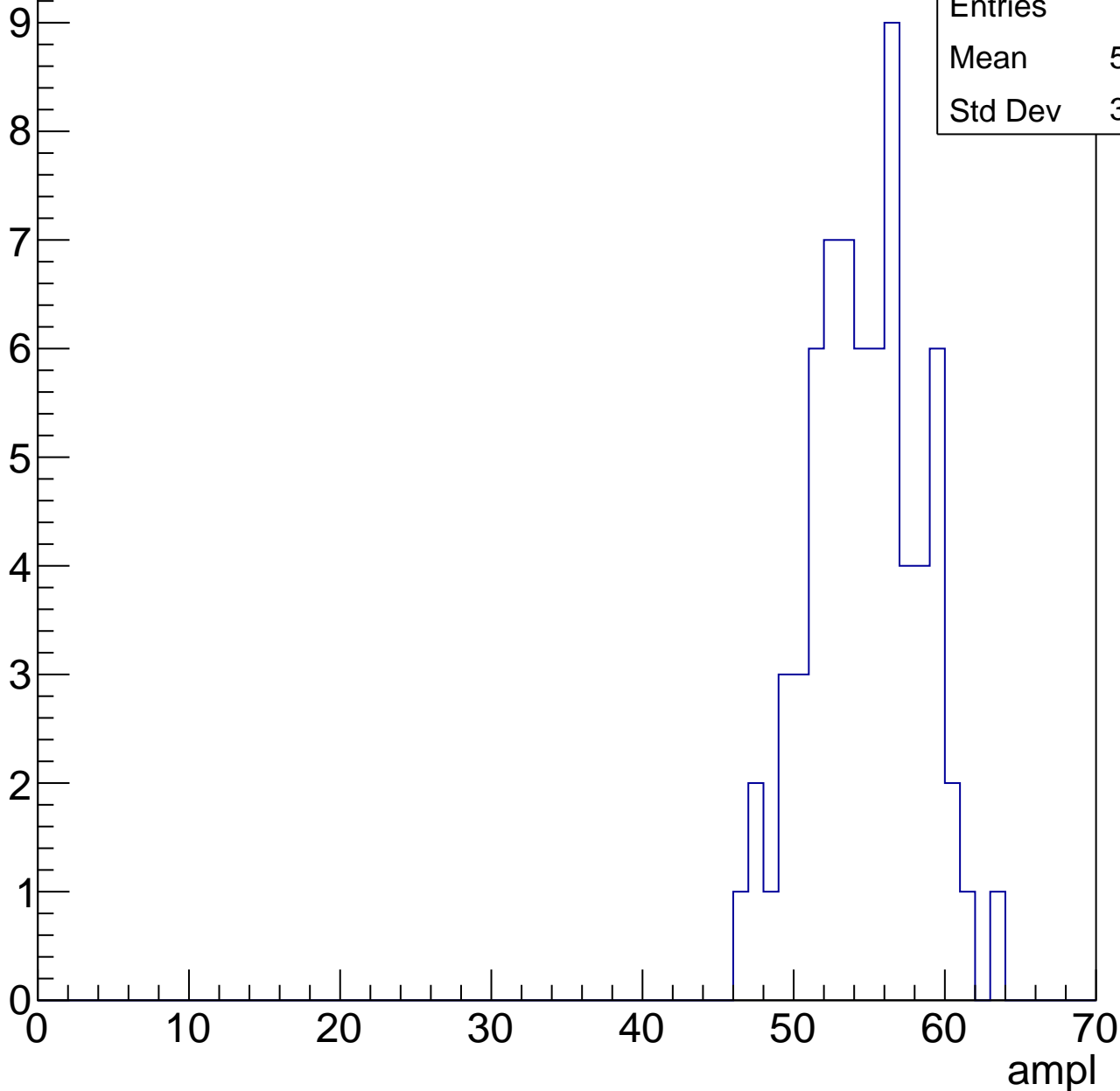


B1L102S, U6-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	54.23
Std Dev	3.612

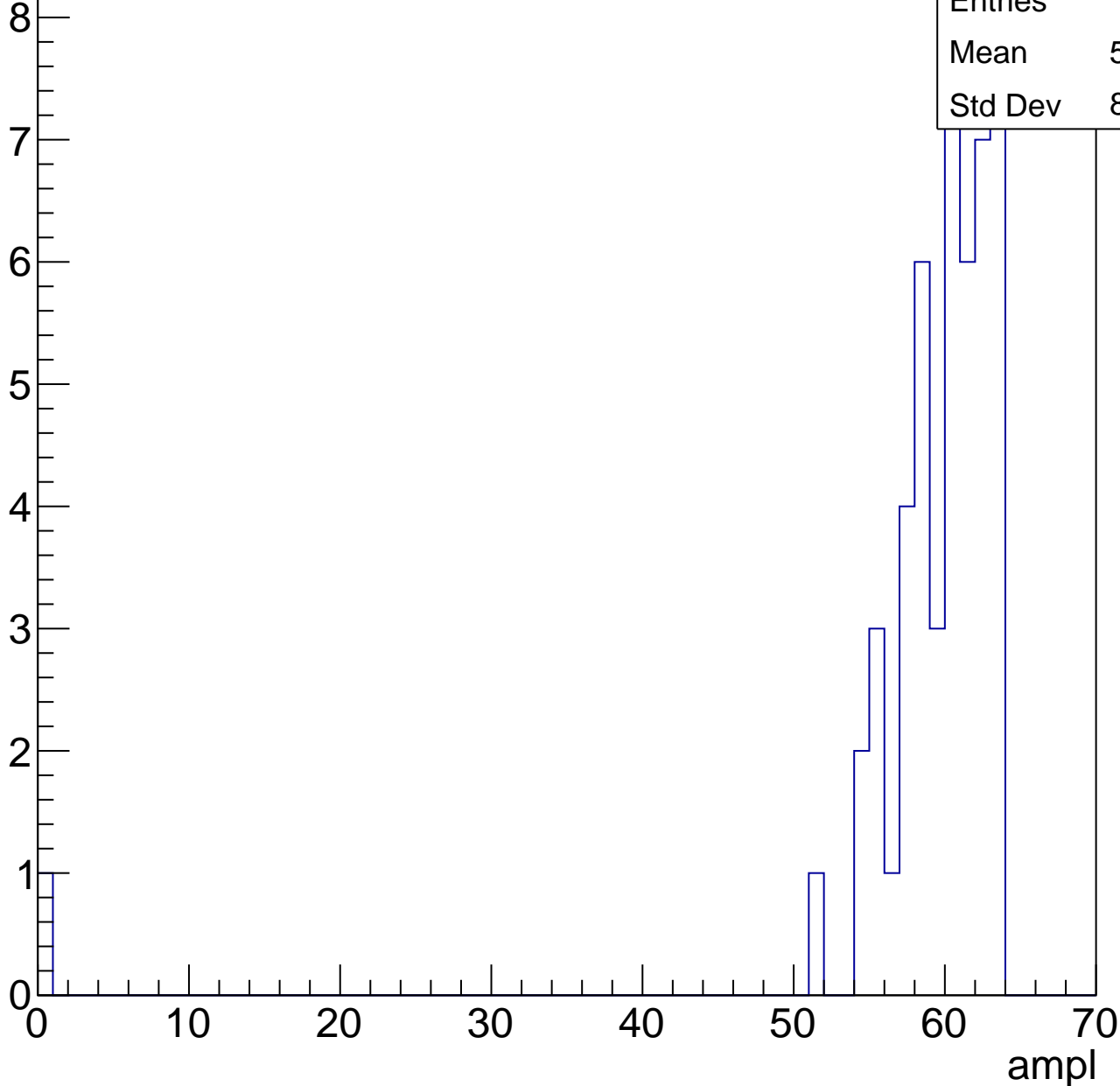


B1L102S, U6-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

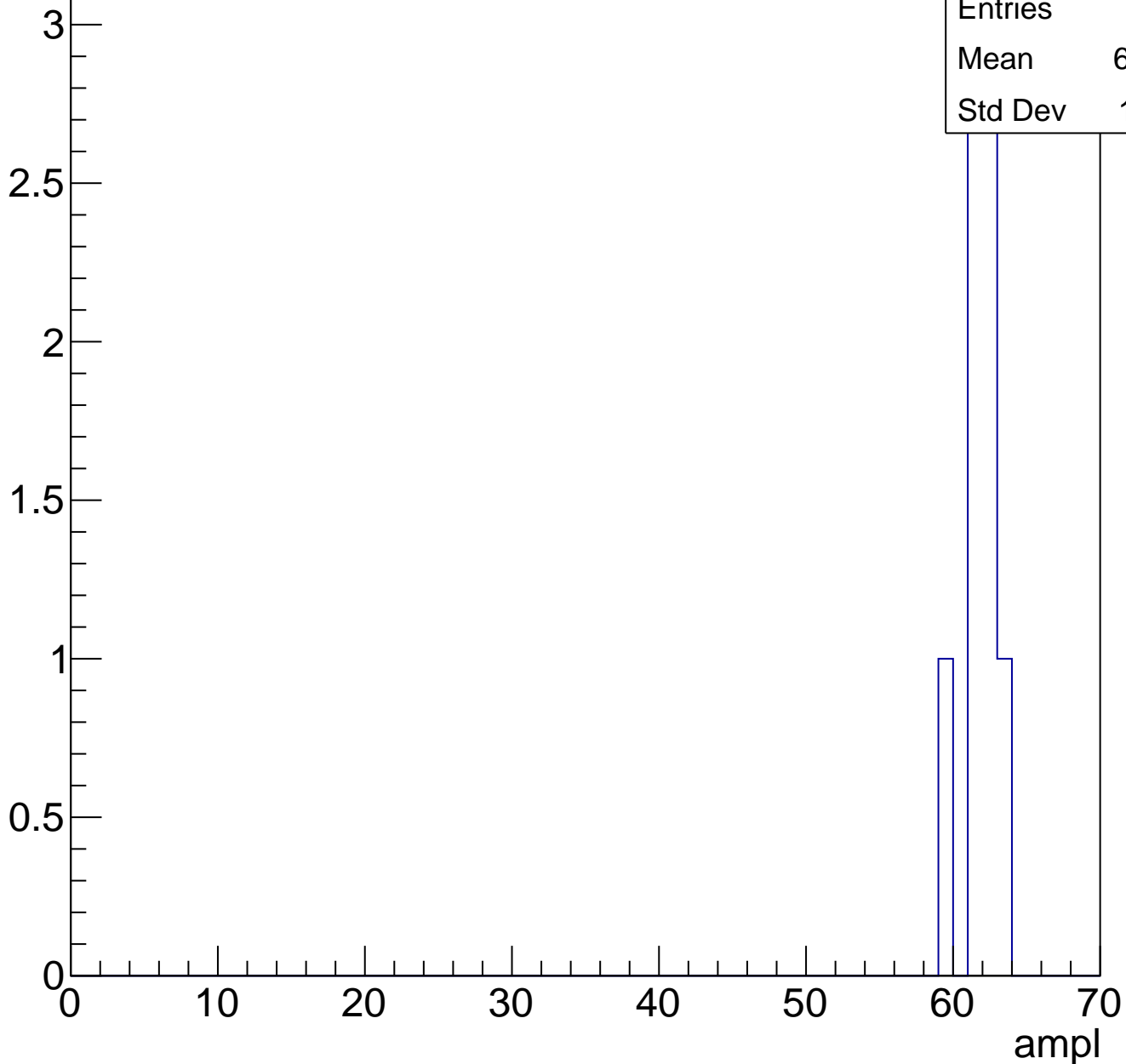
Entries	50
Mean	58.34
Std Dev	8.804



B1L102S, U6-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch124, adc0

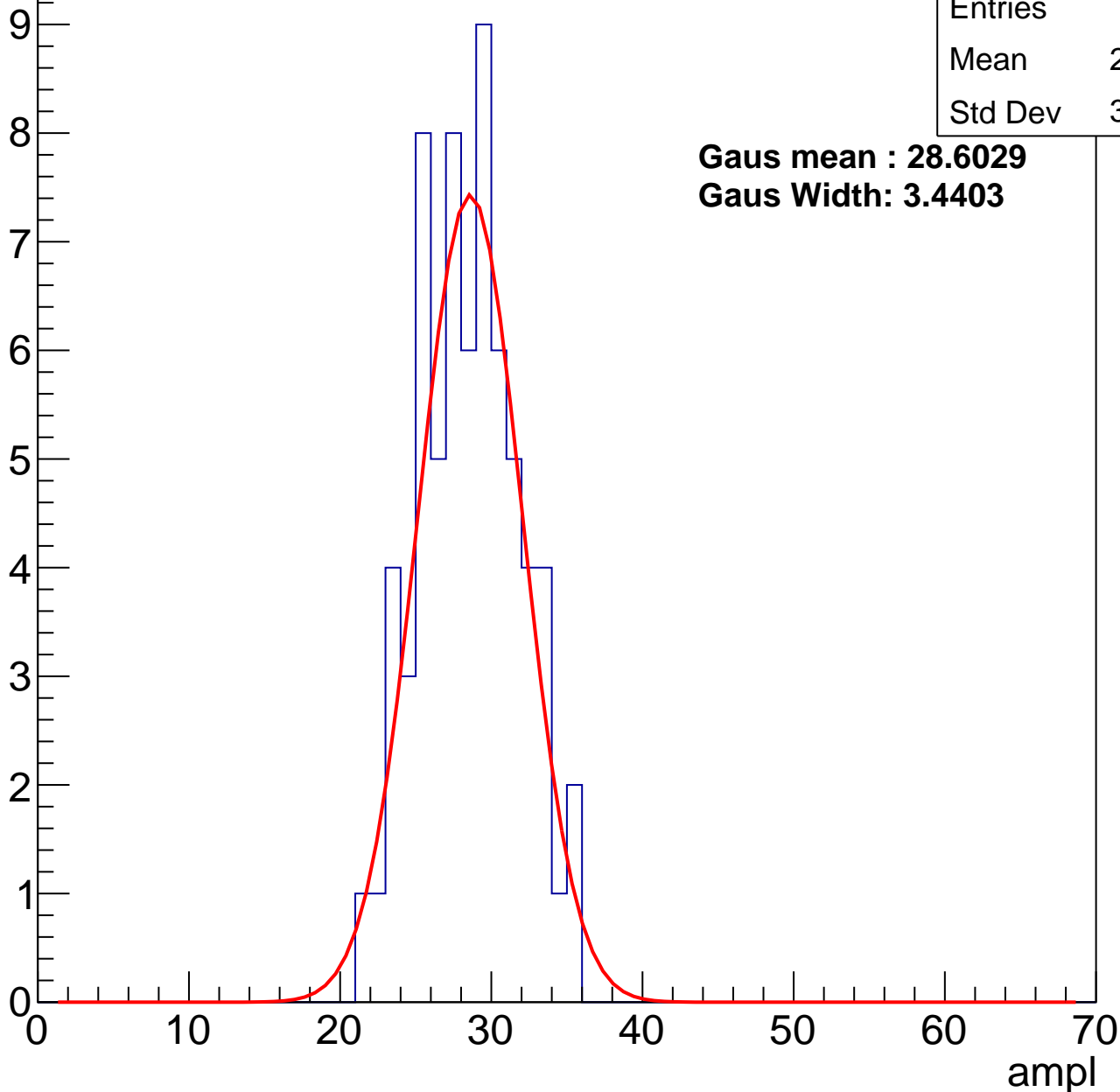
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.07
Std Dev	3.252

Gaus mean : 28.6029

Gaus Width: 3.4403



B1L102S, U6-ch124, adc1

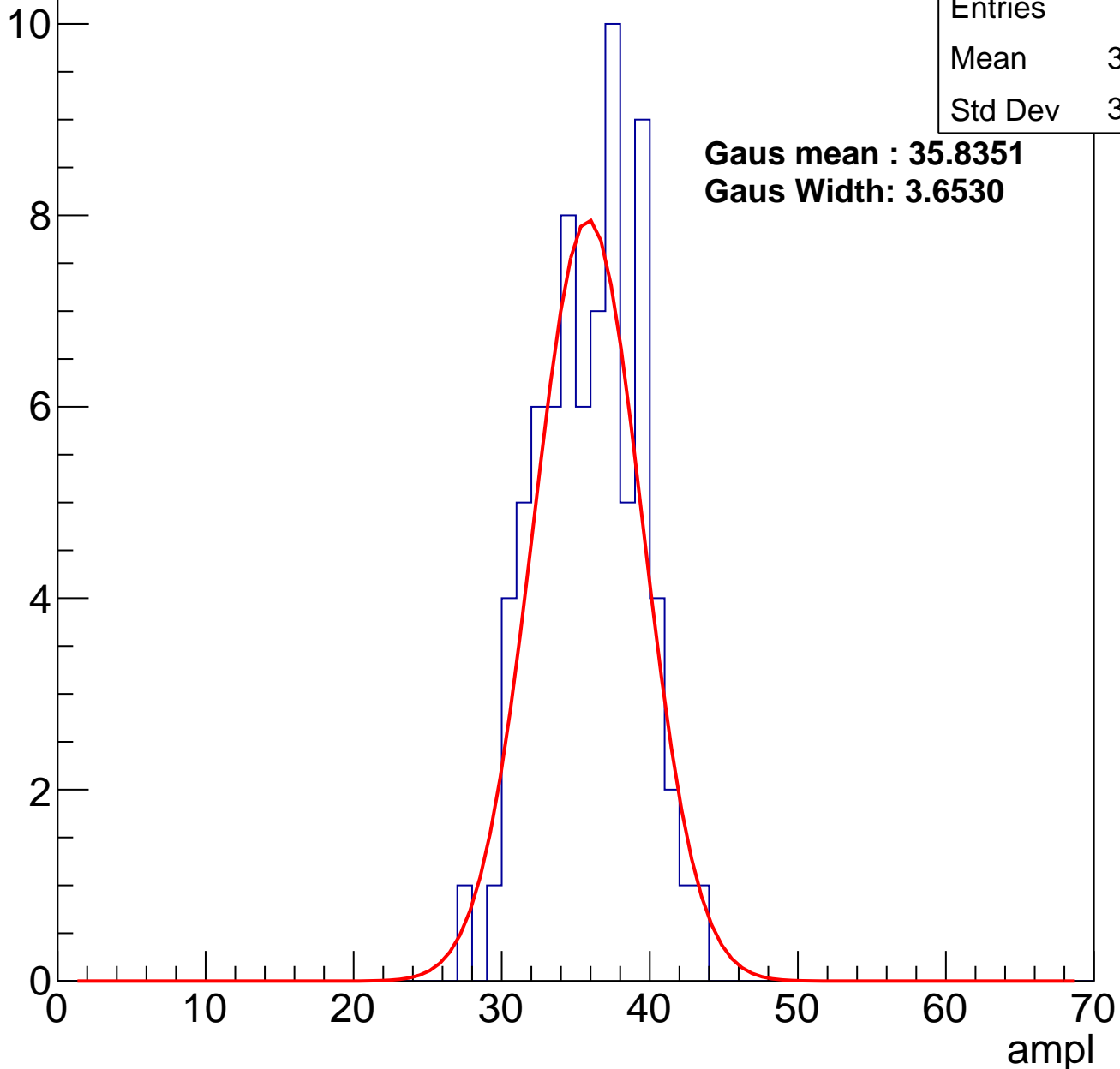
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	35.43
Std Dev	3.389

Gaus mean : 35.8351

Gaus Width: 3.6530

Entry



B1L102S, U6-ch124, adc2

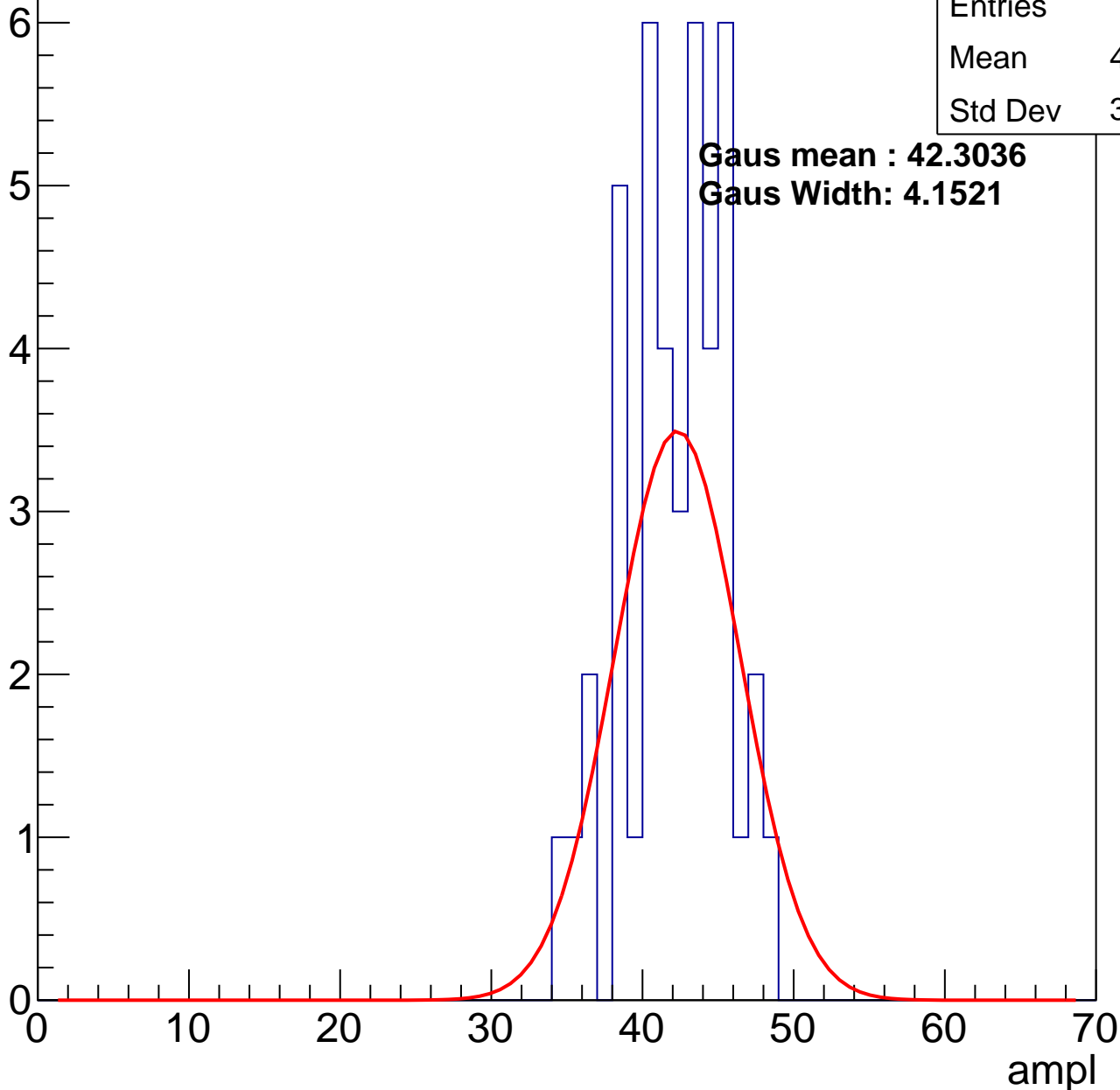
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	41.67
Std Dev	3.325

Gaus mean : 42.3036

Gaus Width: 4.1521

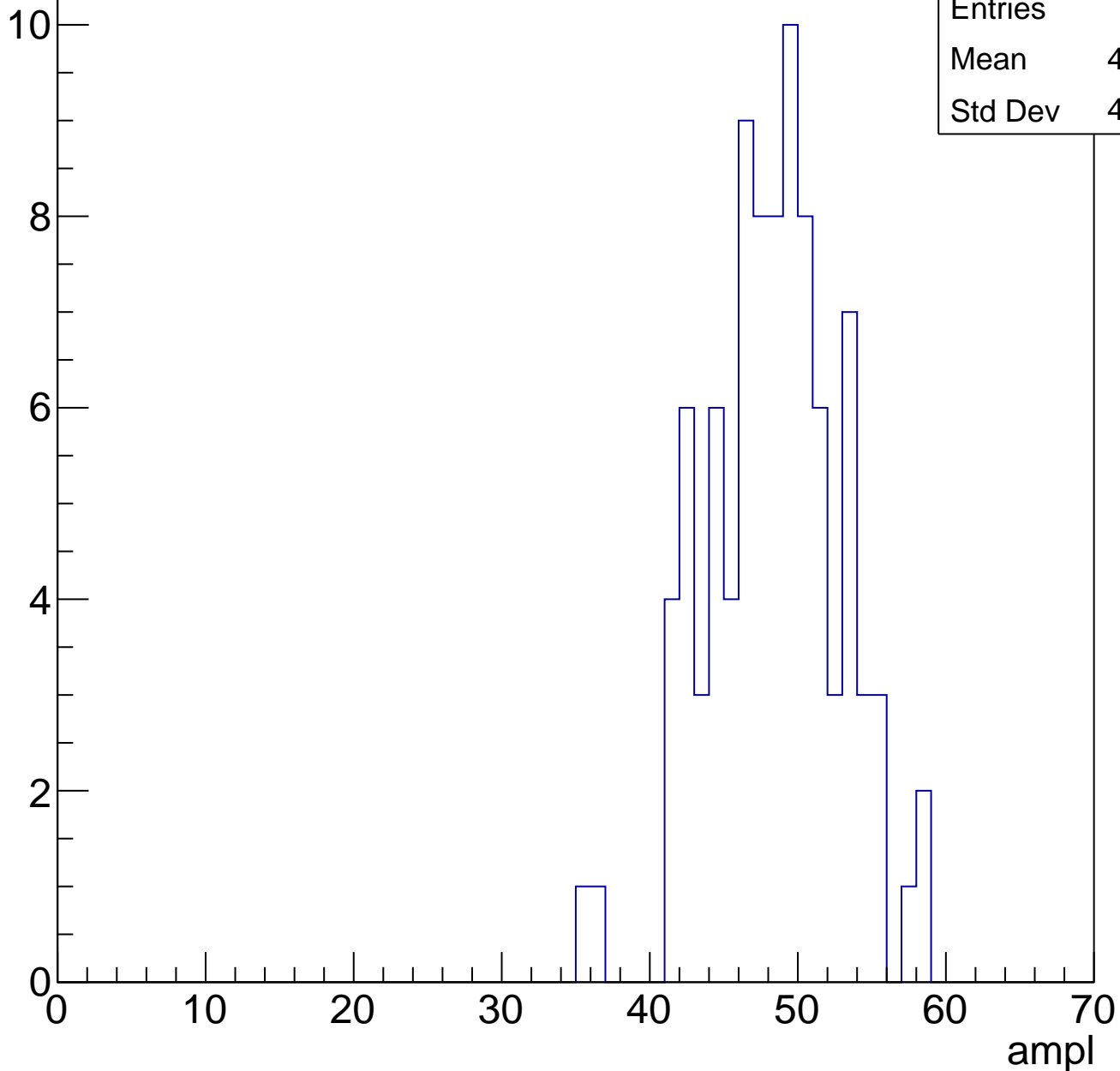


B1L102S, U6-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	93
Mean	47.92
Std Dev	4.436

Entry

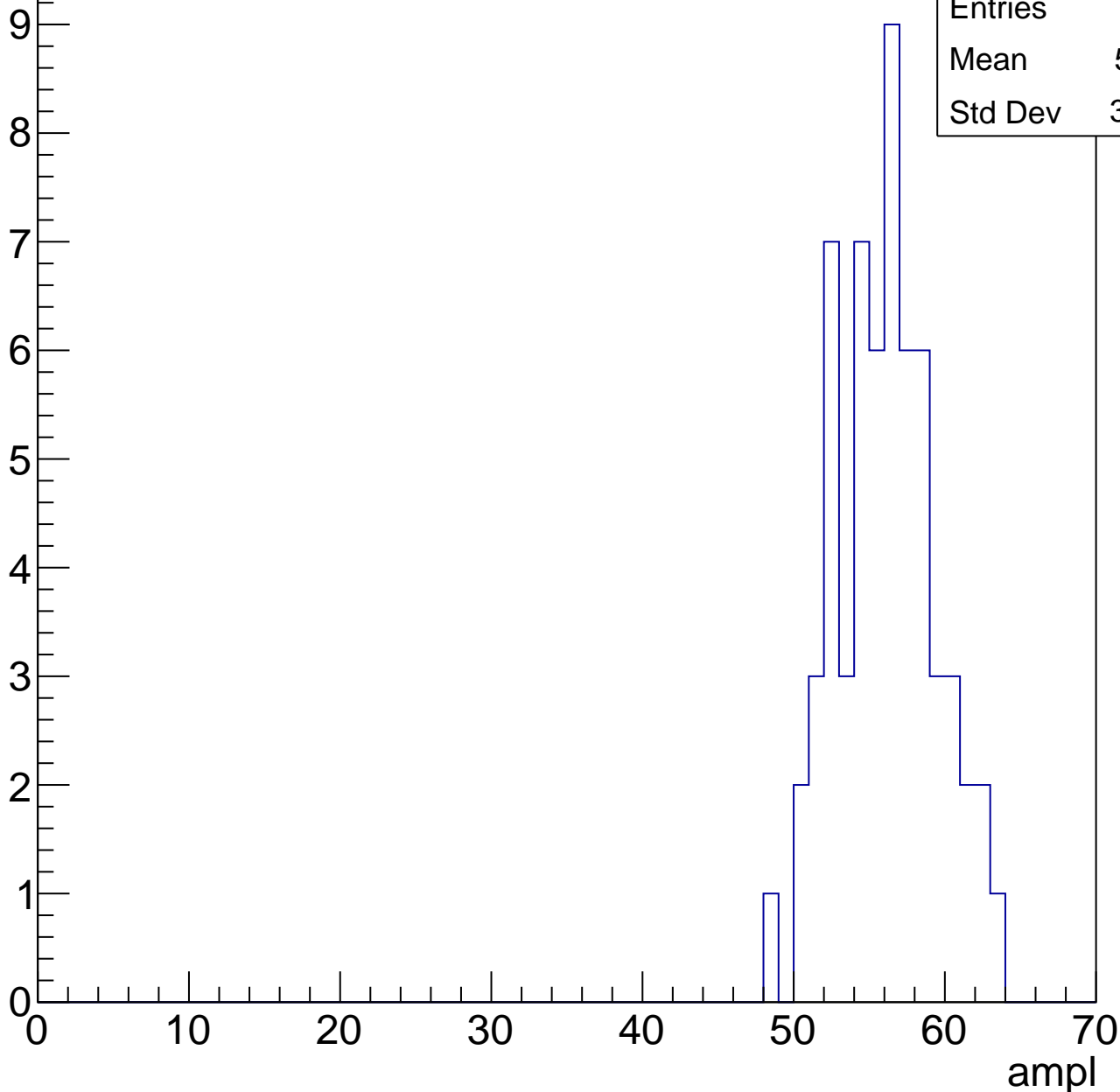


B1L102S, U6-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.61
Std Dev	3.266



B1L102S, U6-ch124, adc5

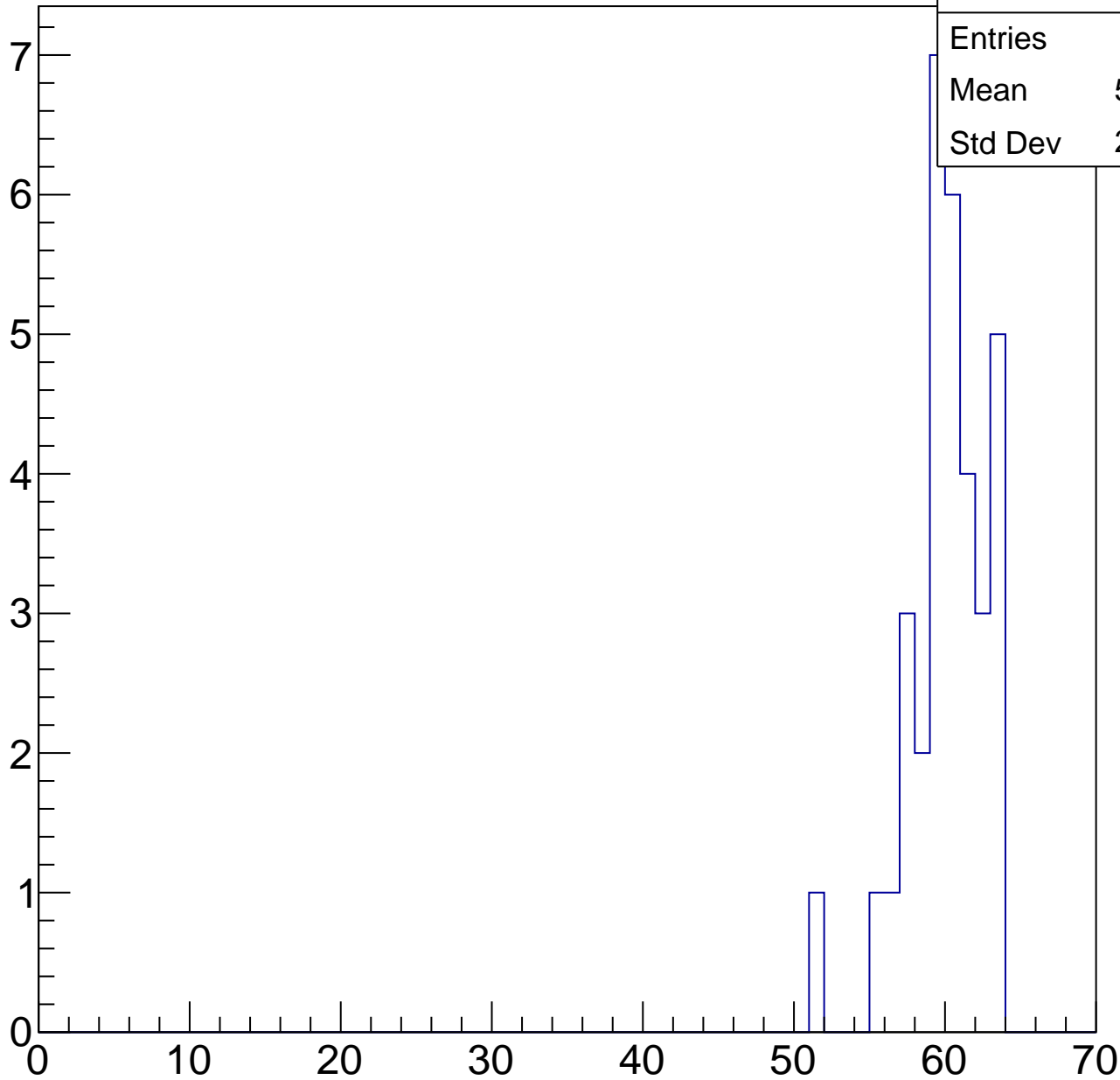
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	33
Mean	59.61
Std Dev	2.581

ampl

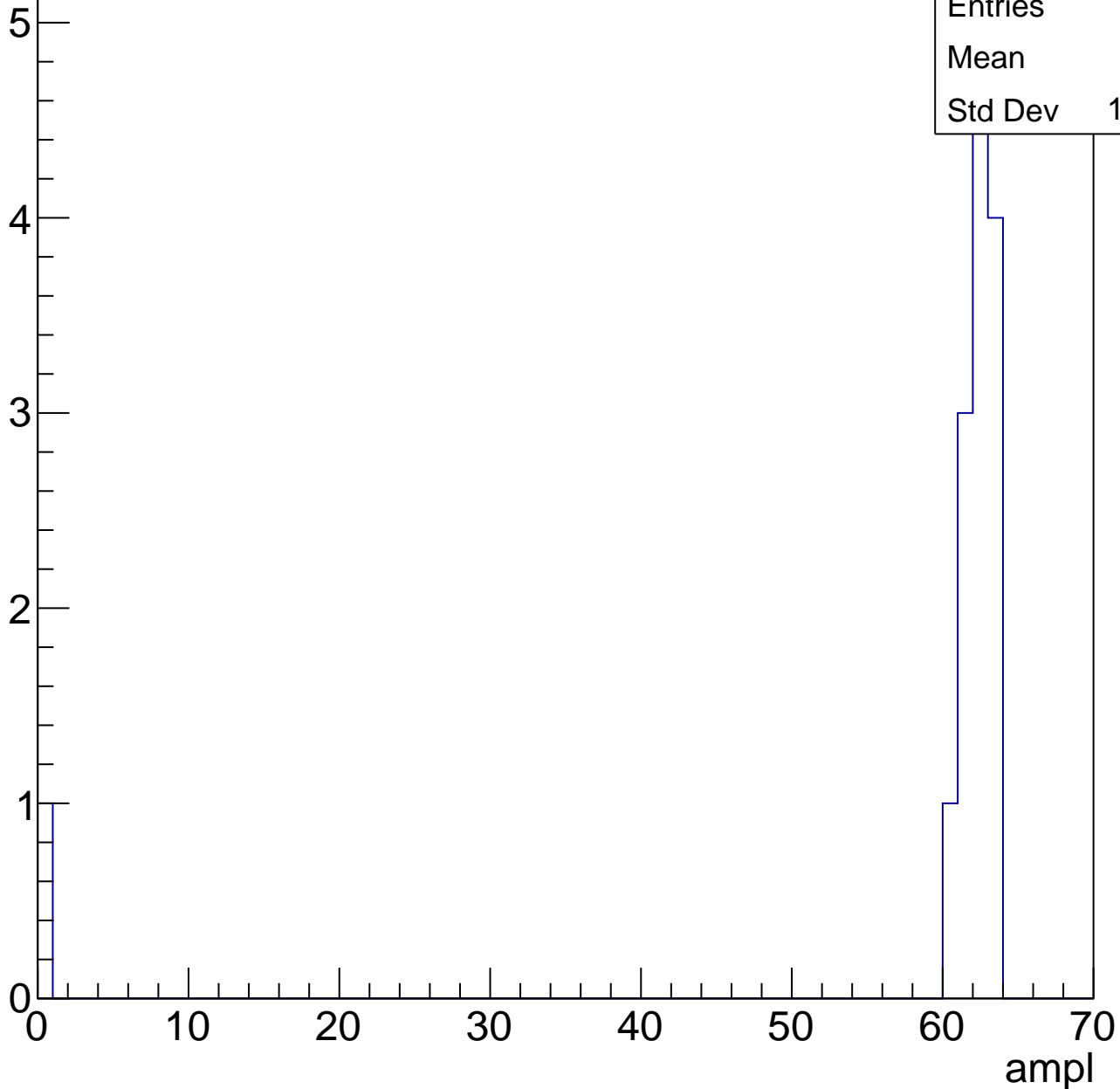


B1L102S, U6-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

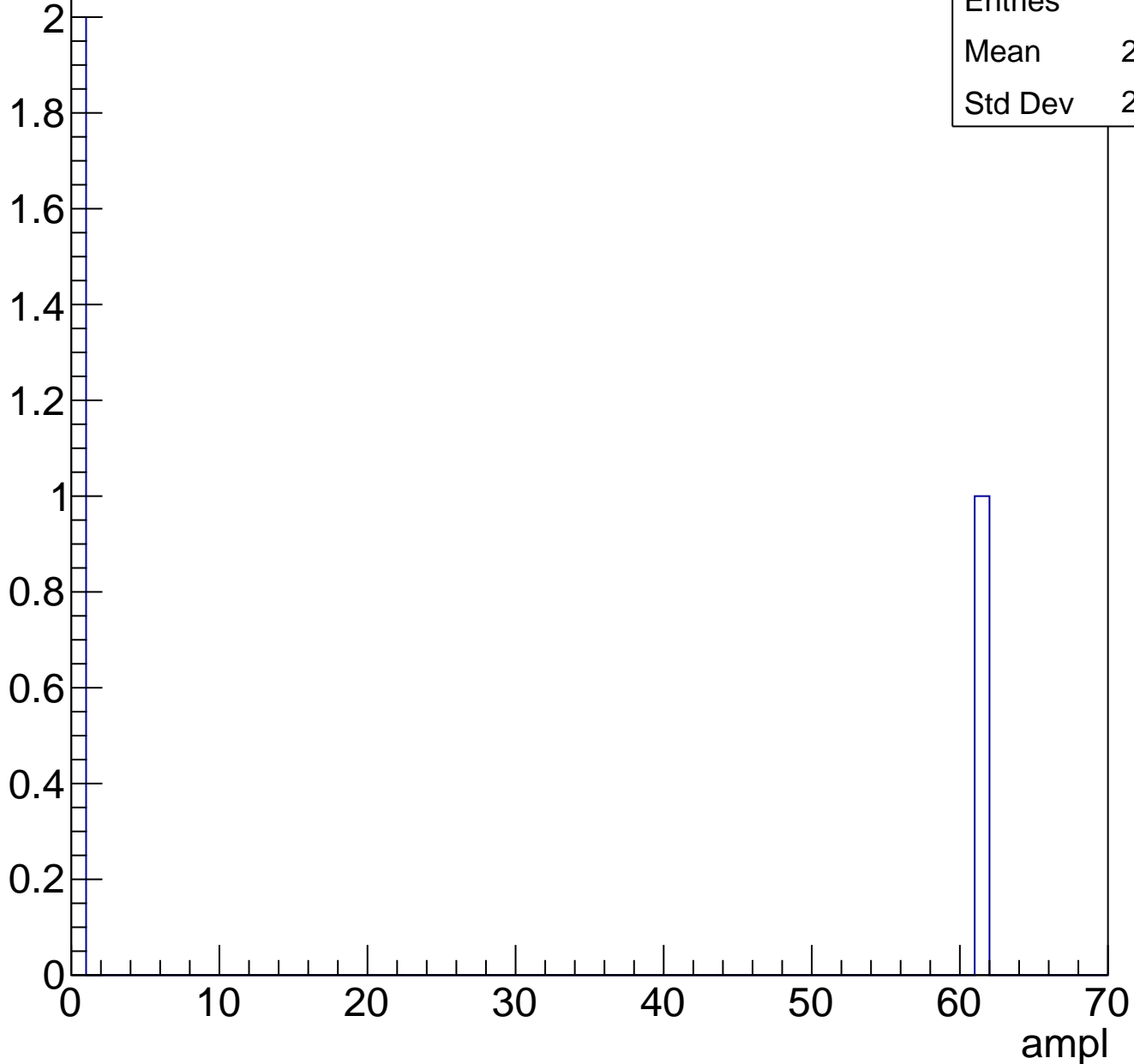
Entries	14
Mean	57.5
Std Dev	15.97



B1L102S, U6-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch125, adc0

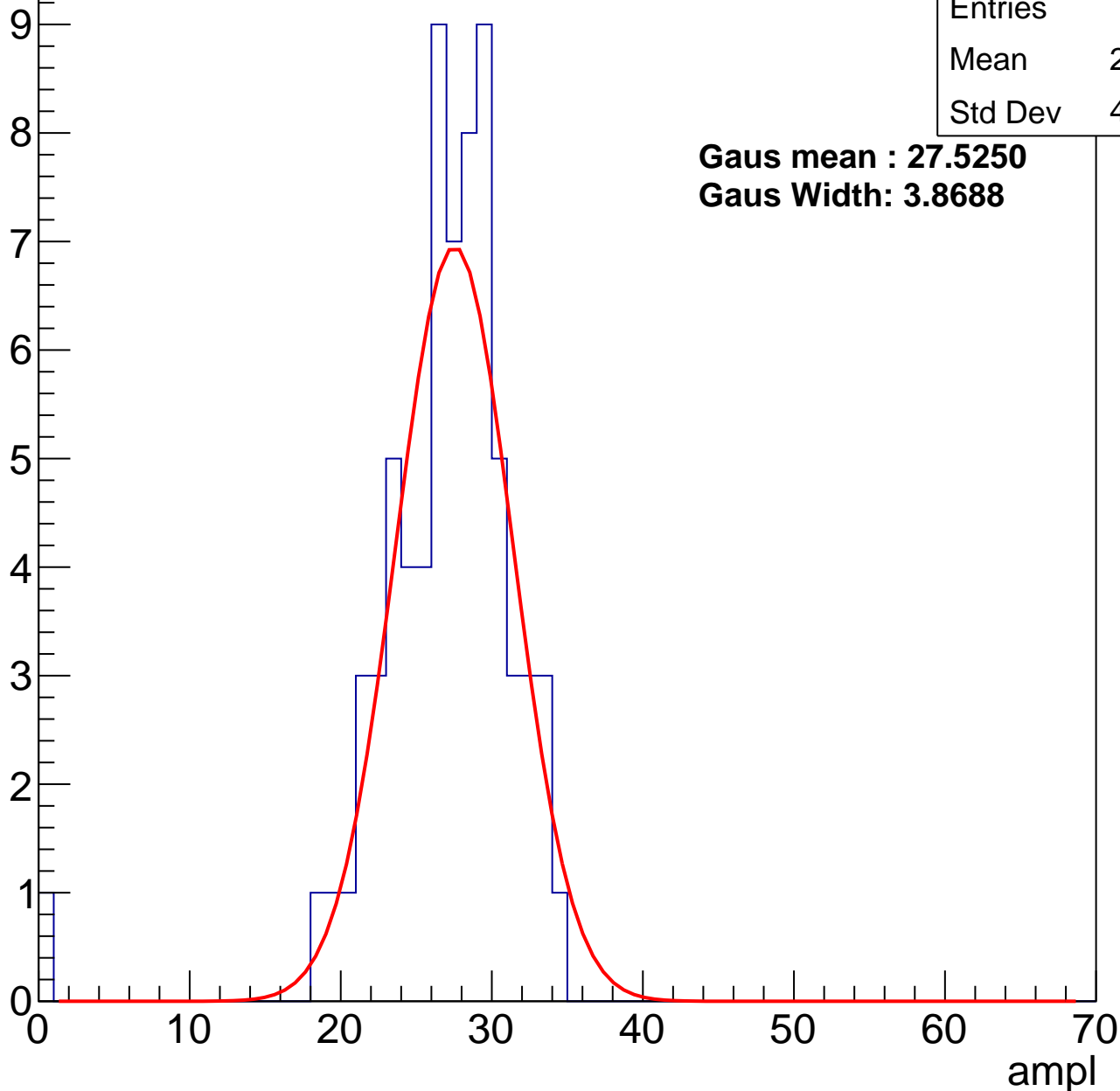
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.44
Std Dev	4.737

Gaus mean : 27.5250

Gaus Width: 3.8688



B1L102S, U6-ch125, adc1

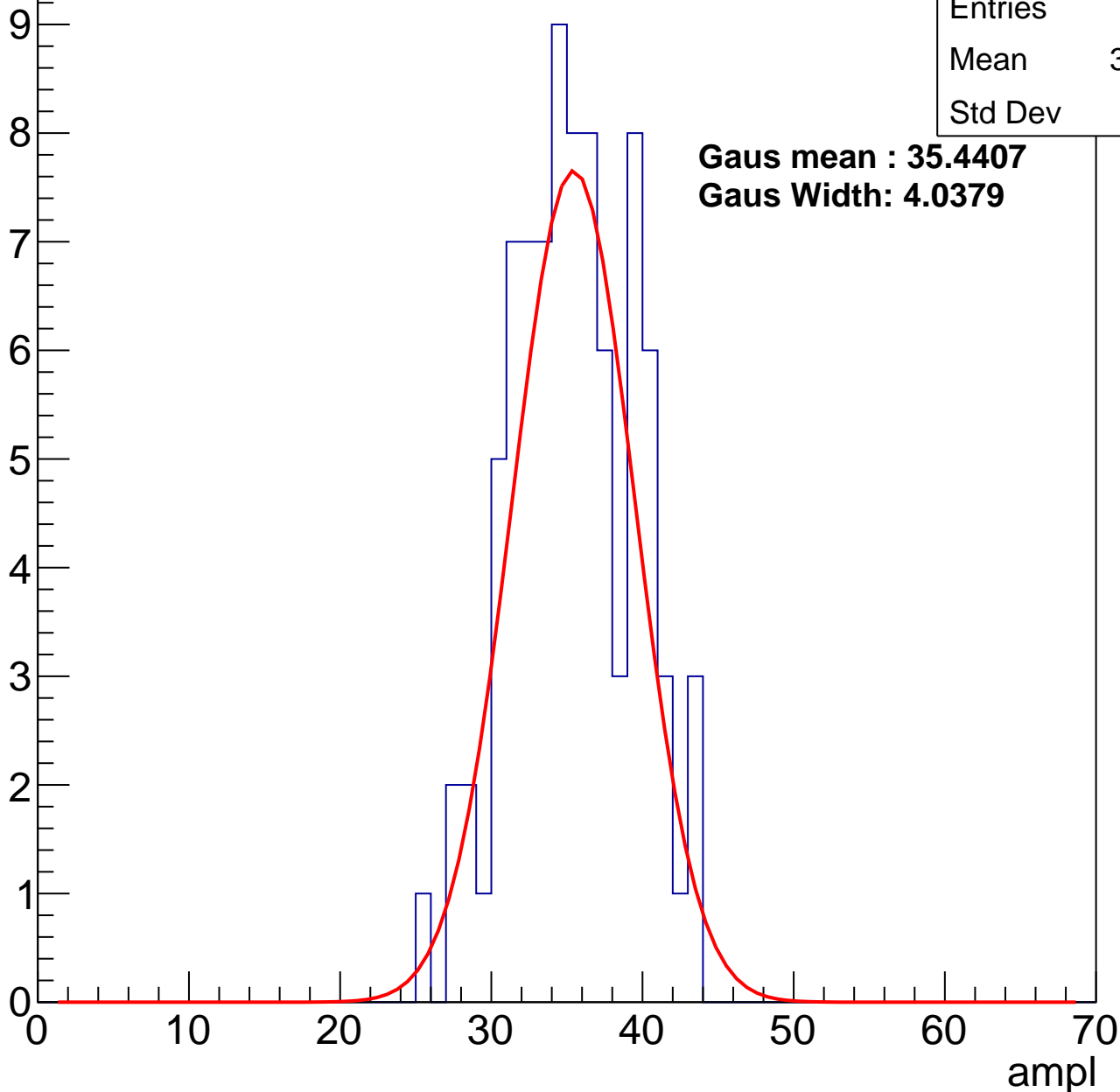
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	34.97
Std Dev	3.99

Gaus mean : 35.4407

Gaus Width: 4.0379



B1L102S, U6-ch125, adc2

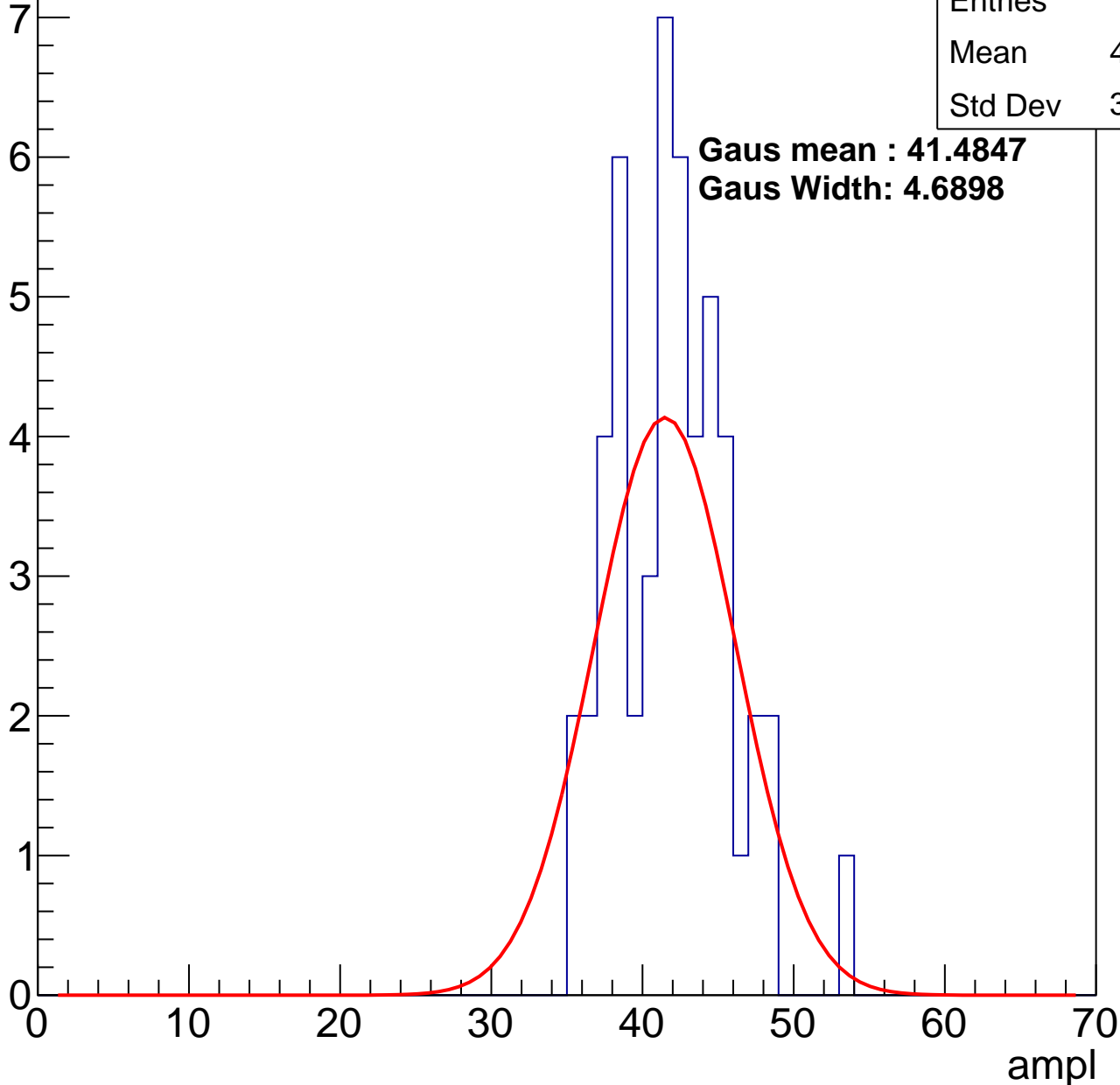
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.49
Std Dev	3.728

Gaus mean : 41.4847

Gaus Width: 4.6898

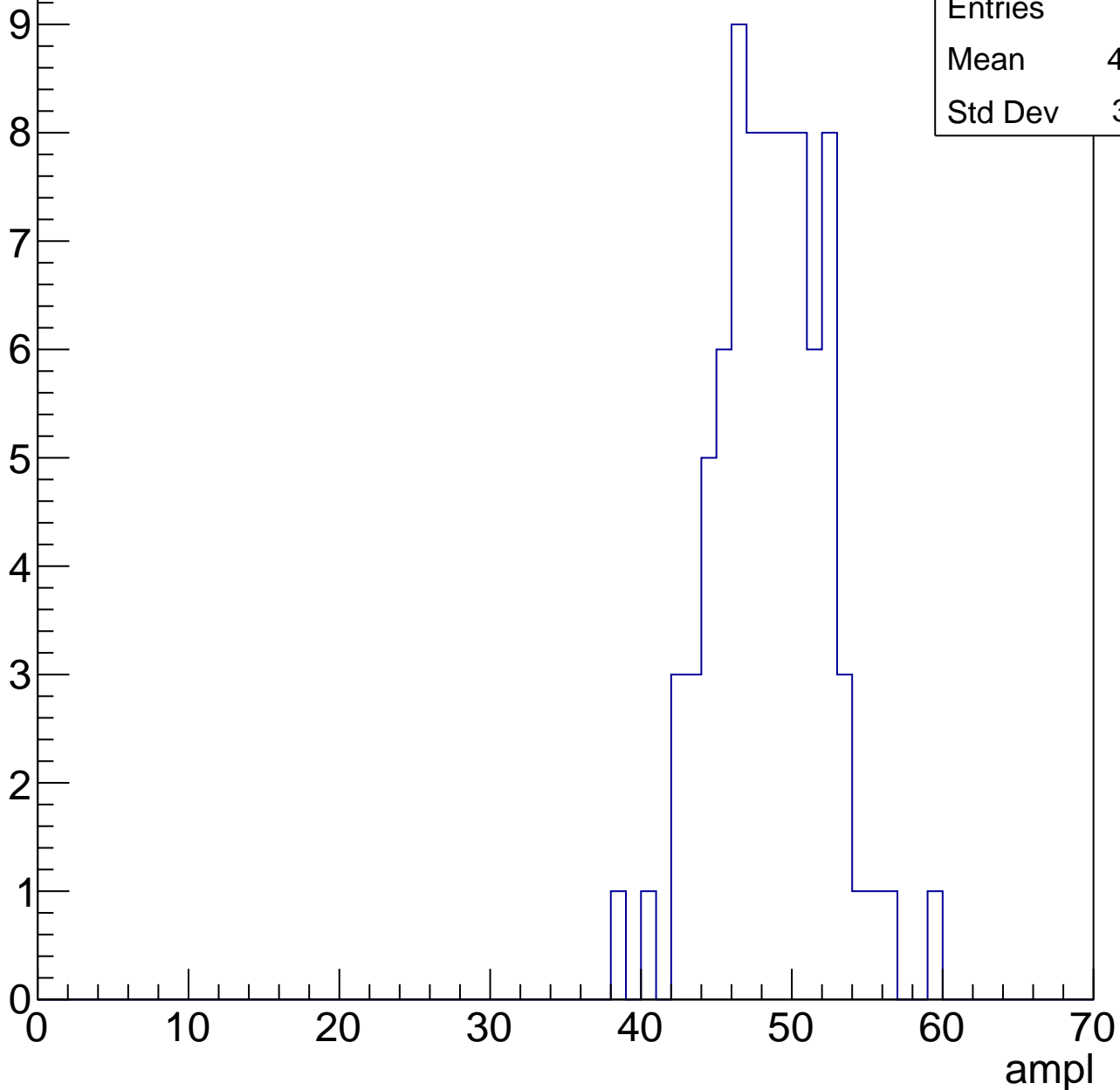


B1L102S, U6-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	48.07
Std Dev	3.671

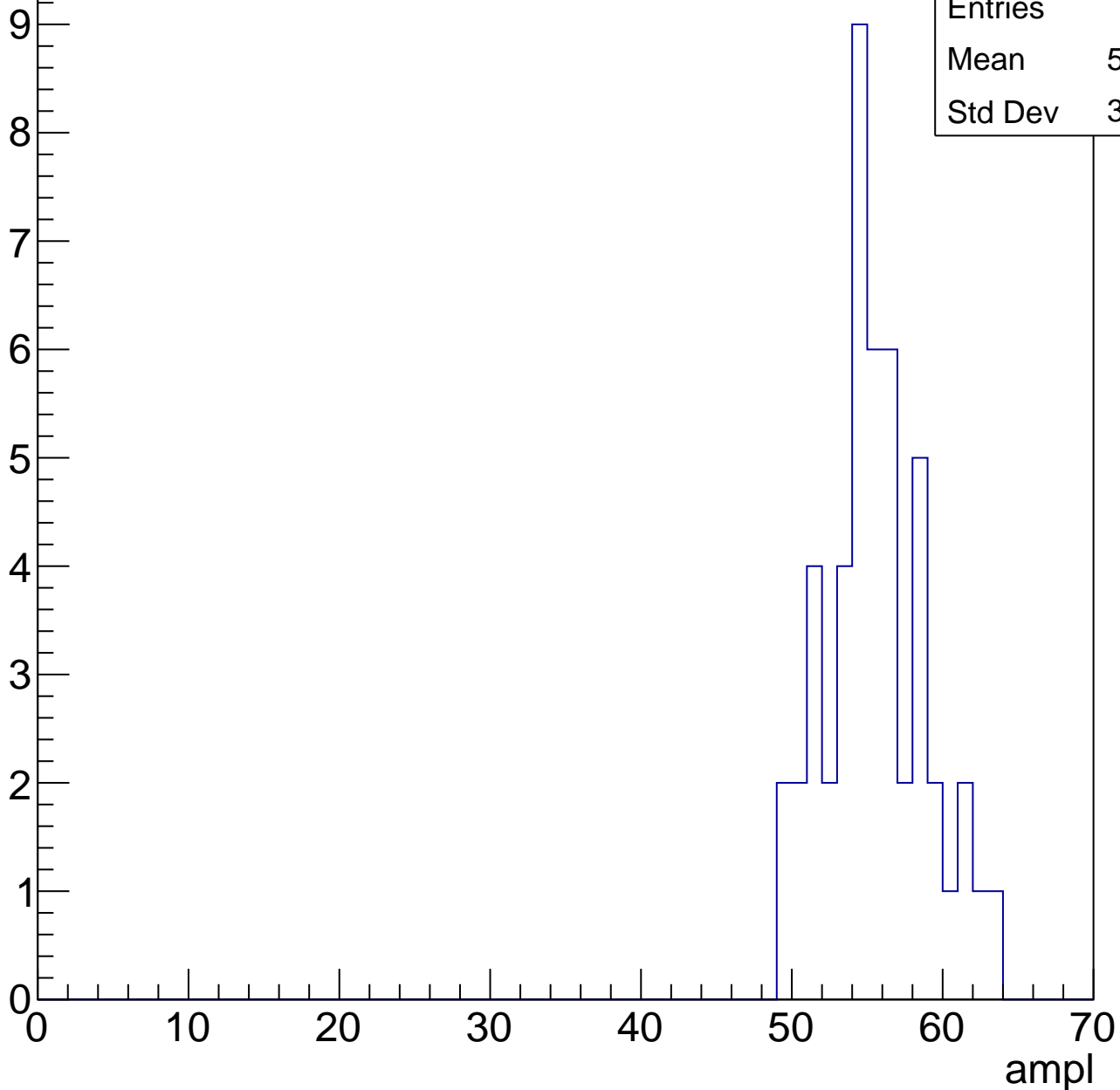


B1L102S, U6-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55.08
Std Dev	3.288



B1L102S, U6-ch125, adc5

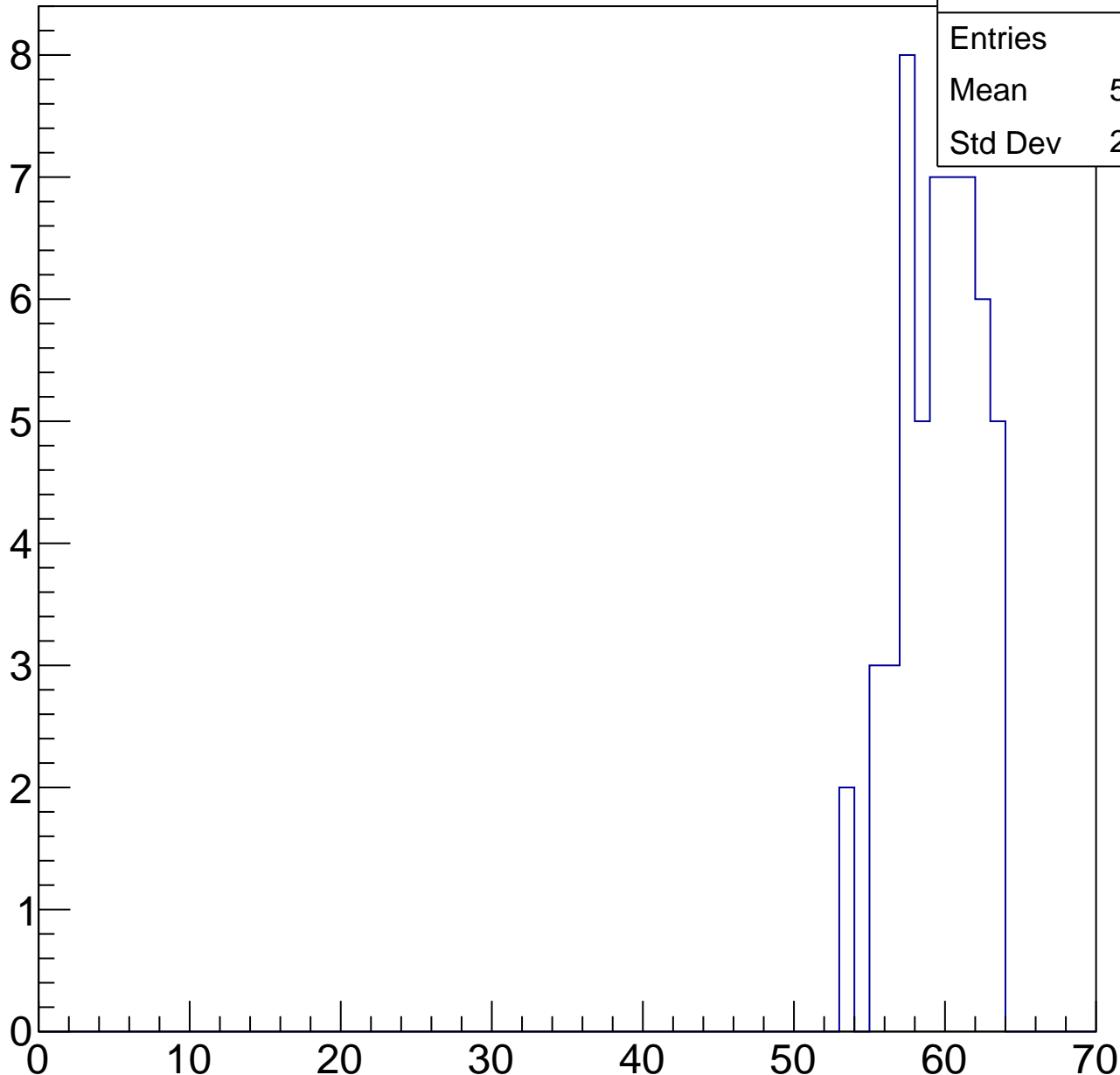
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.09
Std Dev	2.579

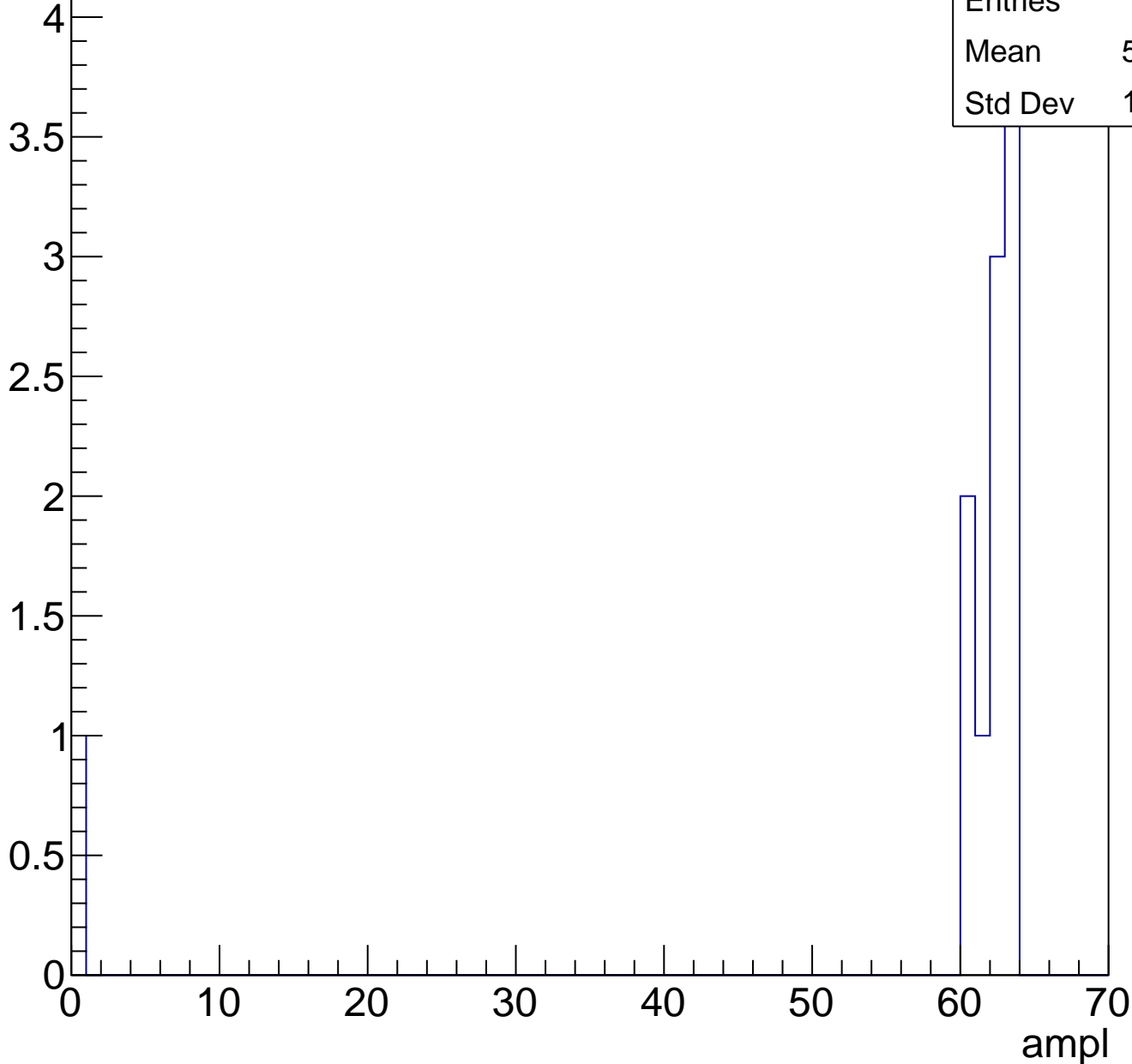
ampl



B1L102S, U6-ch125, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch126, adc0

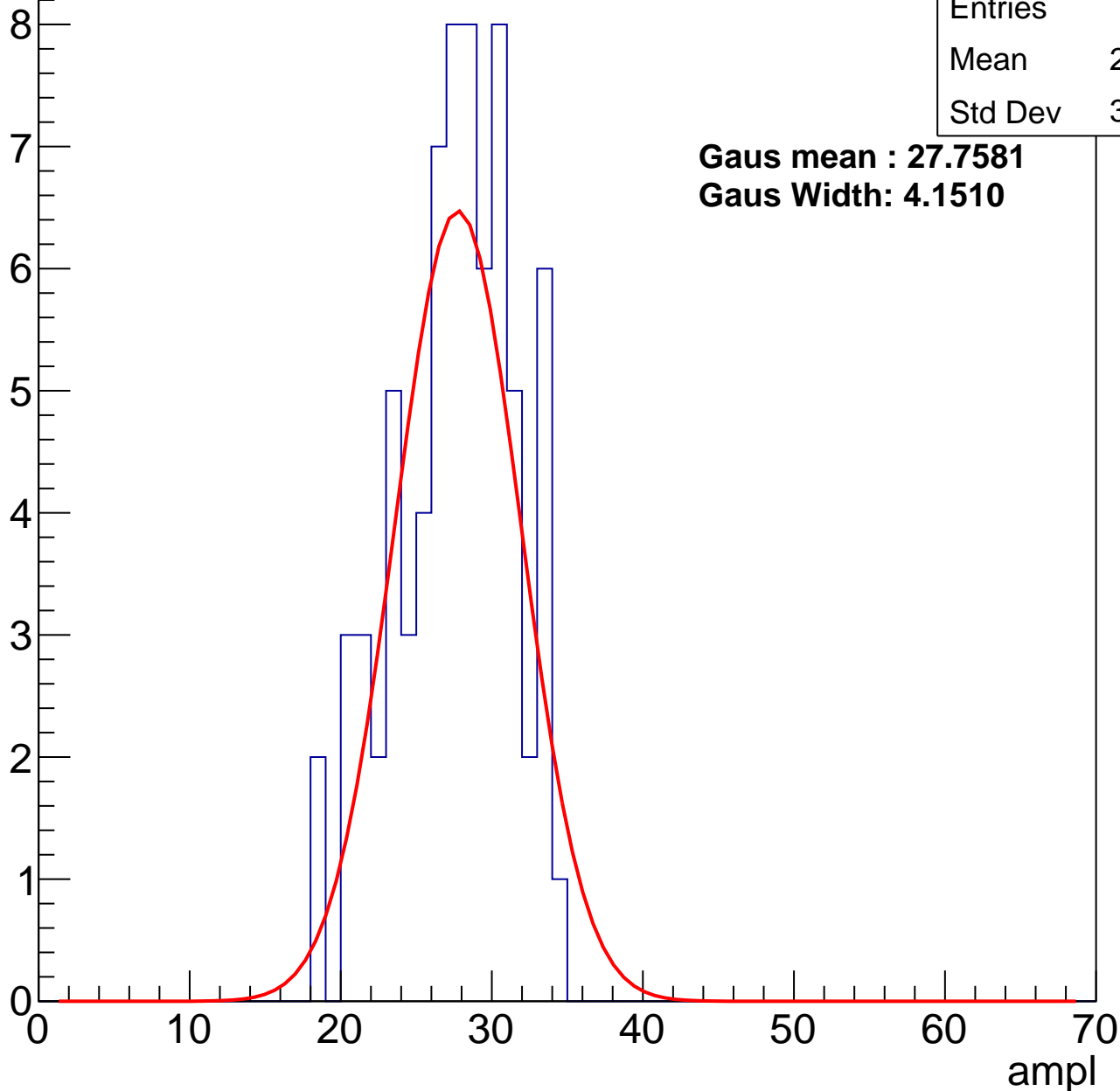
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.08
Std Dev	3.874

Gaus mean : 27.7581

Gaus Width: 4.1510



B1L102S, U6-ch126, adc1

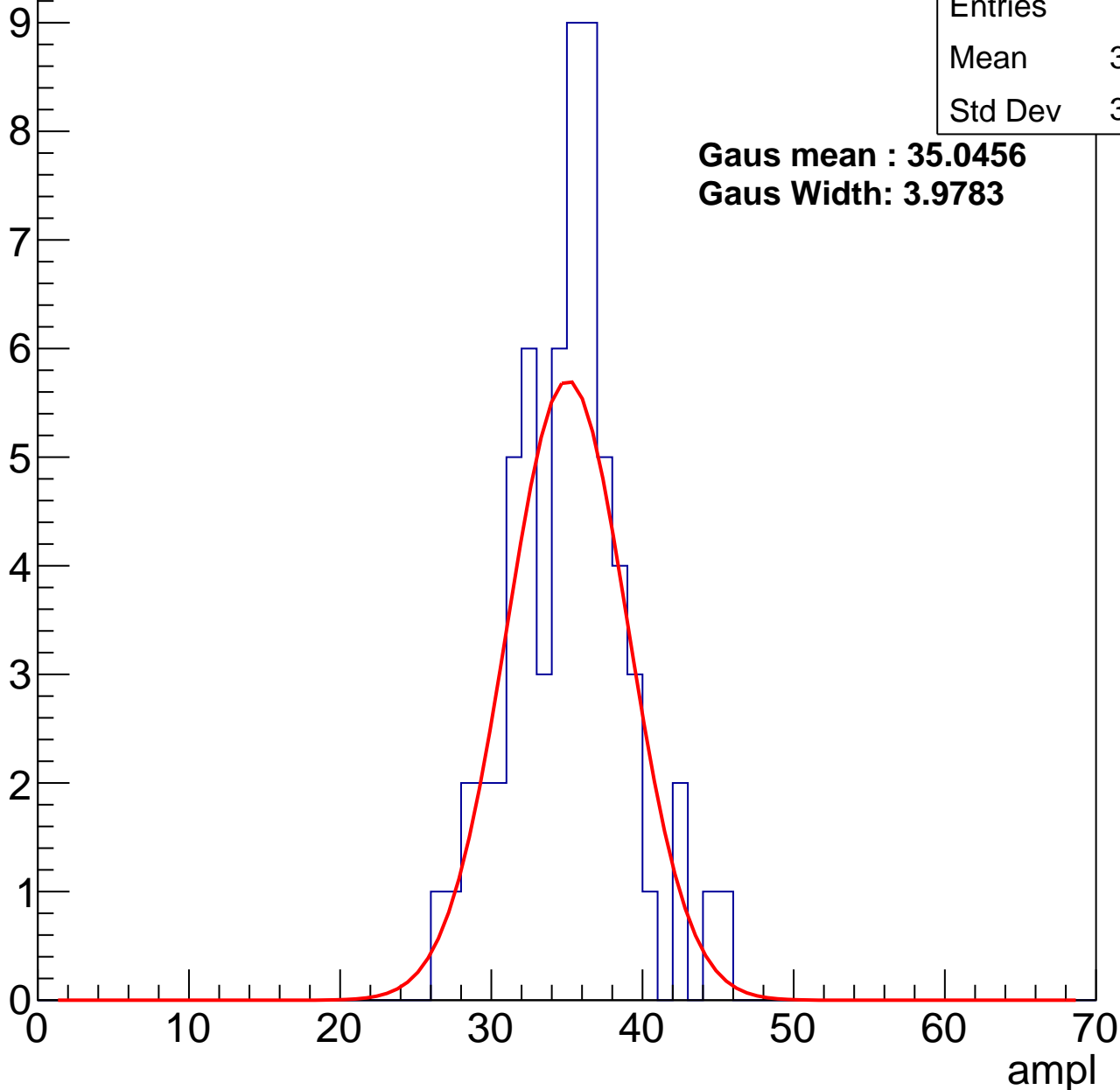
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.65
Std Dev	3.835

Gaus mean : 35.0456

Gaus Width: 3.9783



B1L102S, U6-ch126, adc2

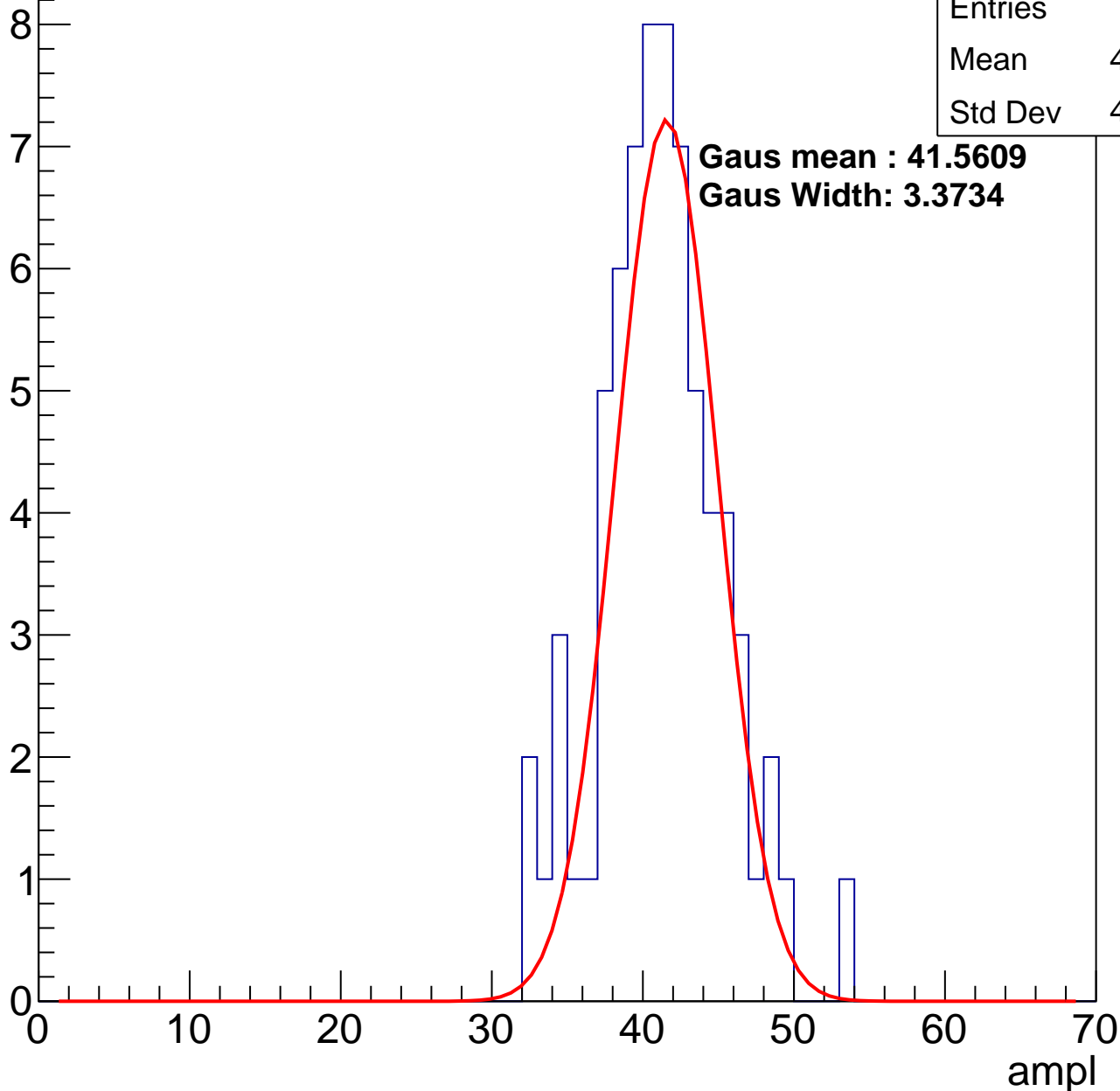
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	40.74
Std Dev	4.042

Gaus mean : 41.5609

Gaus Width: 3.3734

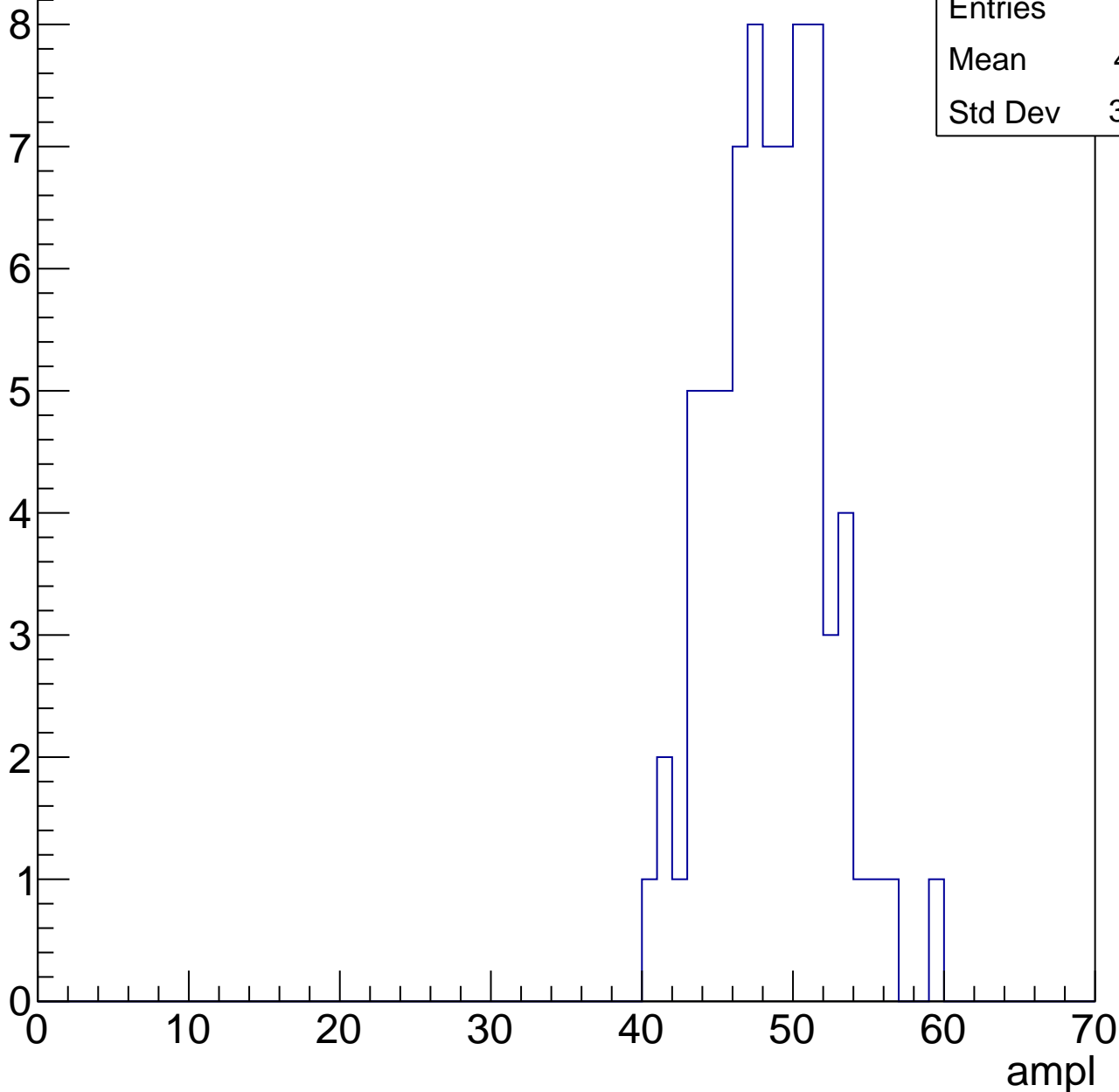


B1L102S, U6-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	48.01
Std Dev	3.675

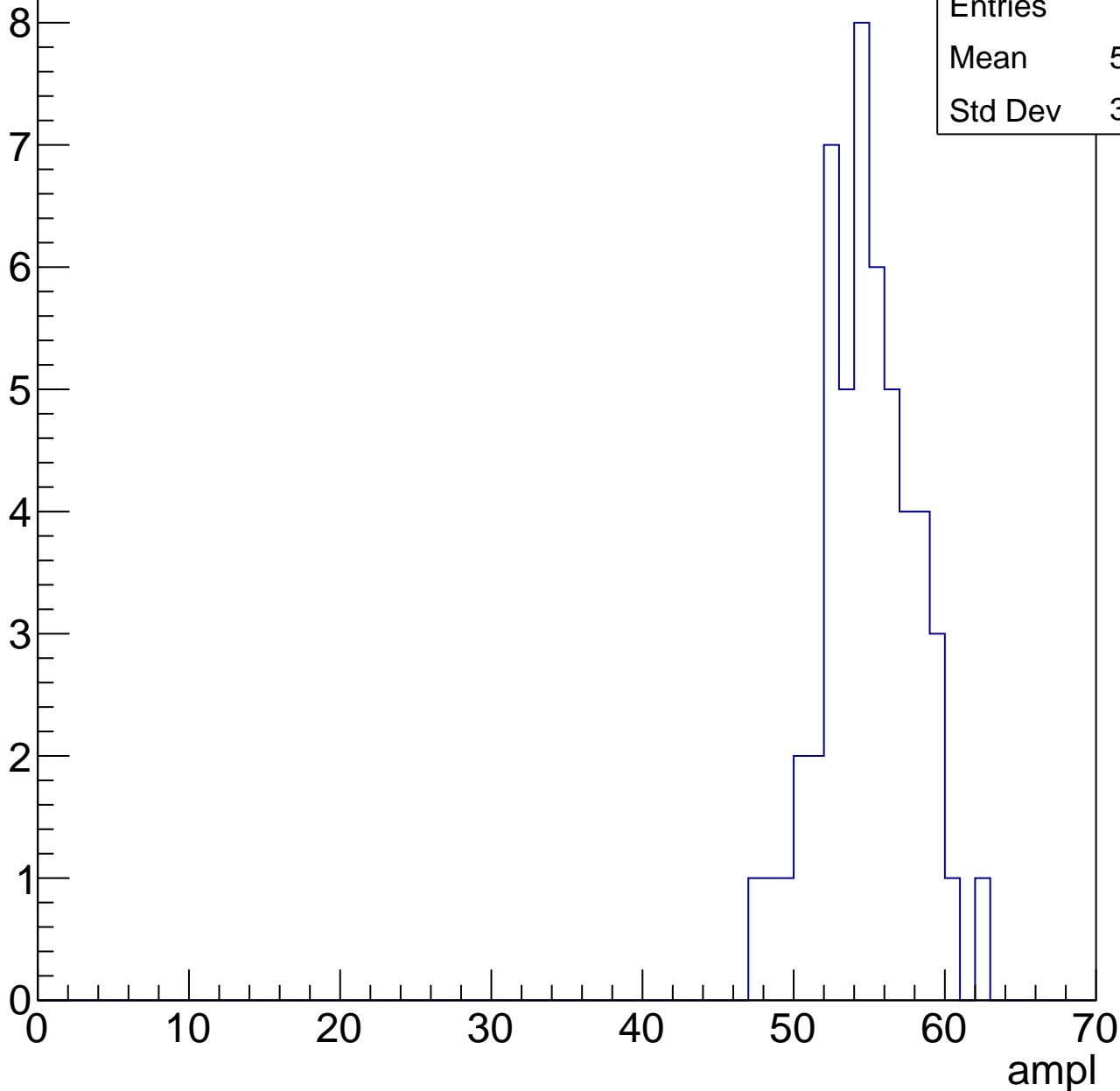


B1L102S, U6-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

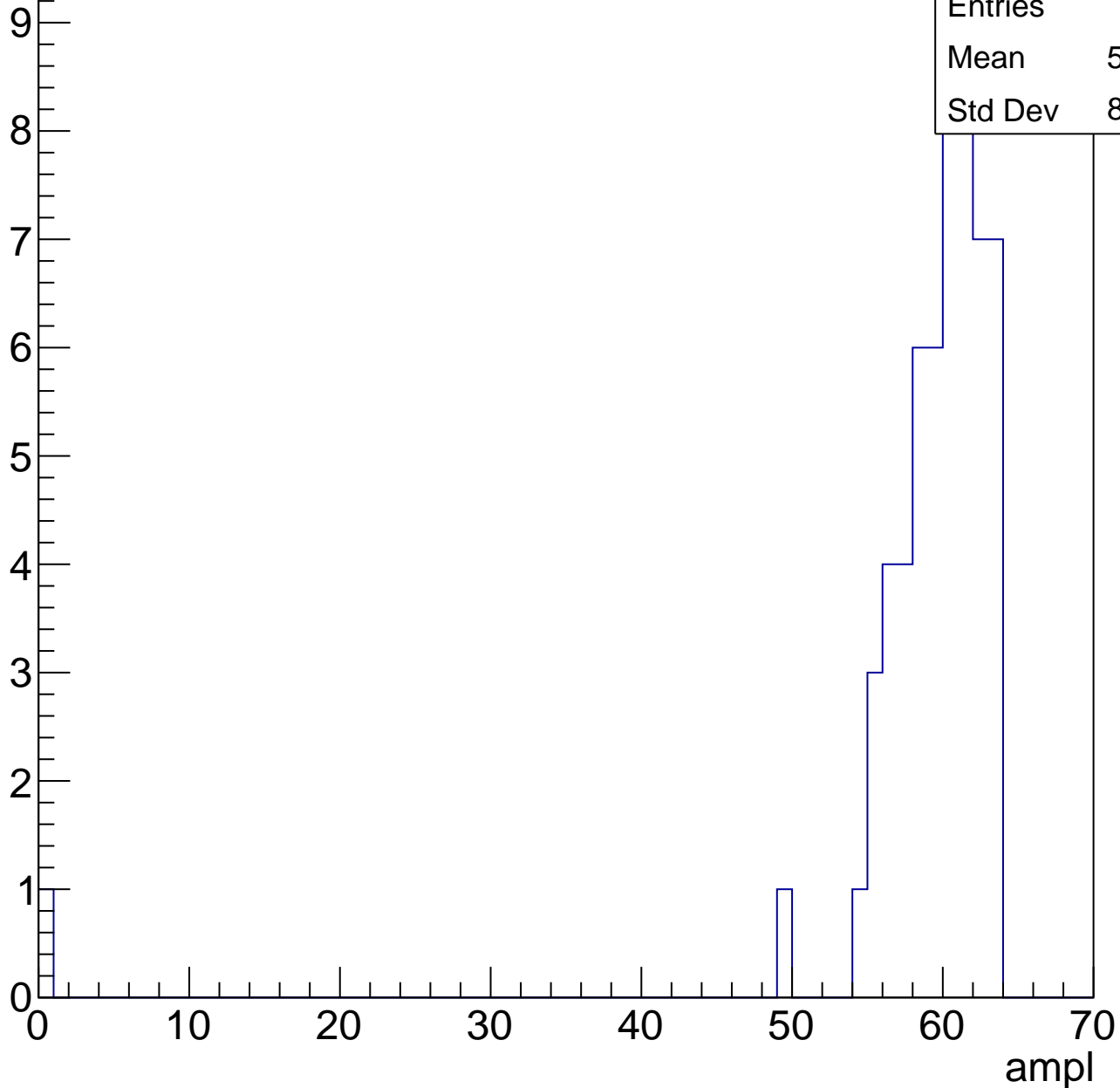
Entries	51
Mean	54.43
Std Dev	3.082



B1L102S, U6-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

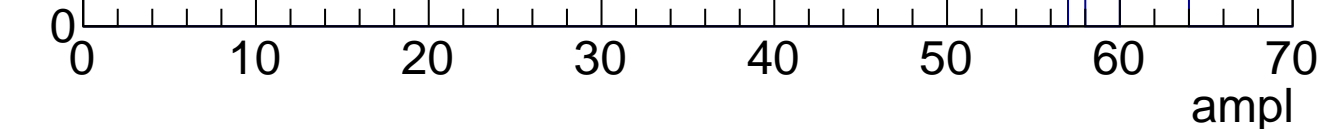
9

Mean

60.89

Std Dev

2.283



B1L102S, U6-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch127, adc0

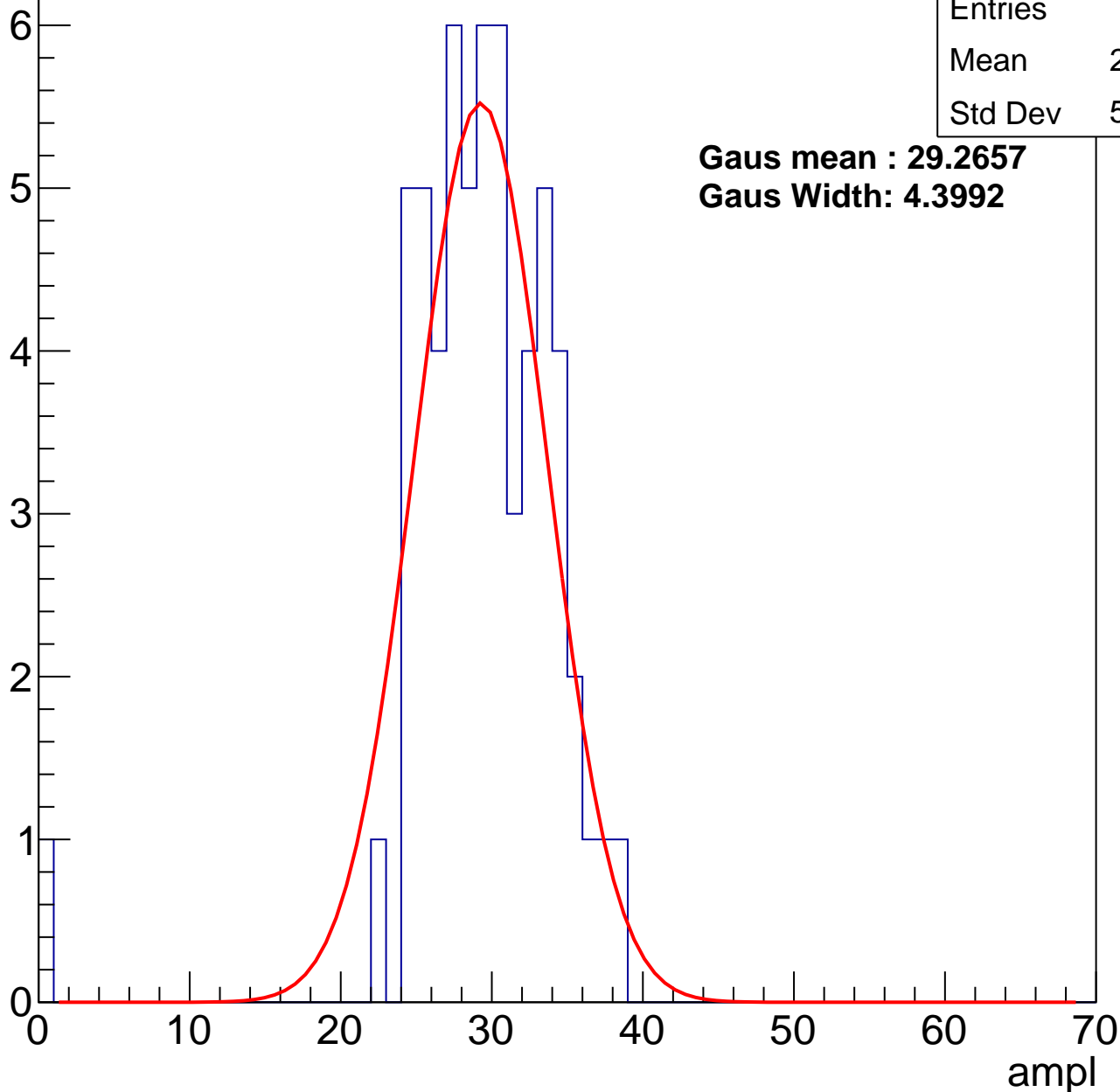
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.83
Std Dev	5.257

Gaus mean : 29.2657

Gaus Width: 4.3992



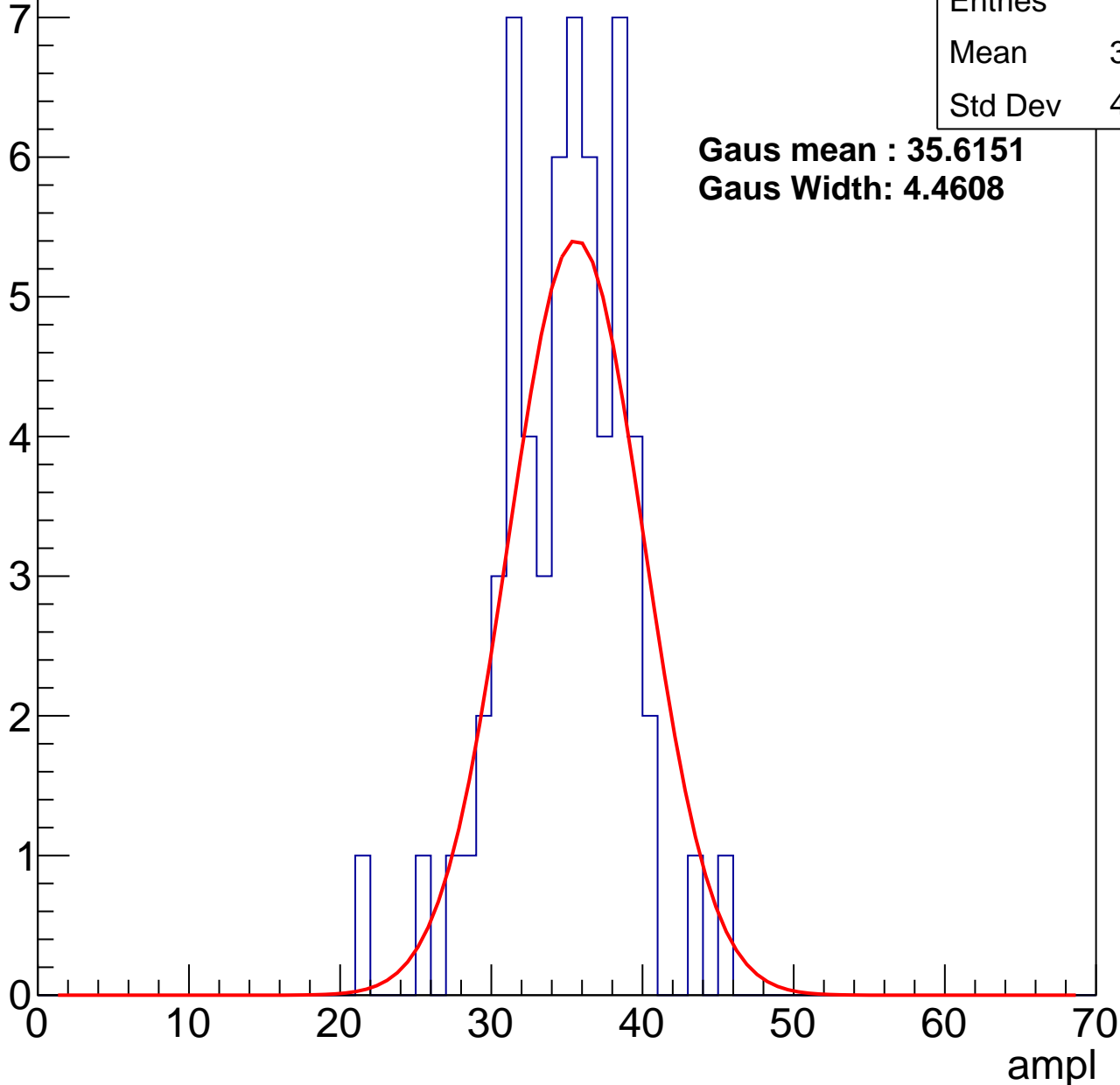
B1L102S, U6-ch127, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.36
Std Dev	4.169

Gaus mean : 35.6151
Gaus Width: 4.4608



B1L102S, U6-ch127, adc2

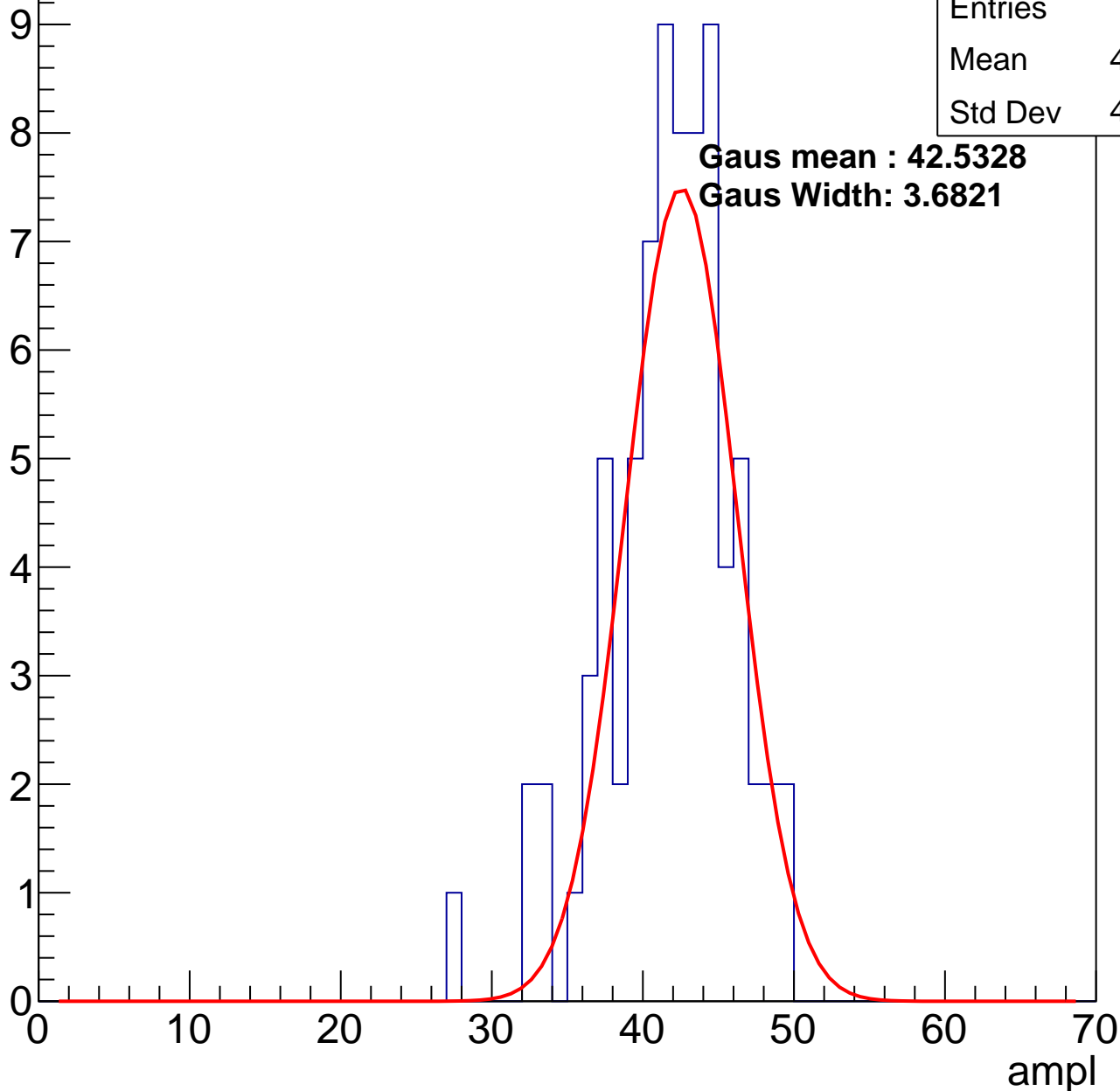
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.29
Std Dev	4.146

Gaus mean : 42.5328

Gaus Width: 3.6821

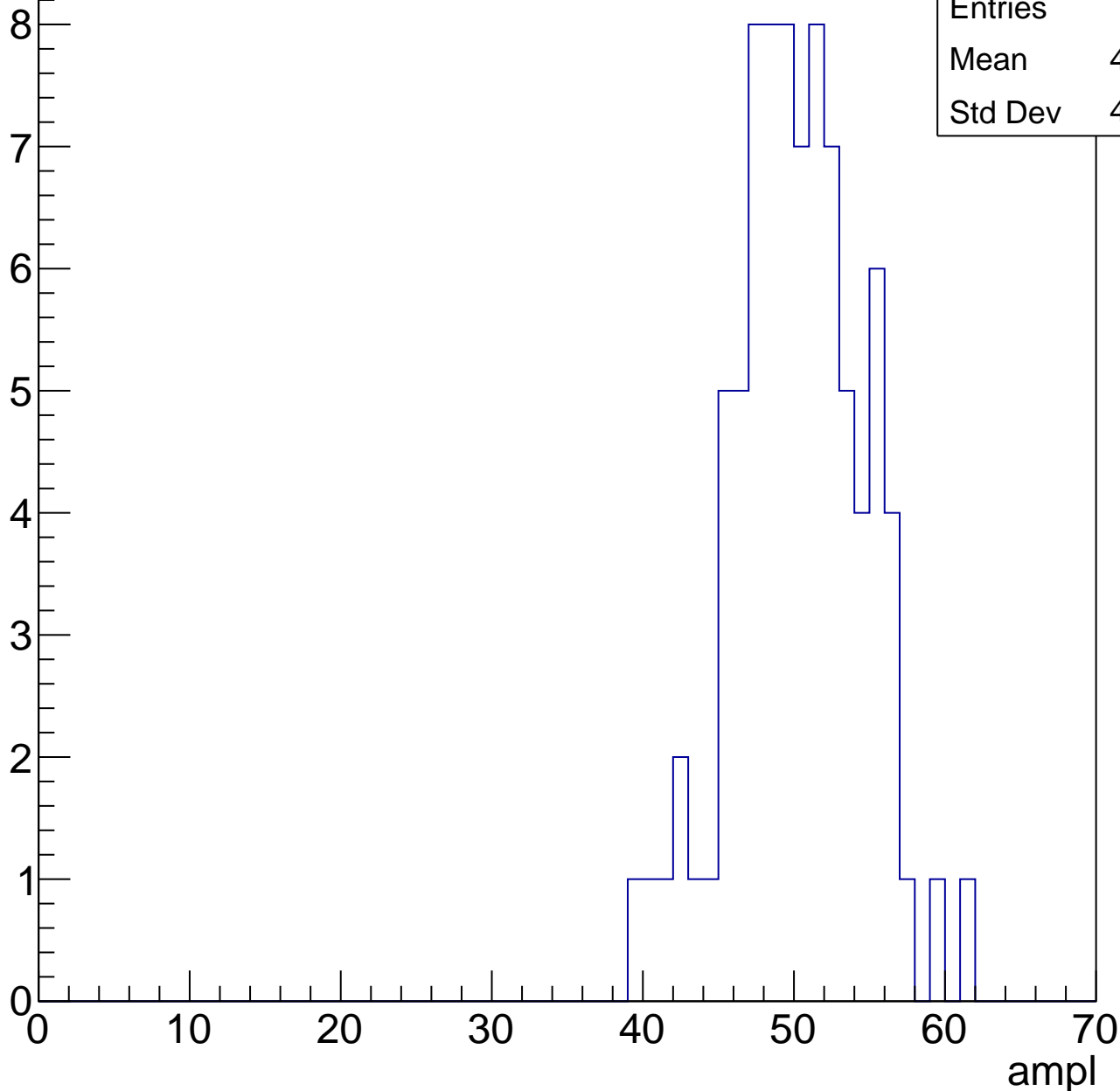


B1L102S, U6-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	49.79
Std Dev	4.226

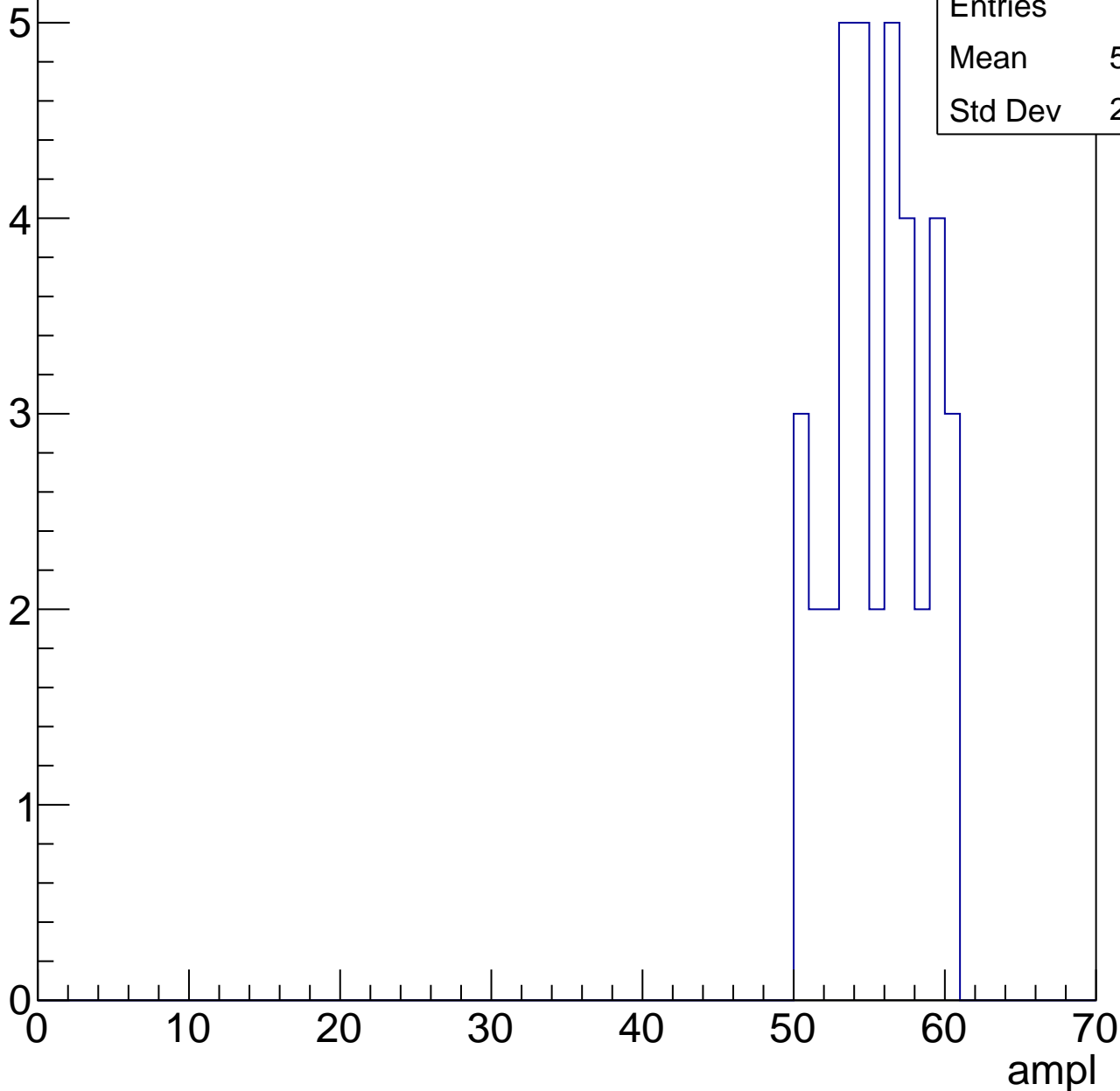


B1L102S, U6-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

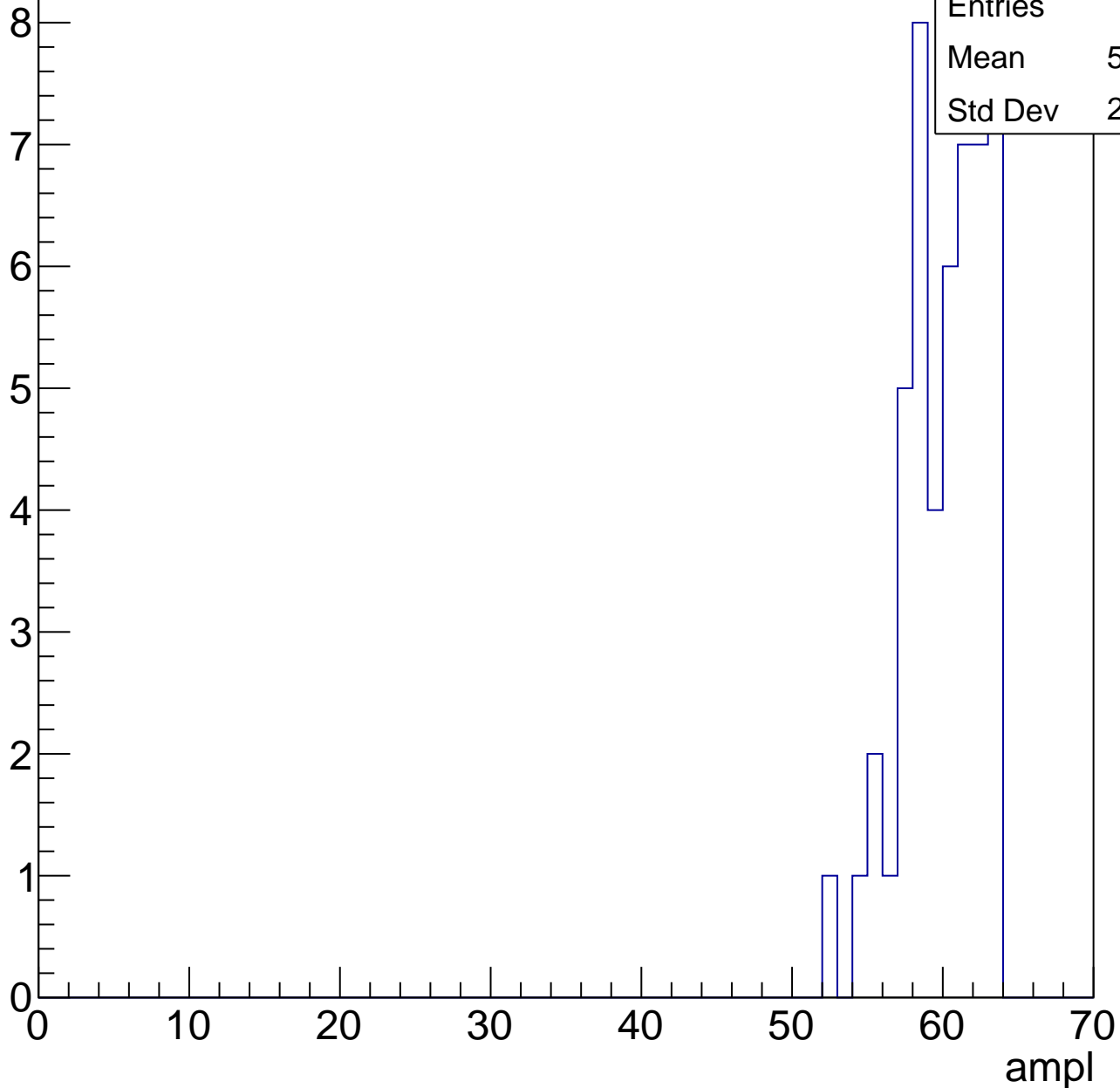
Entries	37
Mean	55.16
Std Dev	2.973



B1L102S, U6-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

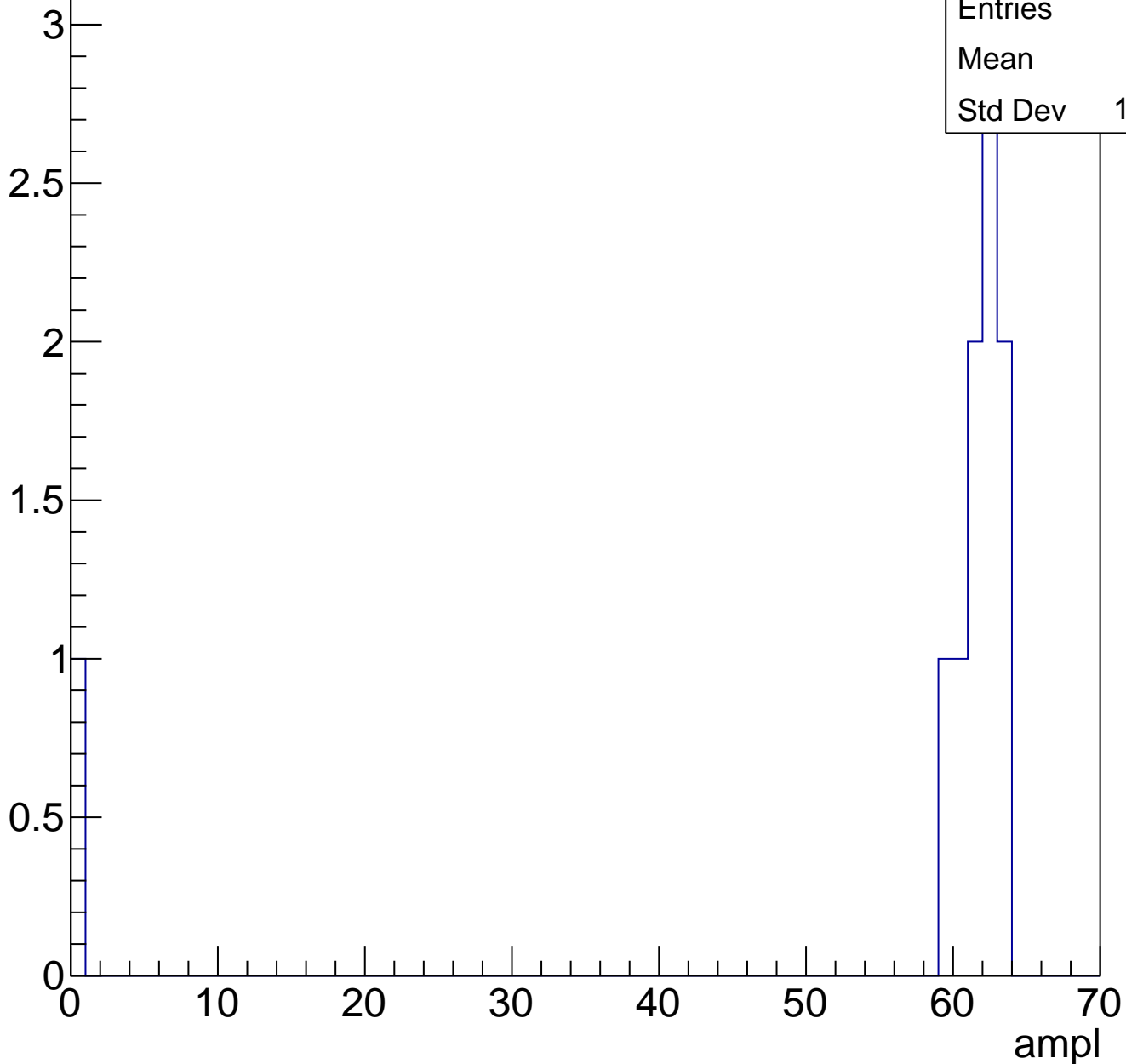
Entry



B1L102S, U6-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U6-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U6-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0