

B1L101S, U21-ch0, adc0

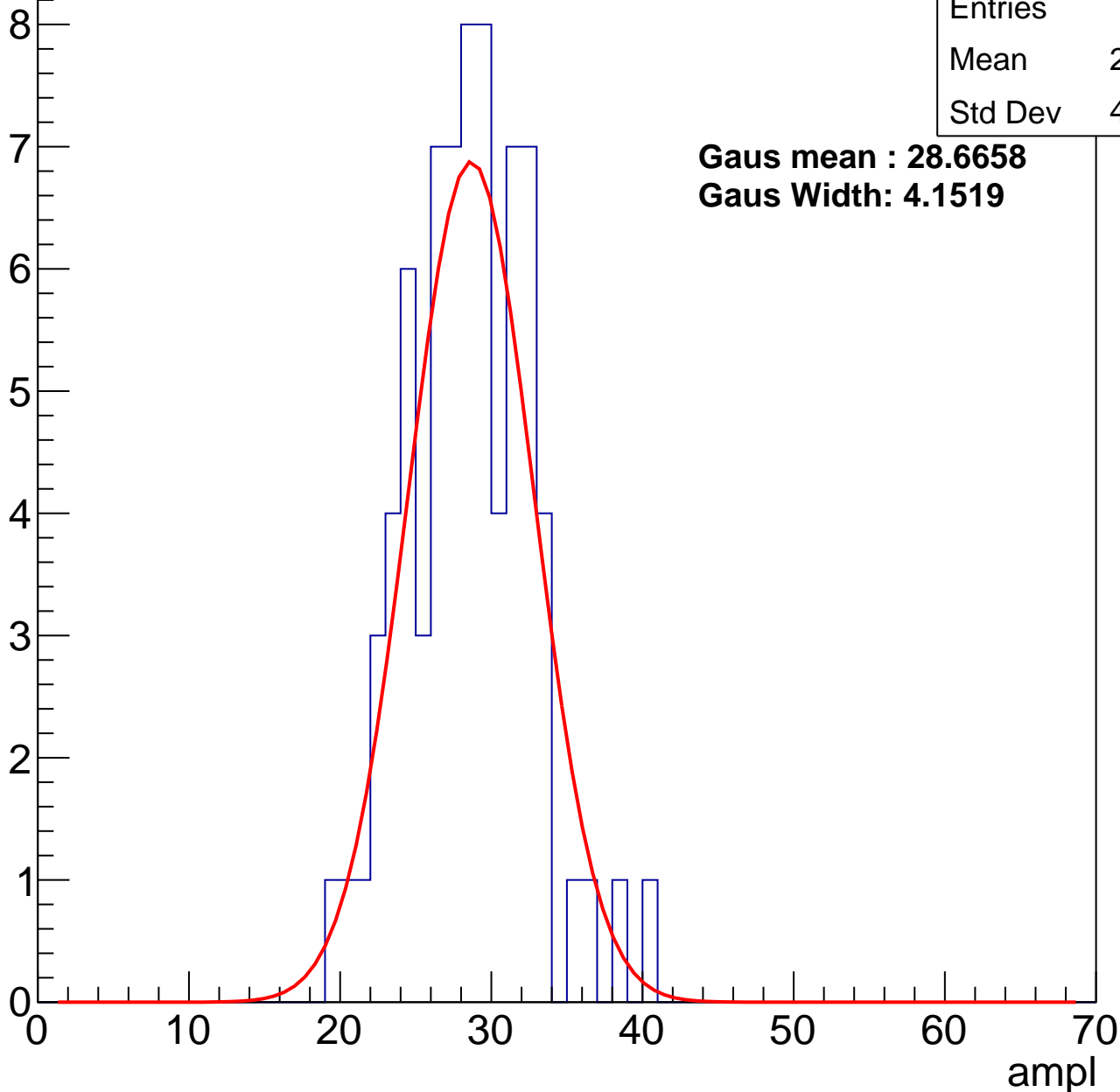
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	28.08
Std Dev	4.026

Gaus mean : 28.6658

Gaus Width: 4.1519



B1L101S, U21-ch0, adc1

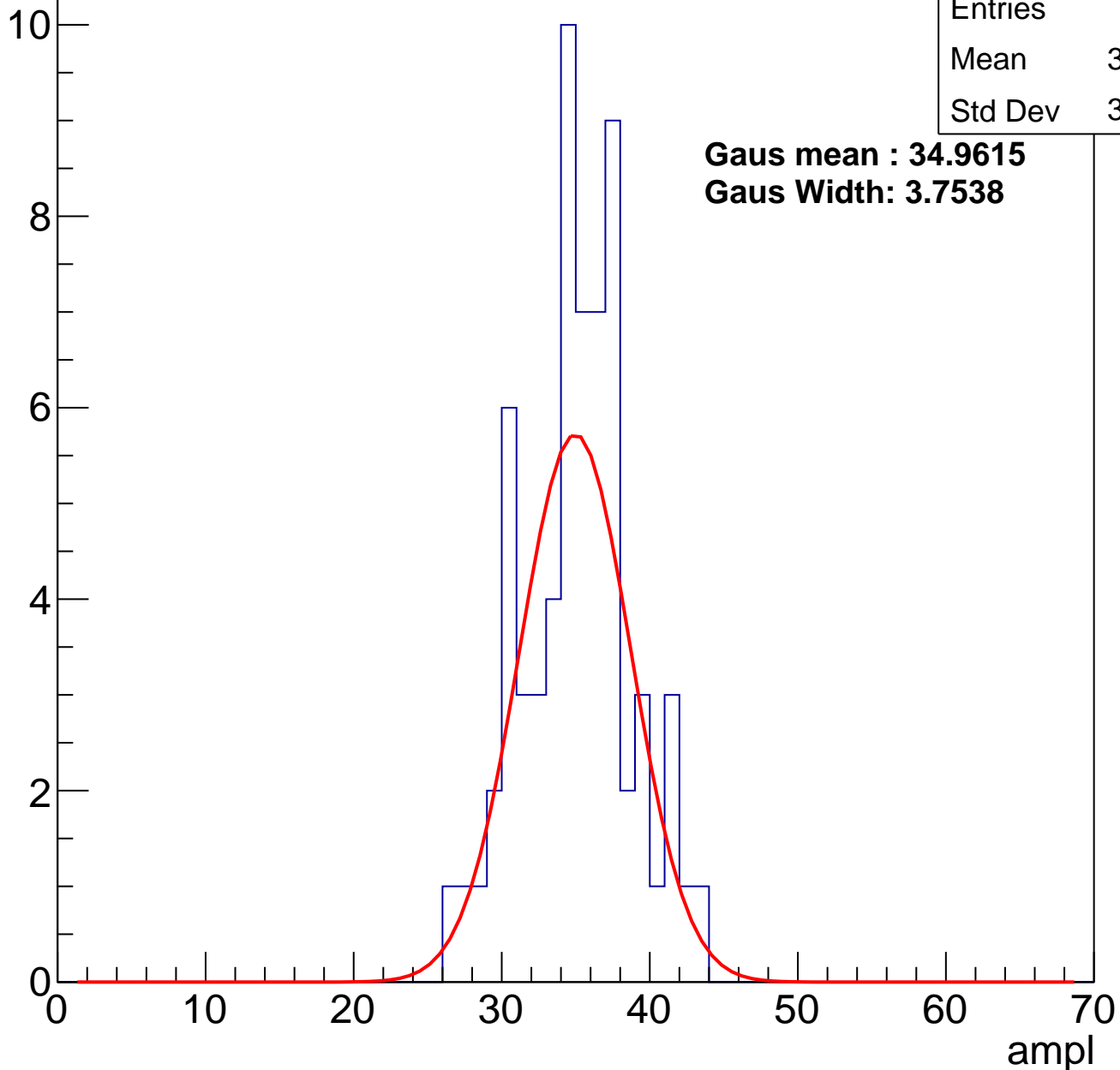
calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	34.63
Std Dev	3.644

Gaus mean : 34.9615

Gaus Width: 3.7538

Entry



B1L101S, U21-ch0, adc2

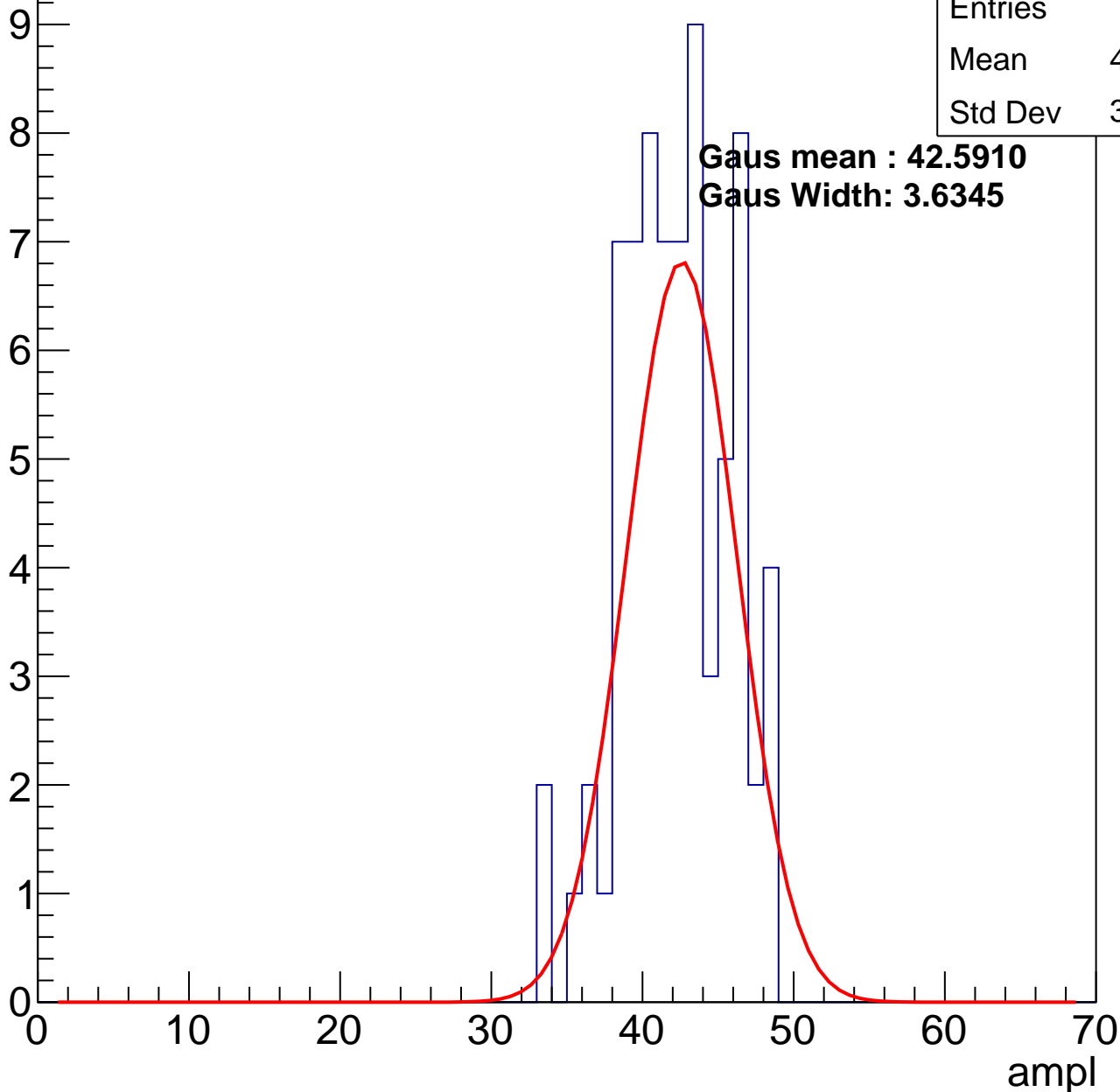
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	41.75
Std Dev	3.522

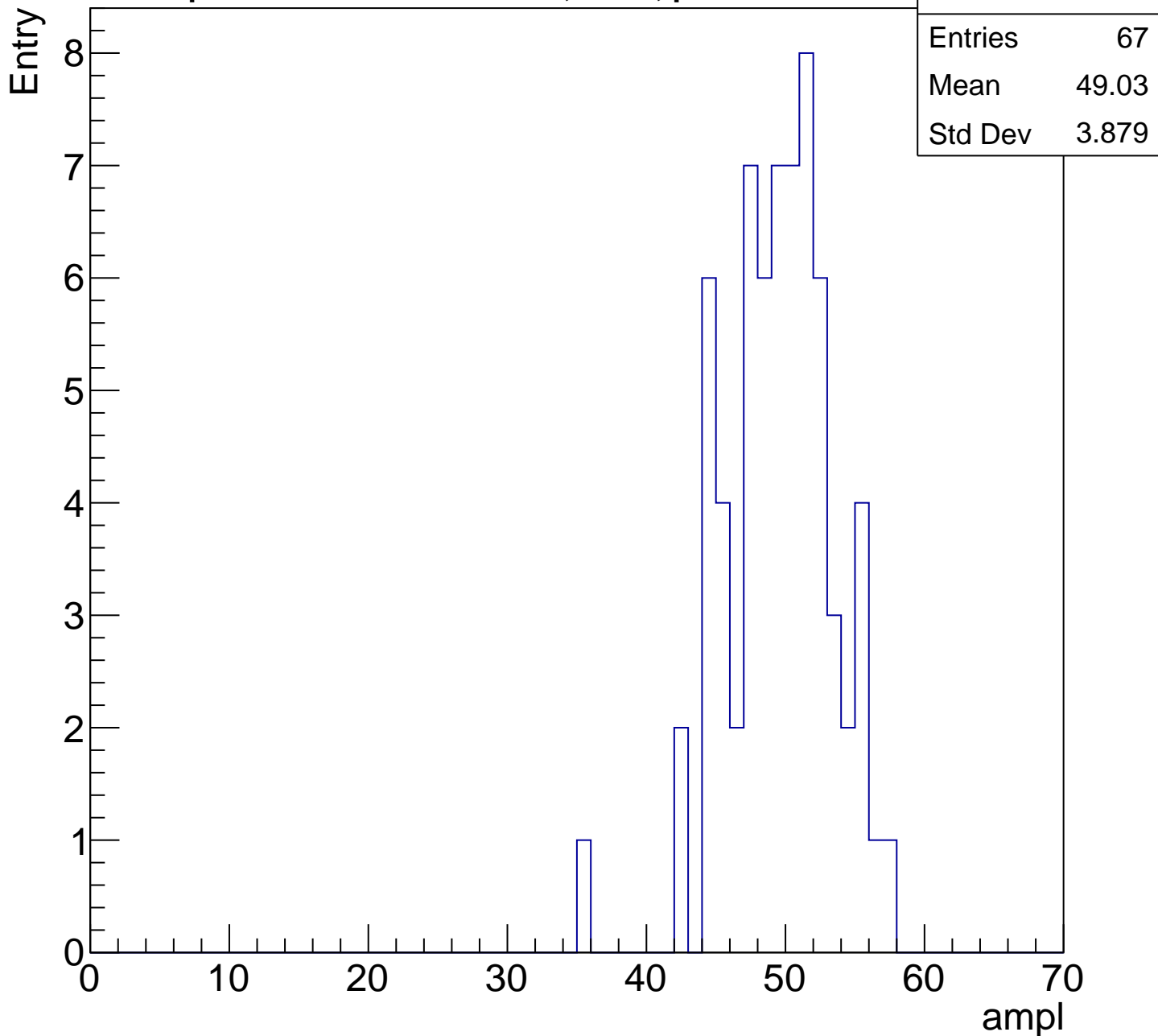
Gaus mean : 42.5910

Gaus Width: 3.6345



B1L101S, U21-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

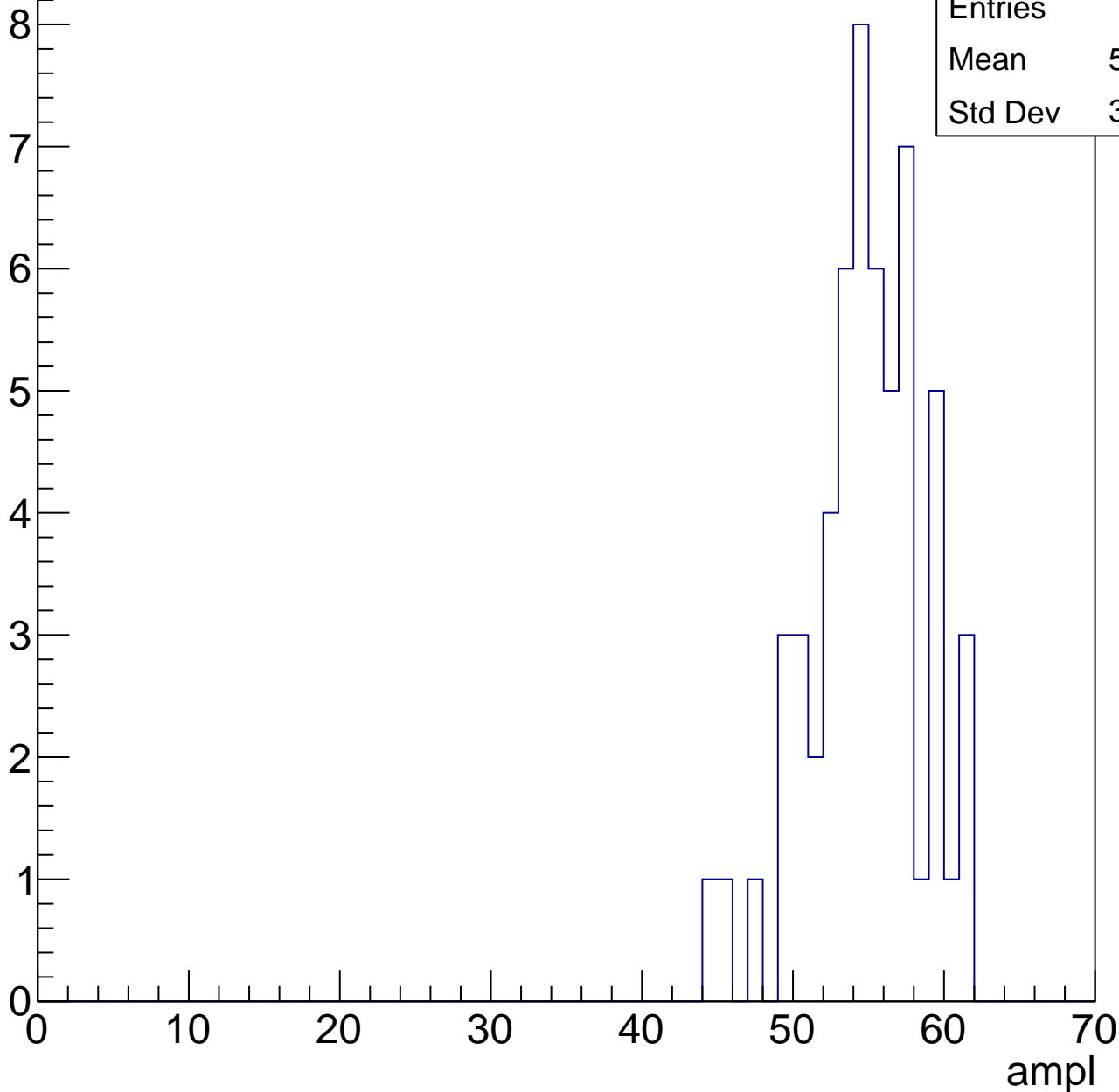


B1L101S, U21-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	54.35
Std Dev	3.744

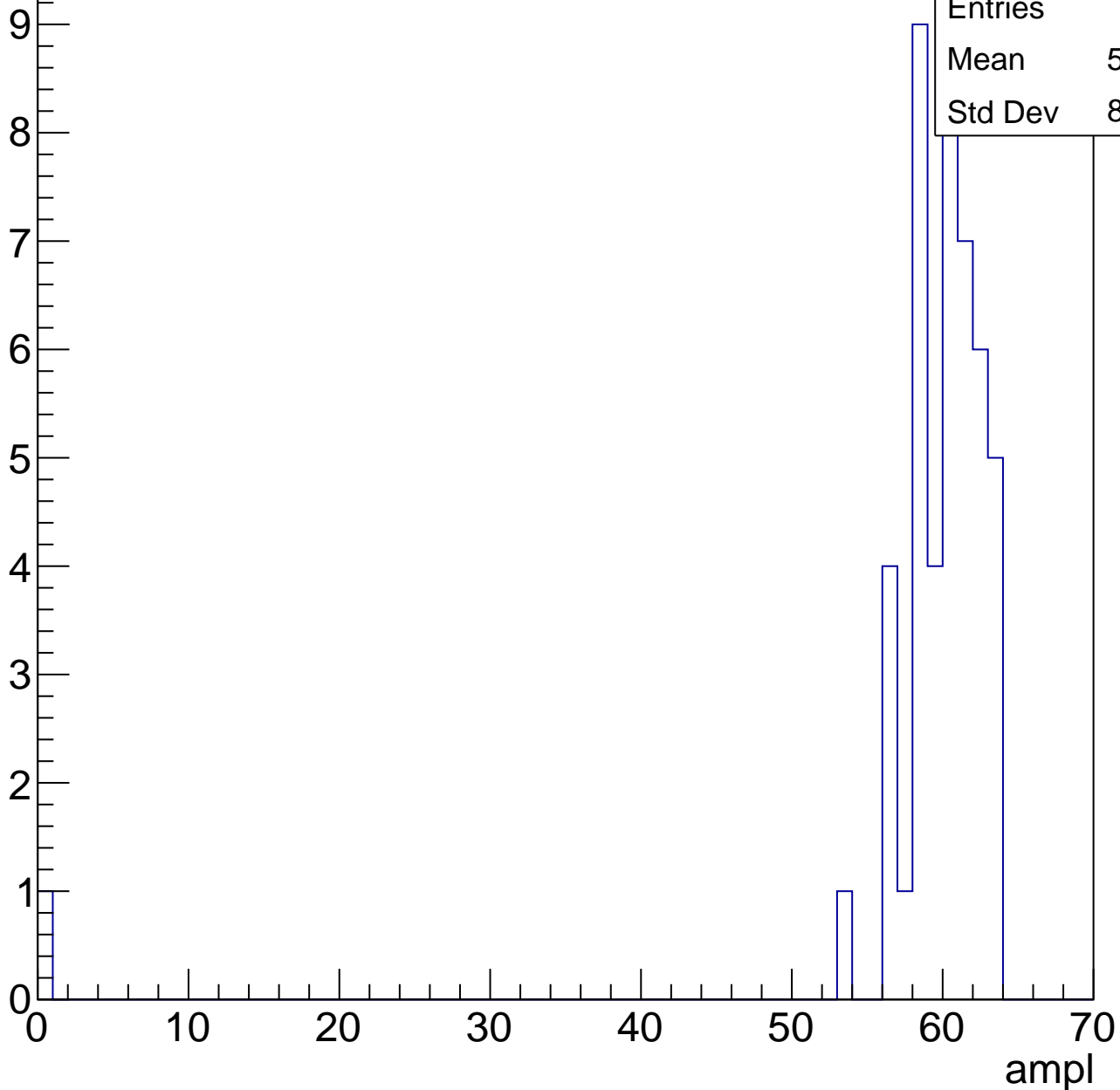


B1L101S, U21-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.43
Std Dev	8.898

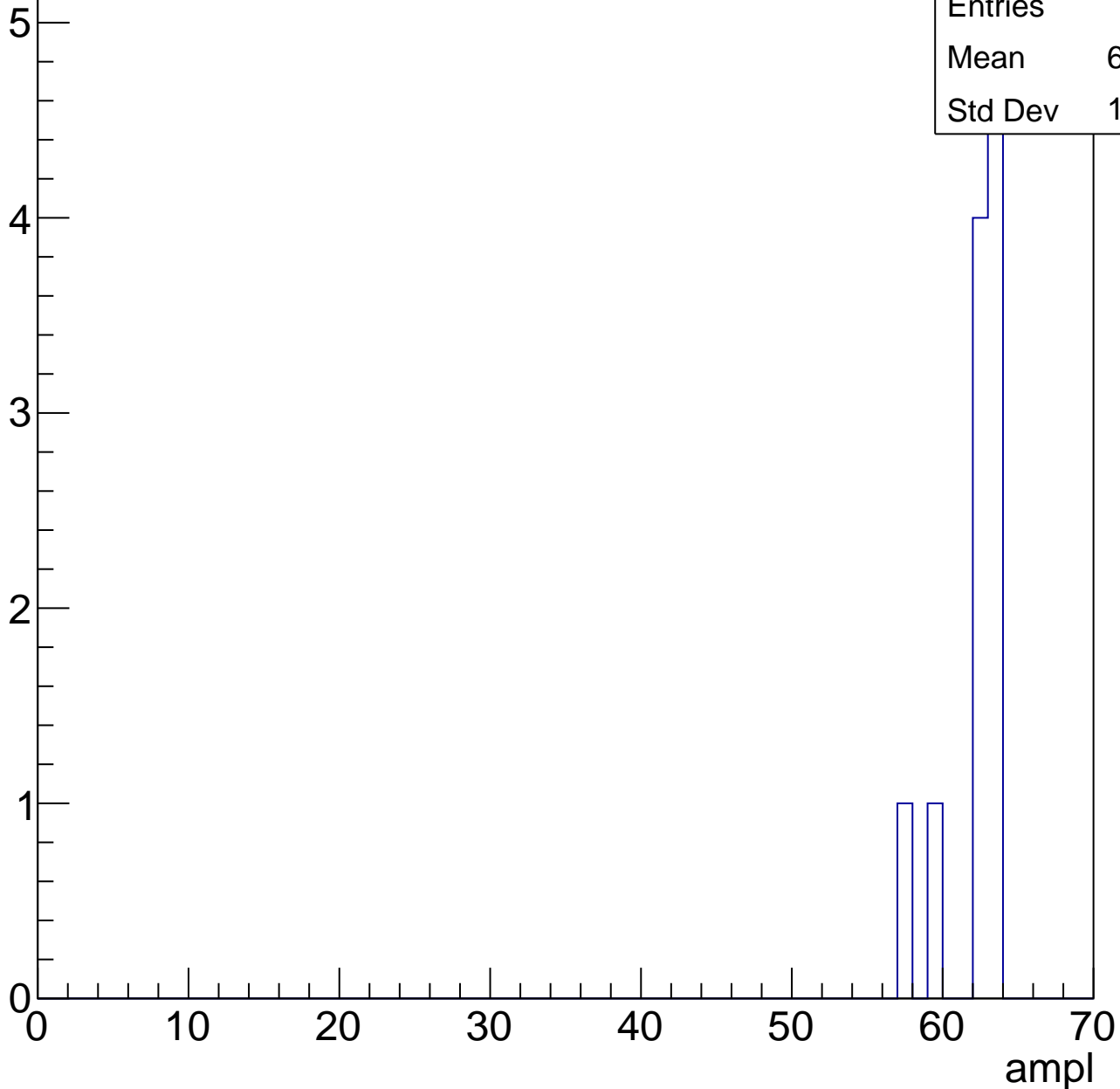


B1L101S, U21-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	61.73
Std Dev	1.863



B1L101S, U21-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L101S, U21-ch1, adc0

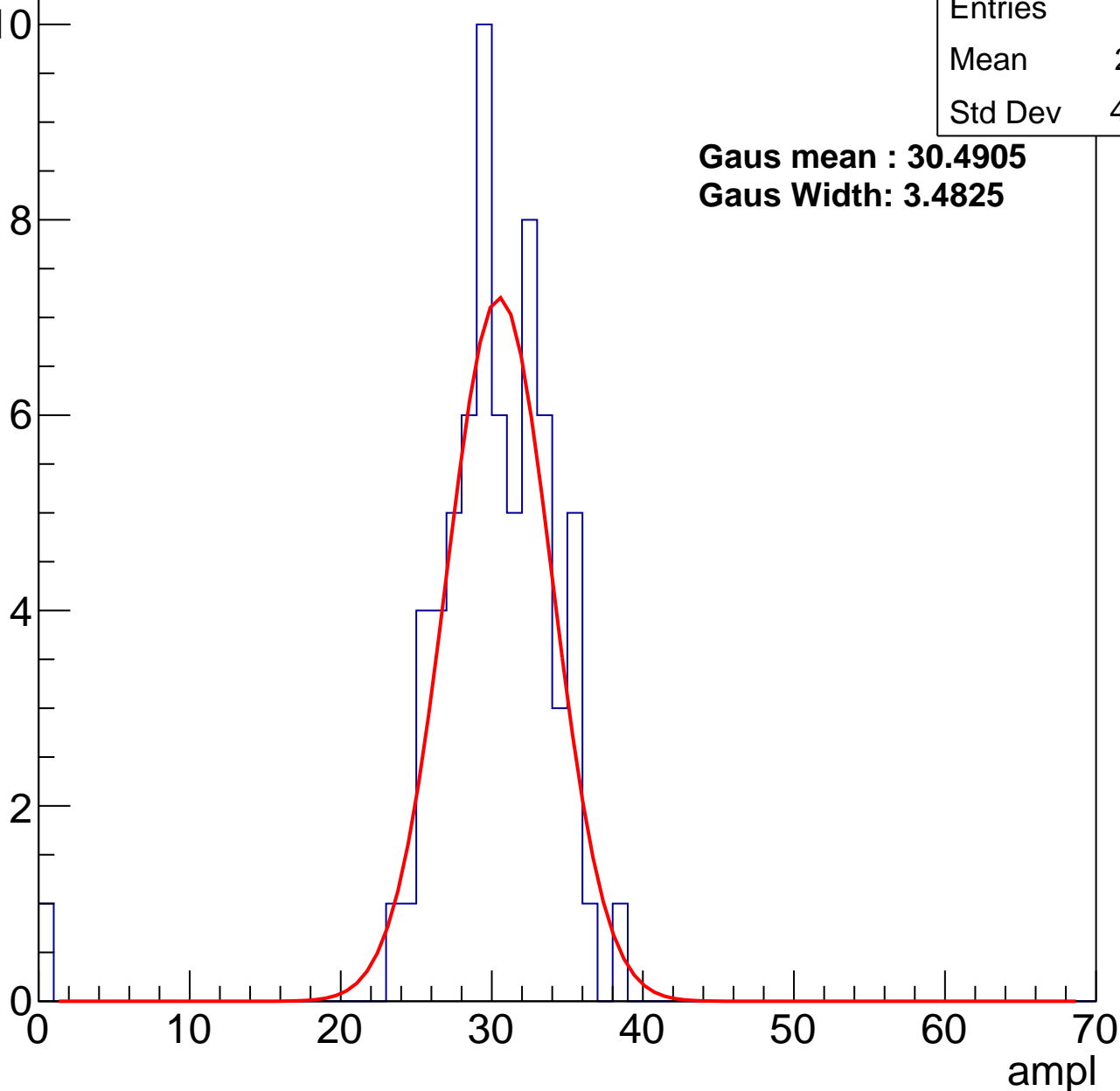
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.61
Std Dev	4.859

Gaus mean : 30.4905

Gaus Width: 3.4825



B1L101S, U21-ch1, adc1

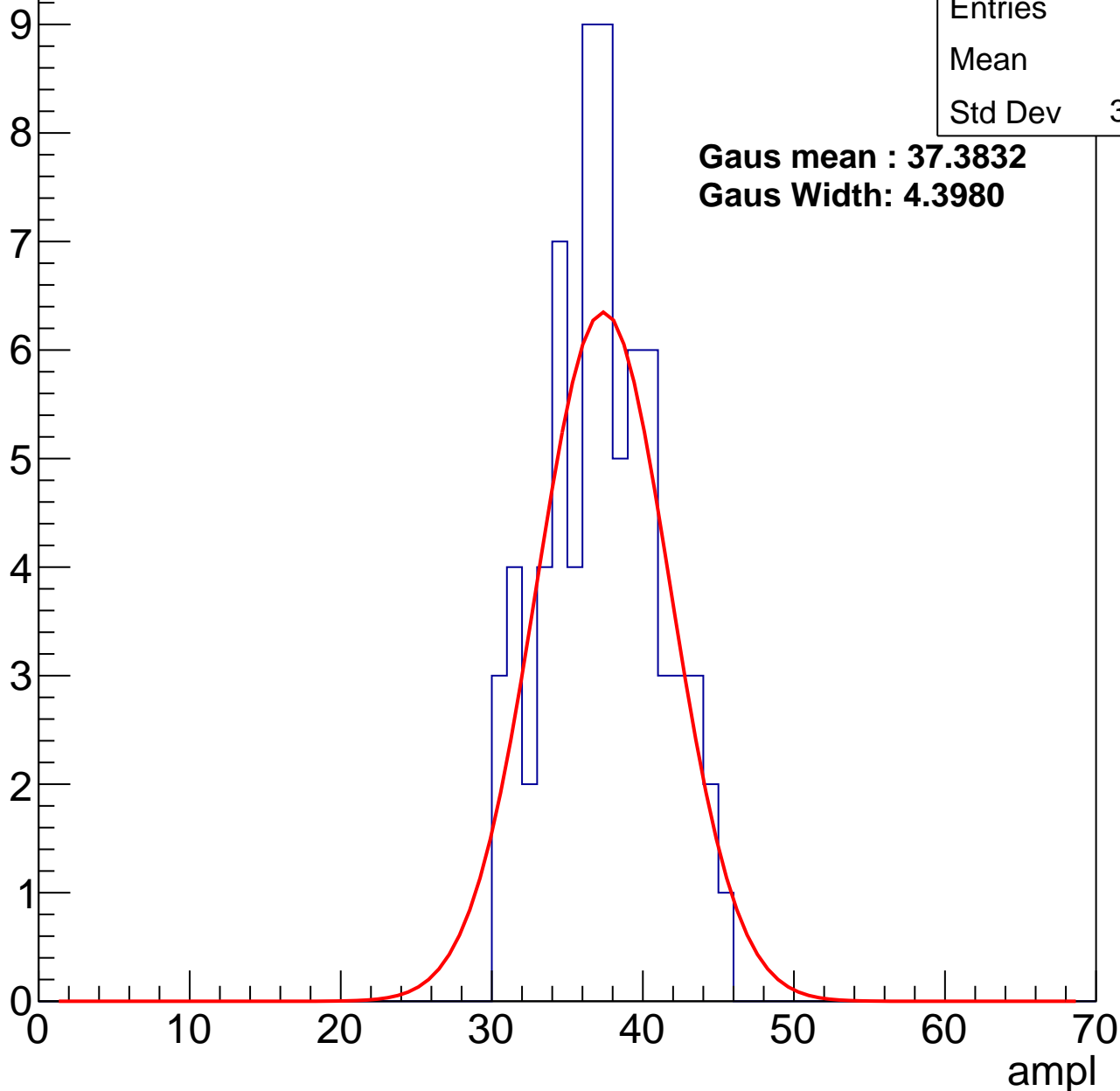
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.9
Std Dev	3.693

Gaus mean : 37.3832

Gaus Width: 4.3980



B1L101S, U21-ch1, adc2

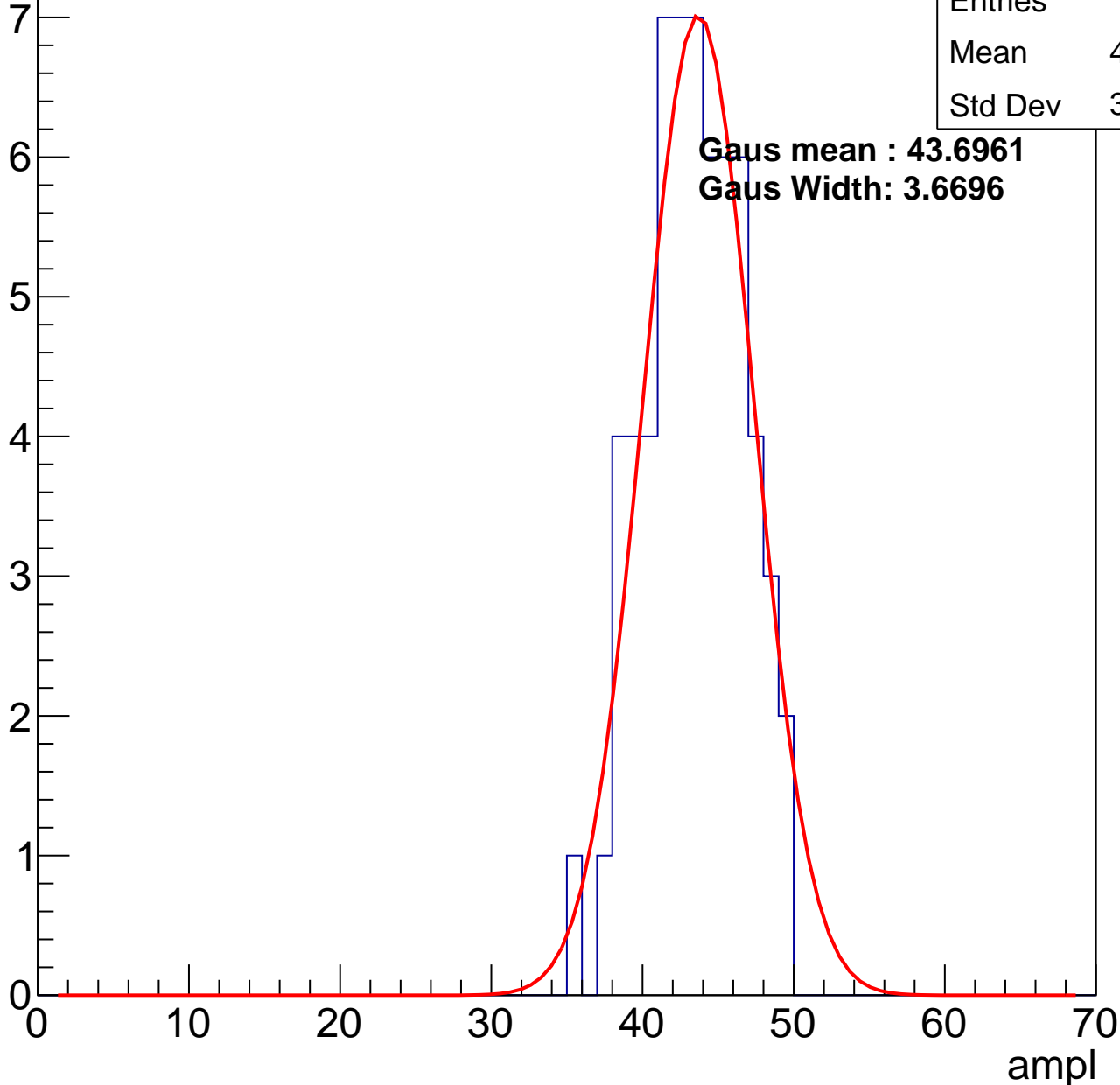
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.94
Std Dev	3.187

Gaus mean : 43.6961

Gaus Width: 3.6696



B1L101S, U21-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	61
Mean	49.7
Std Dev	2.927

Entry

10

8

6

4

2

0

0

10

20

30

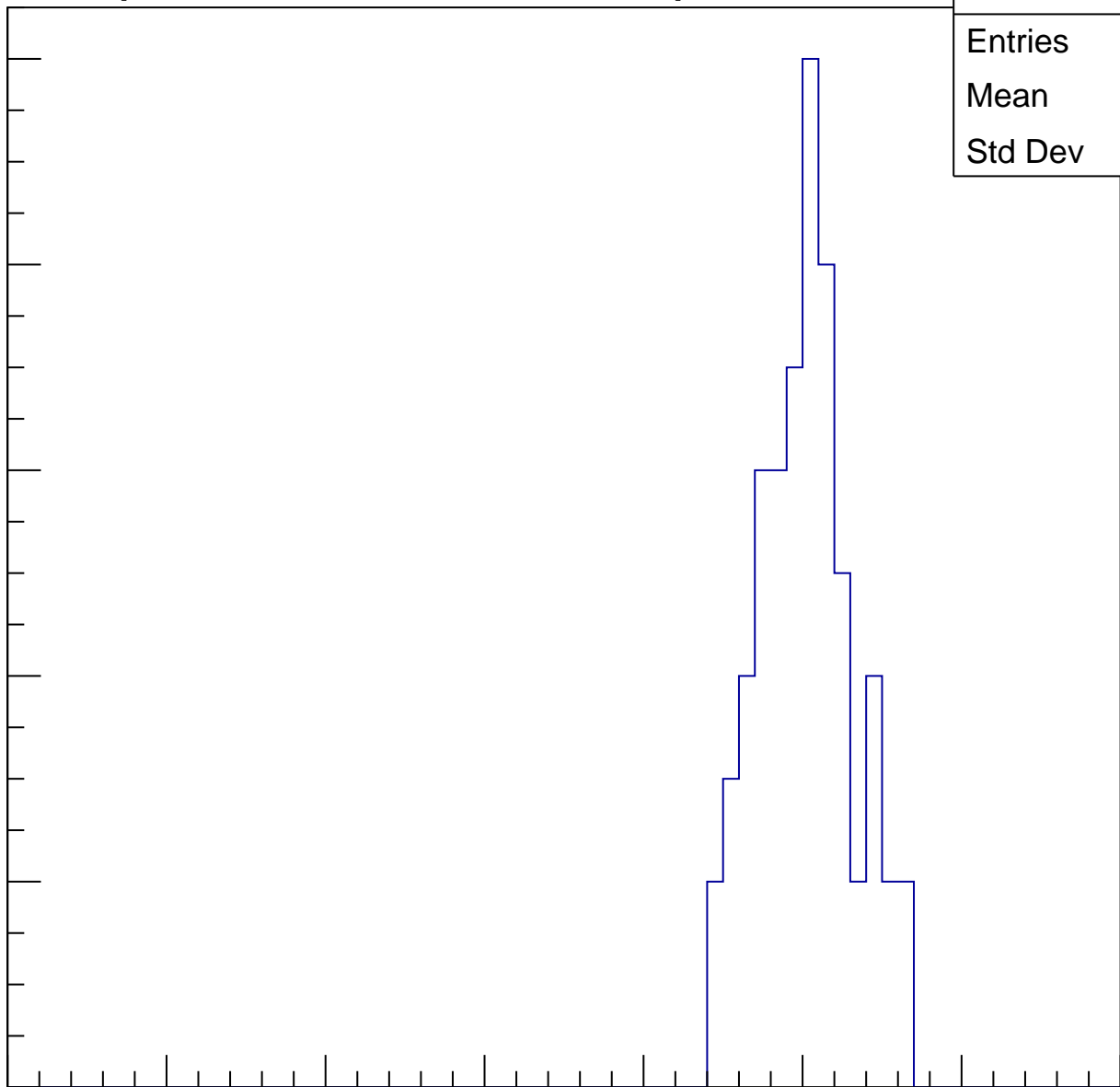
40

50

60

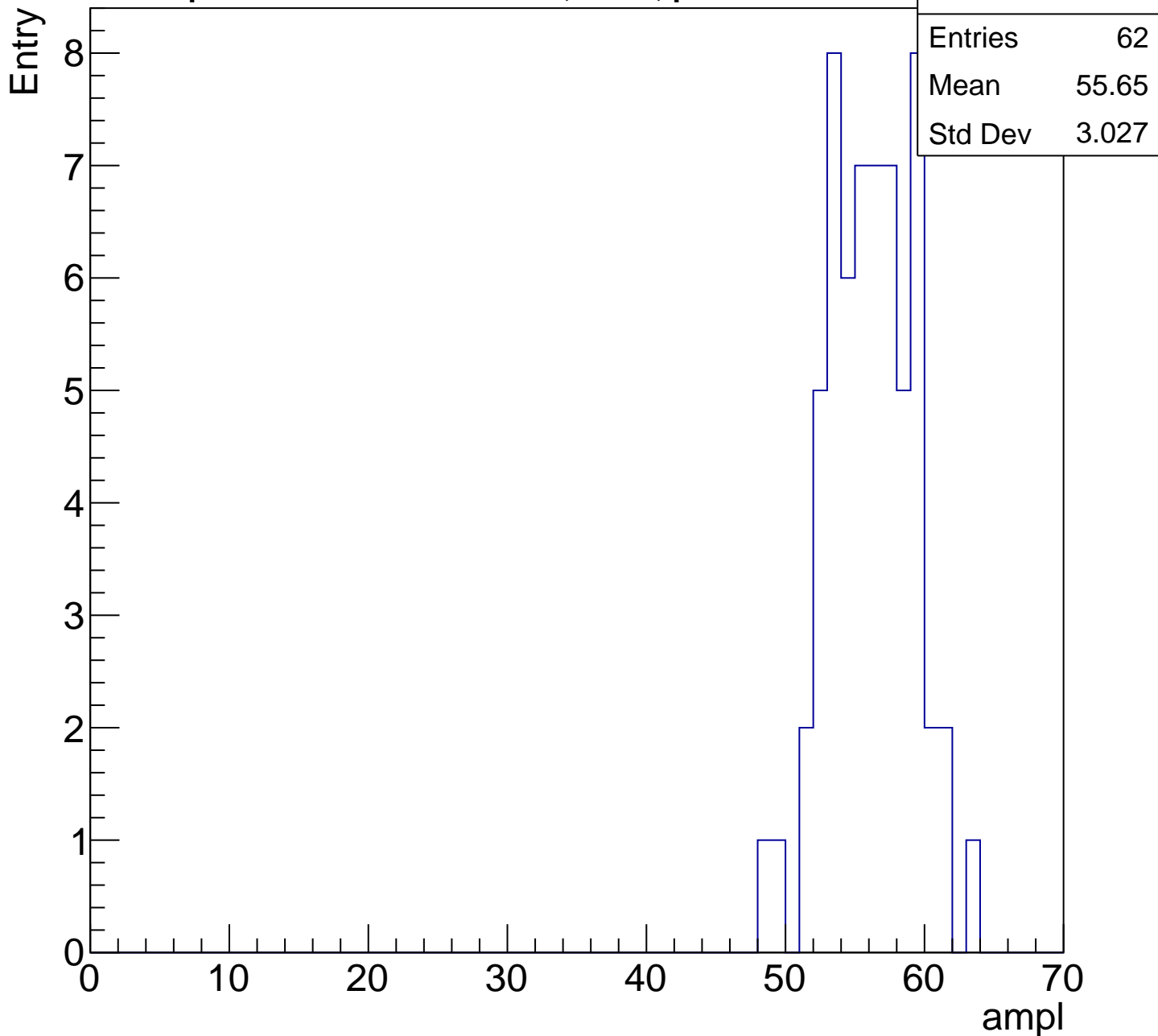
70

ampl



B1L101S, U21-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	60.41
Std Dev	2.103

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B1L101S, U21-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	46.75
Std Dev	26.99

B1L101S, U21-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch2, adc0

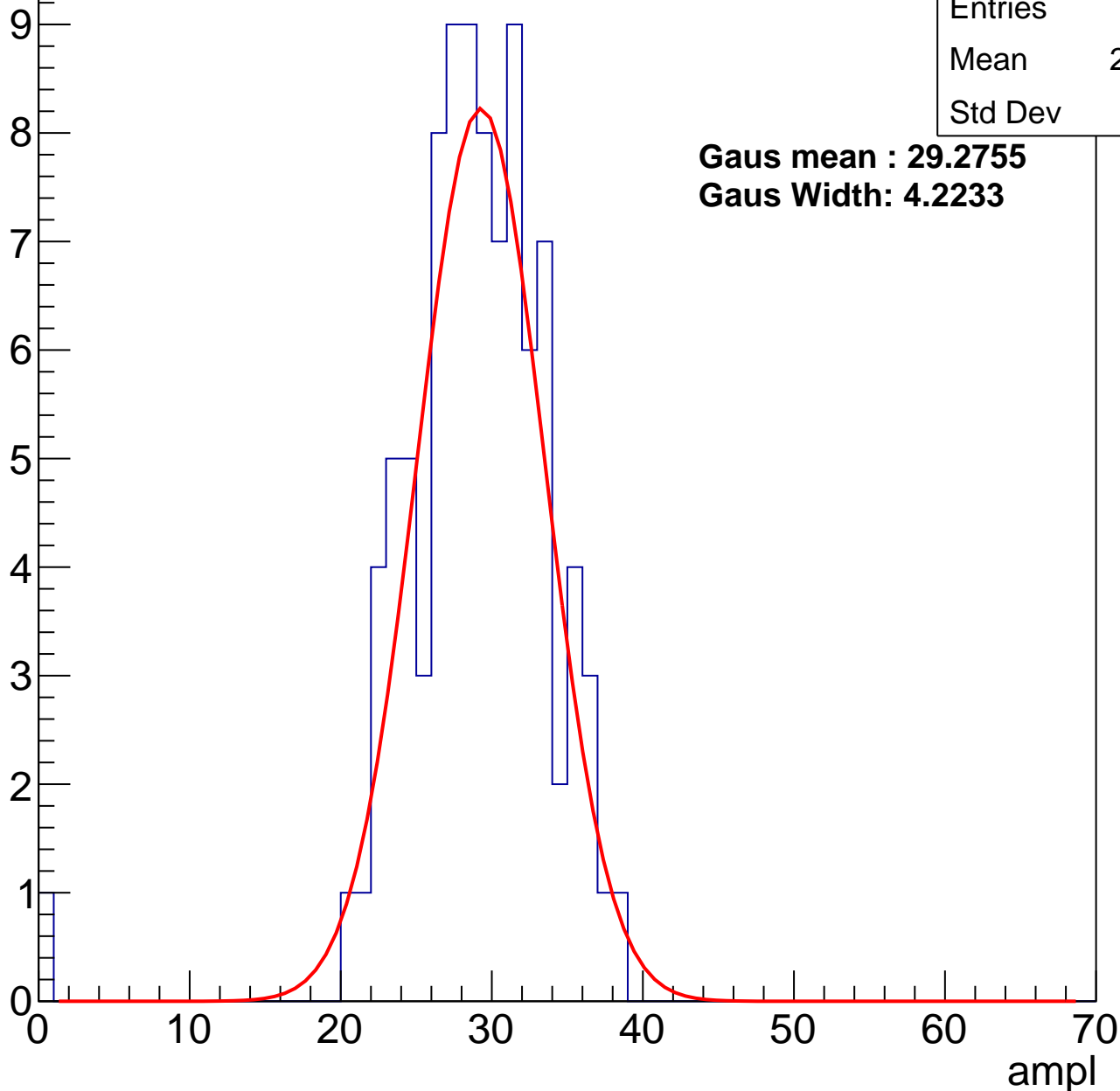
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	94
Mean	28.48
Std Dev	4.97

Gaus mean : 29.2755

Gaus Width: 4.2233



B1L101S, U21-ch2, adc1

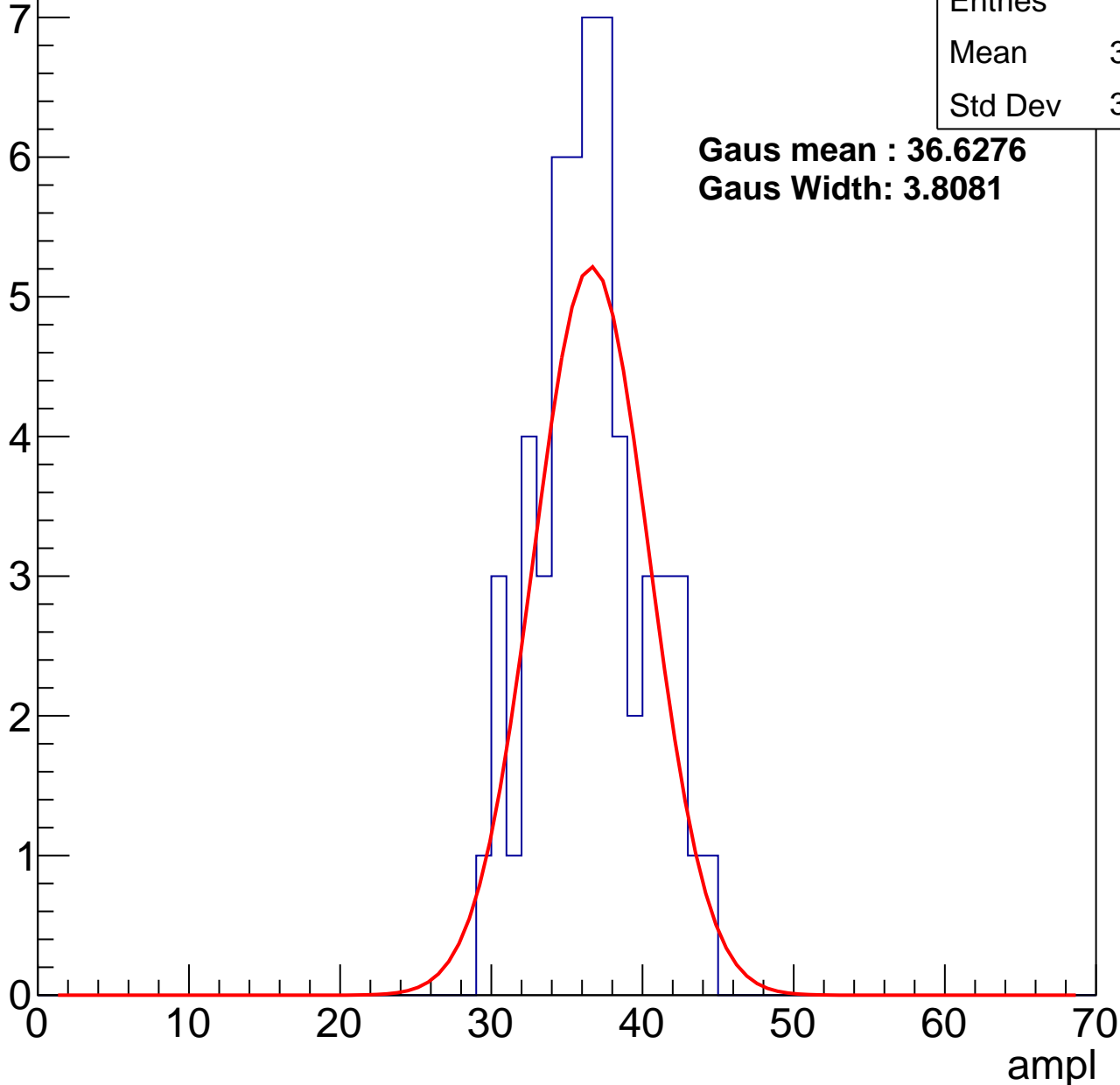
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	36.15
Std Dev	3.534

Gaus mean : 36.6276

Gaus Width: 3.8081



B1L101S, U21-ch2, adc2

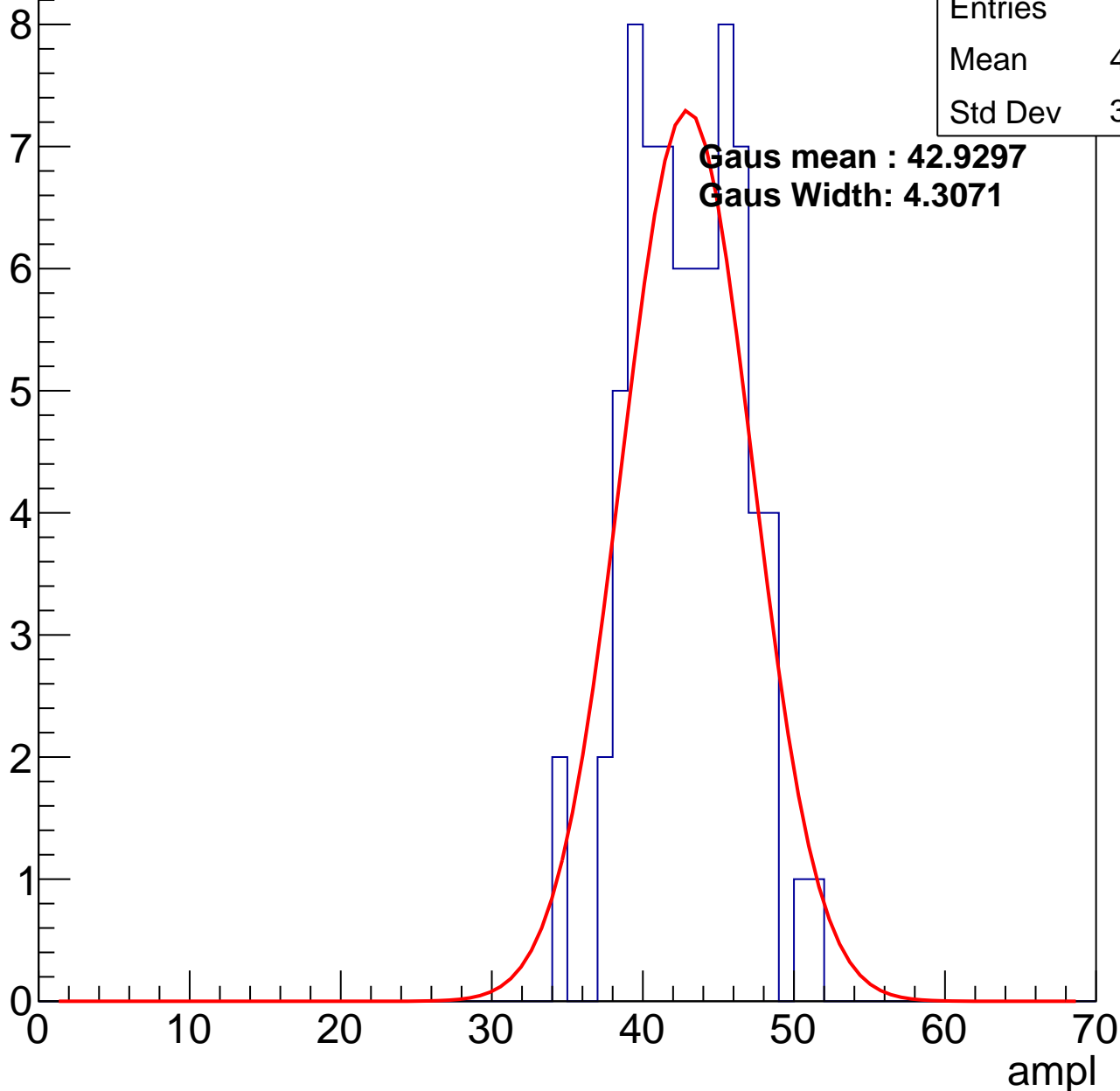
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	42.54
Std Dev	3.576

Gaus mean : 42.9297

Gaus Width: 4.3071

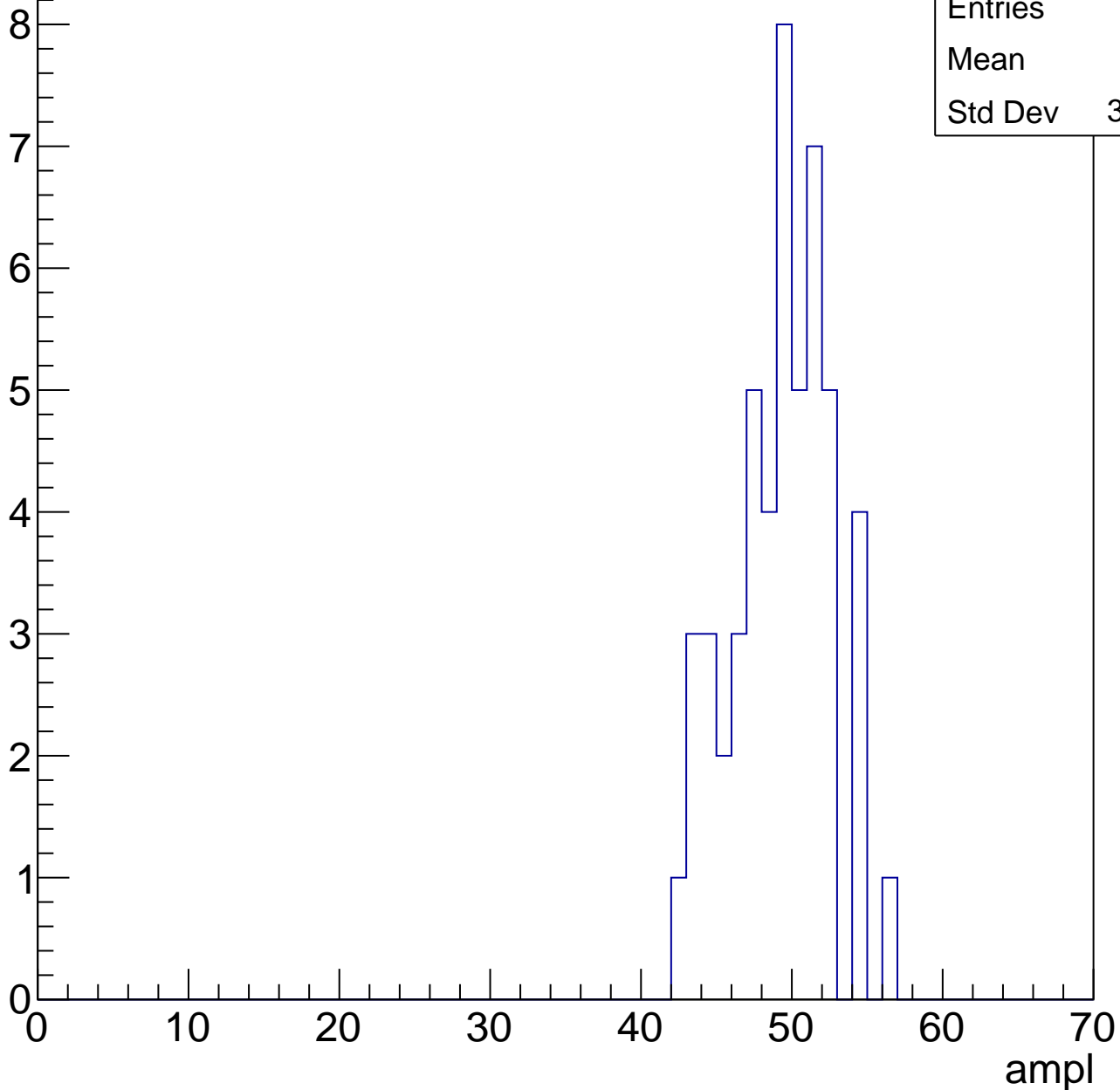


B1L101S, U21-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	48.8
Std Dev	3.254



B1L101S, U21-ch2, adc4

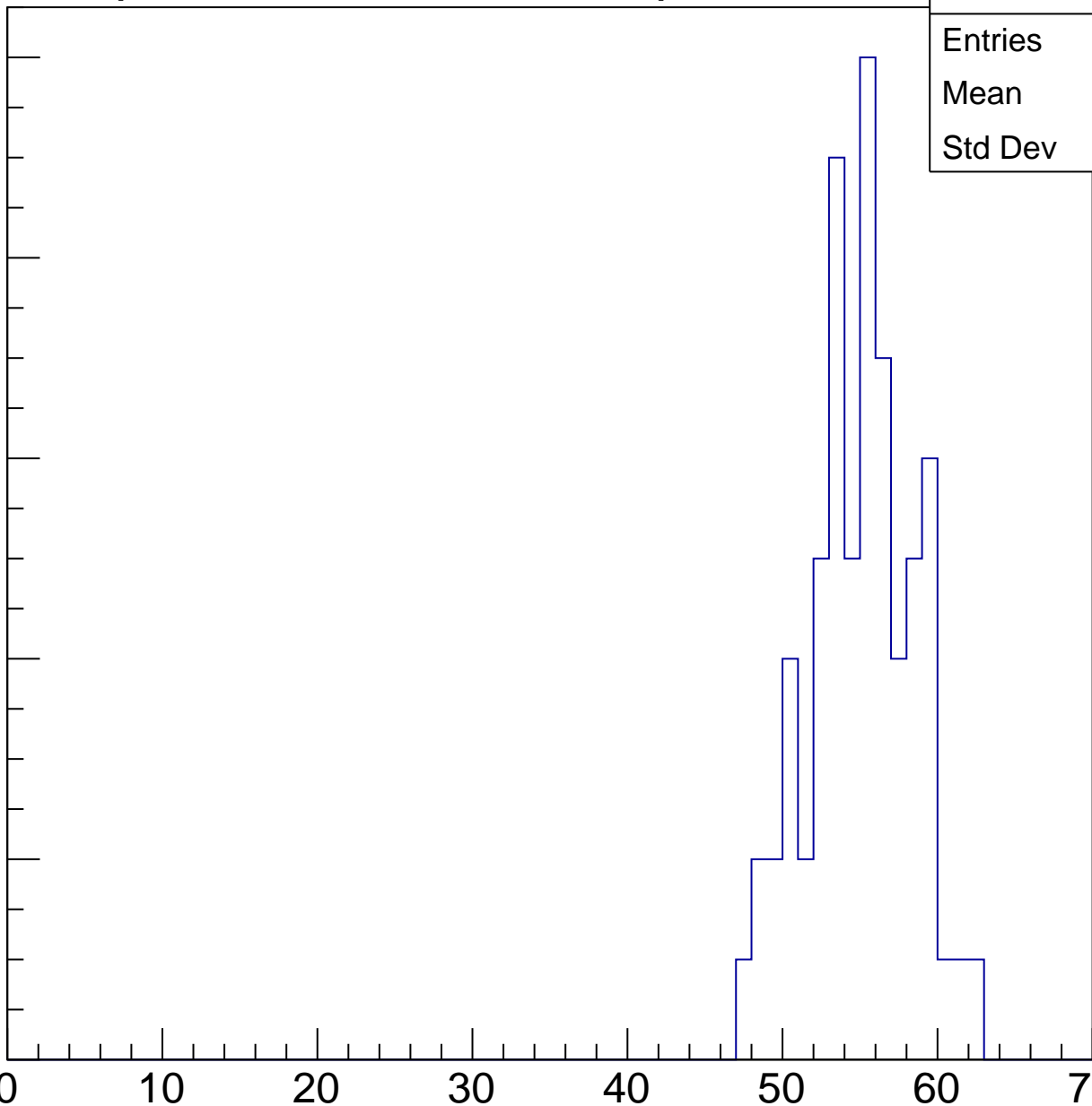
calib_packv5_042523_0143.root, FC#0, port D2

Entry

10
8
6
4
2
0

Entries	65
Mean	54.57
Std Dev	3.314

ampl



B1L101S, U21-ch2, adc5

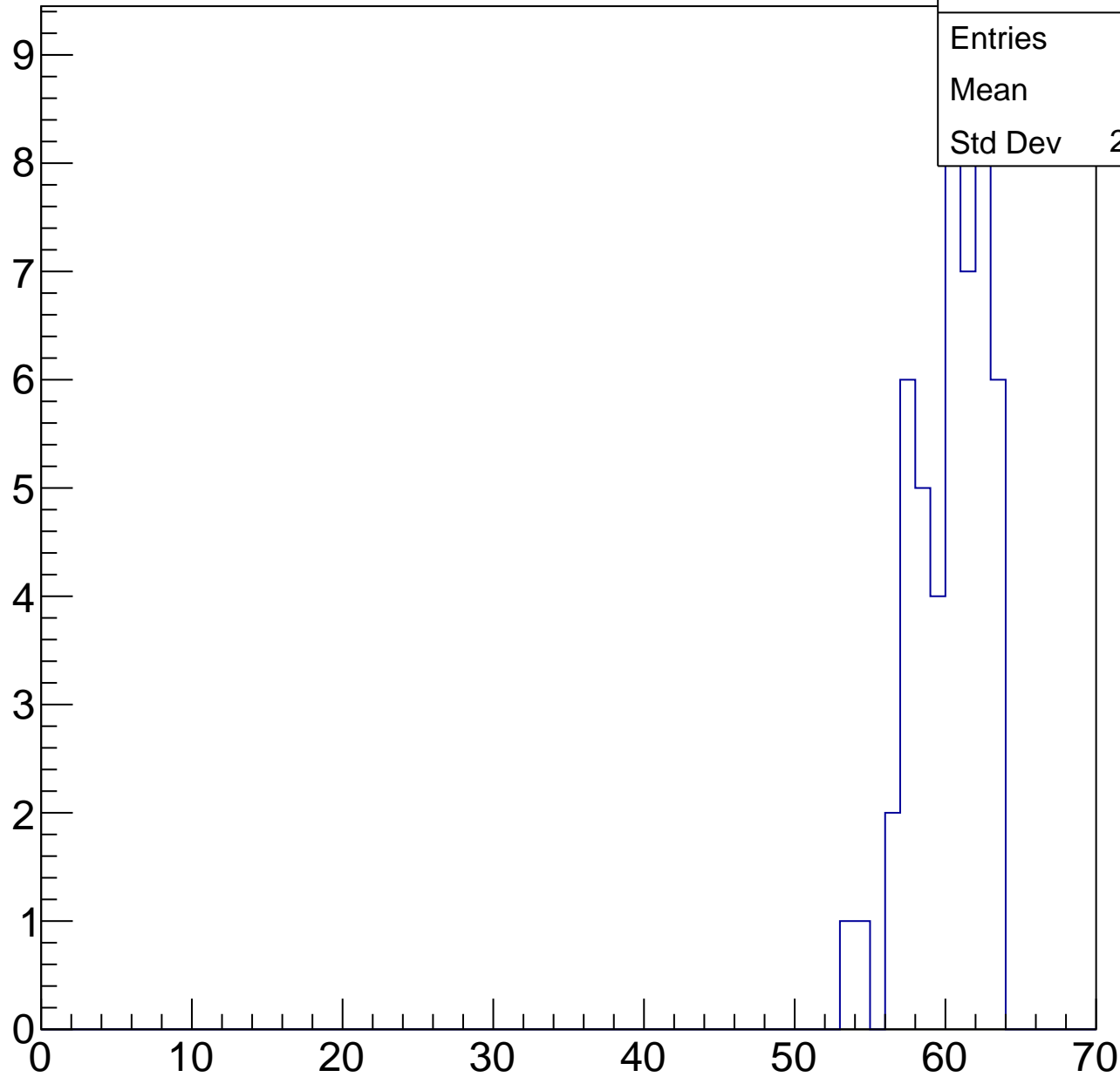
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	59.8
Std Dev	2.408

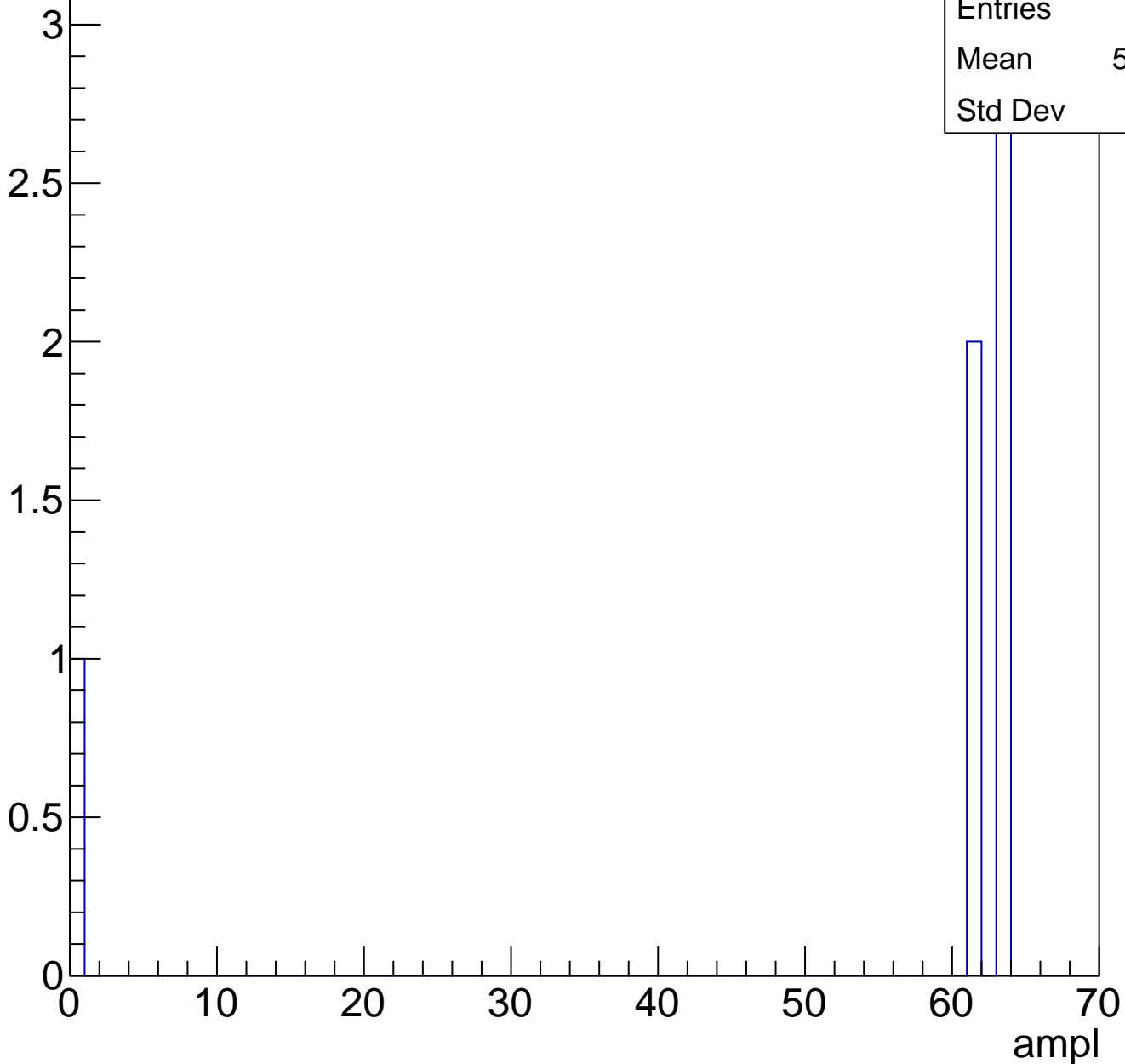
ampl



B1L101S, U21-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch3, adc0

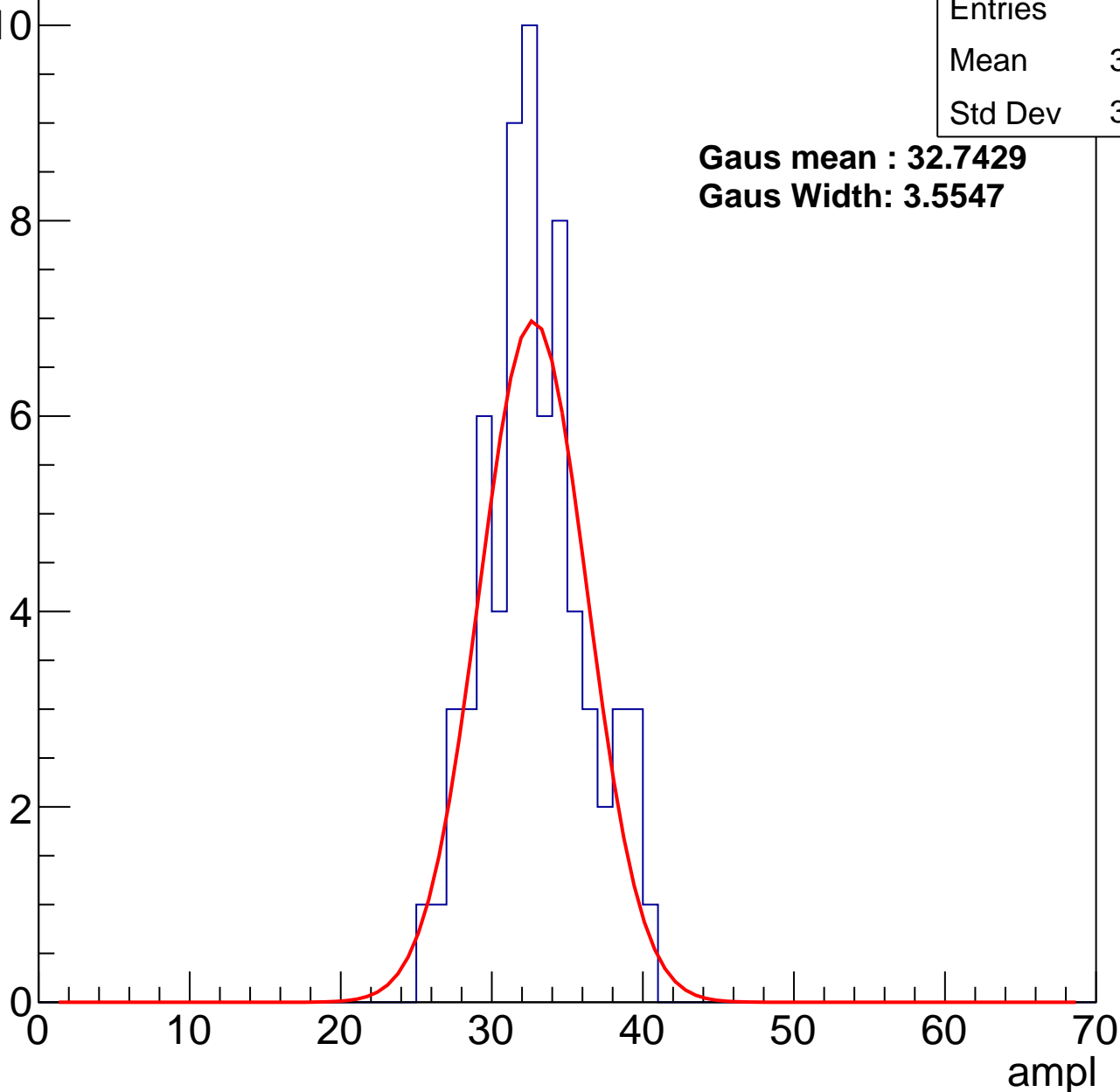
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	32.42
Std Dev	3.382

Gaus mean : 32.7429

Gaus Width: 3.5547



B1L101S, U21-ch3, adc1

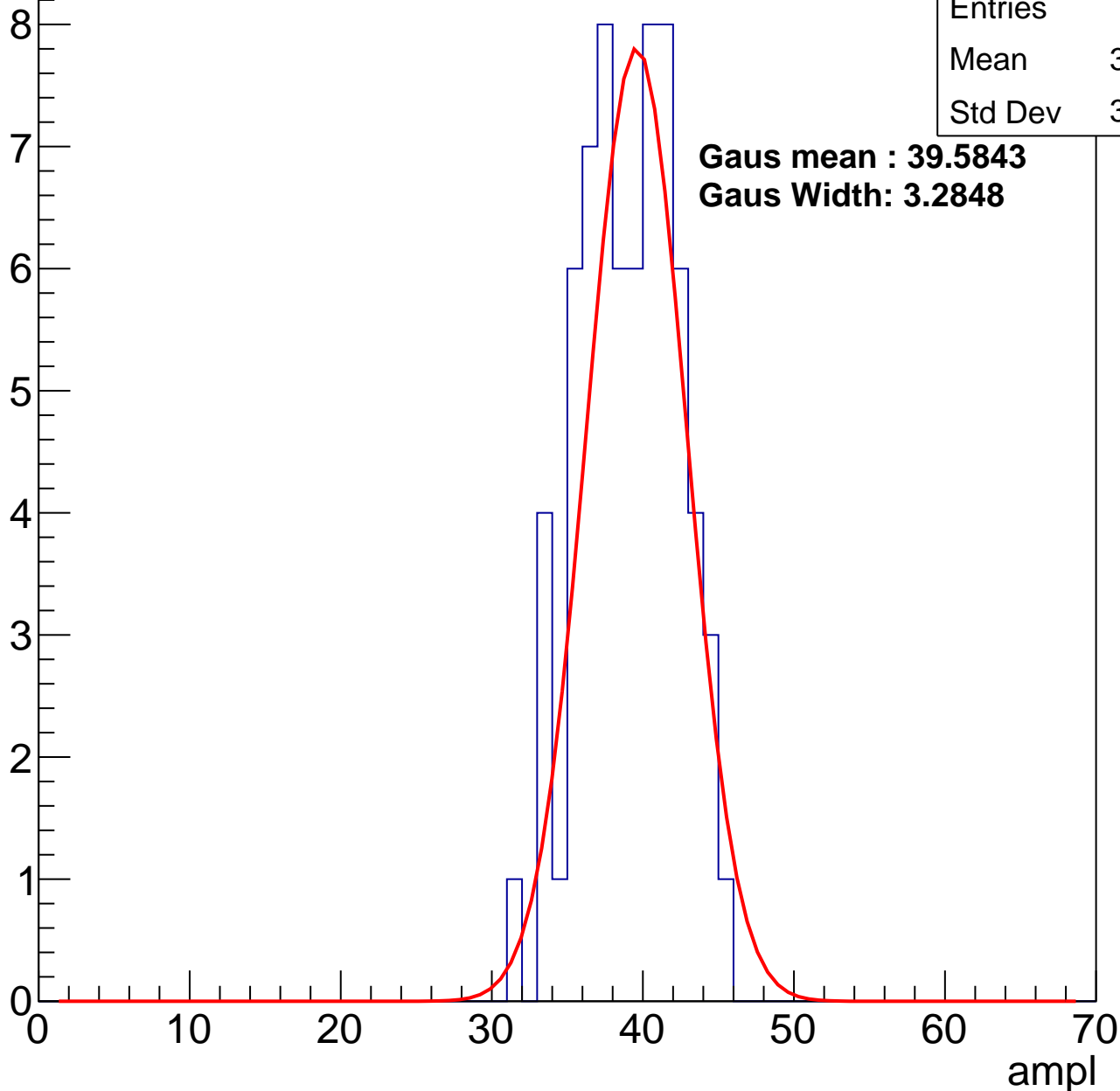
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.64
Std Dev	3.162

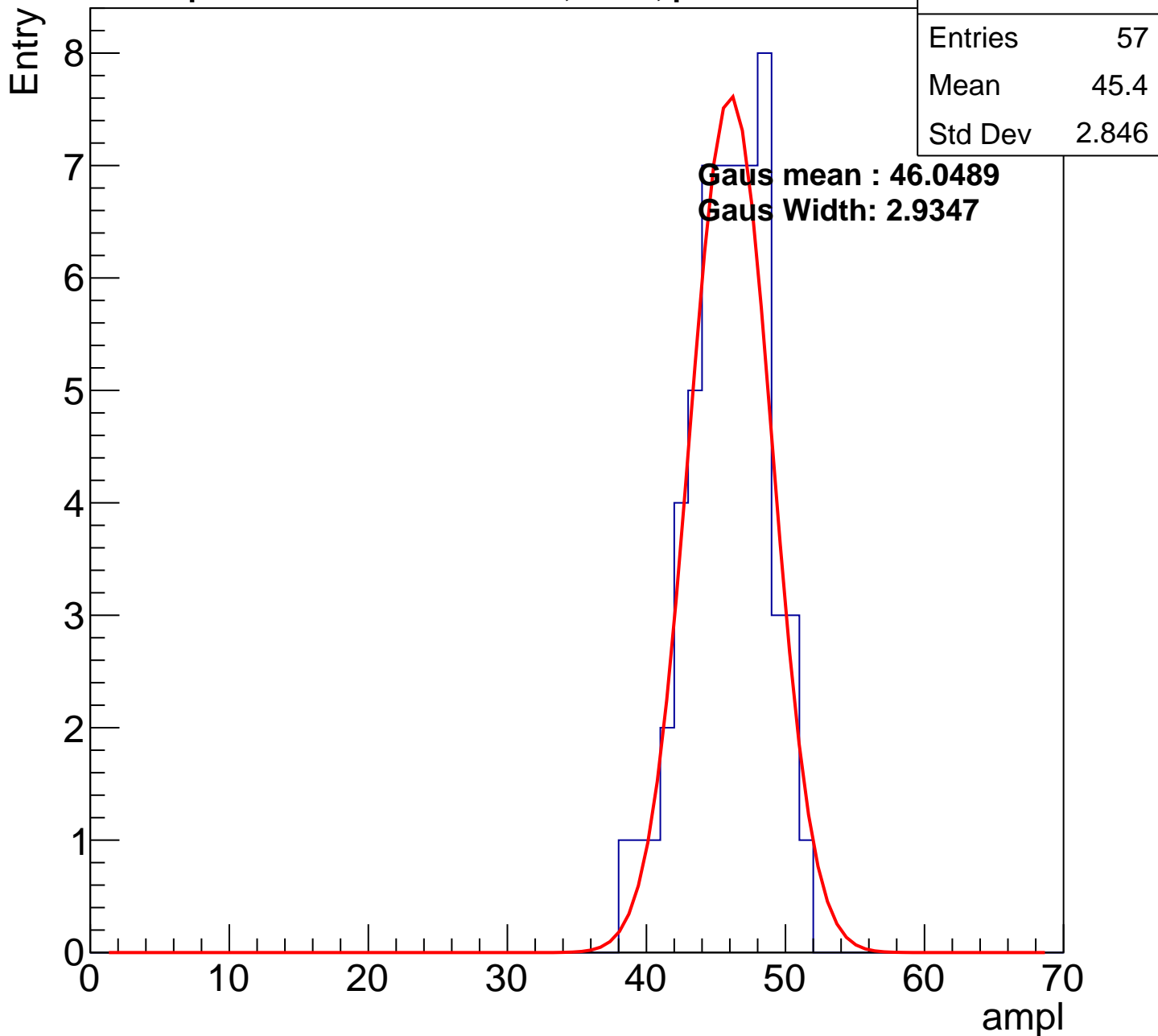
Gaus mean : 39.5843

Gaus Width: 3.2848



B1L101S, U21-ch3, adc2

calib_packv5_042523_0143.root, FC#0, port D2

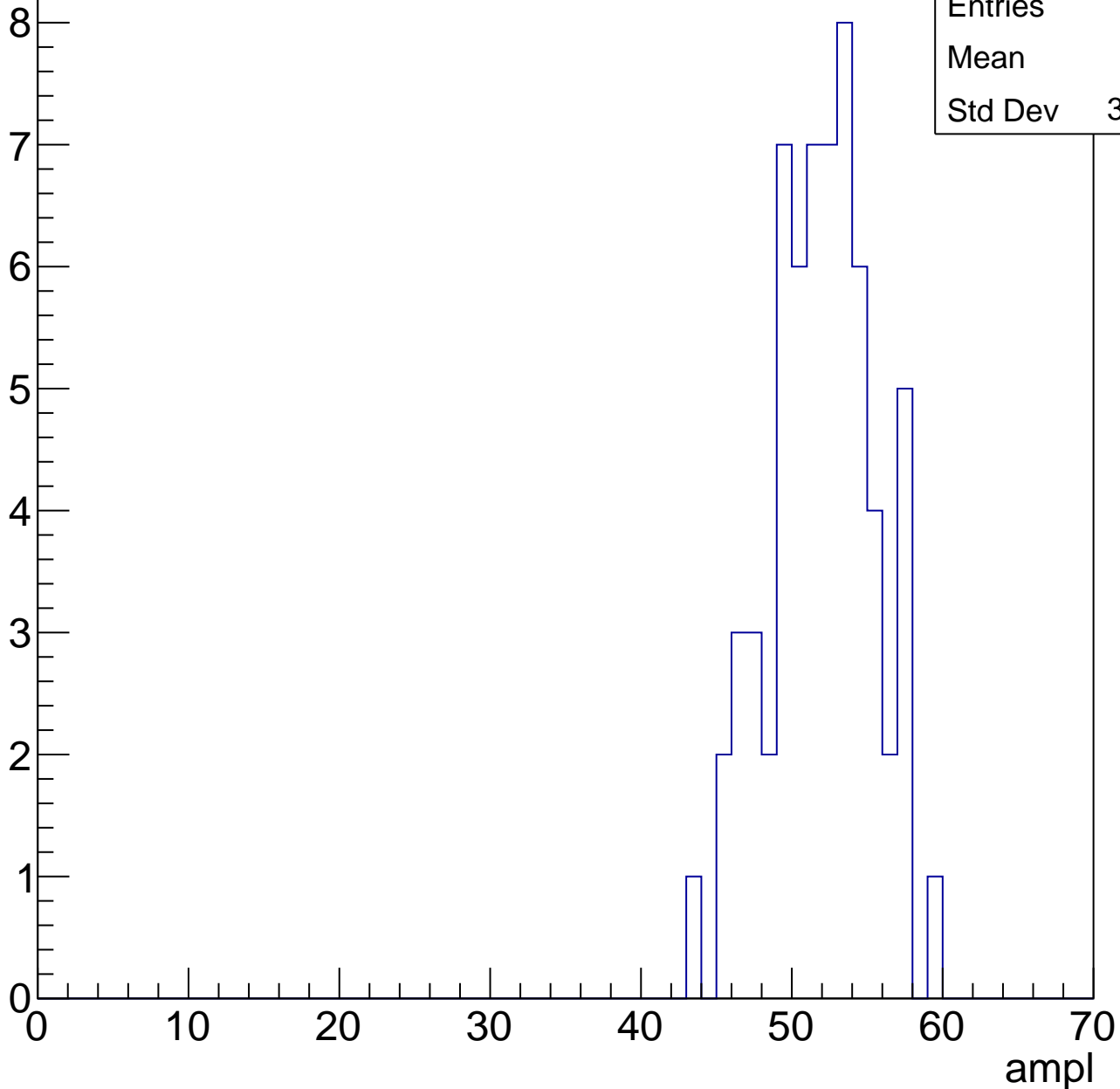


B1L101S, U21-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.5
Std Dev	3.414

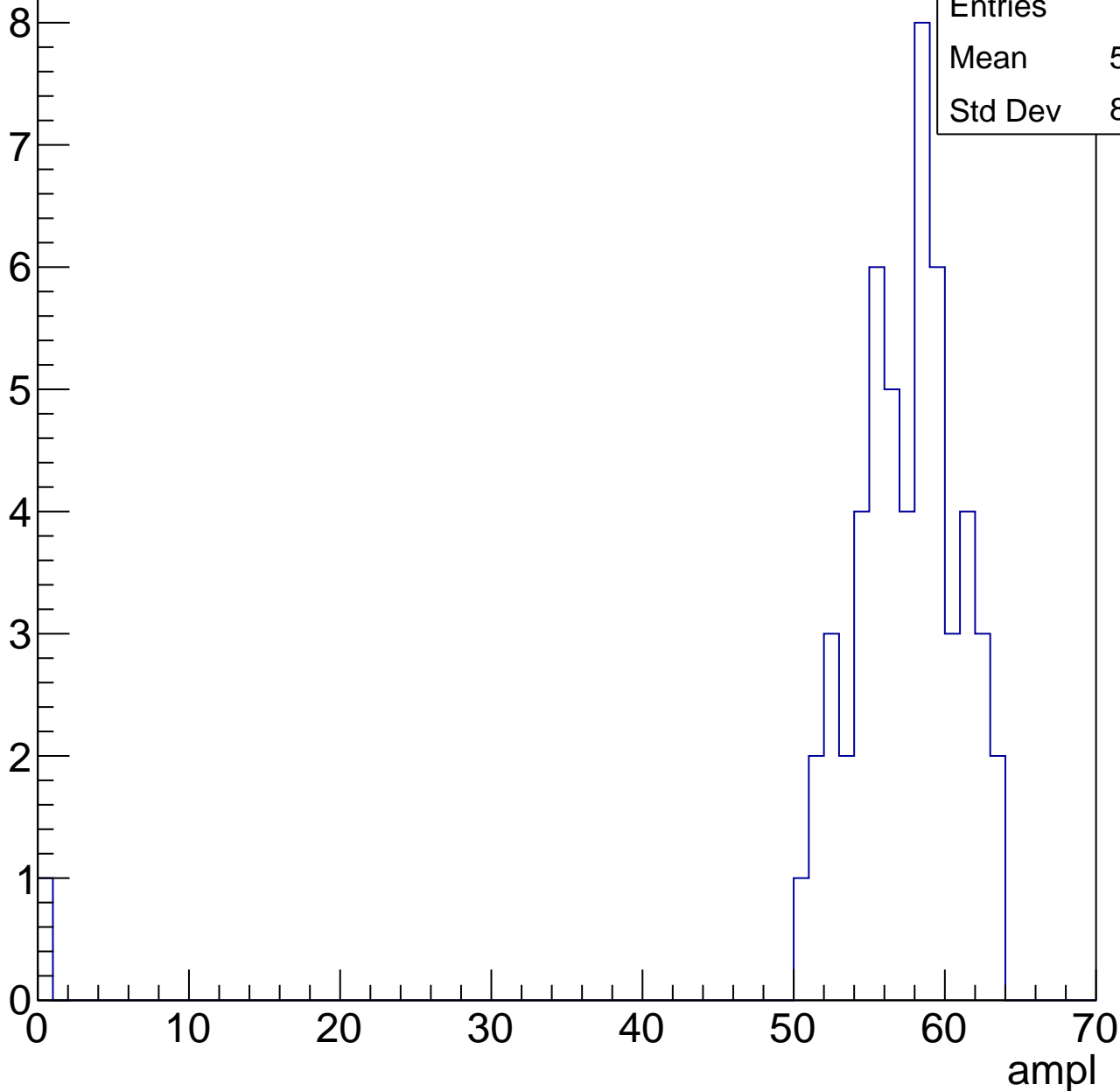


B1L101S, U21-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.96
Std Dev	8.338



B1L101S, U21-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

60.38

Std Dev

2.044

ampl

0

10

20

30

40

50

60

70

B1L101S, U21-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch4, adc0

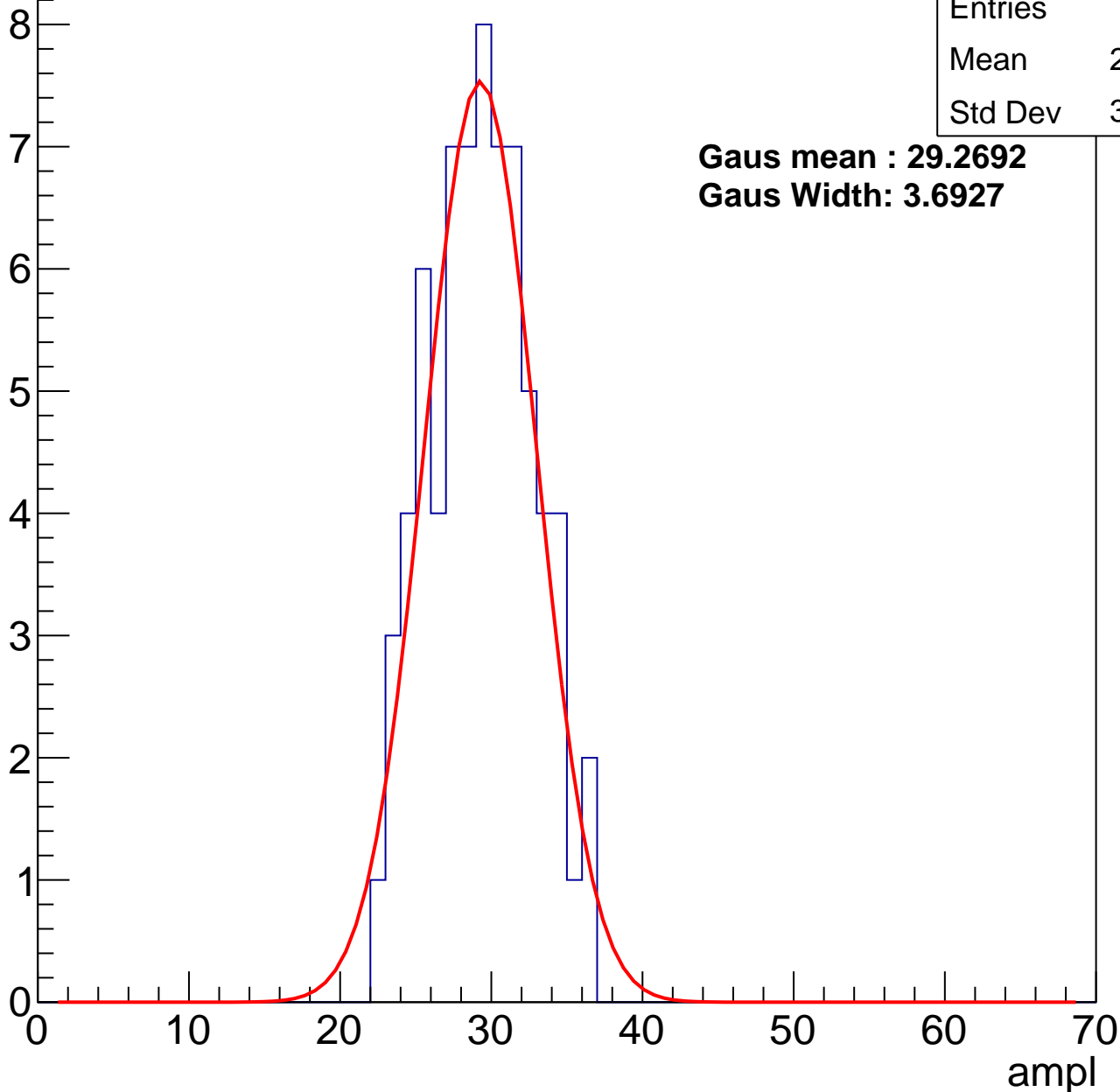
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.86
Std Dev	3.382

Gaus mean : 29.2692

Gaus Width: 3.6927



B1L101S, U21-ch4, adc1

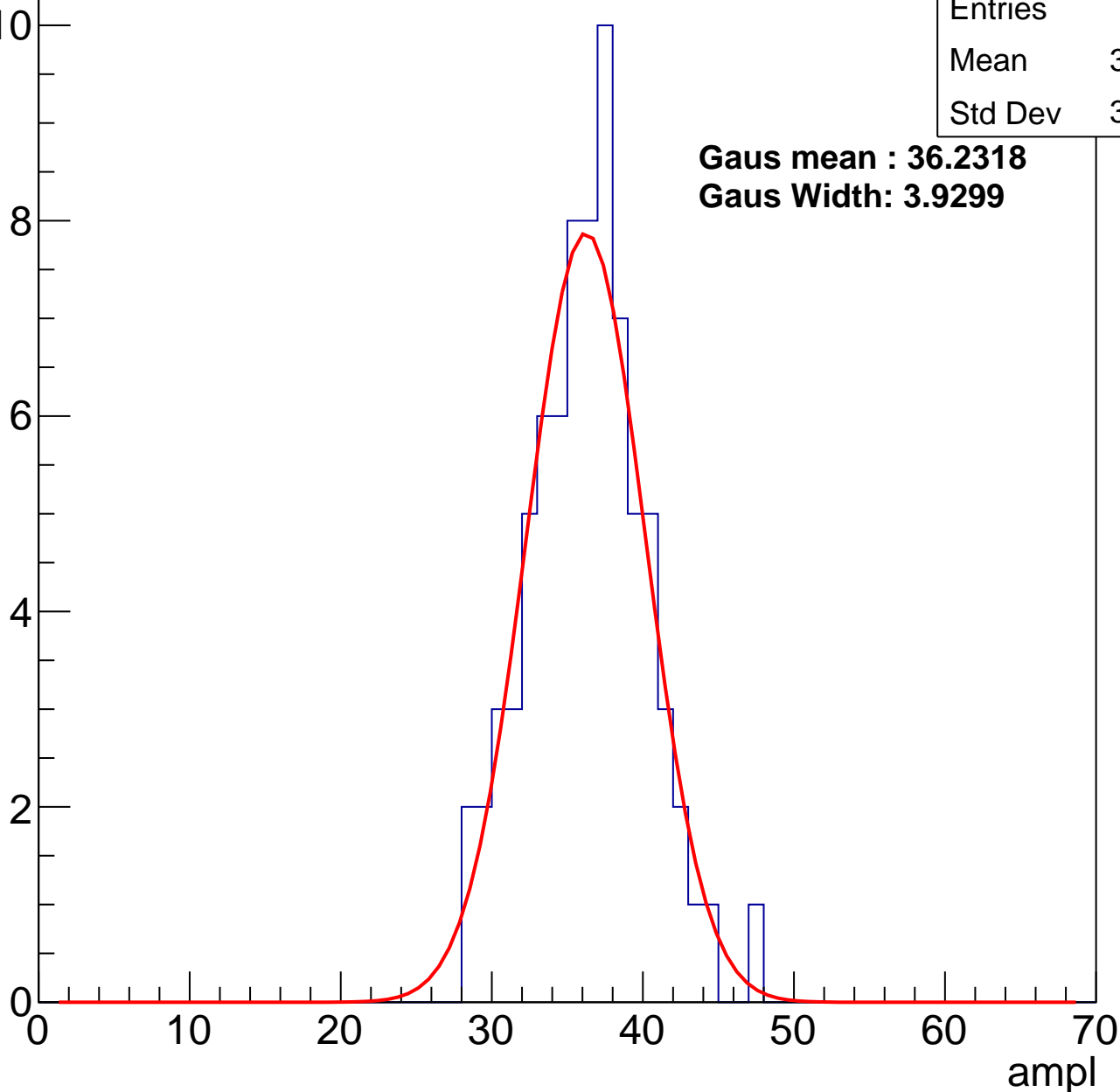
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	35.88
Std Dev	3.786

Gaus mean : 36.2318

Gaus Width: 3.9299



B1L101S, U21-ch4, adc2

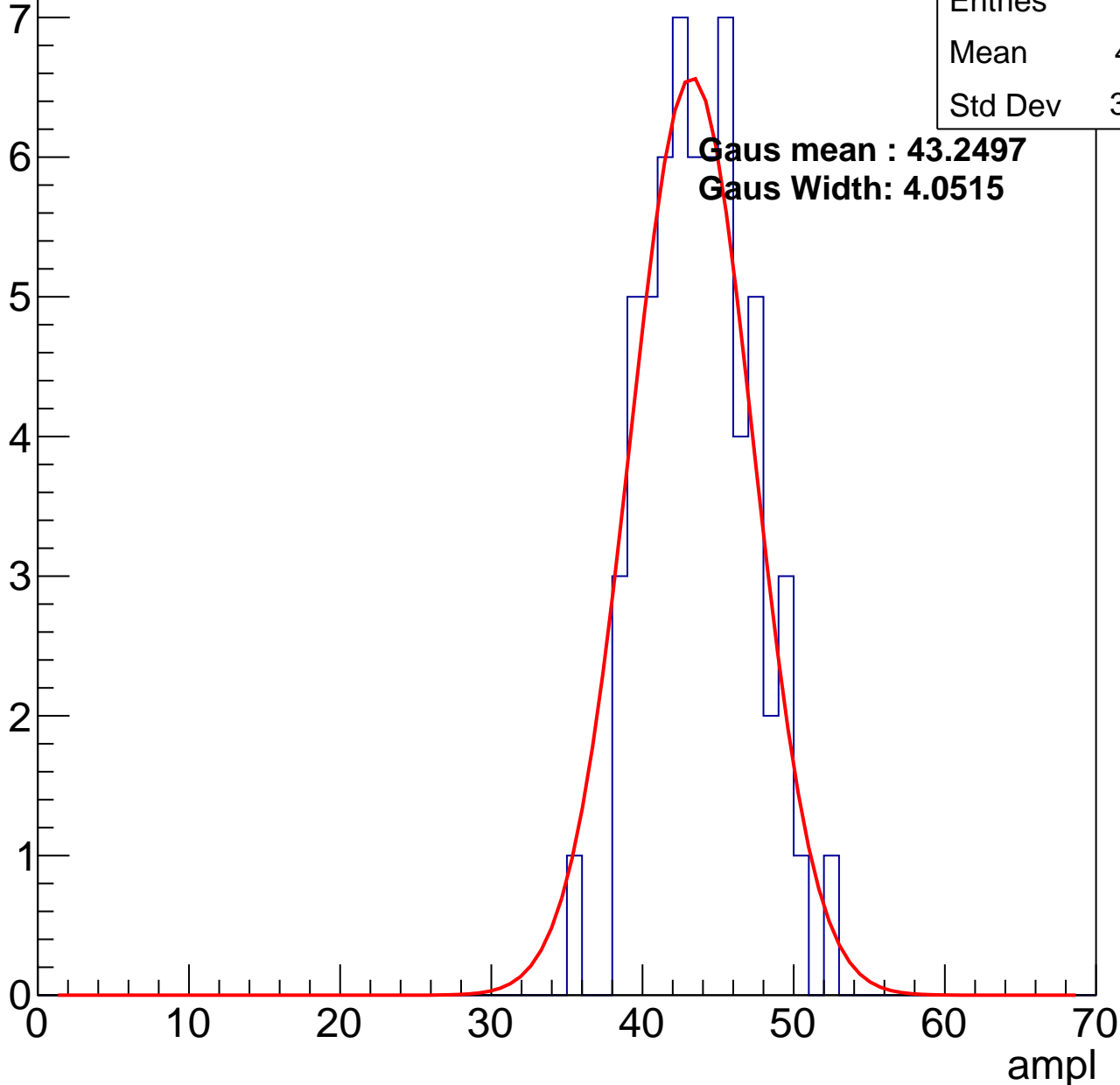
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.31
Std Dev	3.425

Gaus mean : 43.2497

Gaus Width: 4.0515

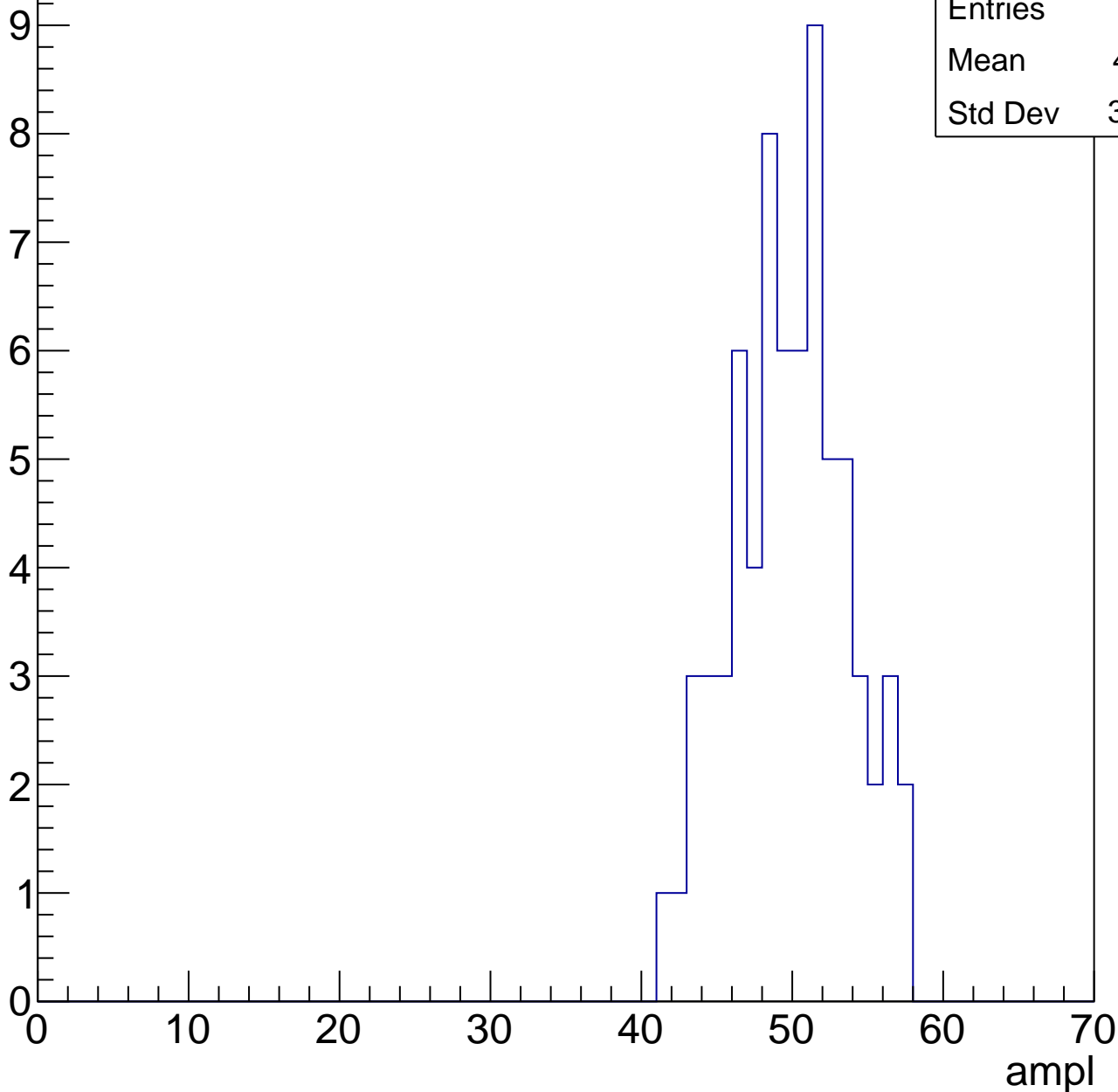


B1L101S, U21-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	49.41
Std Dev	3.778

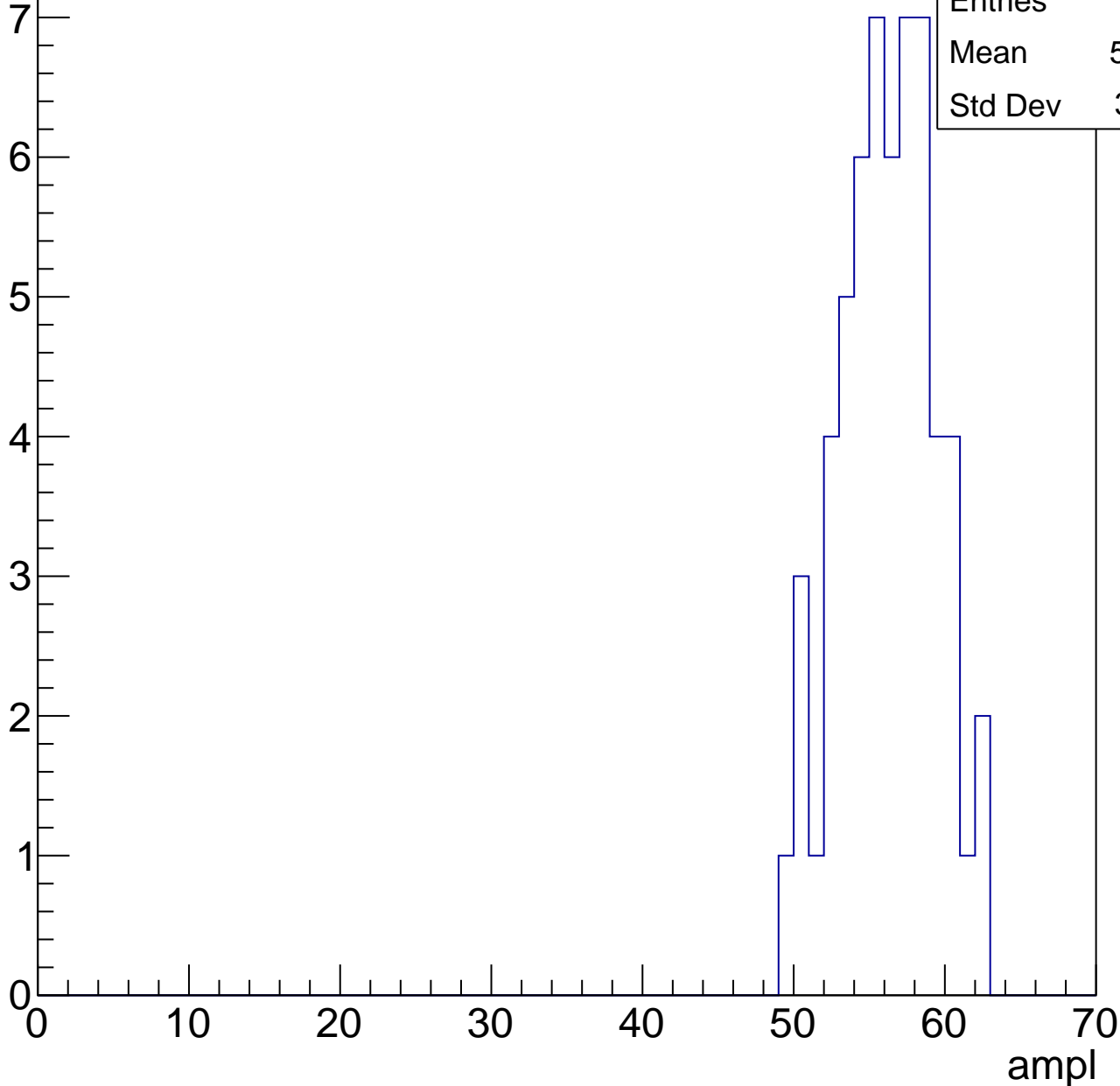


B1L101S, U21-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

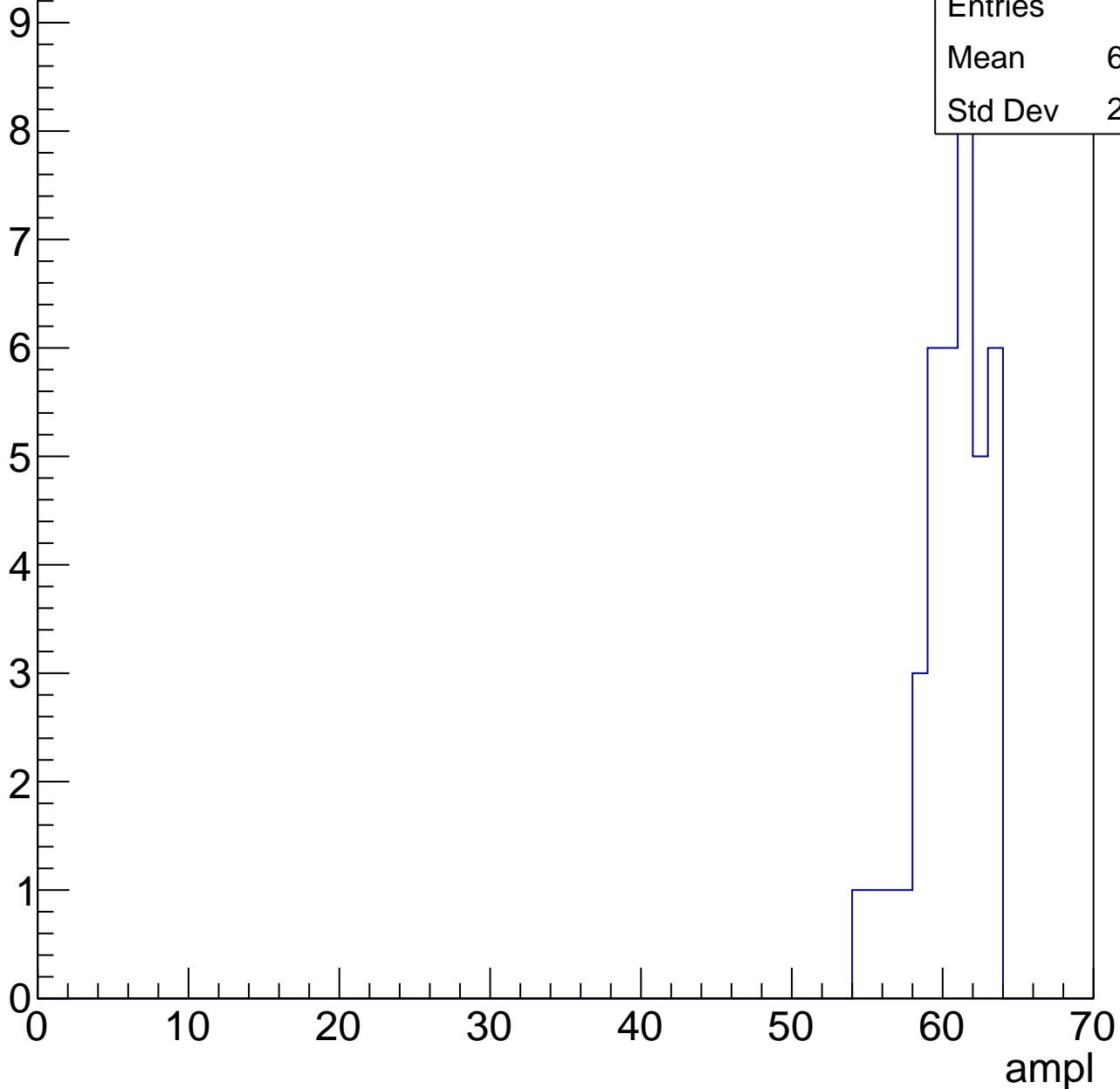
Entries	58
Mean	55.76
Std Dev	3.081



B1L101S, U21-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

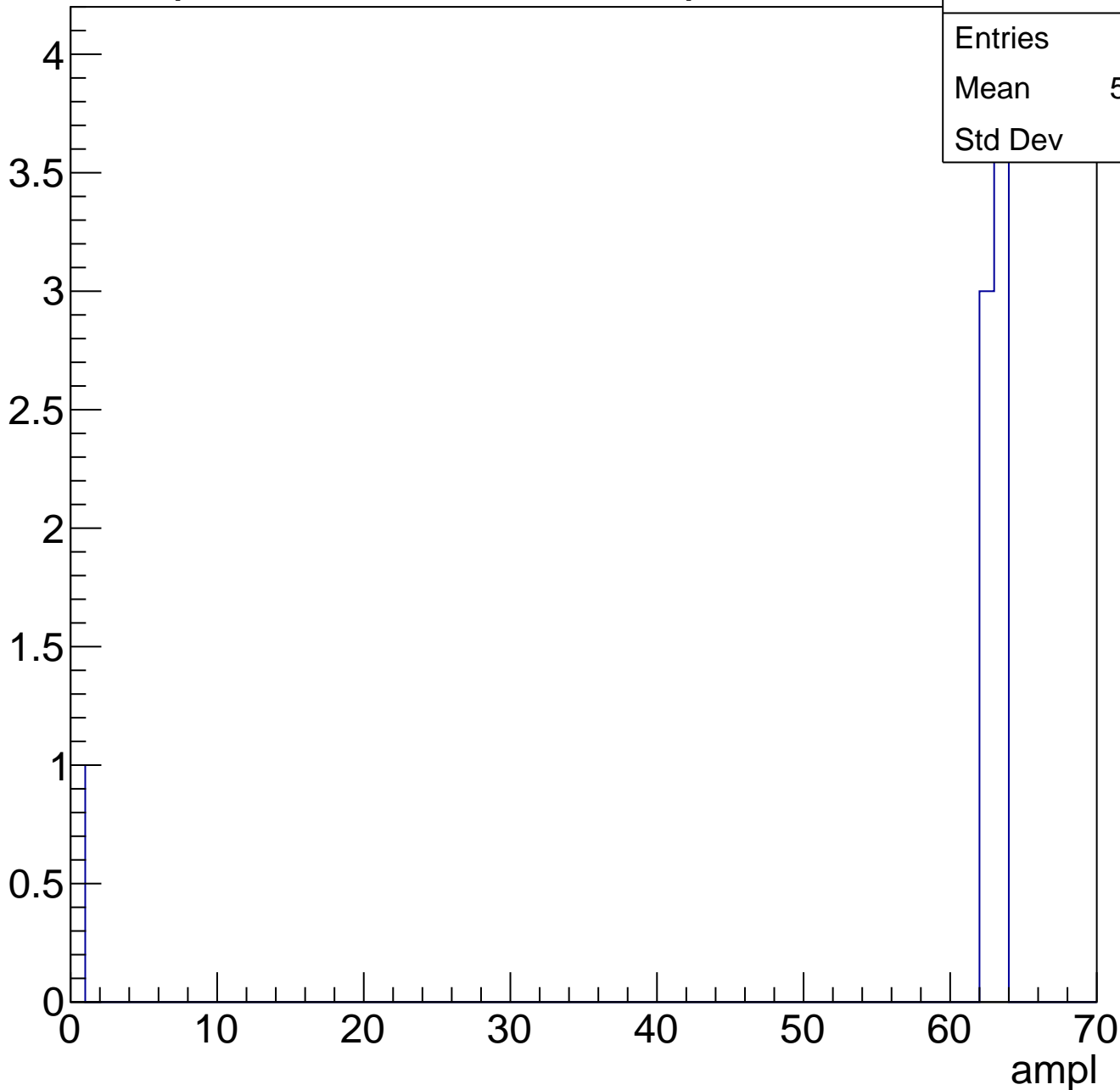


Entries	39
Mean	60.18
Std Dev	2.182

B1L101S, U21-ch4, adc6

calib_packv5_042523_0143.root, FC#0, port D2

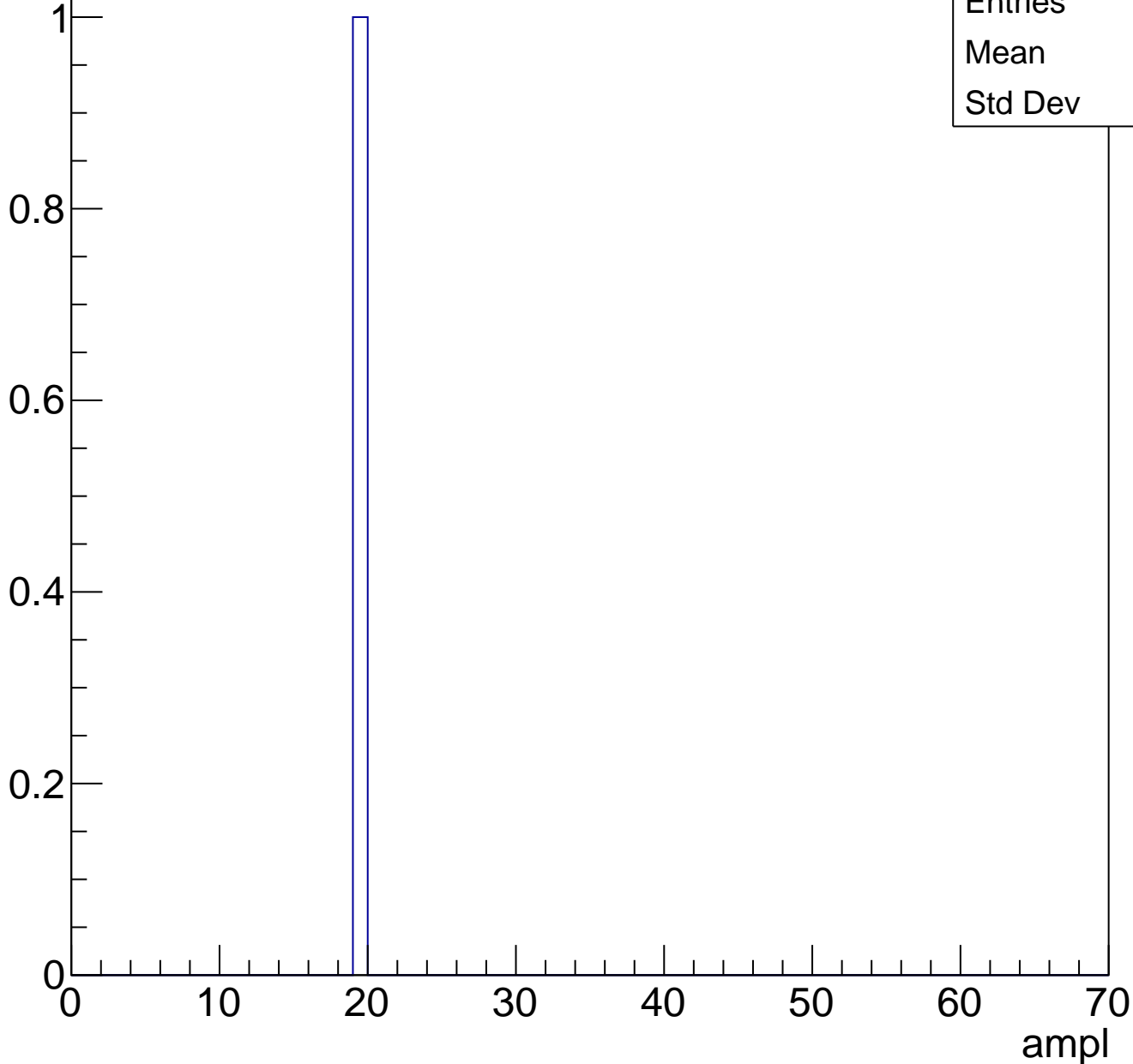
Entry



B1L101S, U21-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch5, adc0

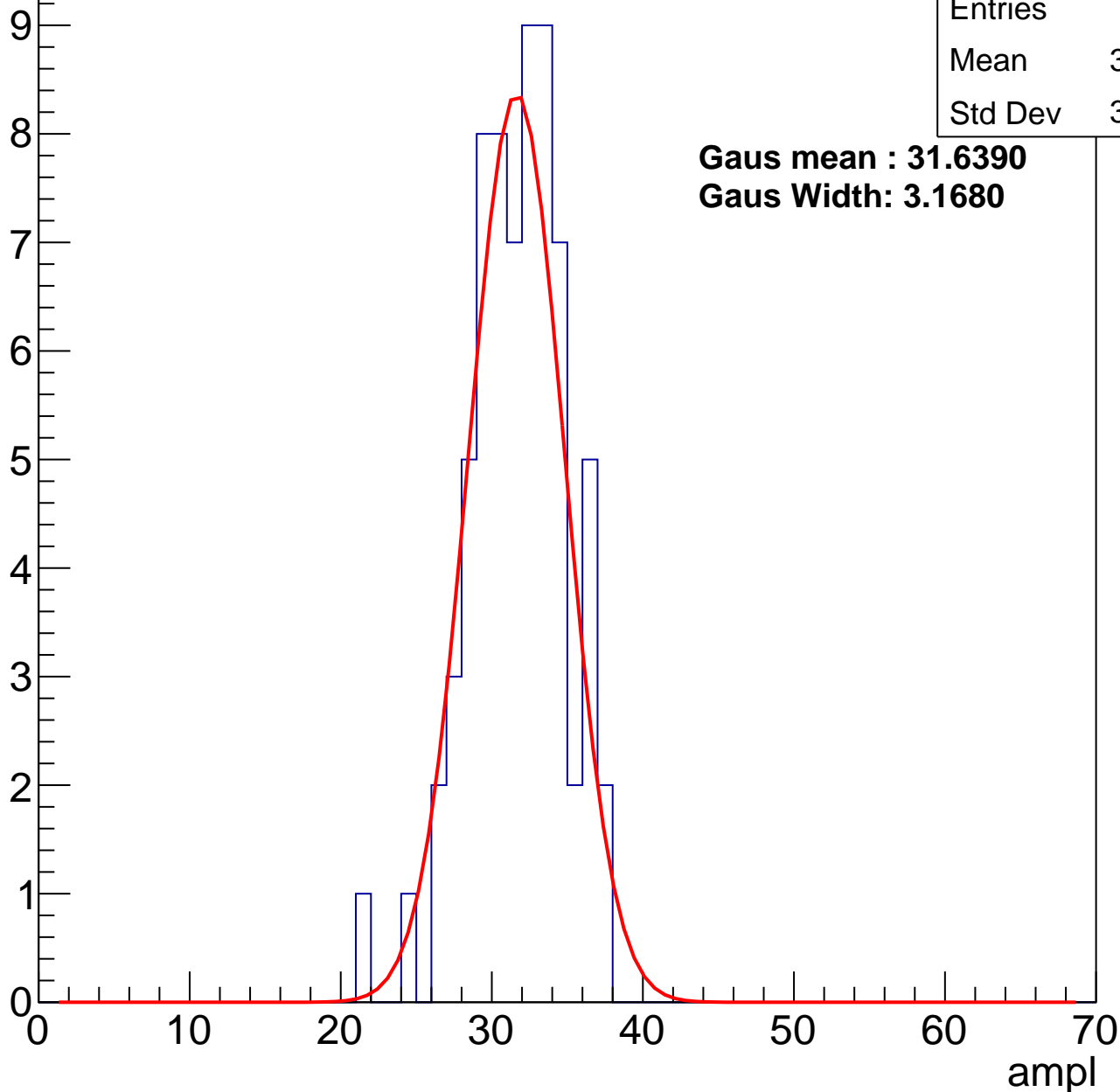
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	31.22
Std Dev	3.106

Gaus mean : 31.6390

Gaus Width: 3.1680



B1L101S, U21-ch5, adc1

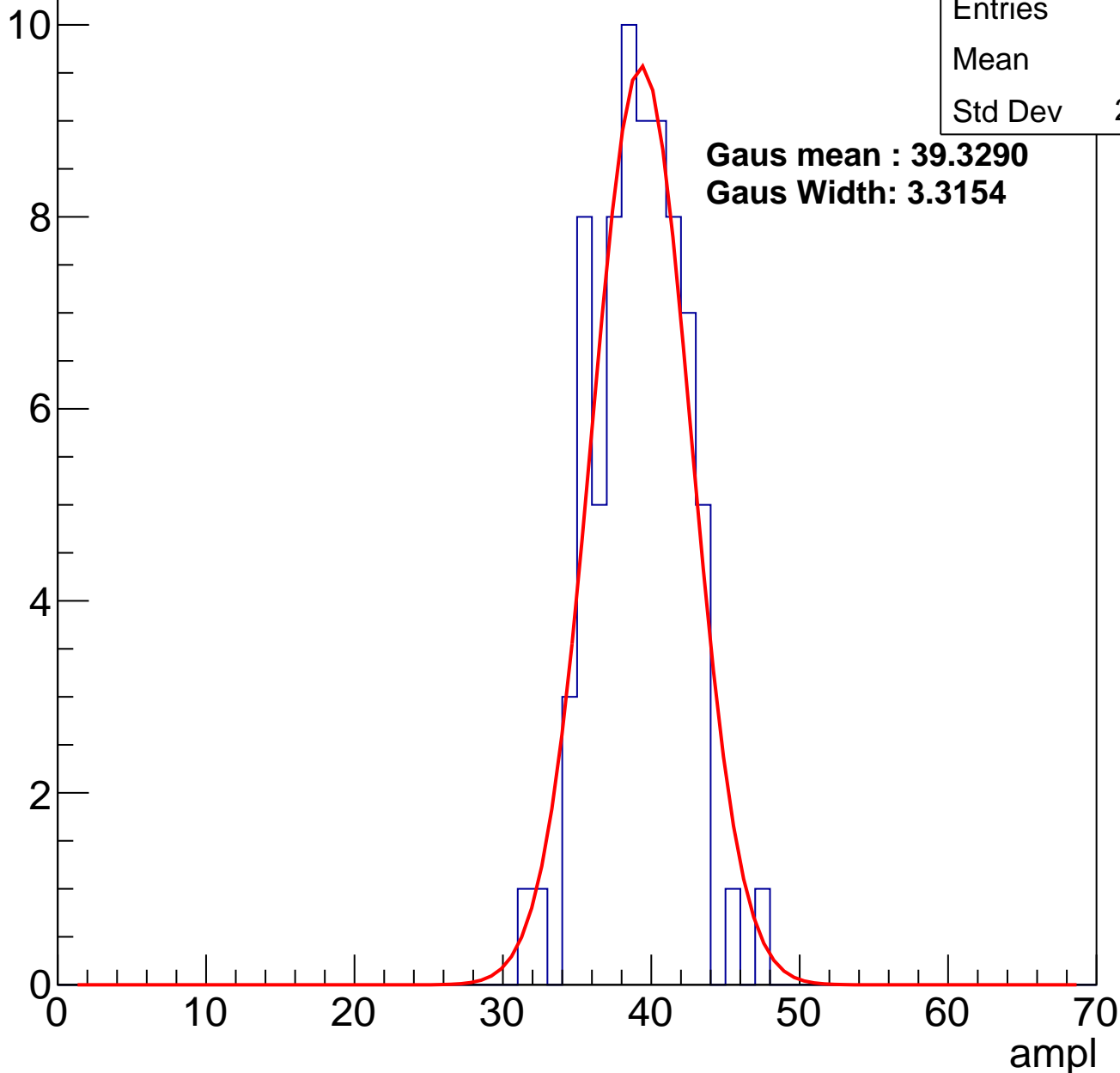
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	38.7
Std Dev	2.991

Gaus mean : 39.3290

Gaus Width: 3.3154

Entry



B1L101S, U21-ch5, adc2

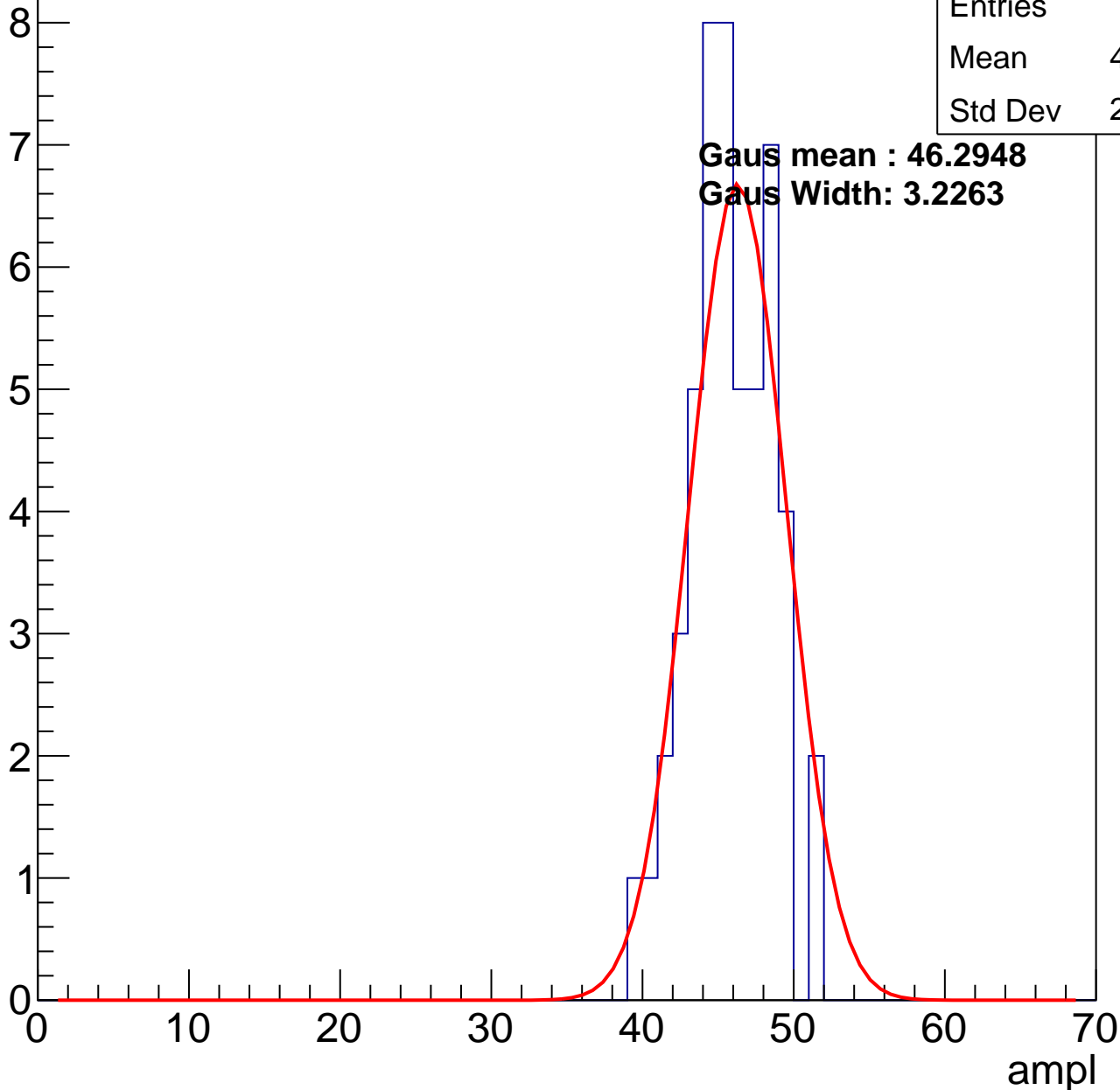
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	45.35
Std Dev	2.678

Gaus mean : 46.2948

Gaus Width: 3.2263



B1L101S, U21-ch5, adc3

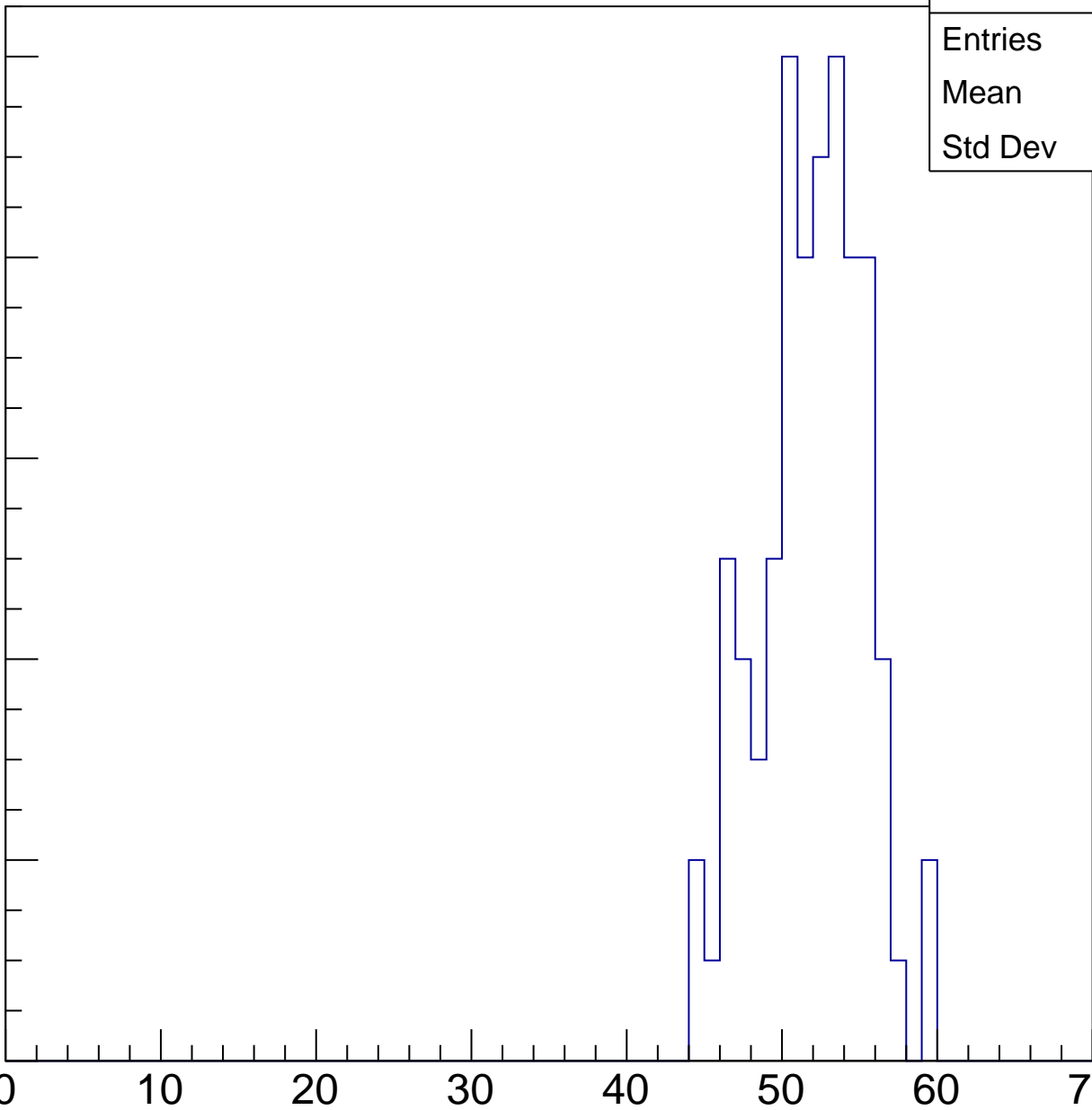
calib_packv5_042523_0143.root, FC#0, port D2

Entry

10
8
6
4
2
0

Entries	80
Mean	51.46
Std Dev	3.309

ampl

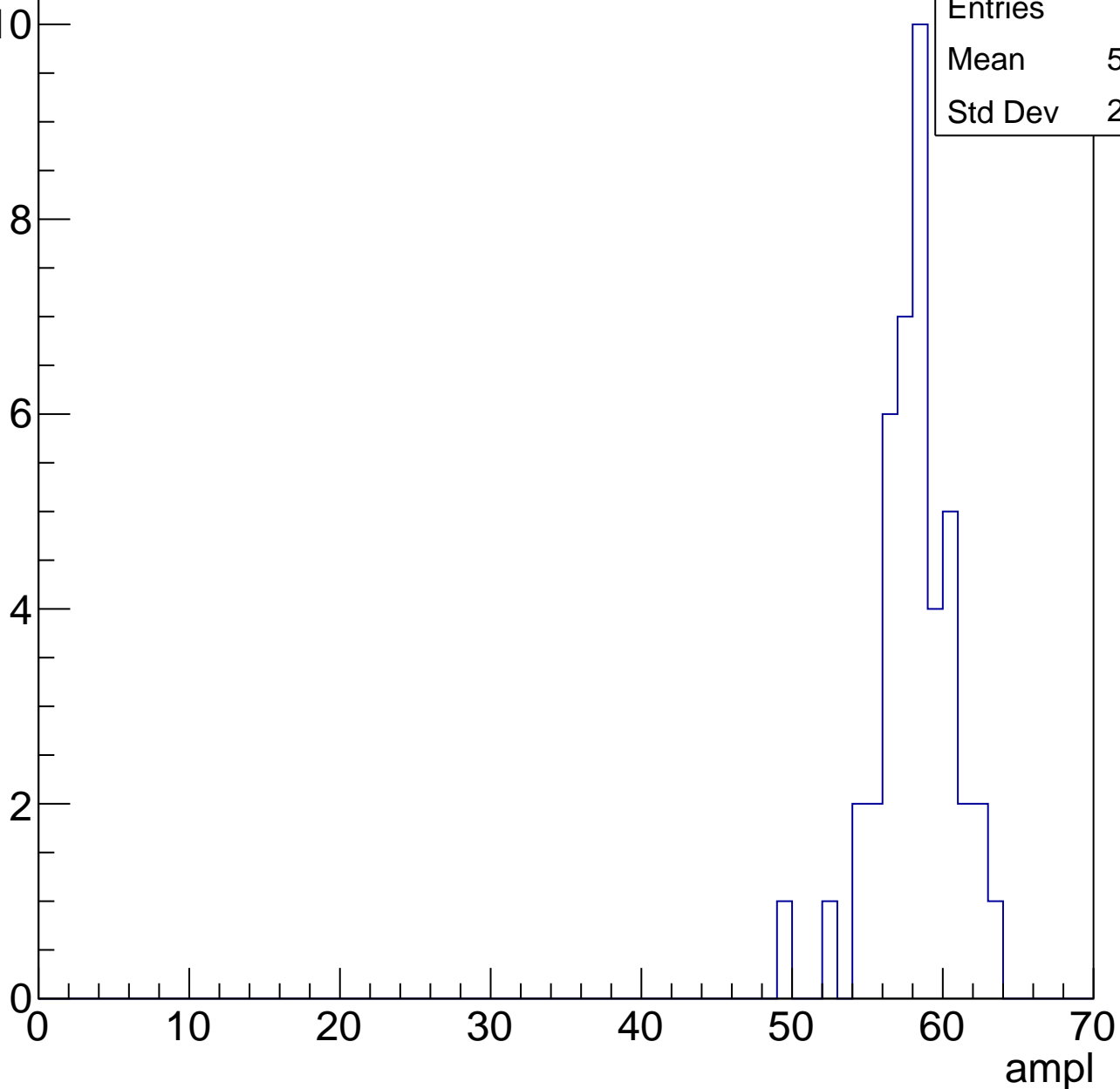


B1L101S, U21-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	57.65
Std Dev	2.605

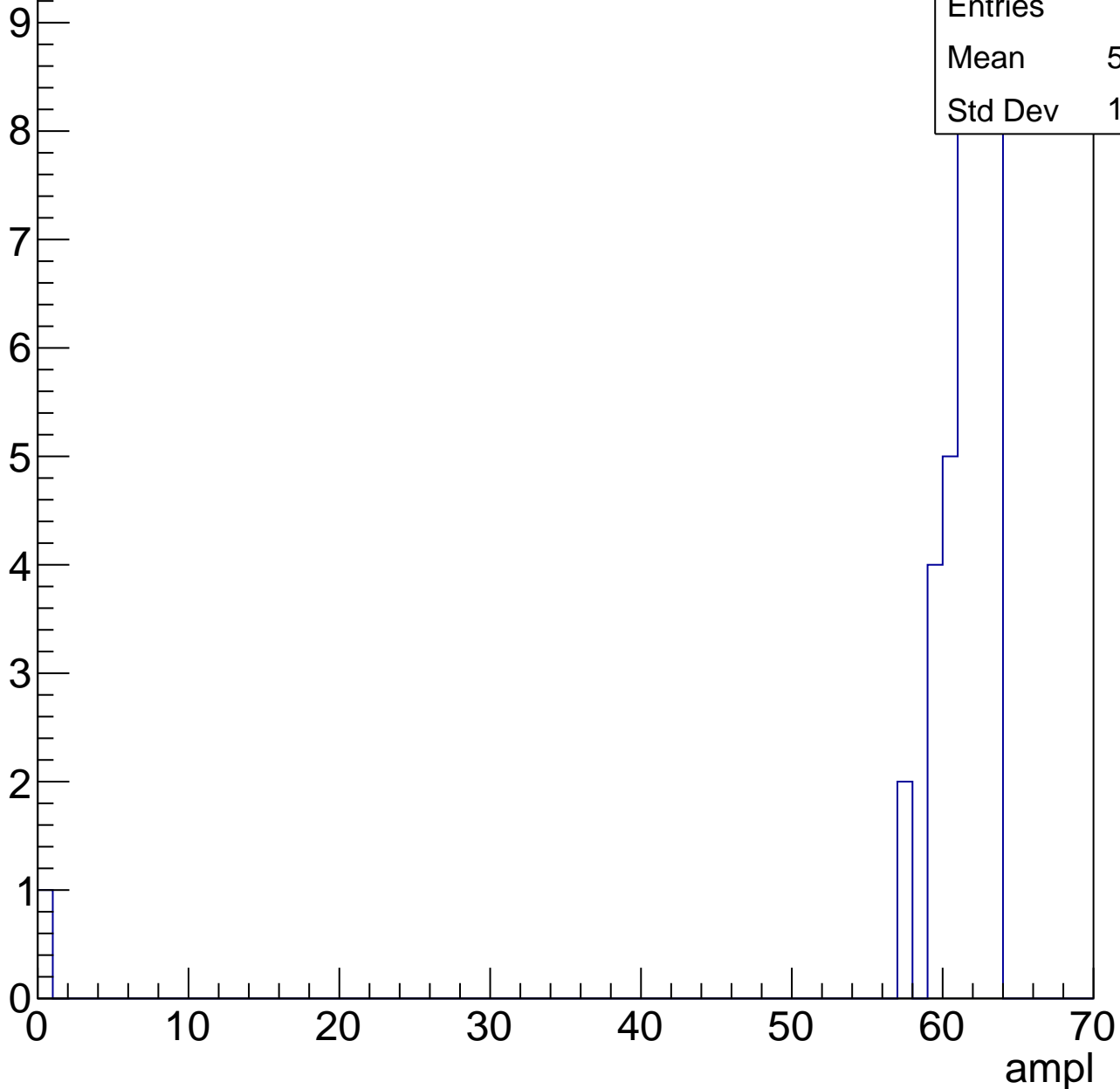


B1L101S, U21-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	59.49
Std Dev	10.04



B1L101S, U21-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch6, adc0

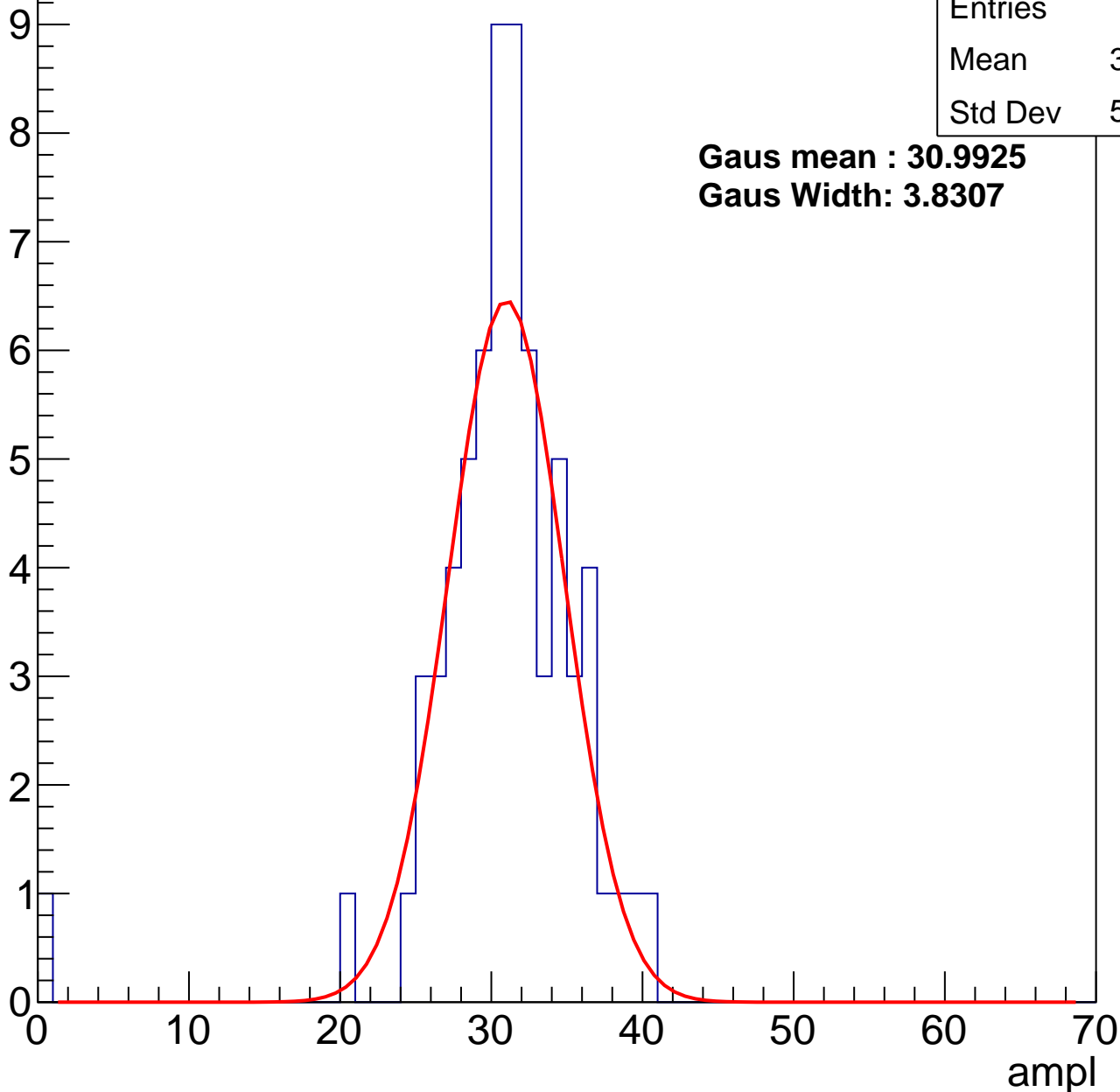
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.33
Std Dev	5.278

Gaus mean : 30.9925

Gaus Width: 3.8307



B1L101S, U21-ch6, adc1

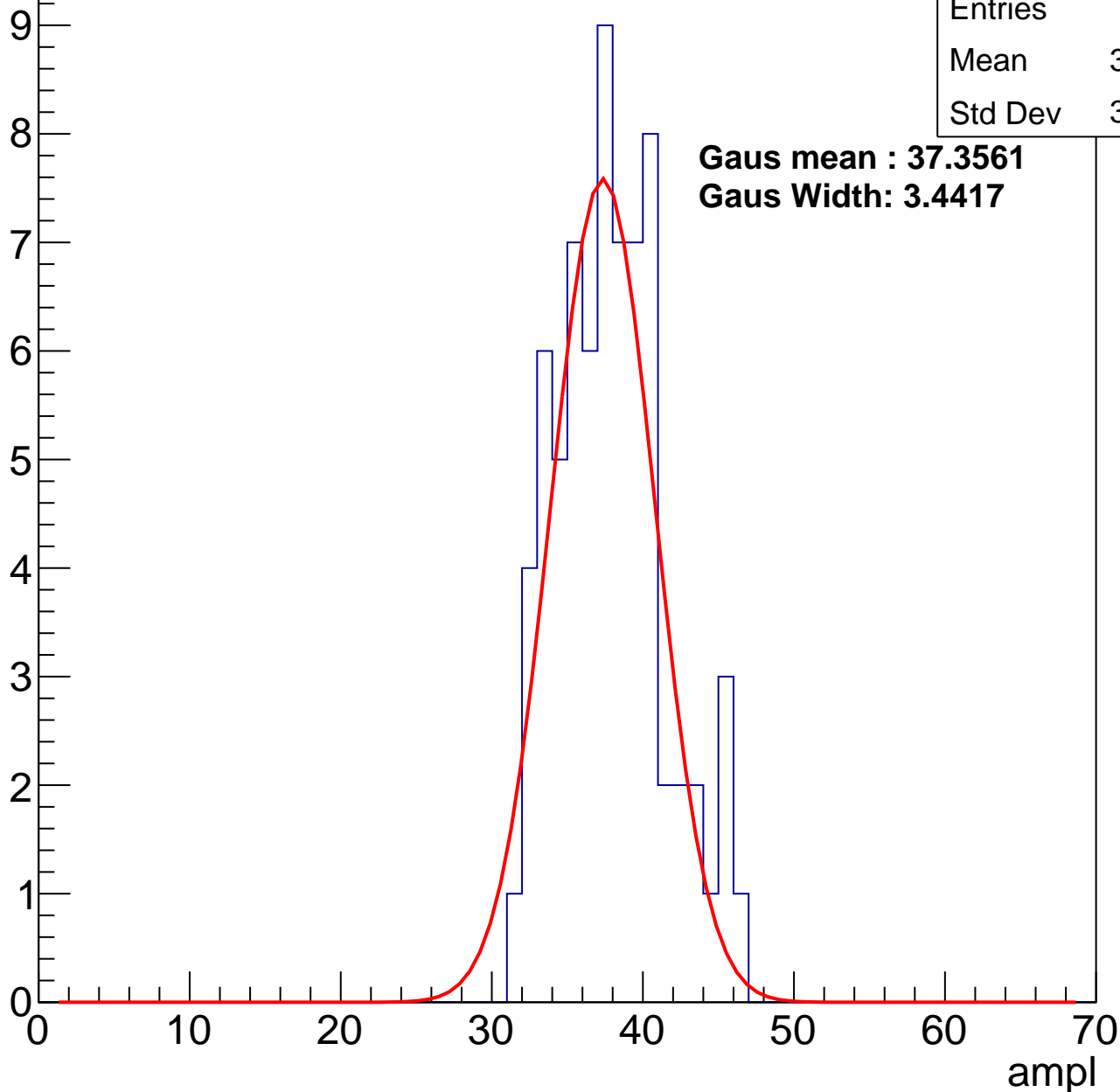
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	37.42
Std Dev	3.523

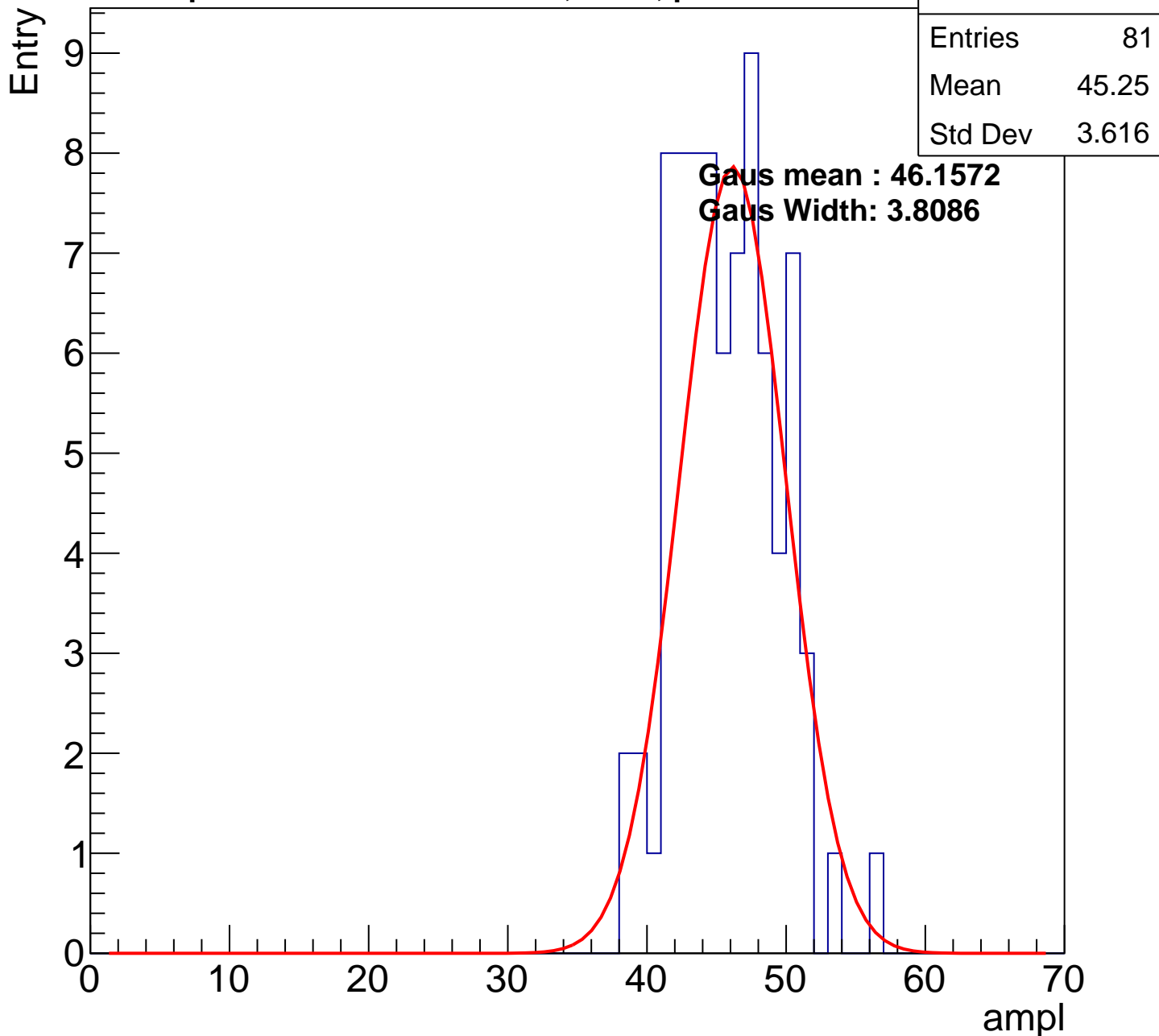
Gaus mean : 37.3561

Gaus Width: 3.4417



B1L101S, U21-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2

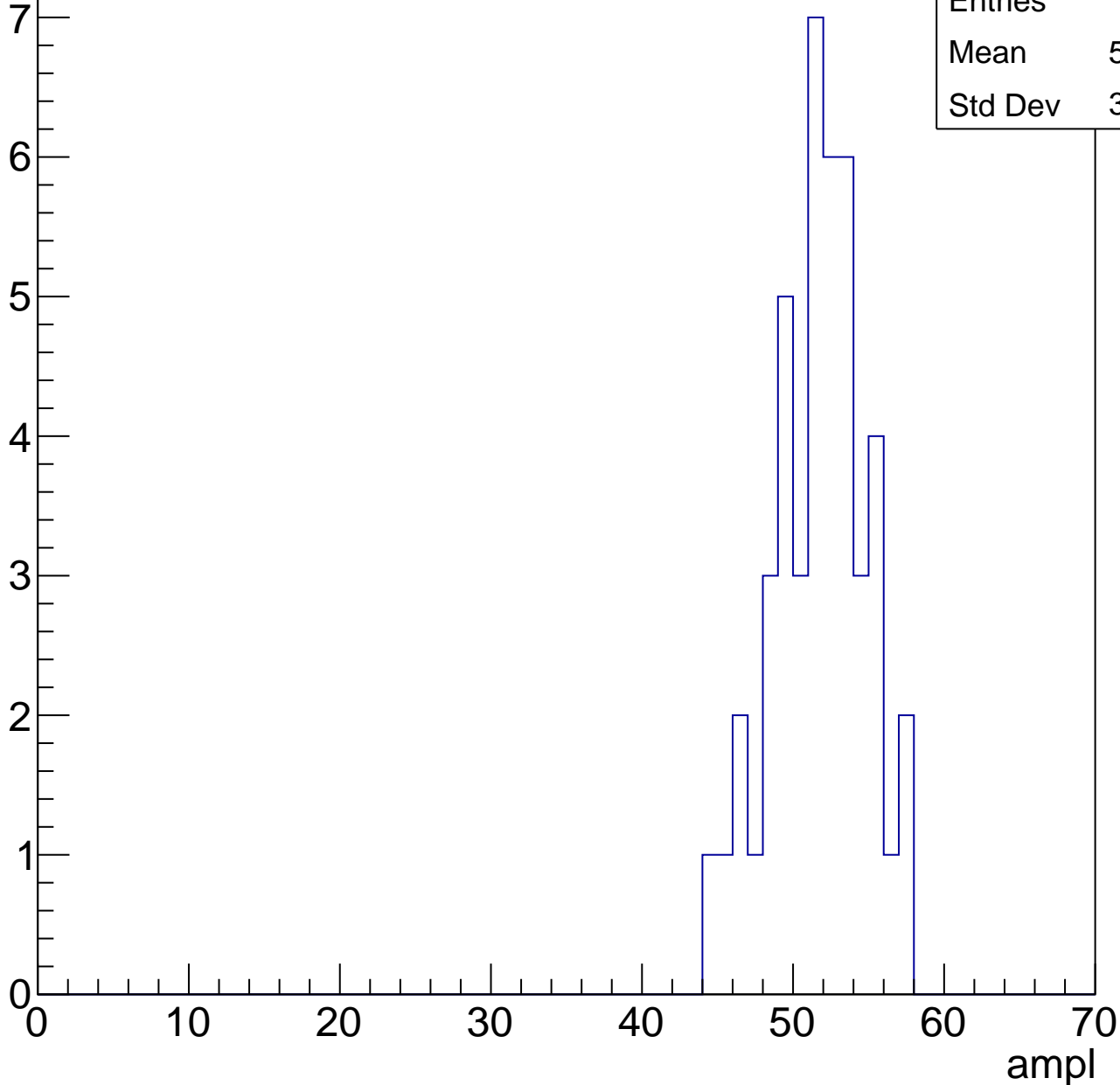


B1L101S, U21-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

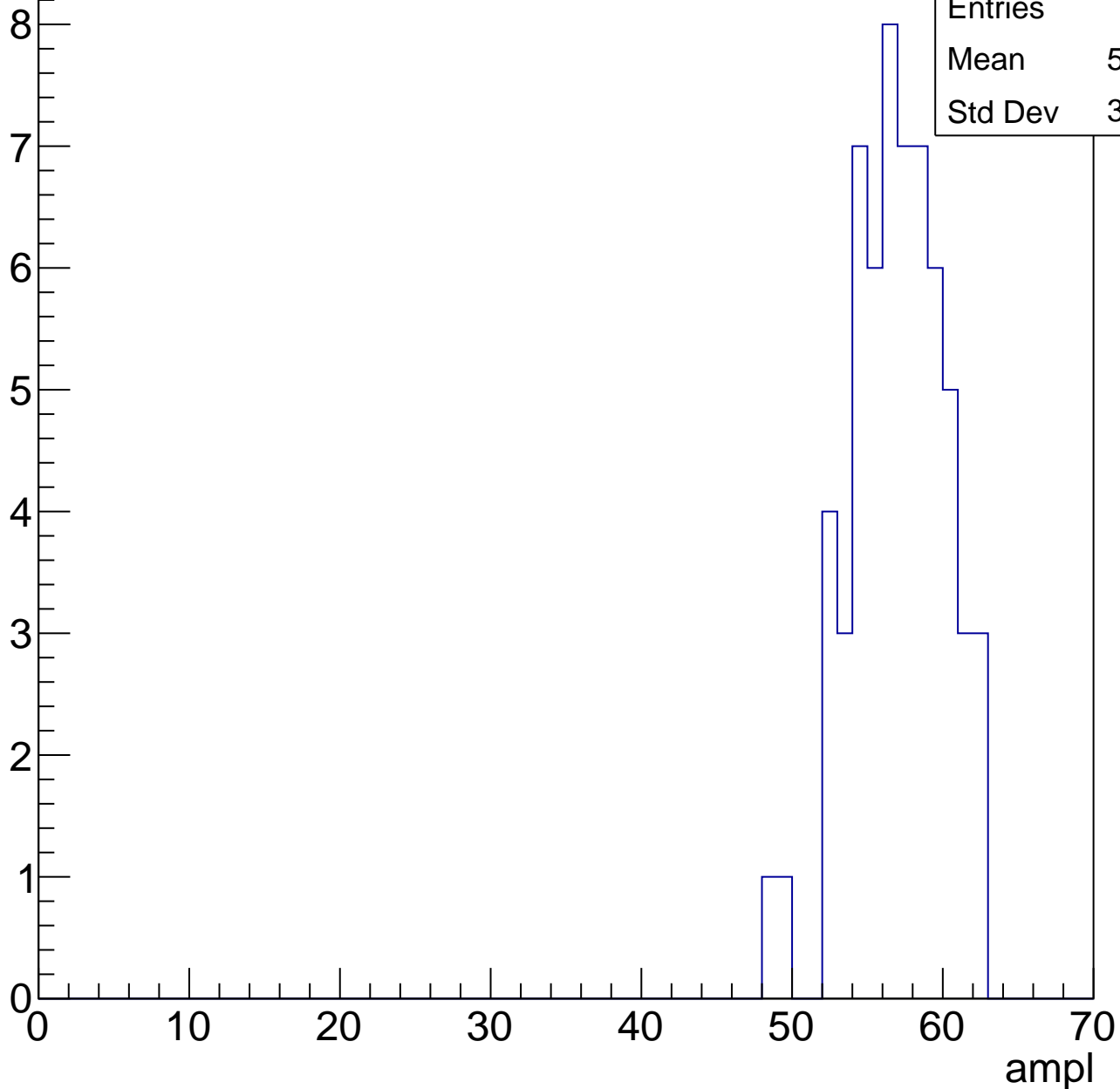
Entries	45
Mean	51.24
Std Dev	3.042



B1L101S, U21-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

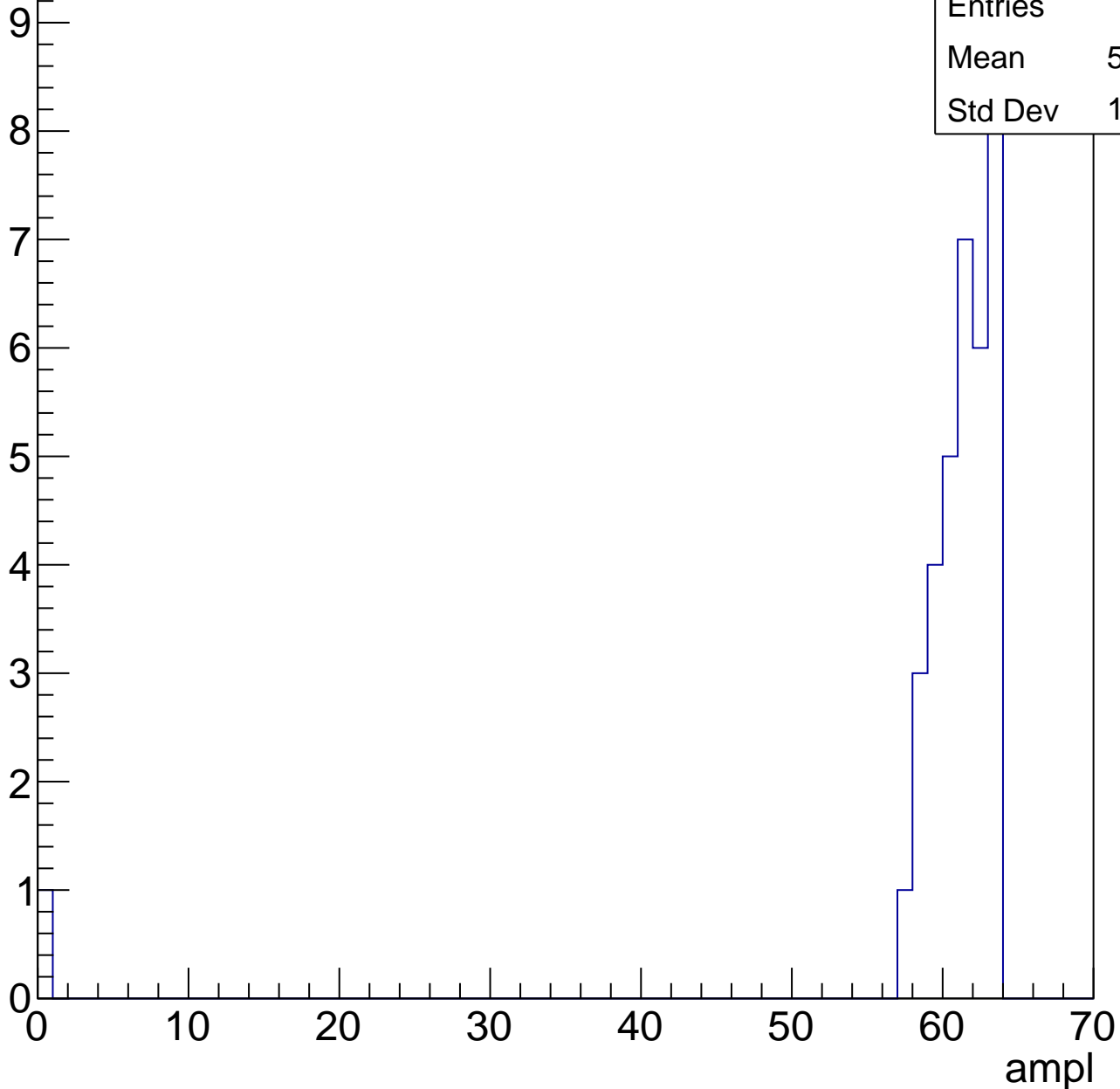


B1L101S, U21-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

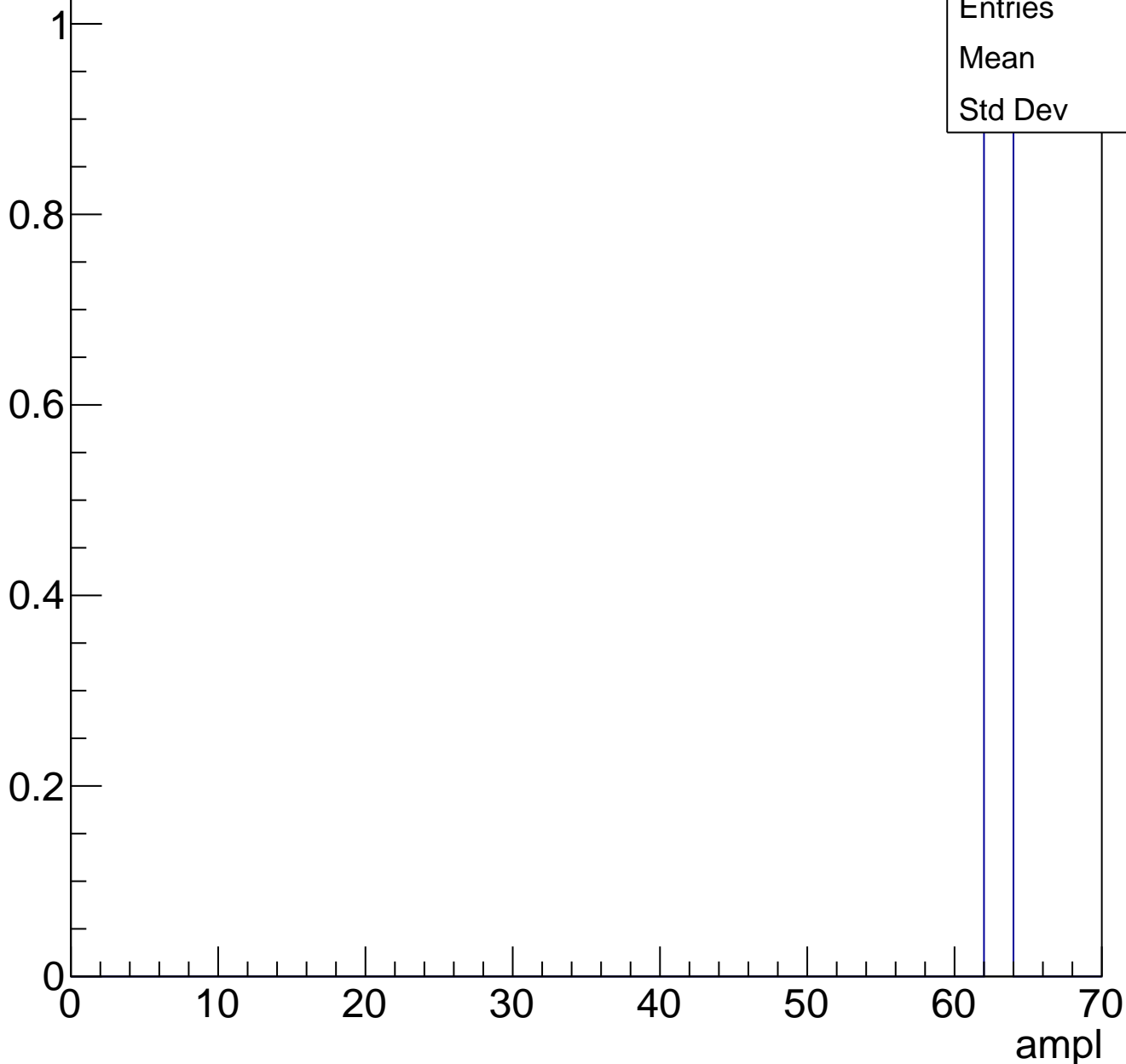
Entries	36
Mean	59.25
Std Dev	10.16



B1L101S, U21-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch7, adc0

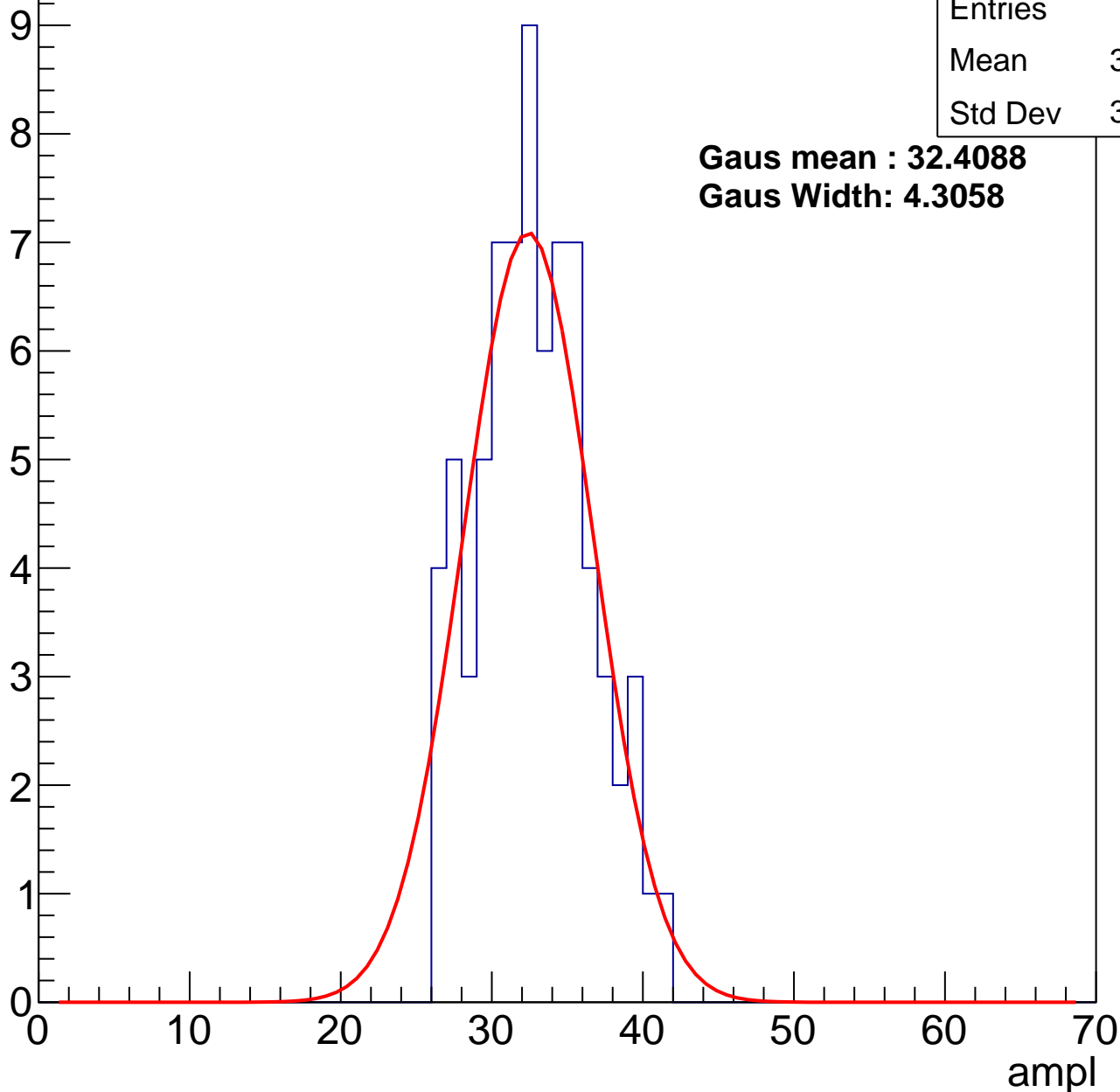
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	32.34
Std Dev	3.655

Gaus mean : 32.4088

Gaus Width: 4.3058



B1L101S, U21-ch7, adc1

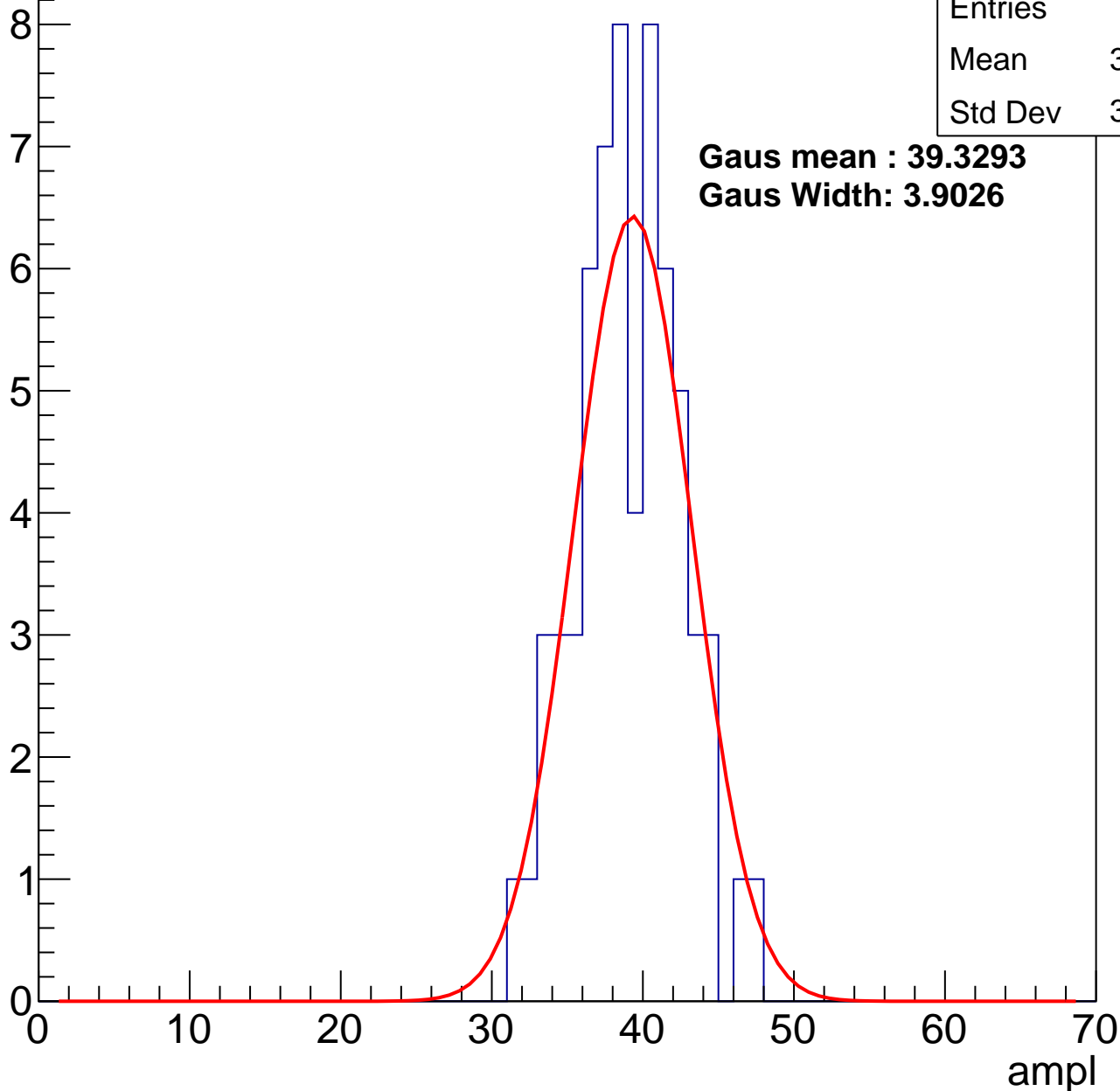
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.63
Std Dev	3.424

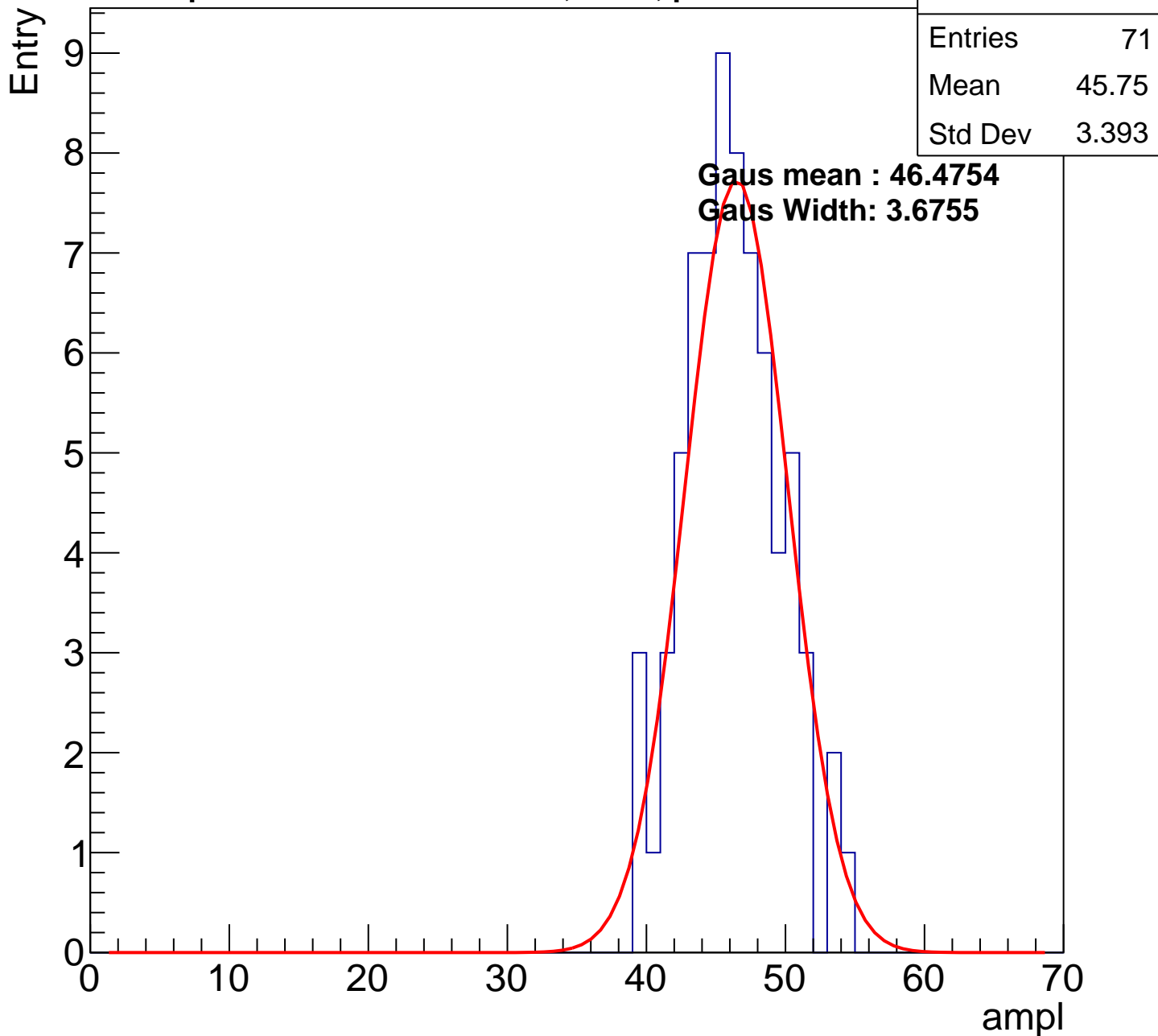
Gaus mean : 39.3293

Gaus Width: 3.9026



B1L101S, U21-ch7, adc2

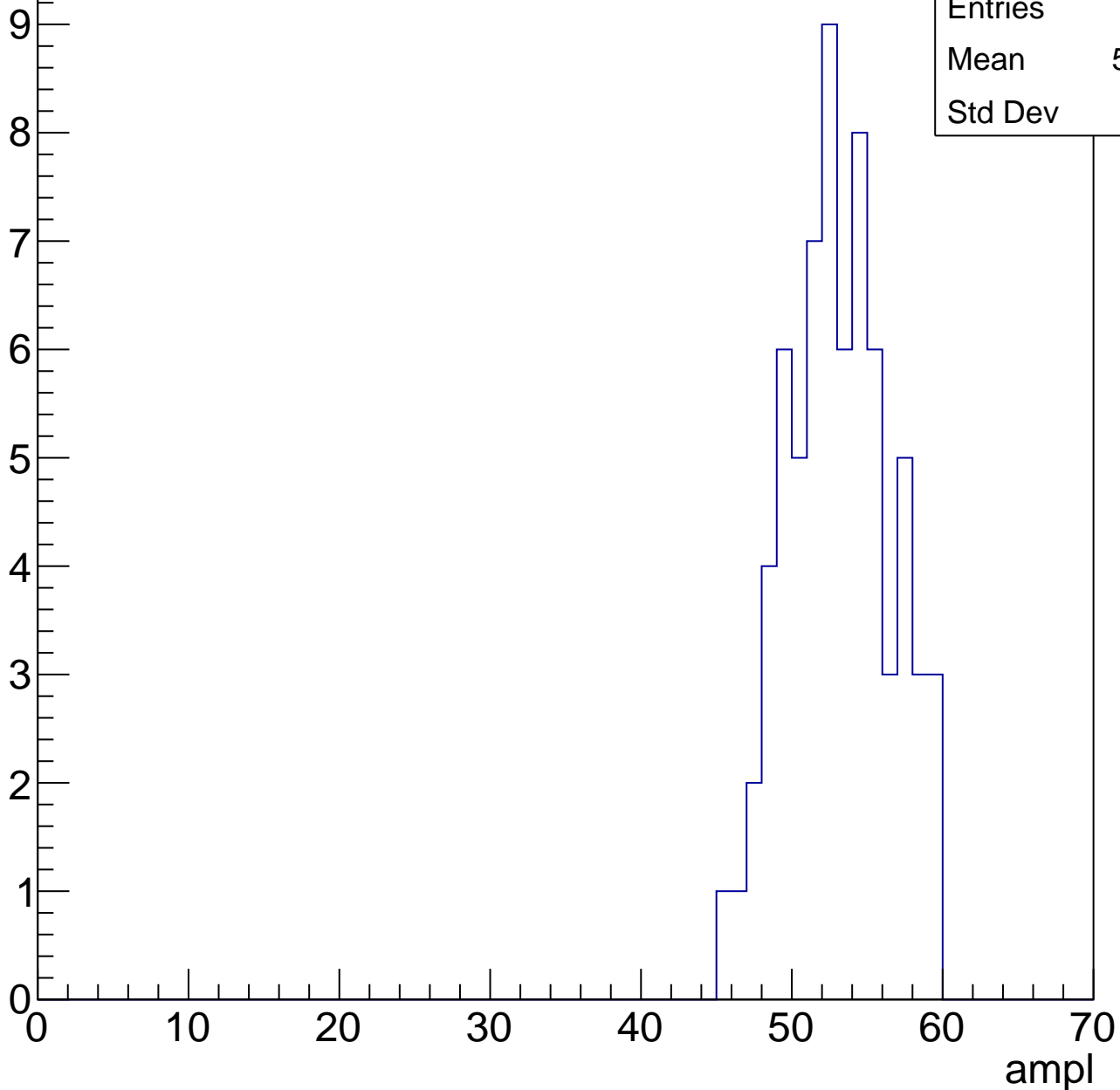
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



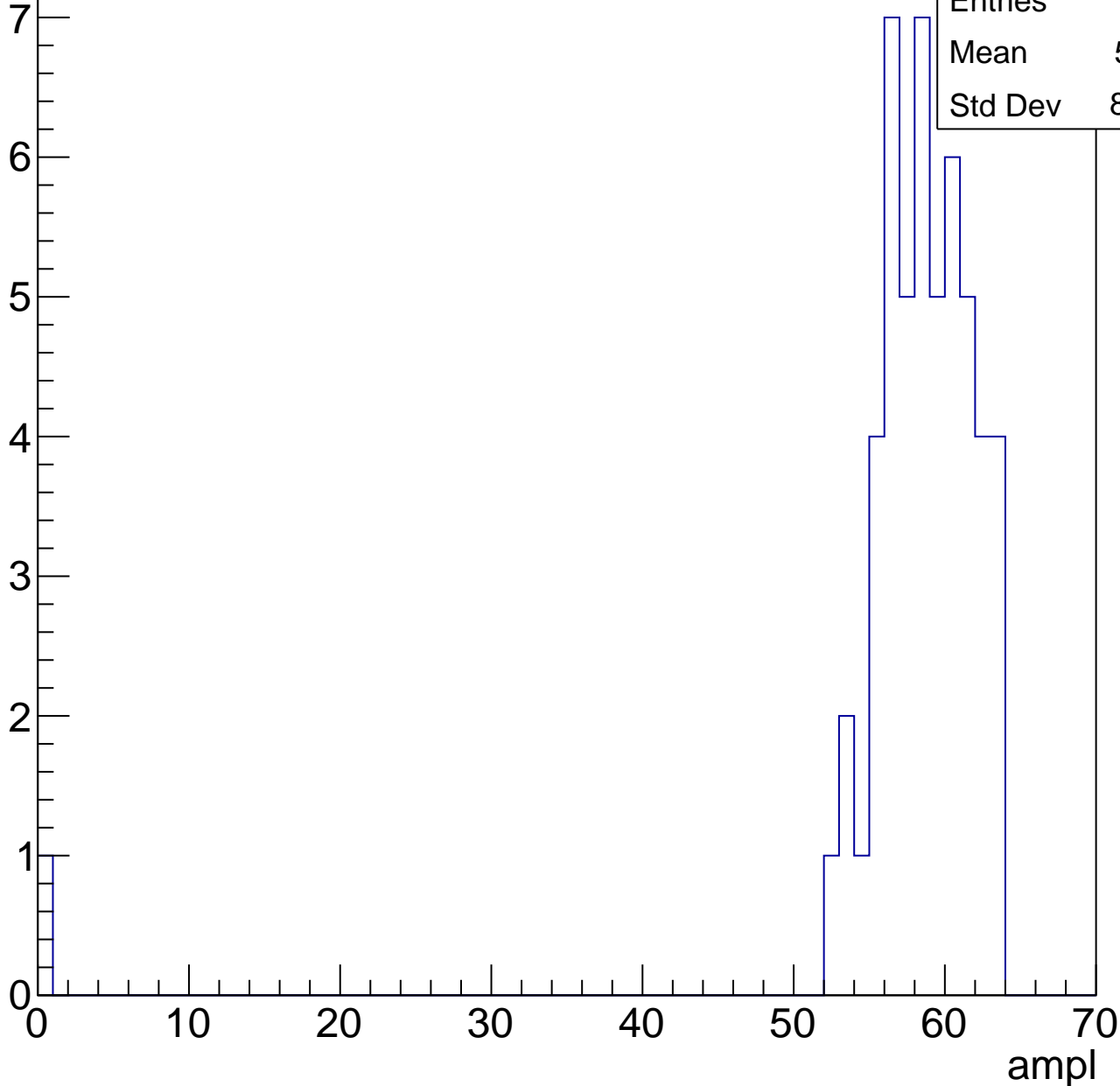
Entries	69
Mean	52.61
Std Dev	3.35

B1L101S, U21-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	57.21
Std Dev	8.482

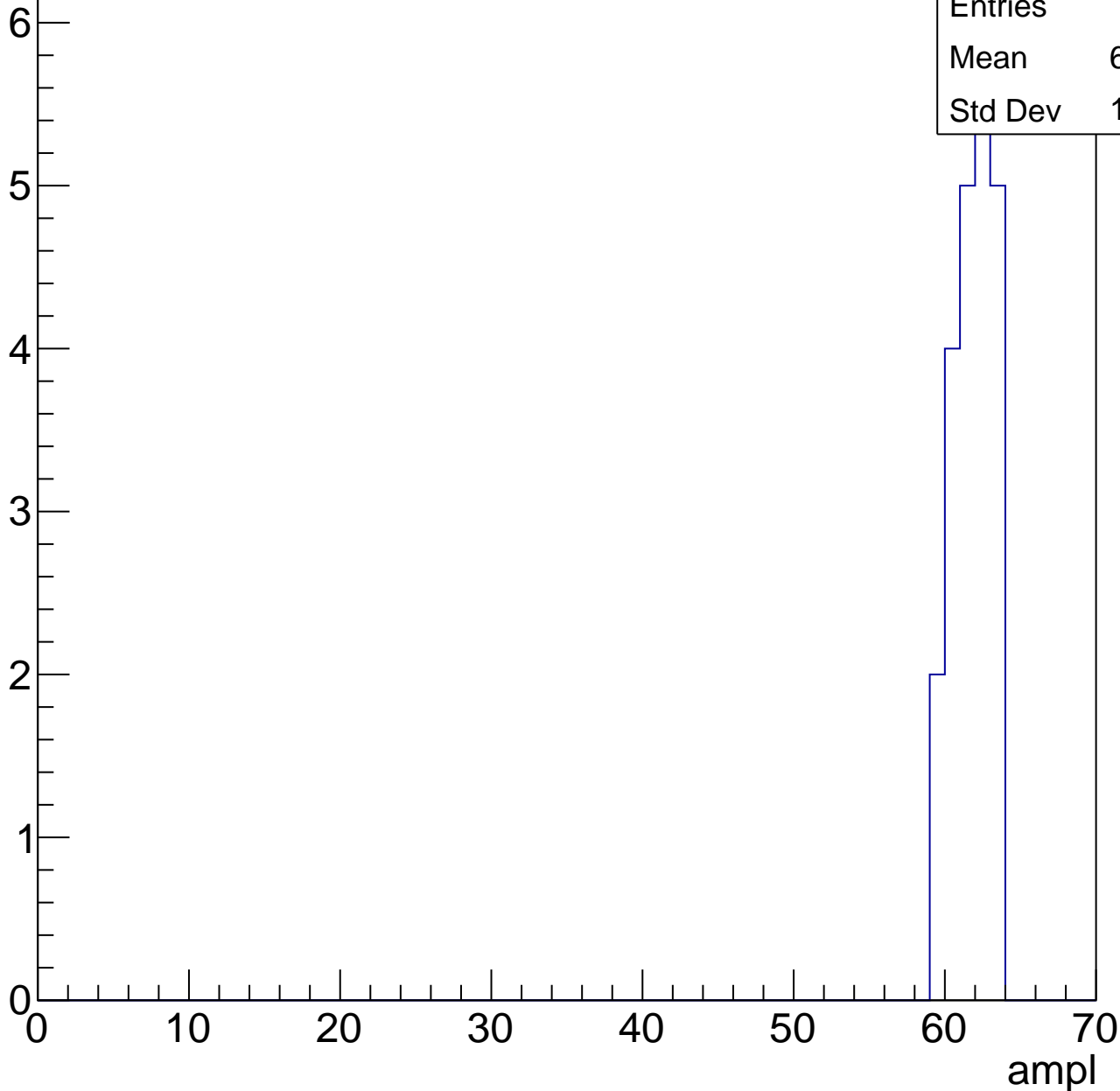


B1L101S, U21-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	61.36
Std Dev	1.263



B1L101S, U21-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B1L101S, U21-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L101S, U21-ch8, adc0

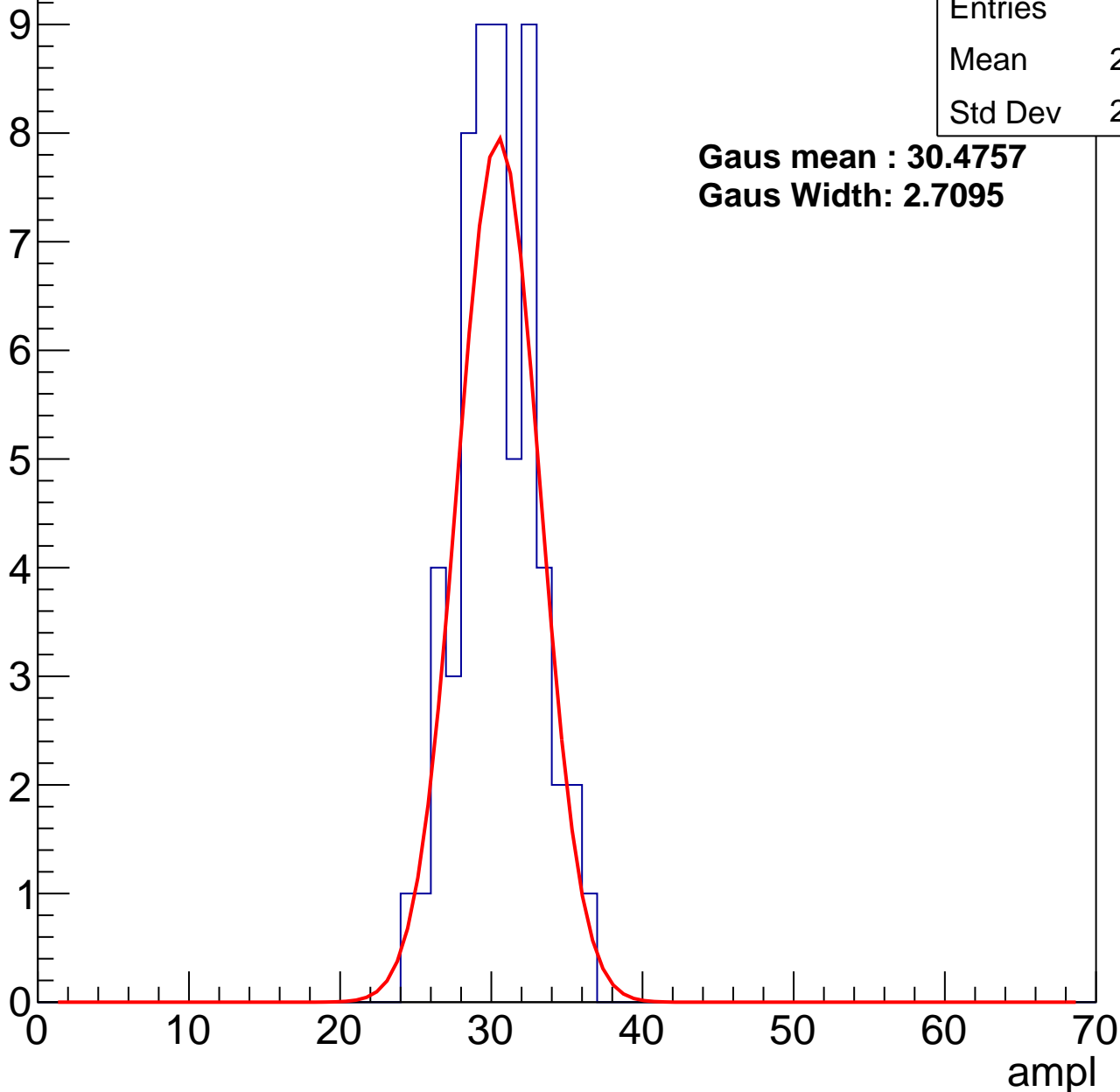
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.97
Std Dev	2.586

Gaus mean : 30.4757

Gaus Width: 2.7095



B1L101S, U21-ch8, adc1

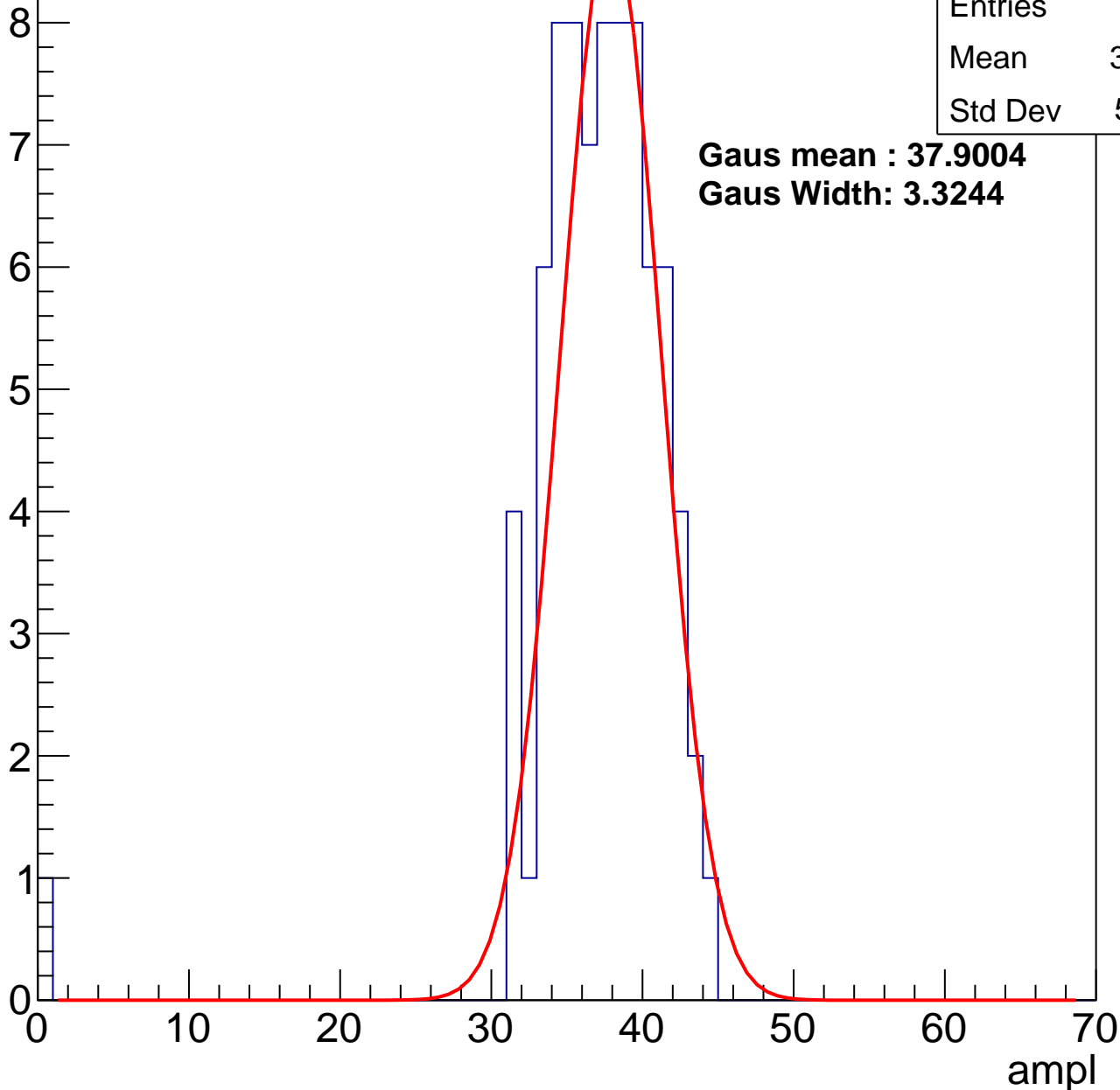
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.59
Std Dev	5.241

Gaus mean : 37.9004

Gaus Width: 3.3244



B1L101S, U21-ch8, adc2

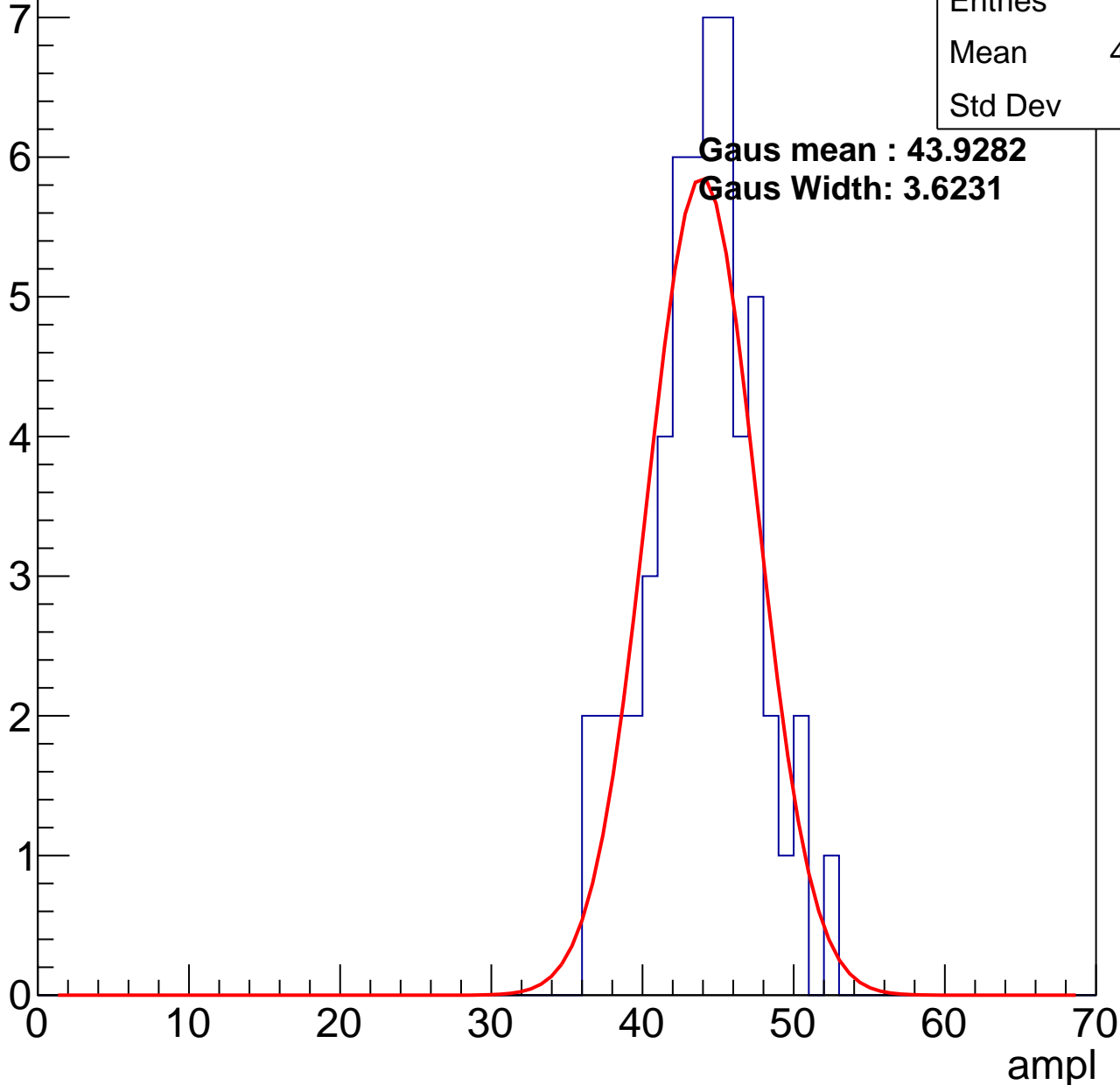
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.45
Std Dev	3.55

Gaus mean : 43.9282

Gaus Width: 3.6231

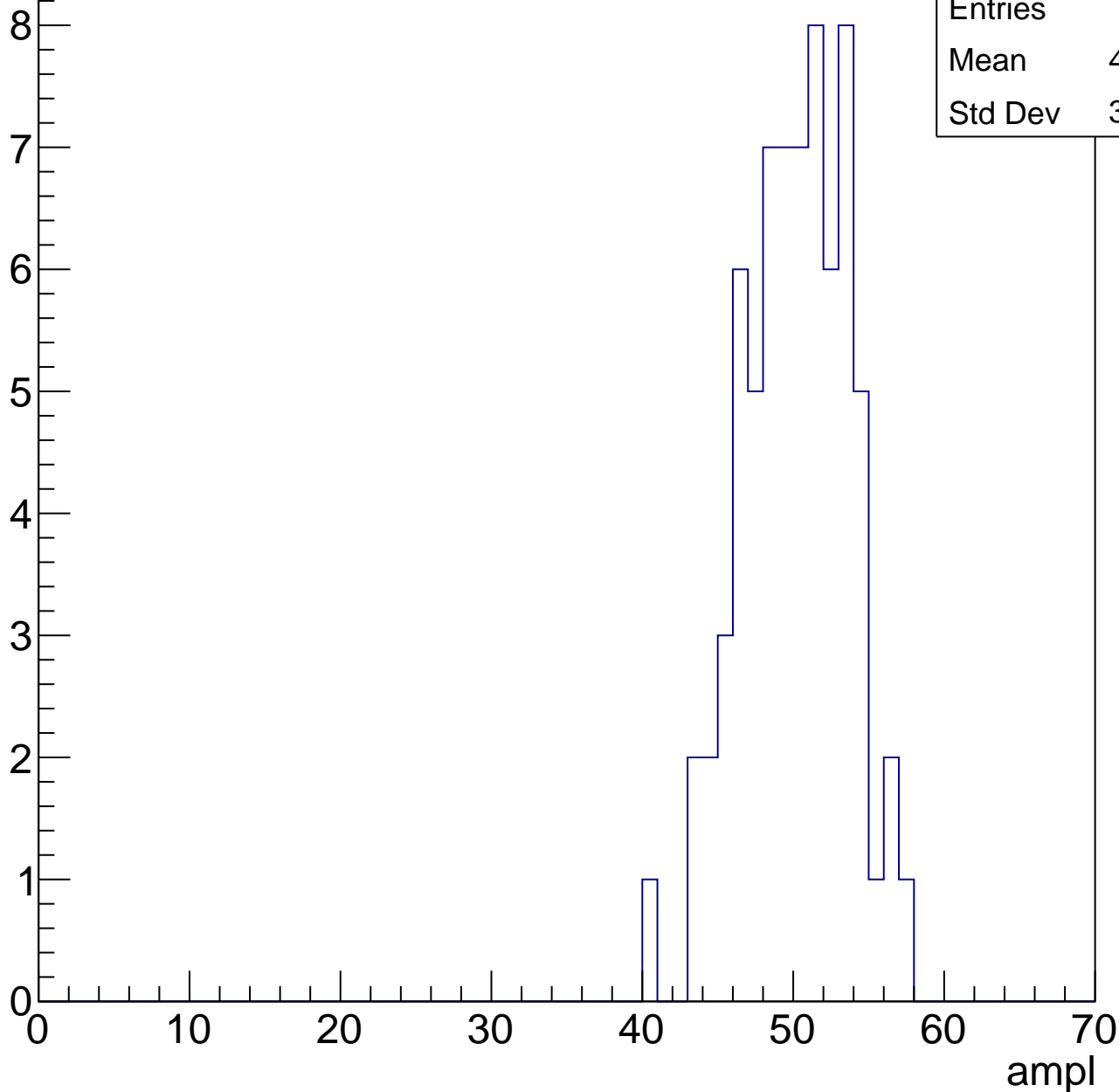


B1L101S, U21-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.68
Std Dev	3.447



B1L101S, U21-ch8, adc4

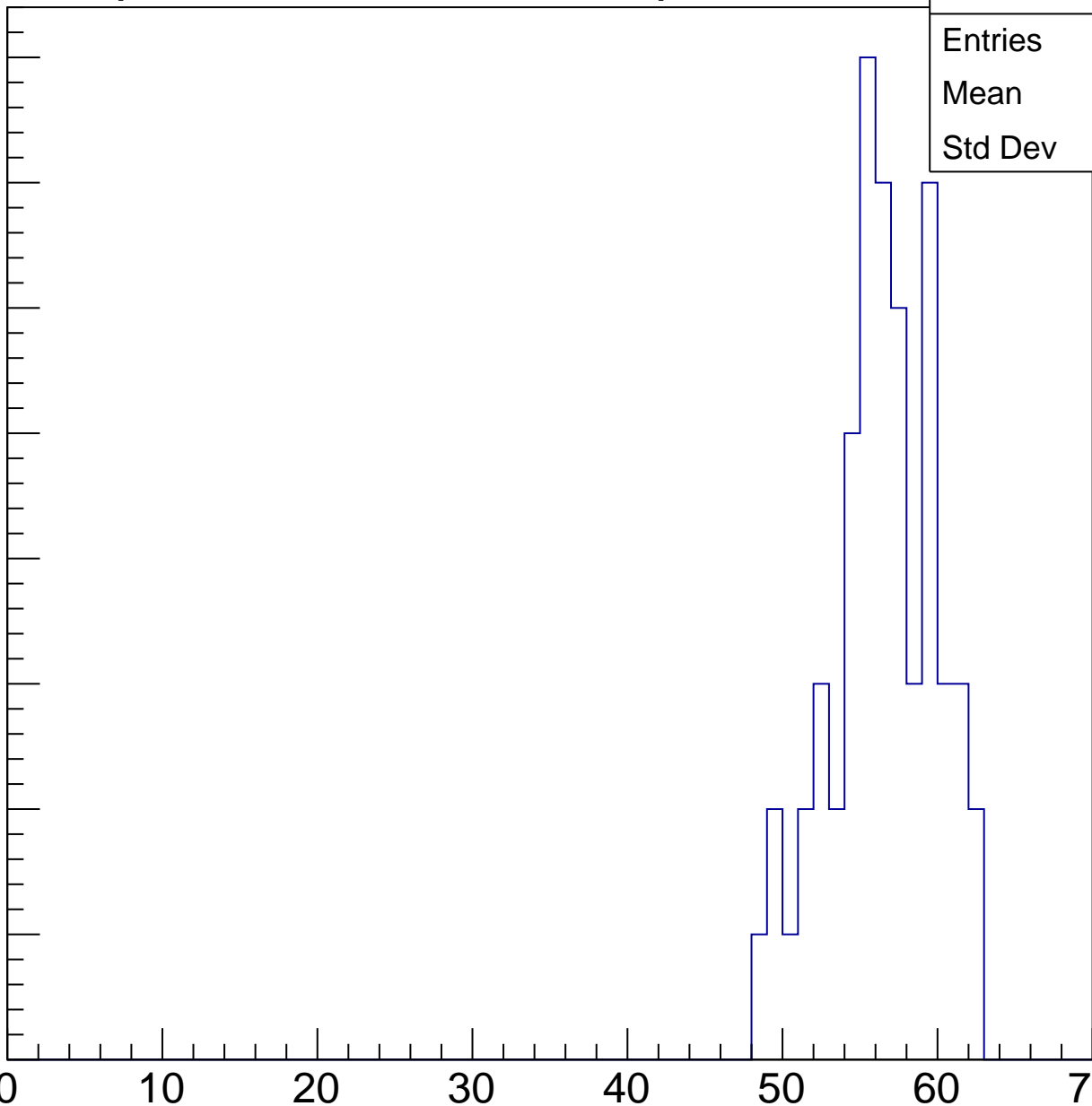
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	55
Mean	55.96
Std Dev	3.352

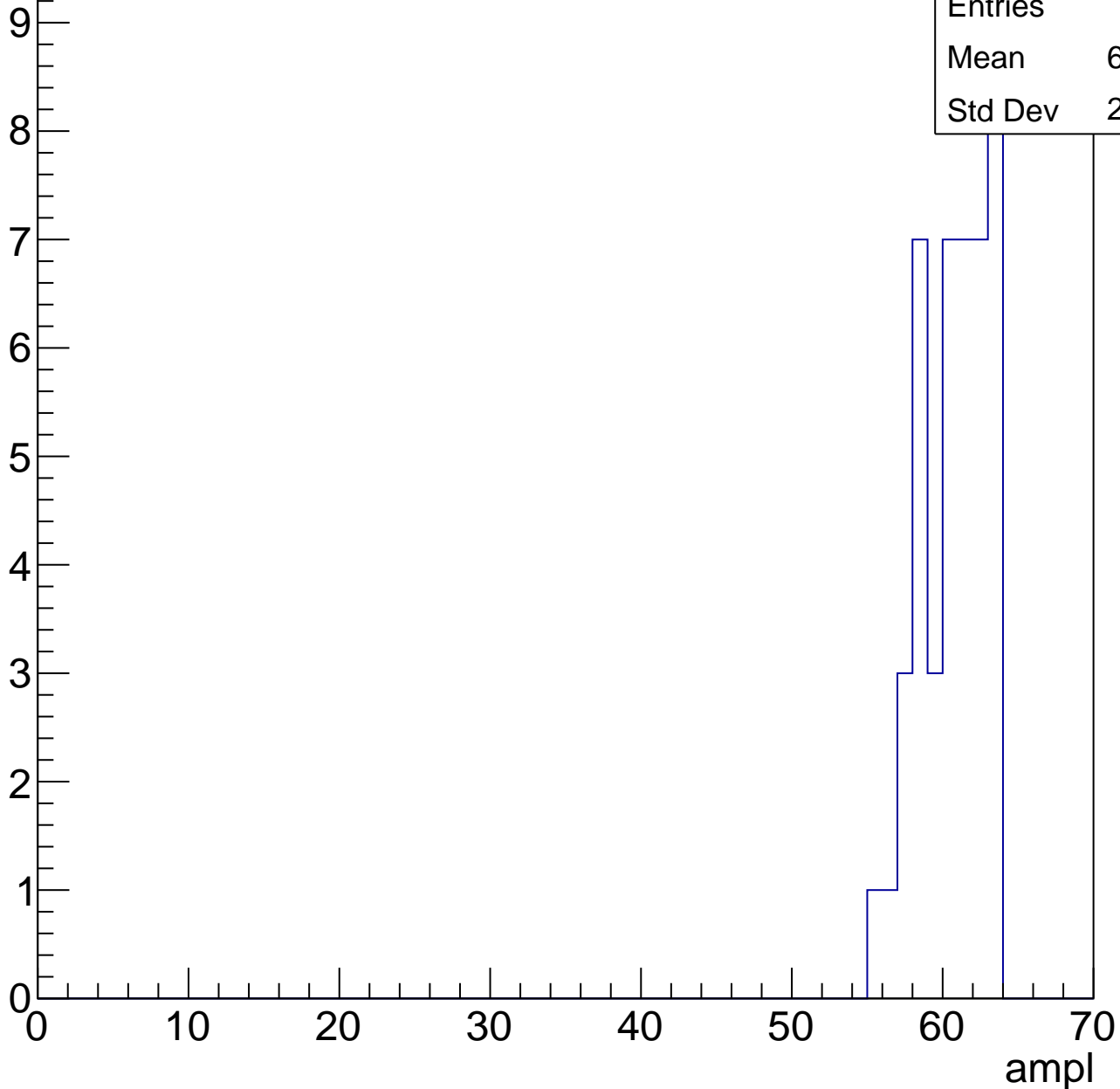
ampl



B1L101S, U21-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

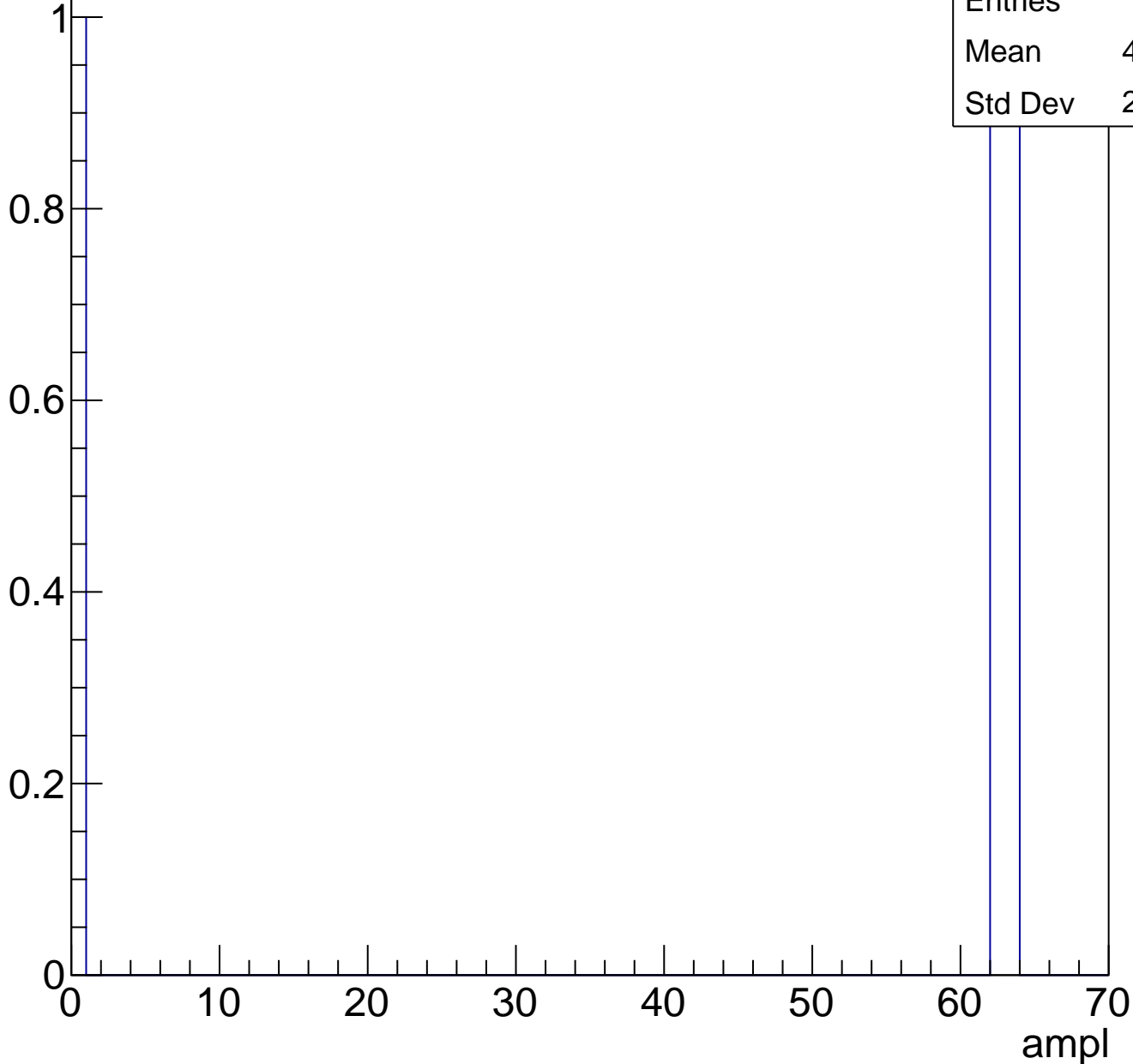


Entries	45
Mean	60.29
Std Dev	2.167

B1L101S, U21-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B1L101S, U21-ch9, adc0

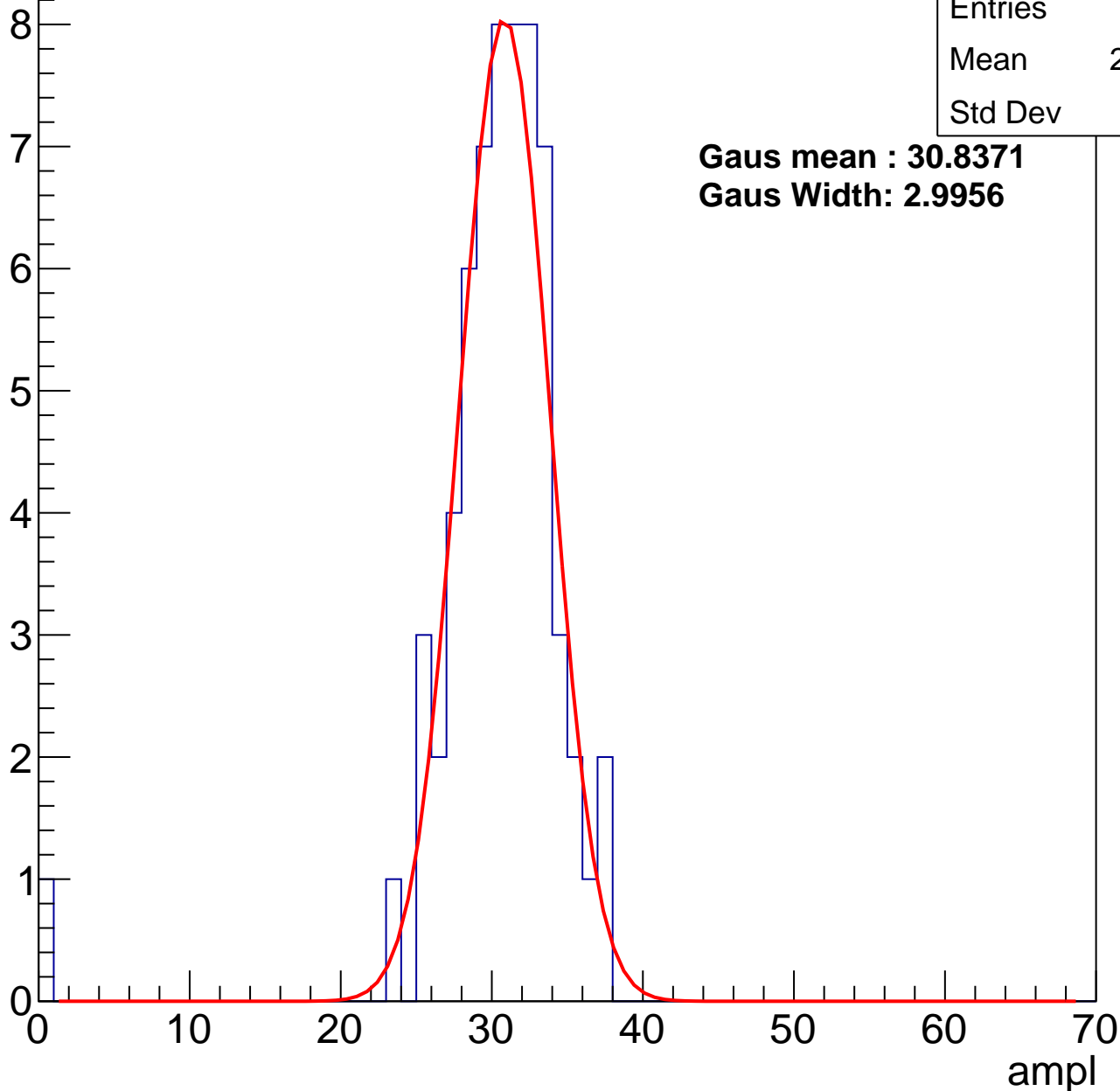
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.94
Std Dev	4.81

Gaus mean : 30.8371

Gaus Width: 2.9956



B1L101S, U21-ch9, adc1

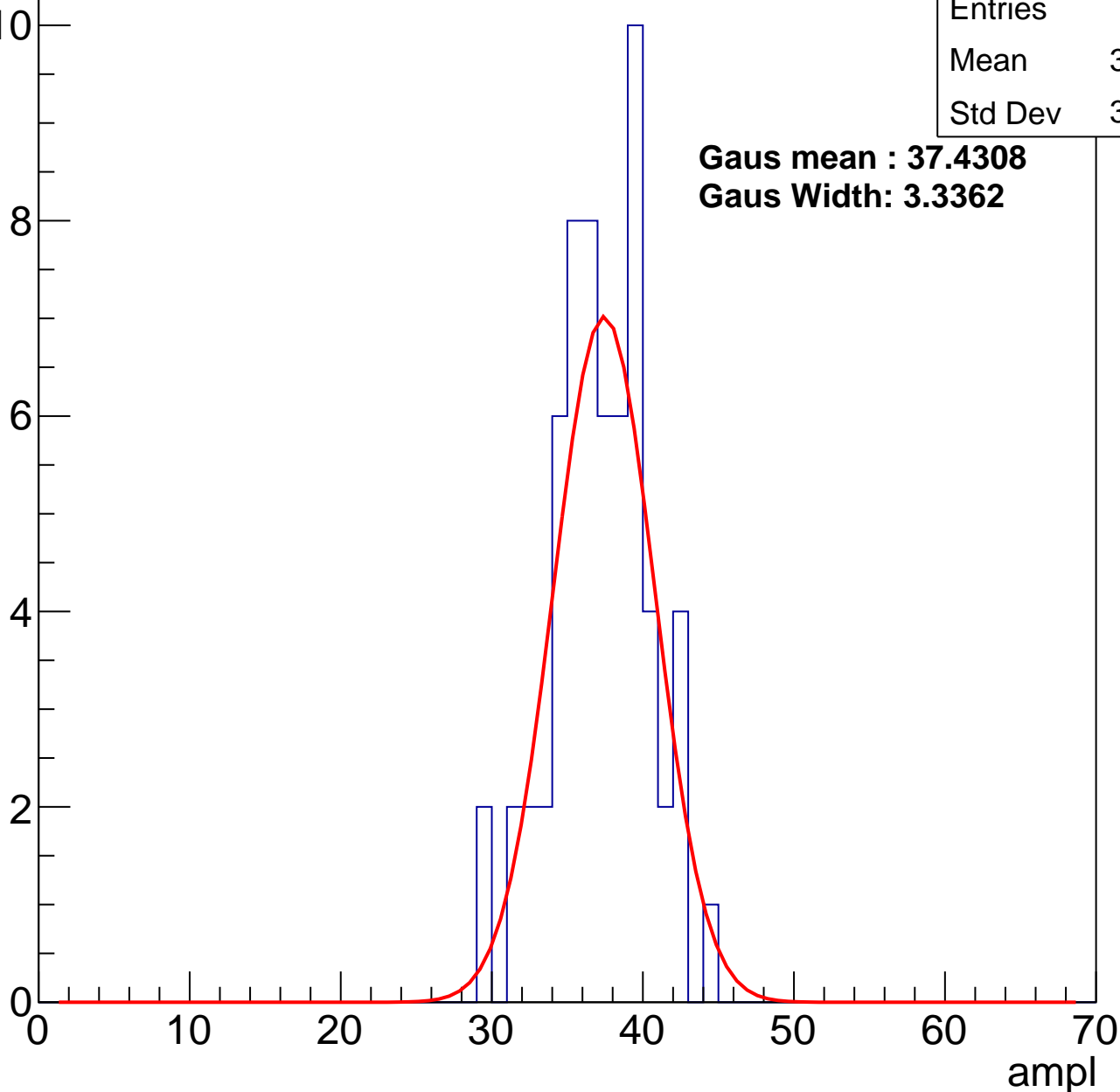
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.76
Std Dev	3.176

Gaus mean : 37.4308

Gaus Width: 3.3362



B1L101S, U21-ch9, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.49
Std Dev	3.595

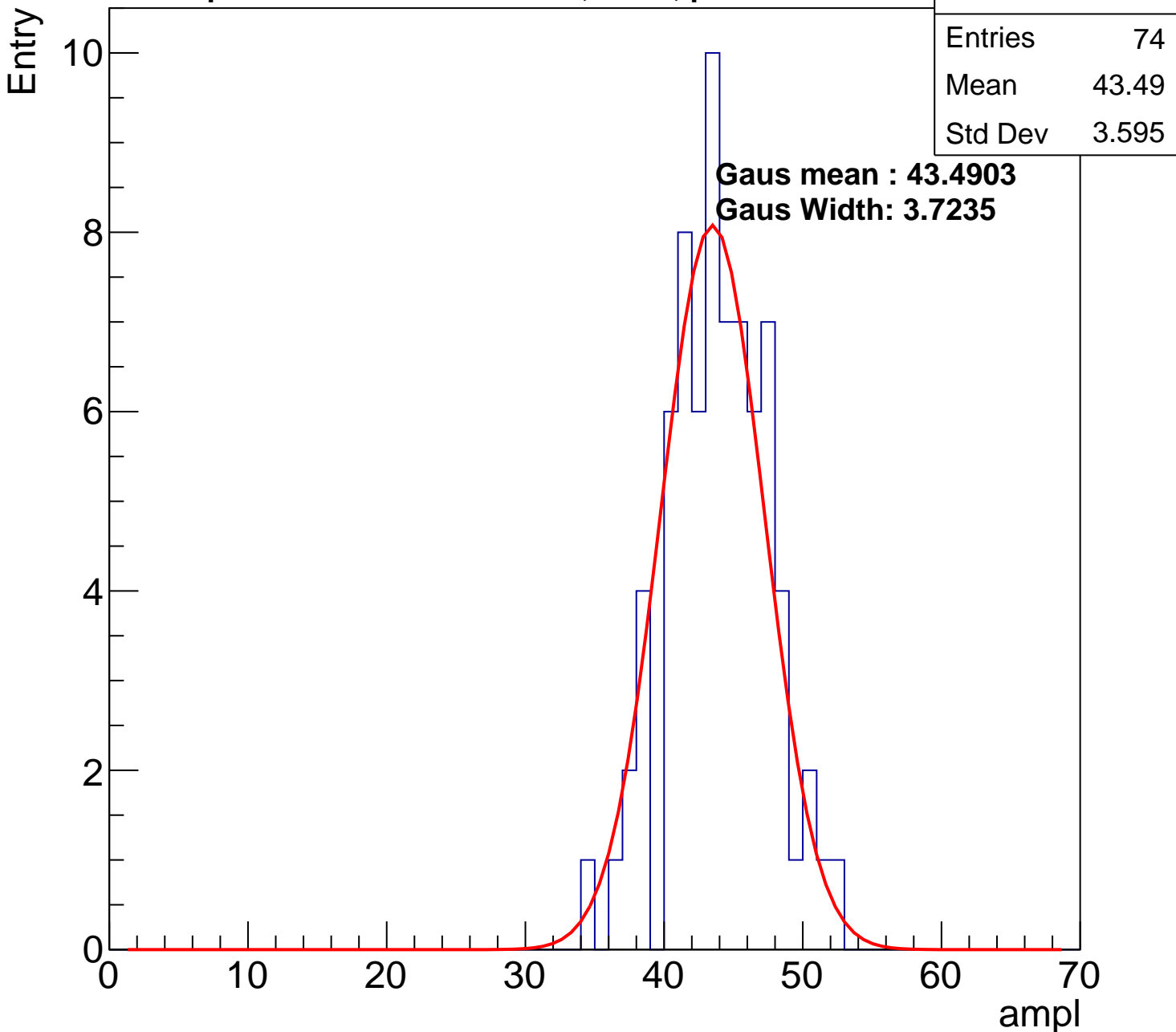
Gaus mean : 43.4903

Gaus Width: 3.7235

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

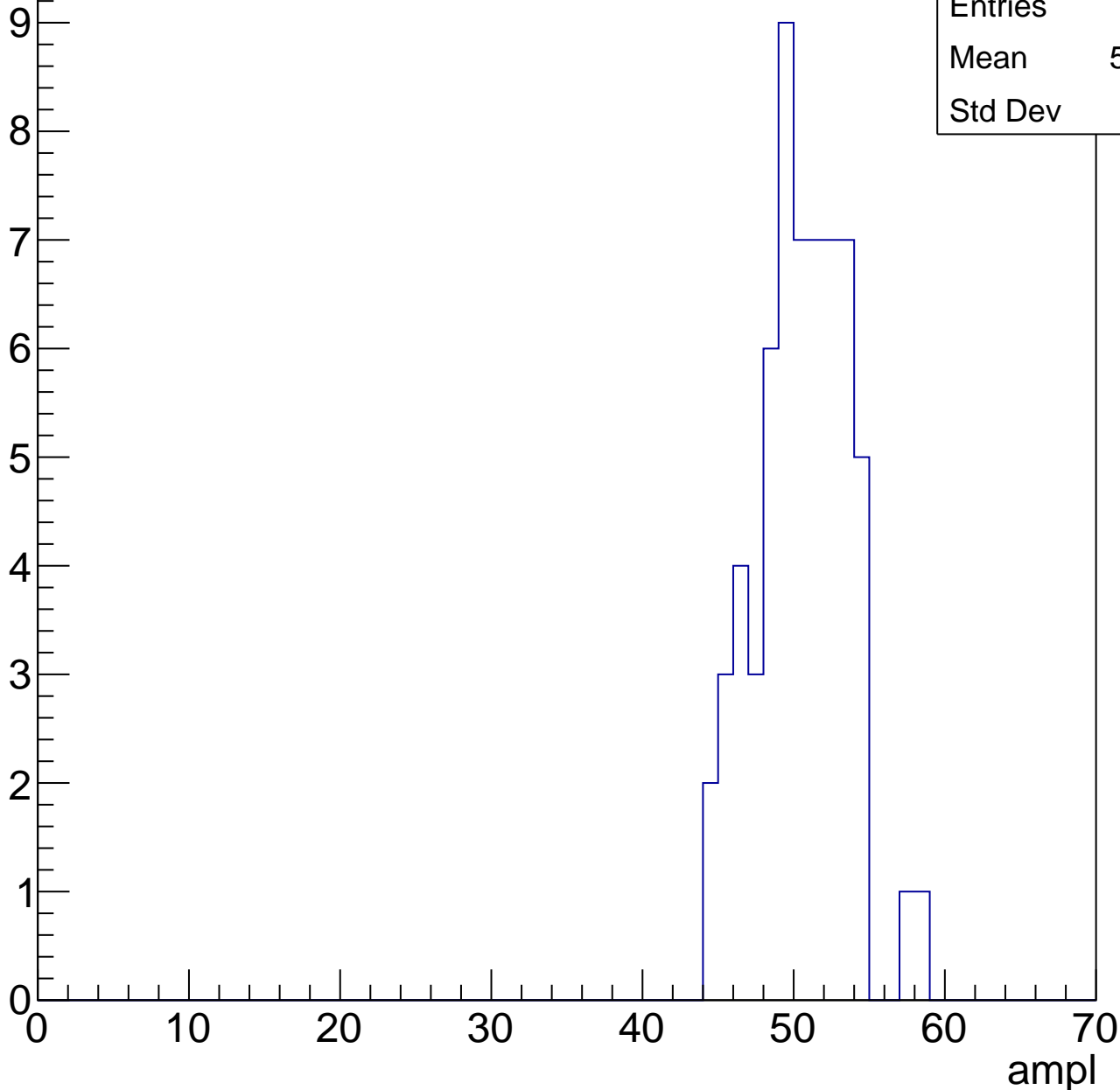


B1L101S, U21-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

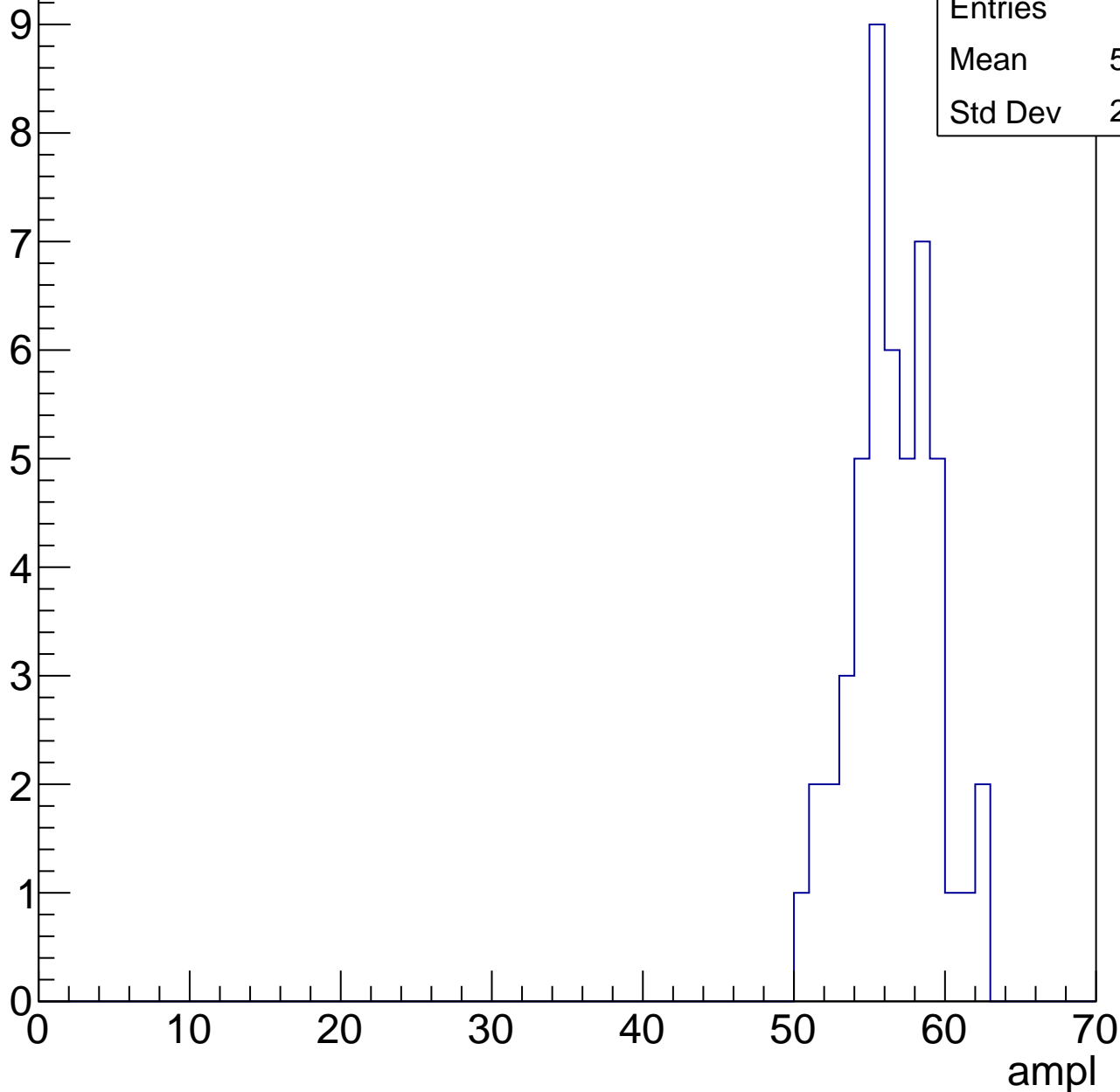
Entries	62
Mean	50.06
Std Dev	3.01



B1L101S, U21-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

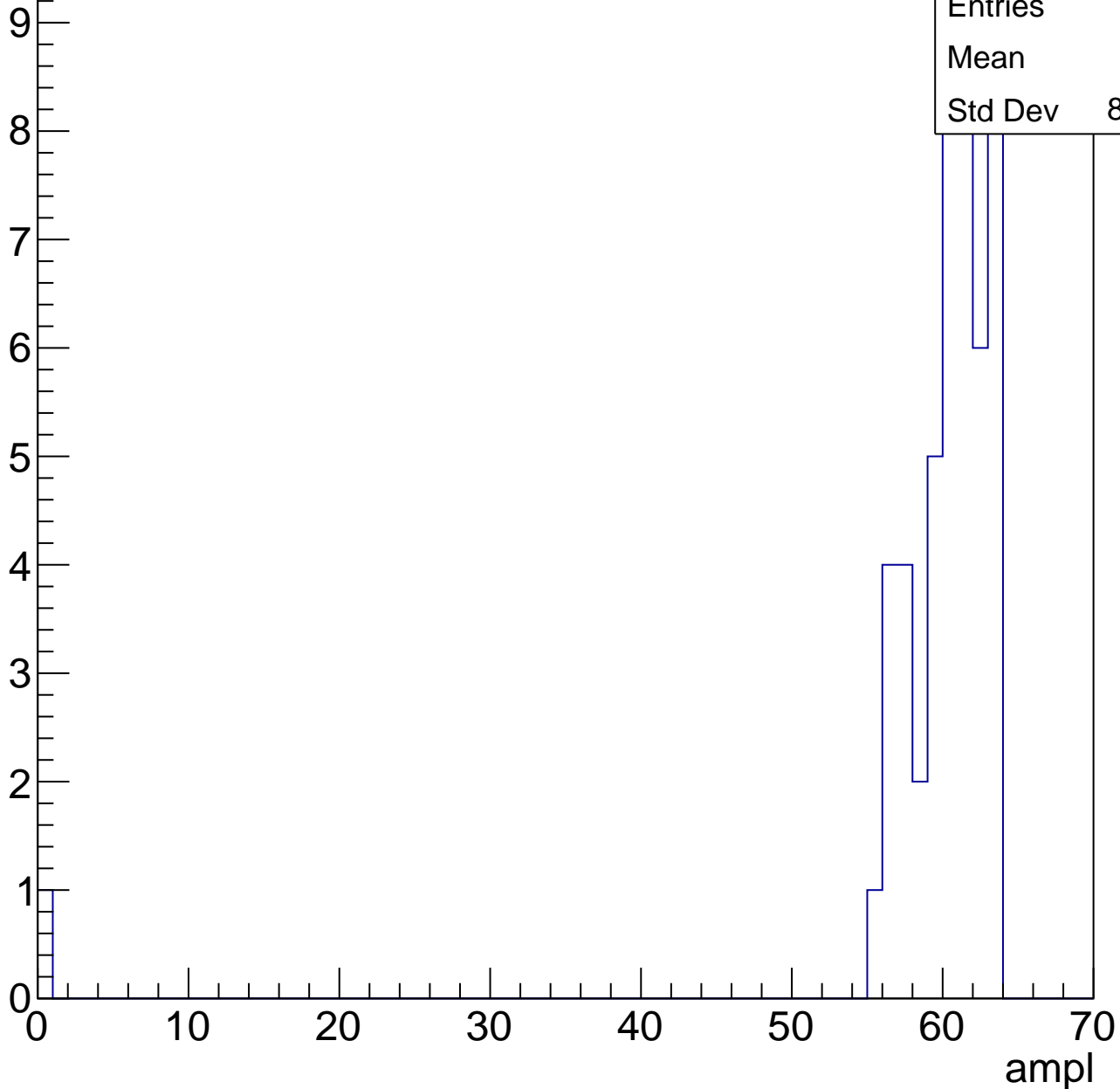
Entry



B1L101S, U21-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

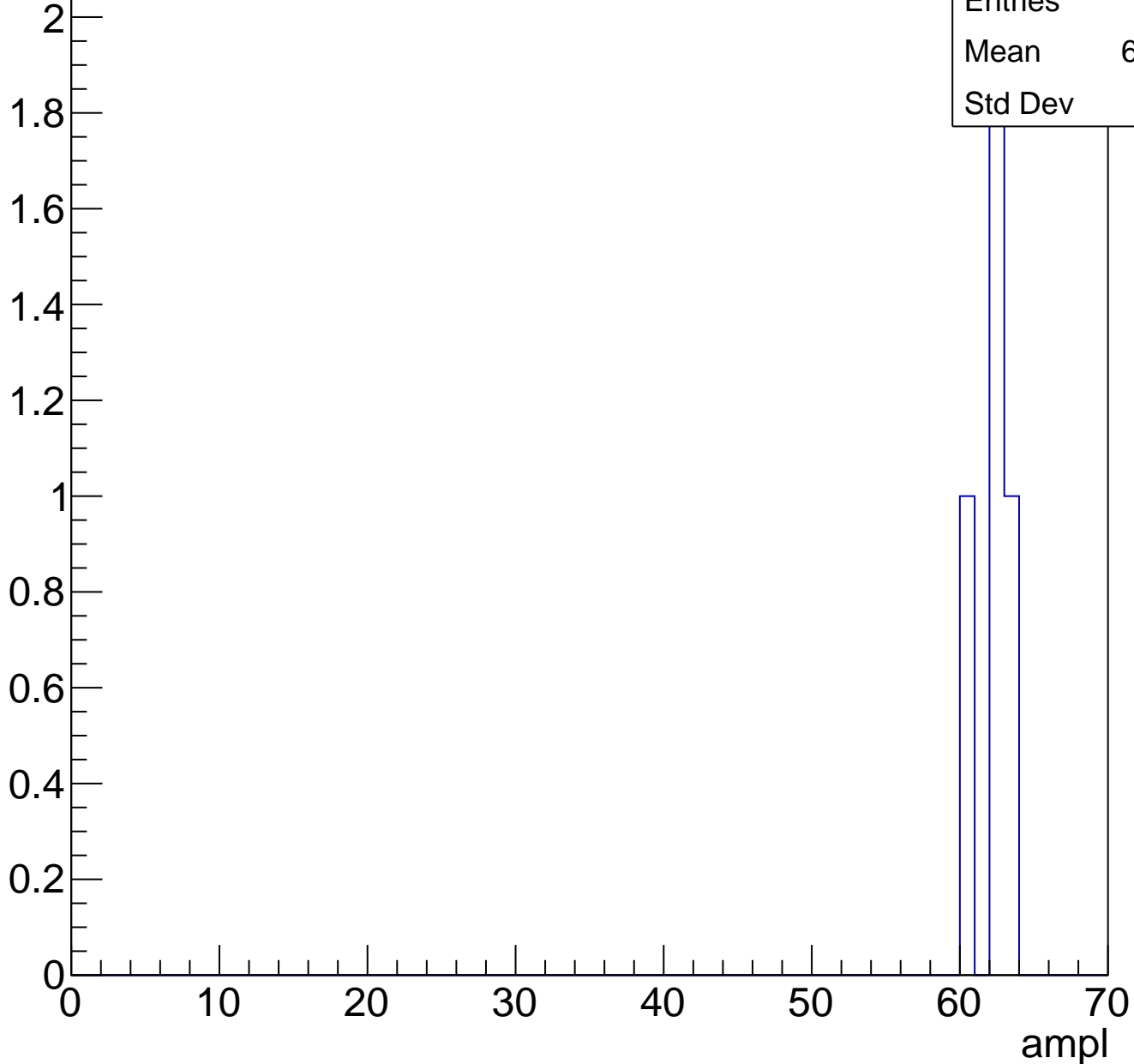


Entries	49
Mean	58.9
Std Dev	8.798

B1L101S, U21-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch10, adc0

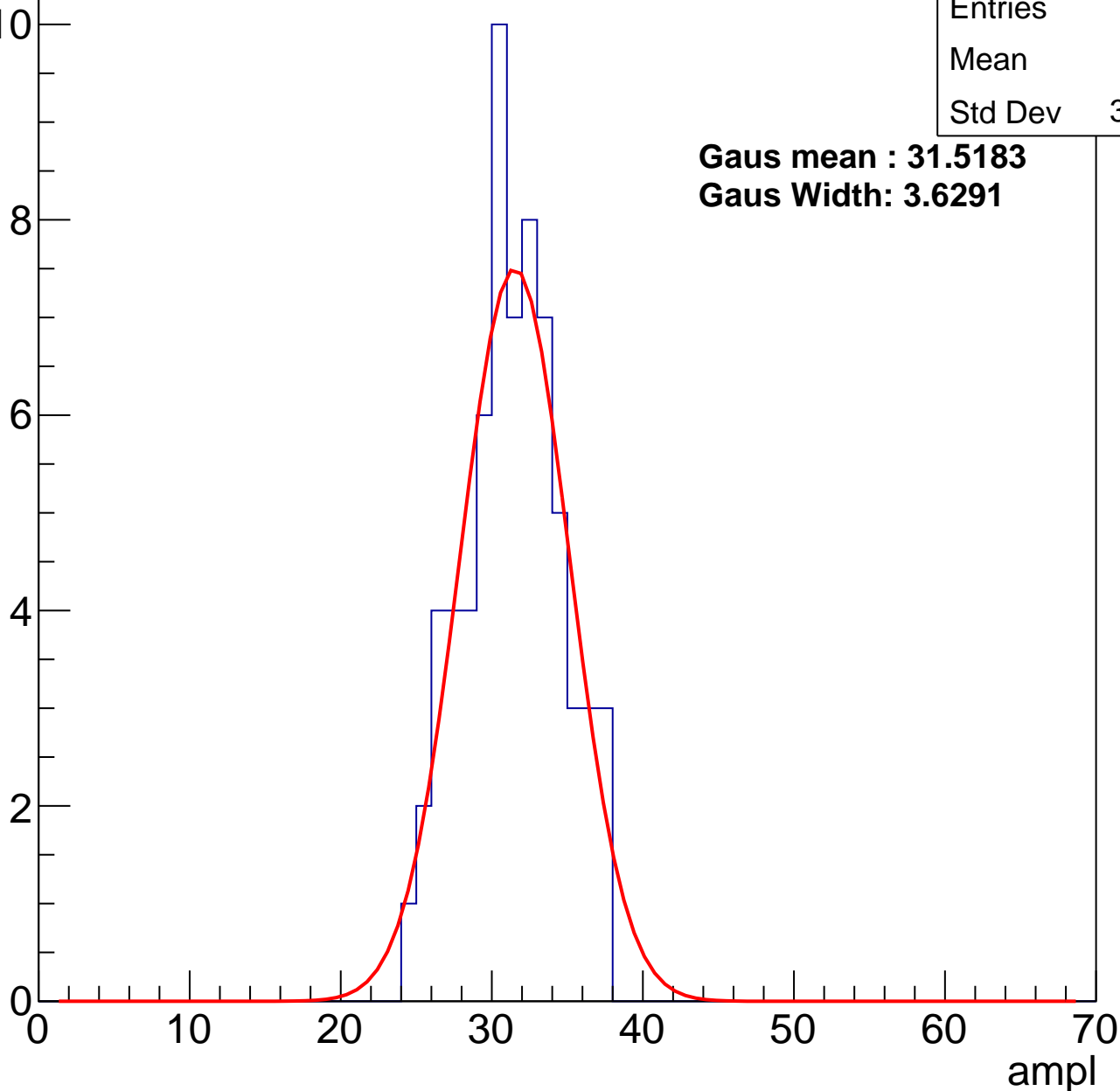
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.9
Std Dev	3.153

Gaus mean : 31.5183

Gaus Width: 3.6291



B1L101S, U21-ch10, adc1

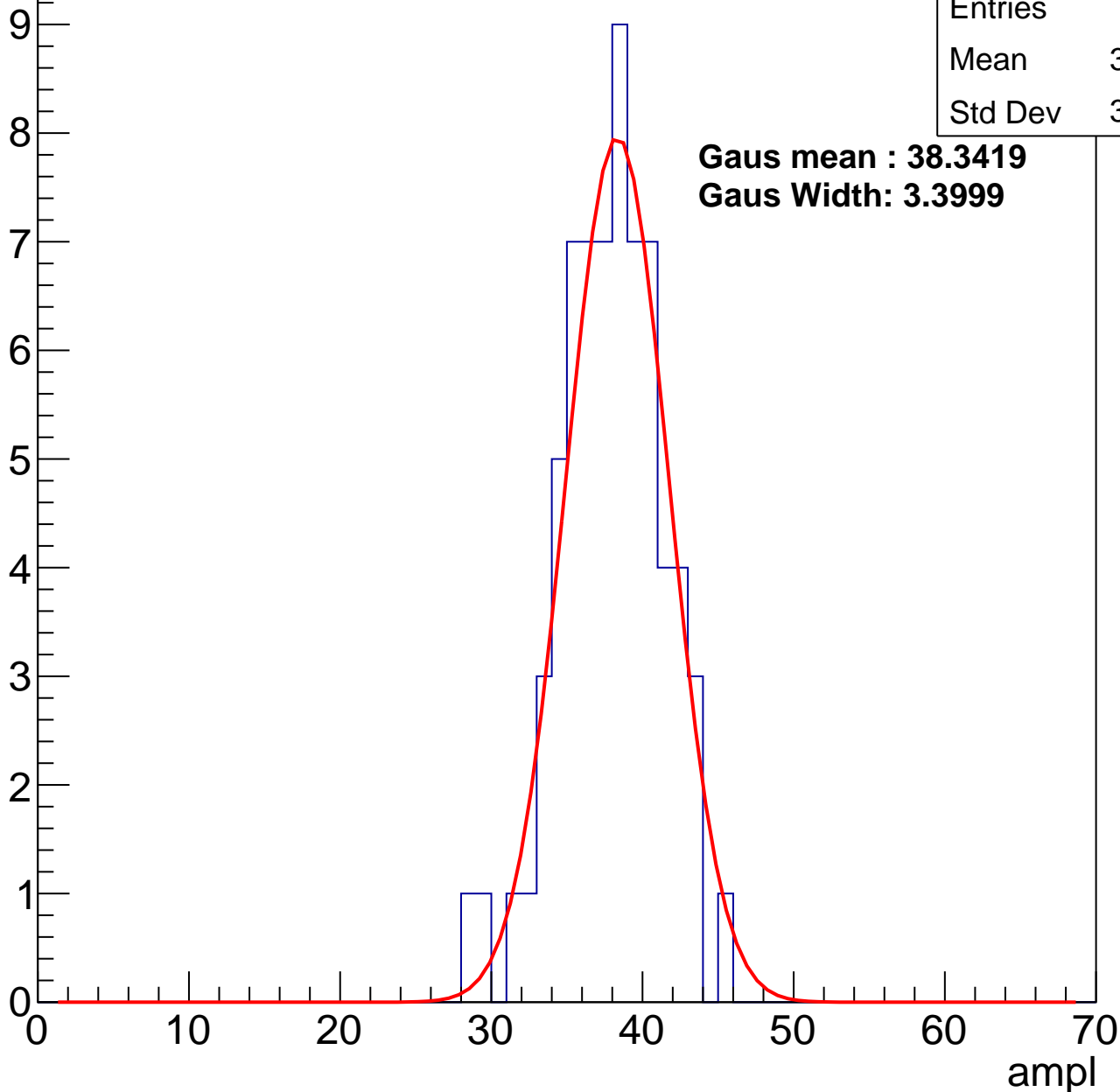
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.44
Std Dev	3.336

Gaus mean : 38.3419

Gaus Width: 3.3999



B1L101S, U21-ch10, adc2

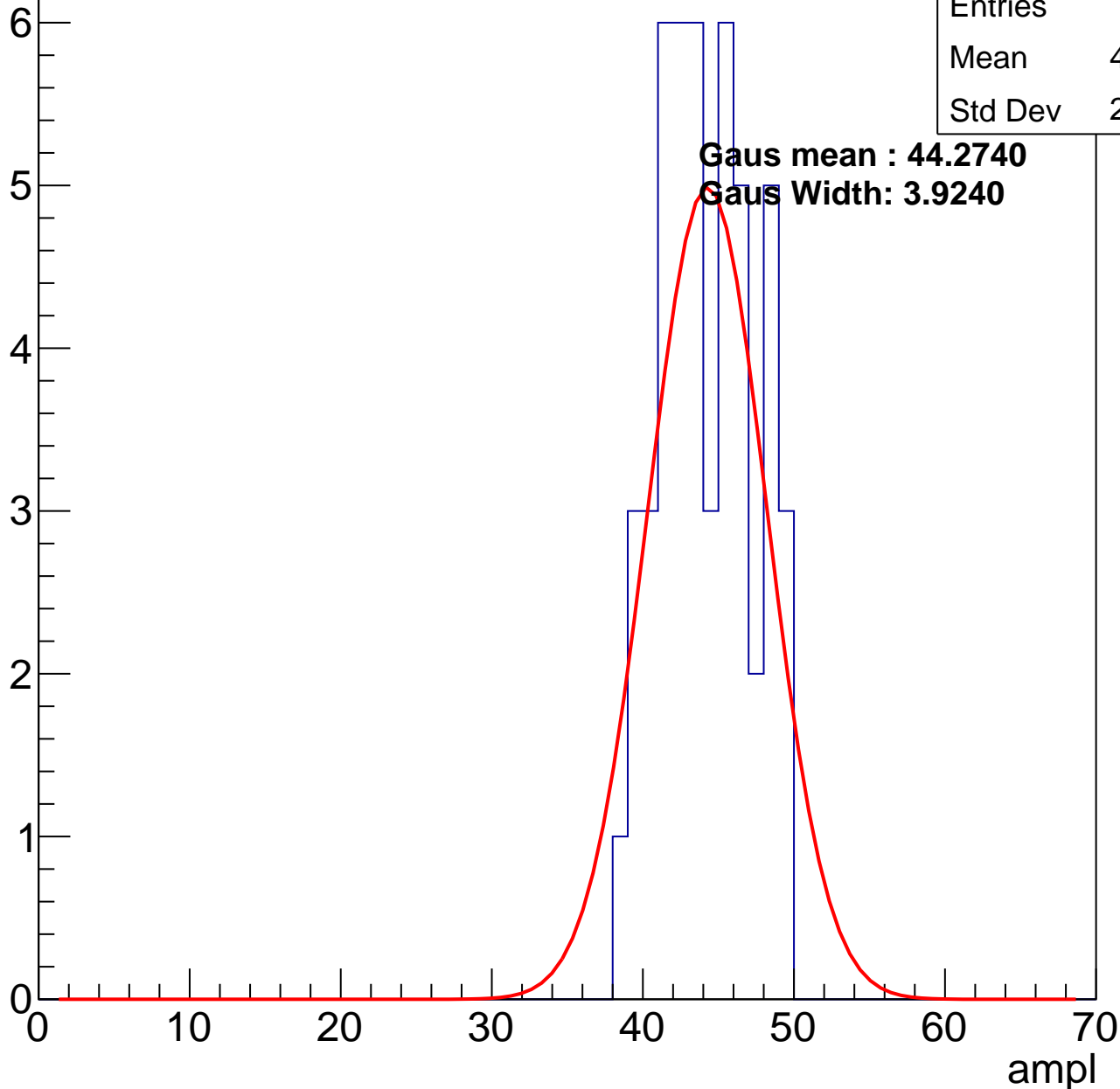
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	43.76
Std Dev	2.993

Gaus mean : 44.2740

Gaus Width: 3.9240

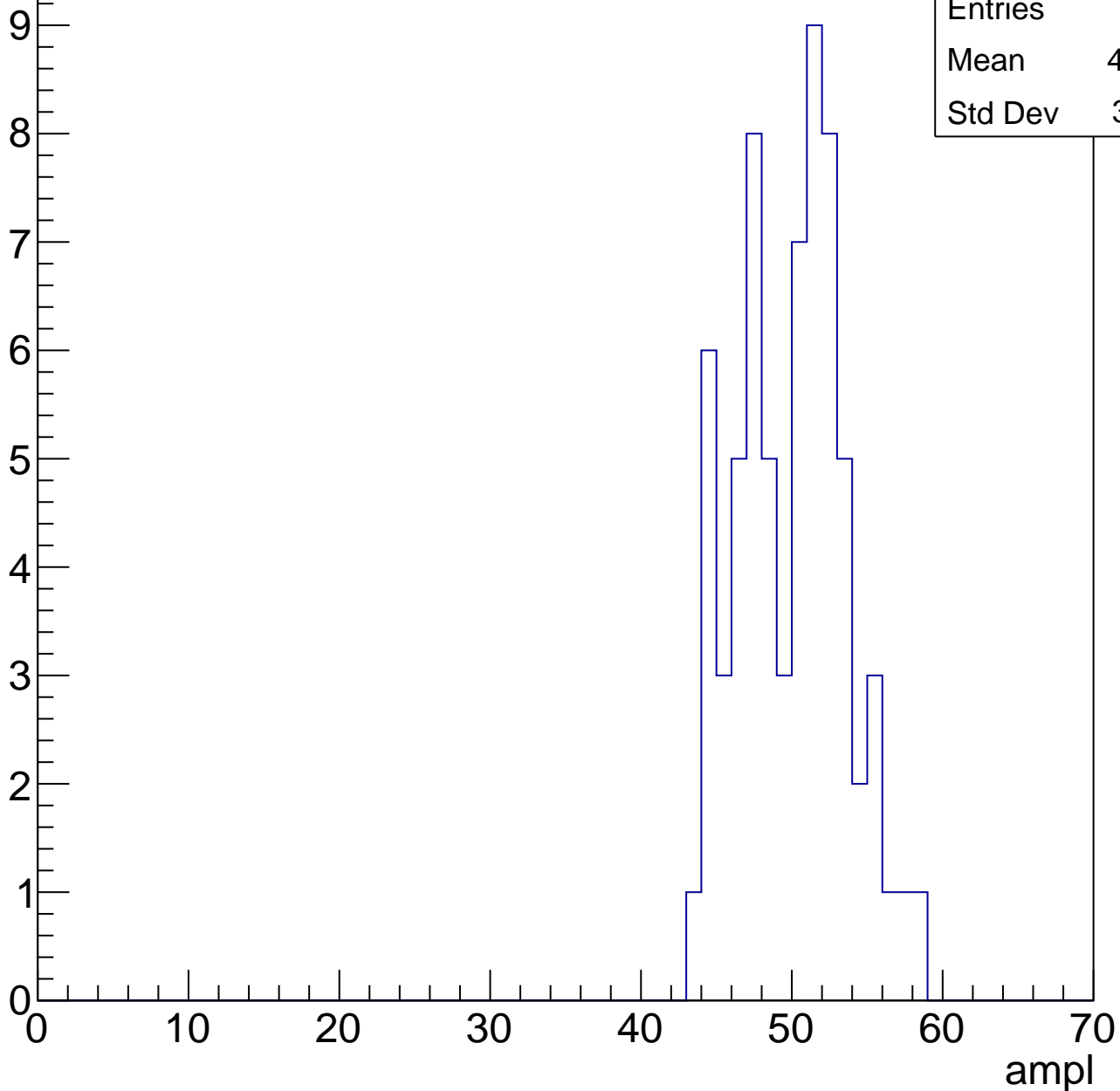


B1L101S, U21-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.54
Std Dev	3.521

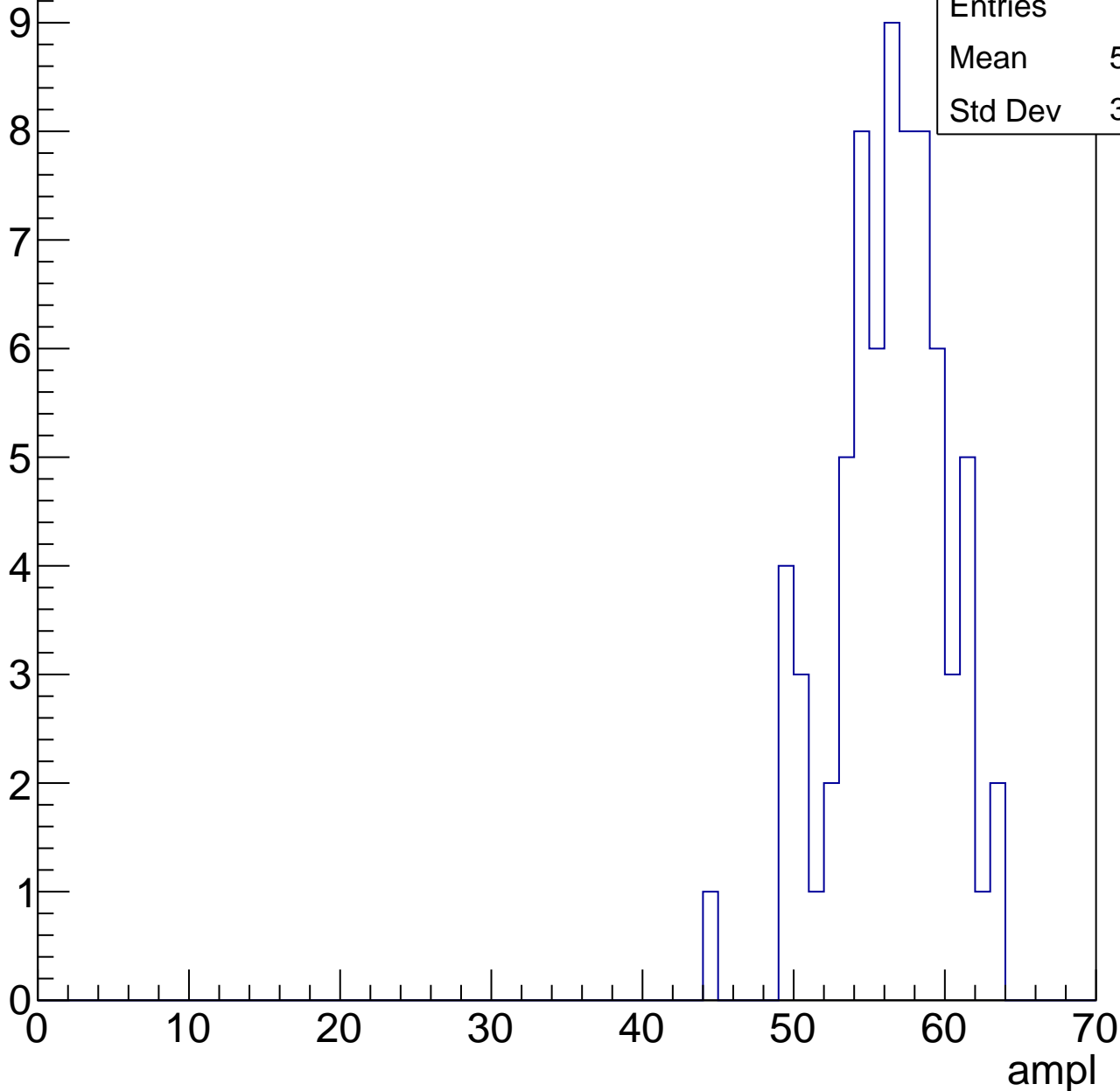


B1L101S, U21-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

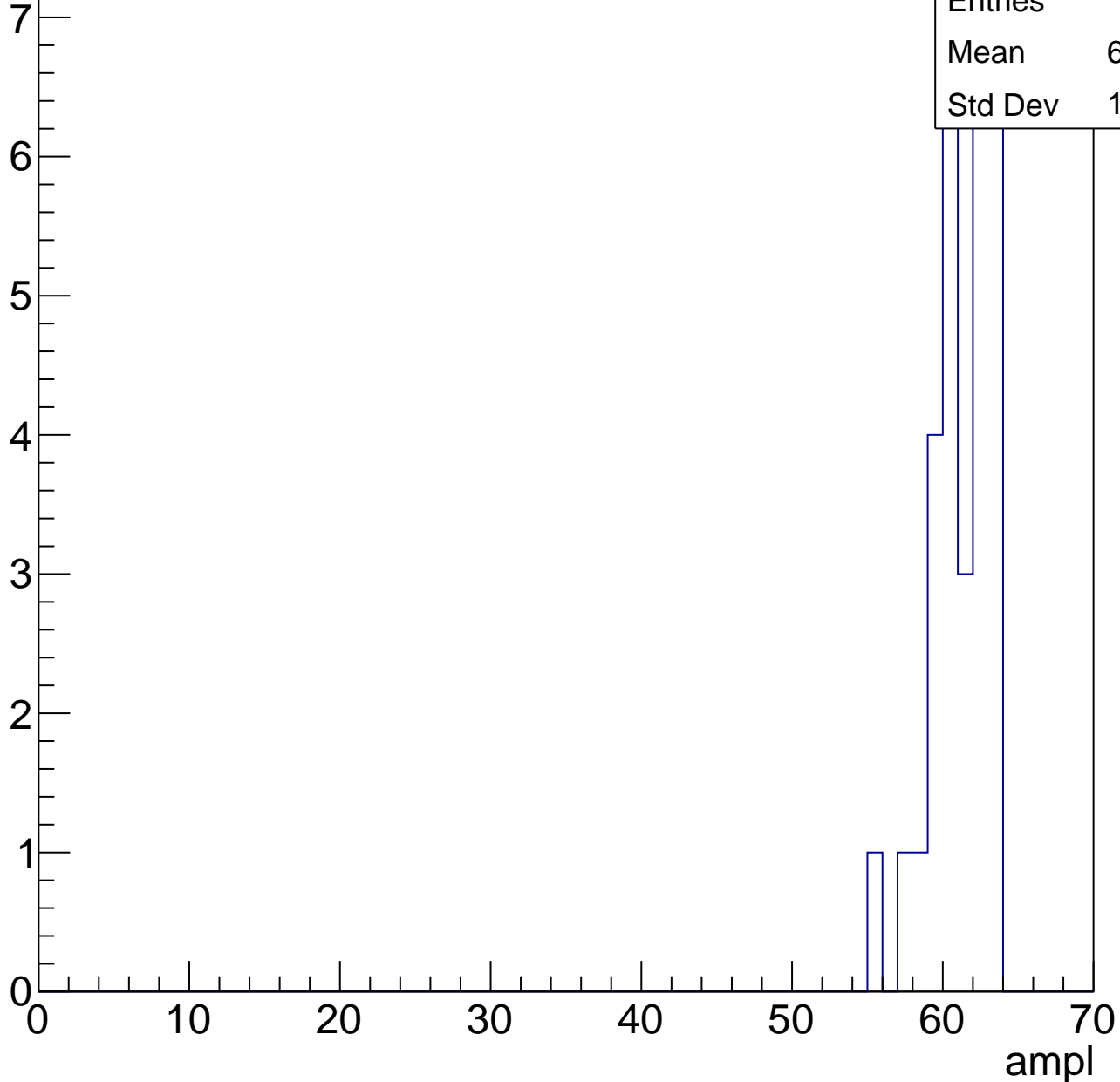
Entries	72
Mean	55.88
Std Dev	3.704



B1L101S, U21-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	31
Mean	60.77
Std Dev	1.946

B1L101S, U21-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.5
Std Dev	23.04

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch11, adc0

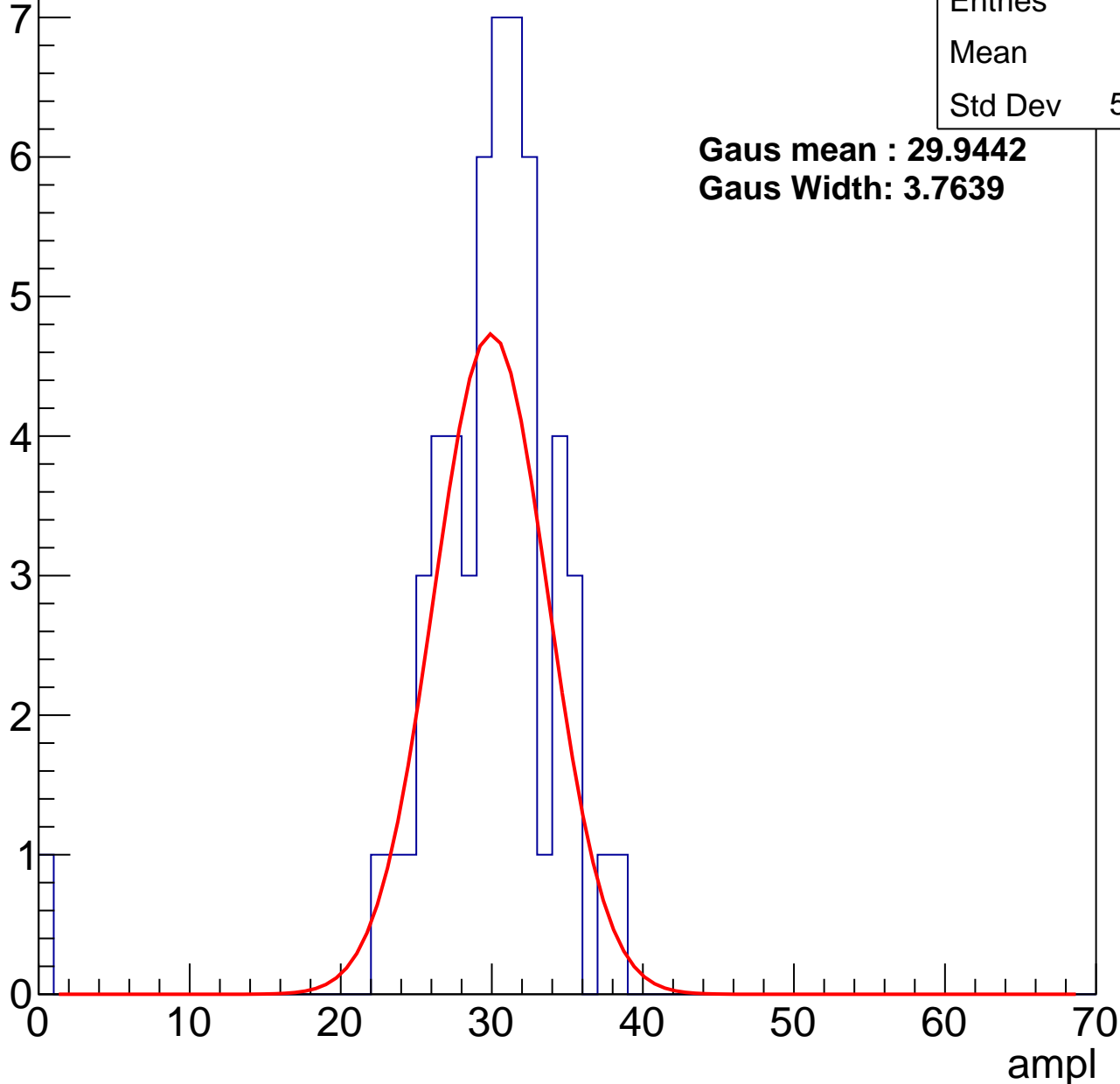
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	29.3
Std Dev	5.283

Gaus mean : 29.9442

Gaus Width: 3.7639



B1L101S, U21-ch11, adc1

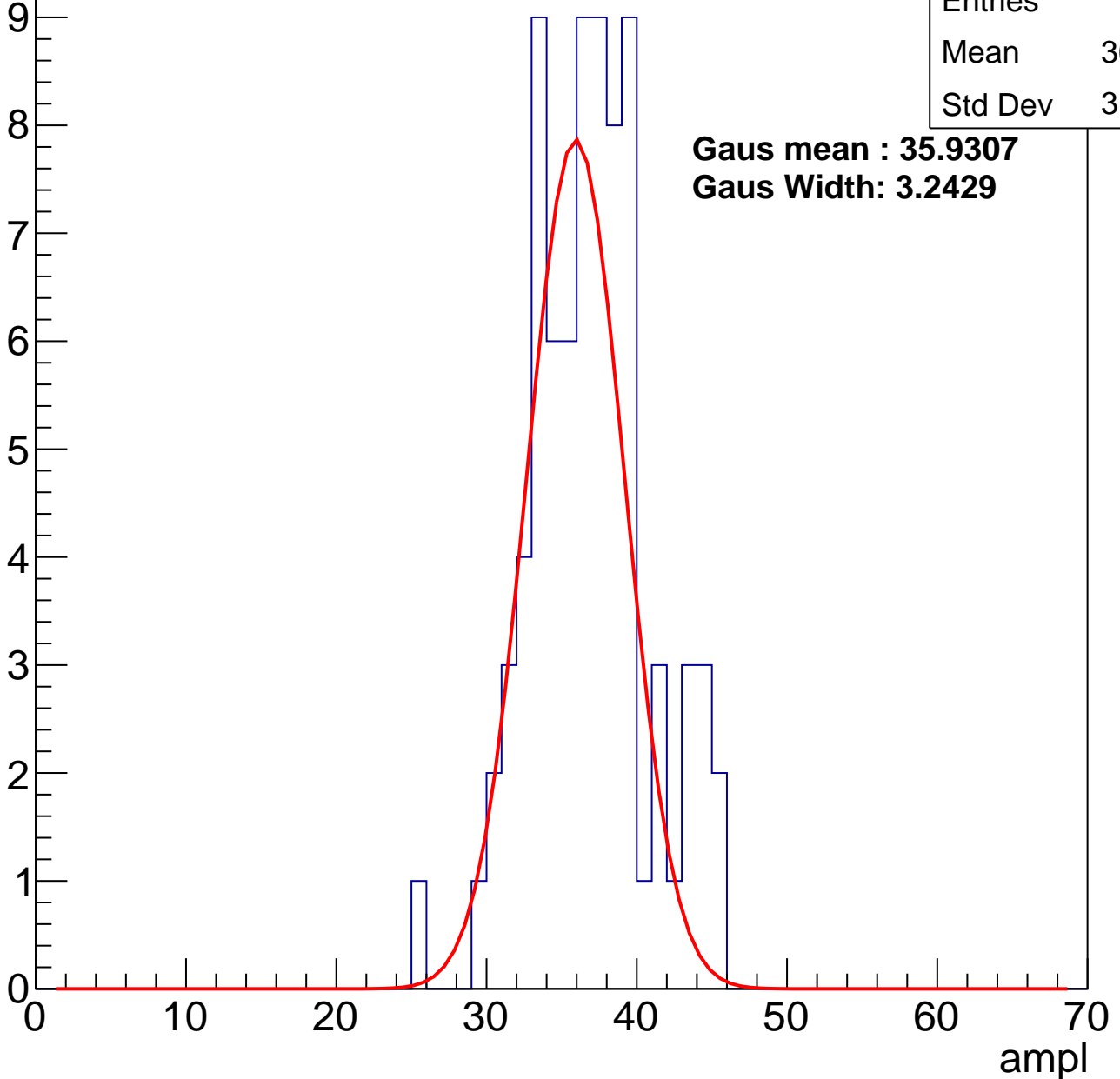
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	36.42
Std Dev	3.917

Gaus mean : 35.9307

Gaus Width: 3.2429



B1L101S, U21-ch11, adc2

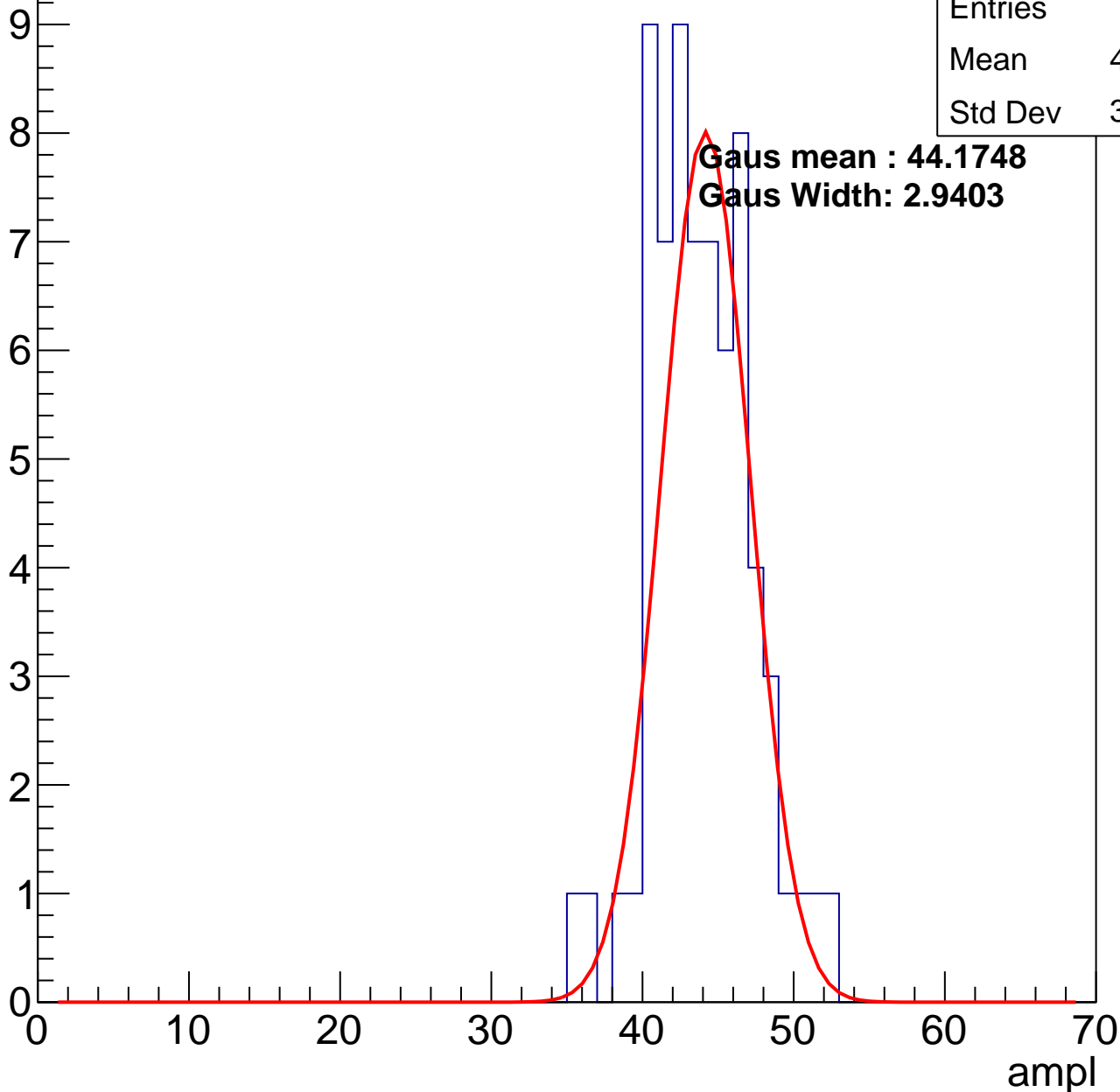
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.44
Std Dev	3.278

Gaus mean : 44.1748

Gaus Width: 2.9403

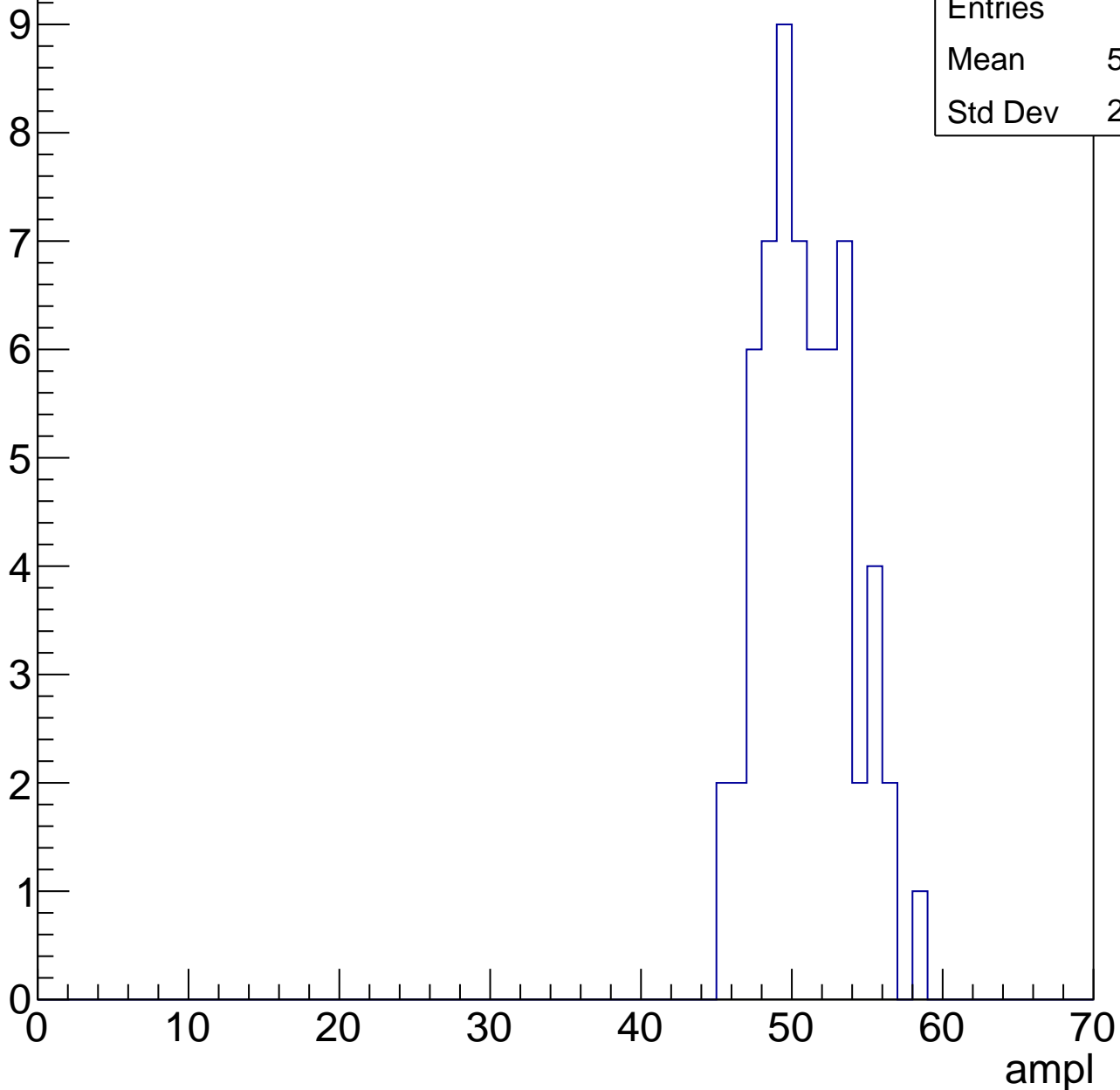


B1L101S, U21-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

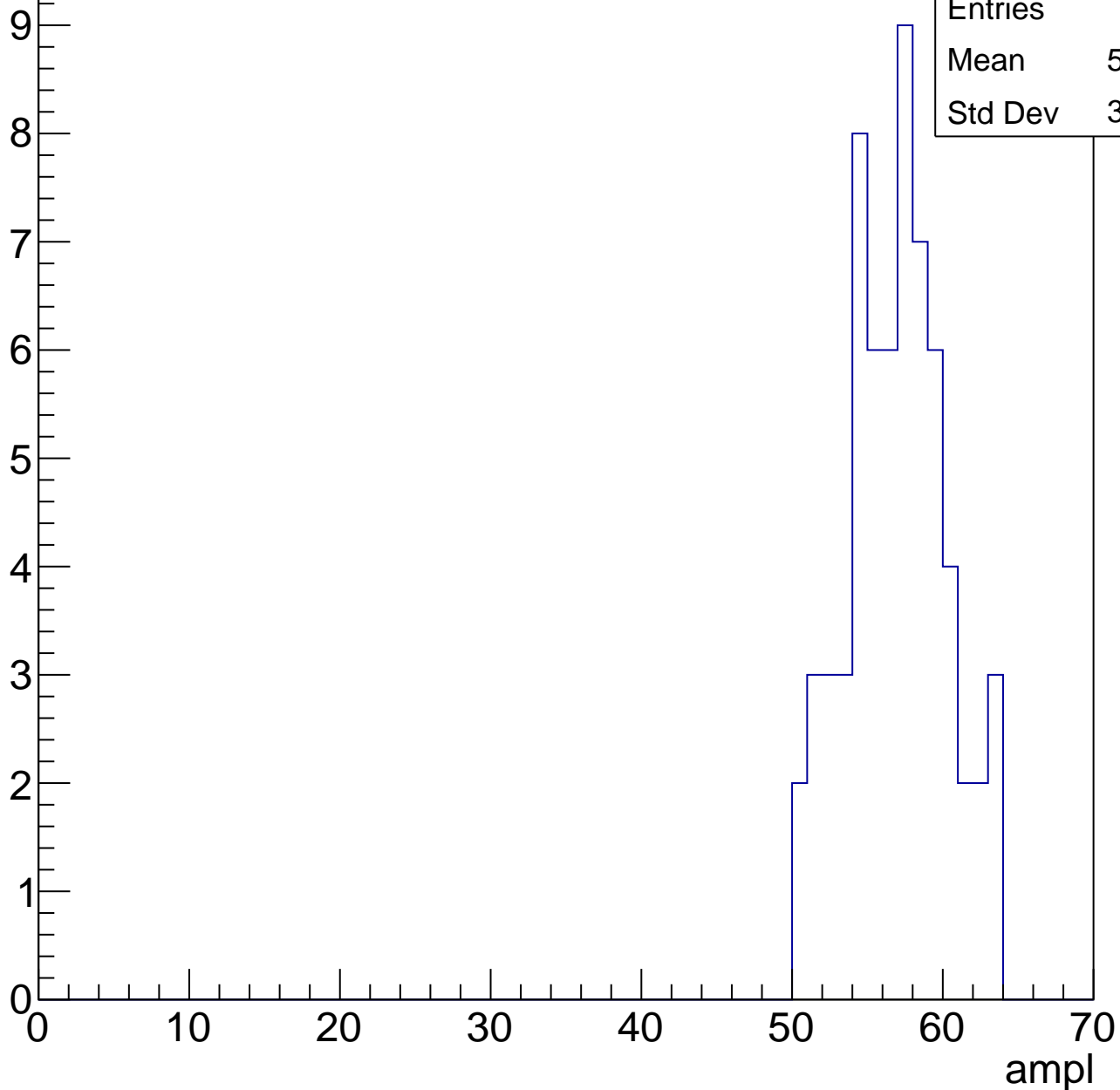
Entries	61
Mean	50.46
Std Dev	2.923



B1L101S, U21-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

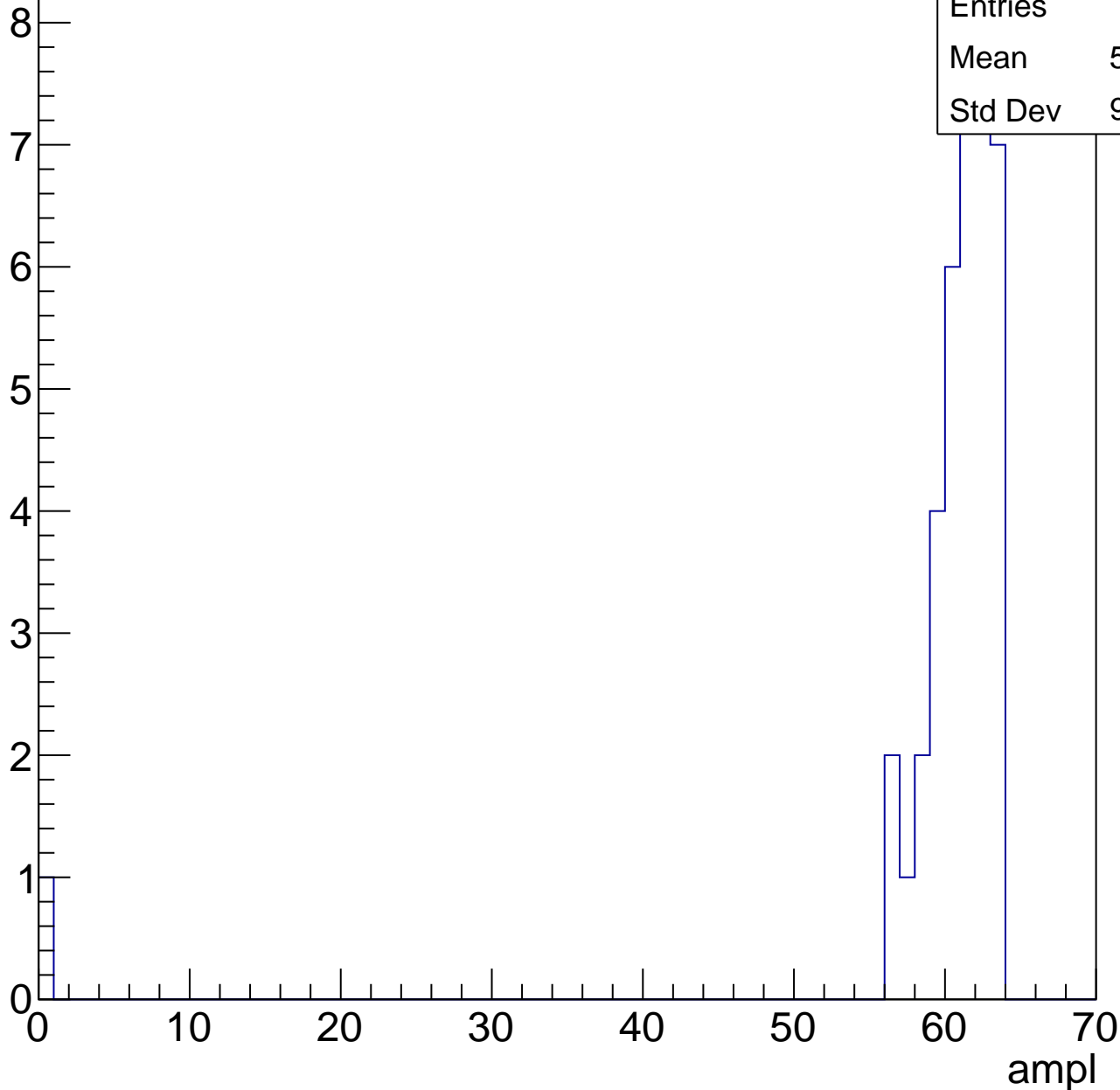


B1L101S, U21-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	59.13
Std Dev	9.775



B1L101S, U21-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch12, adc0

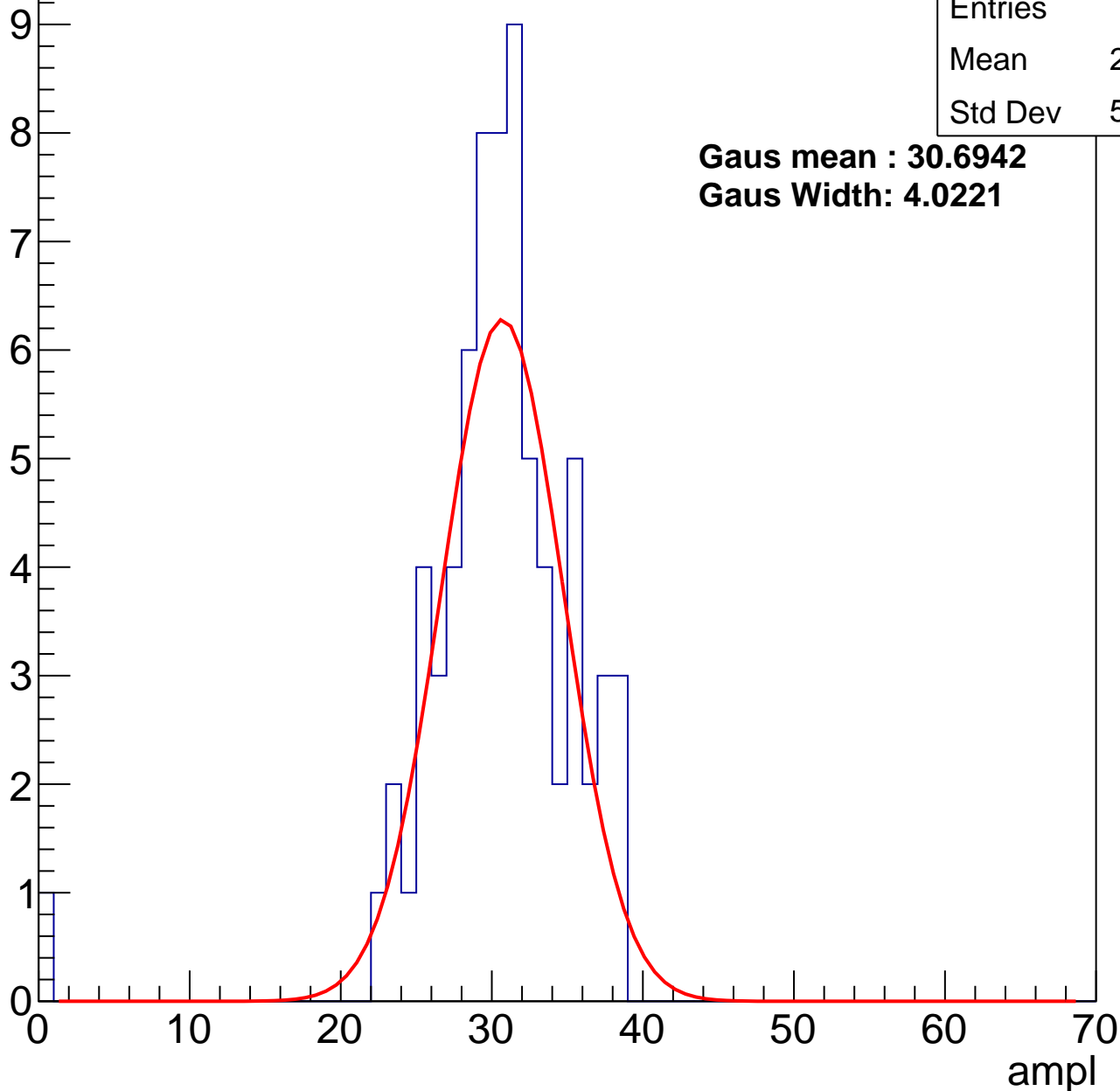
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.99
Std Dev	5.237

Gaus mean : 30.6942

Gaus Width: 4.0221



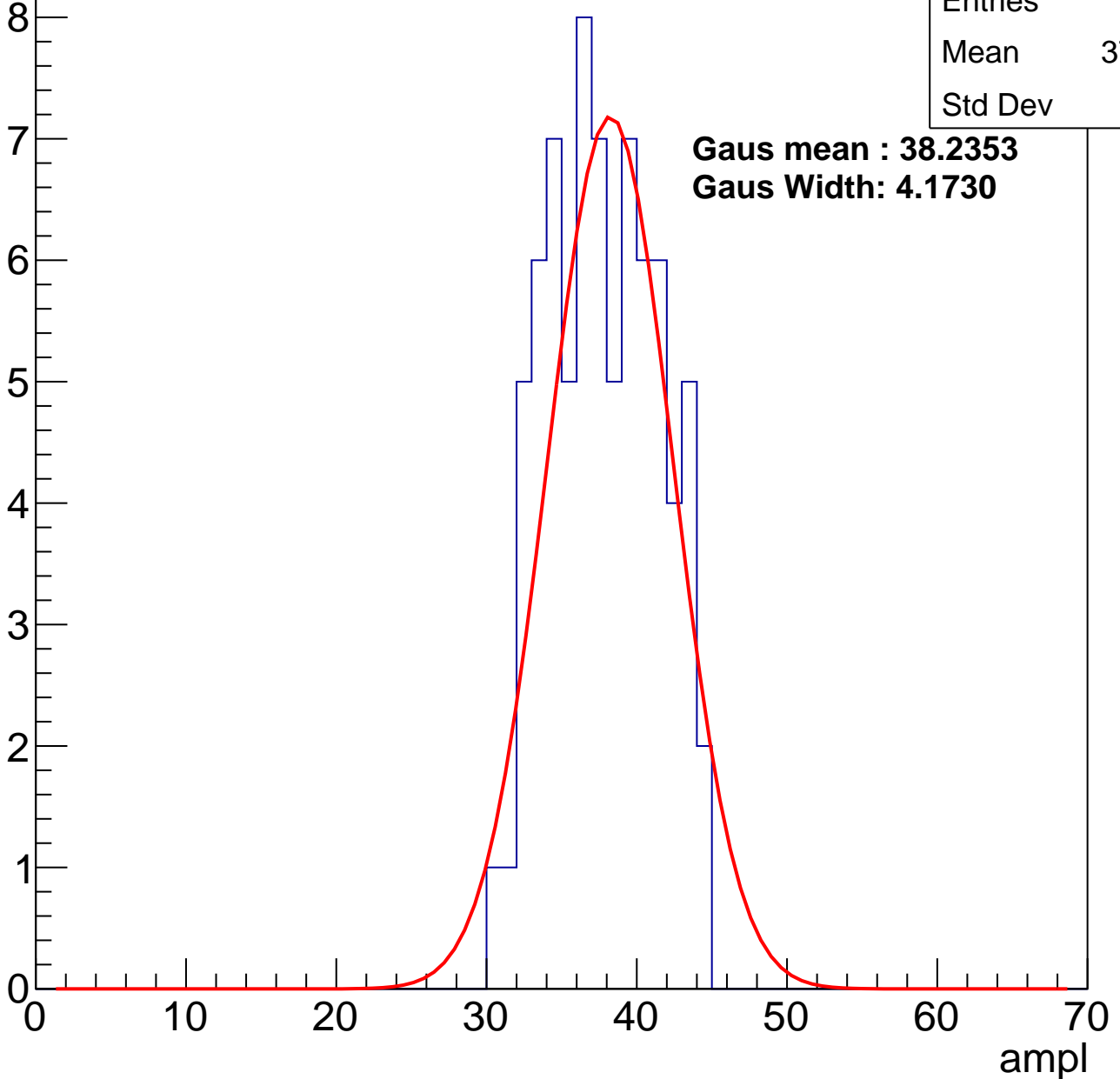
B1L101S, U21-ch12, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.32
Std Dev	3.56

Gaus mean : 38.2353
Gaus Width: 4.1730



B1L101S, U21-ch12, adc2

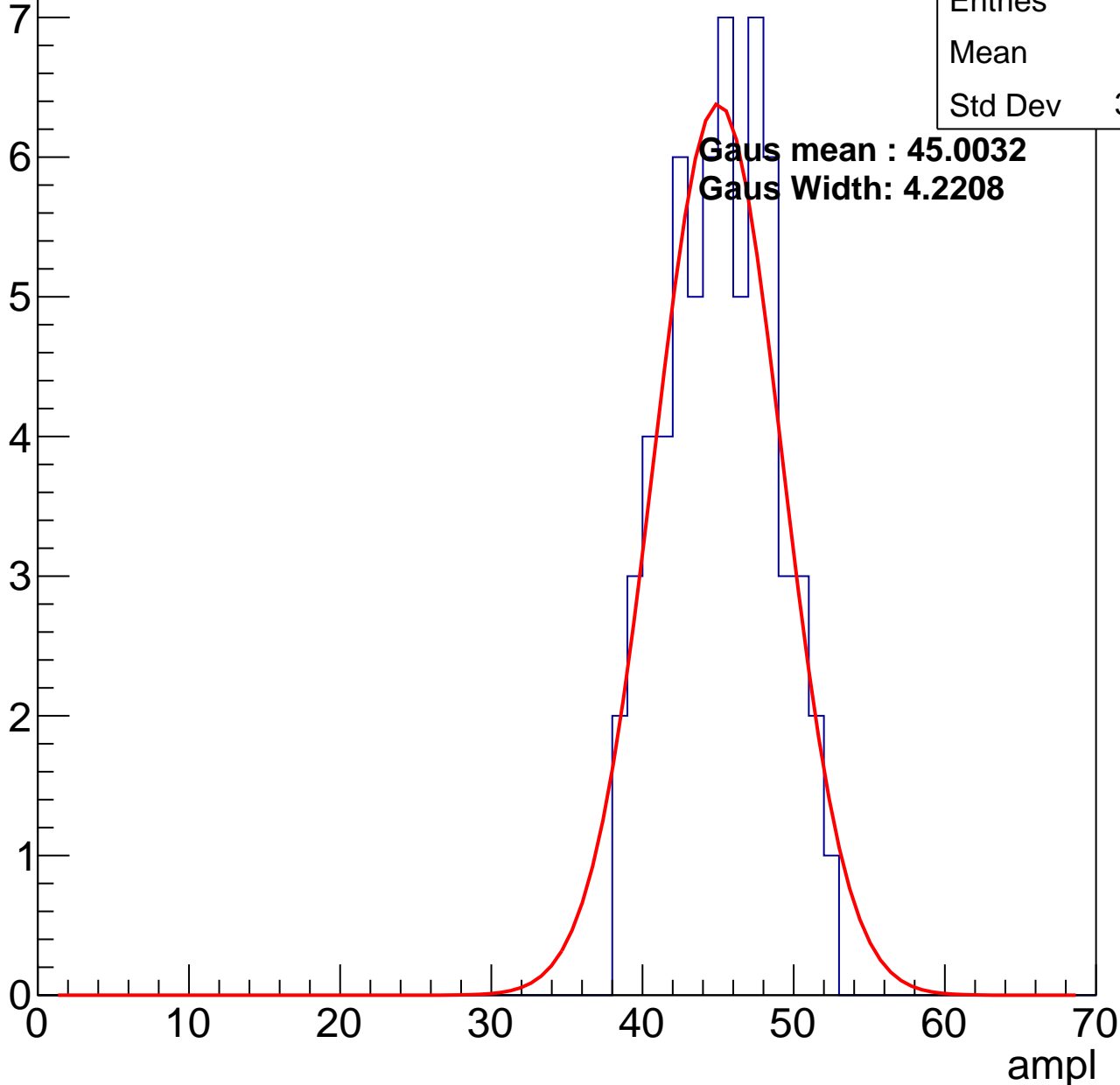
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.7
Std Dev	3.481

Gaus mean : 45.0032

Gaus Width: 4.2208

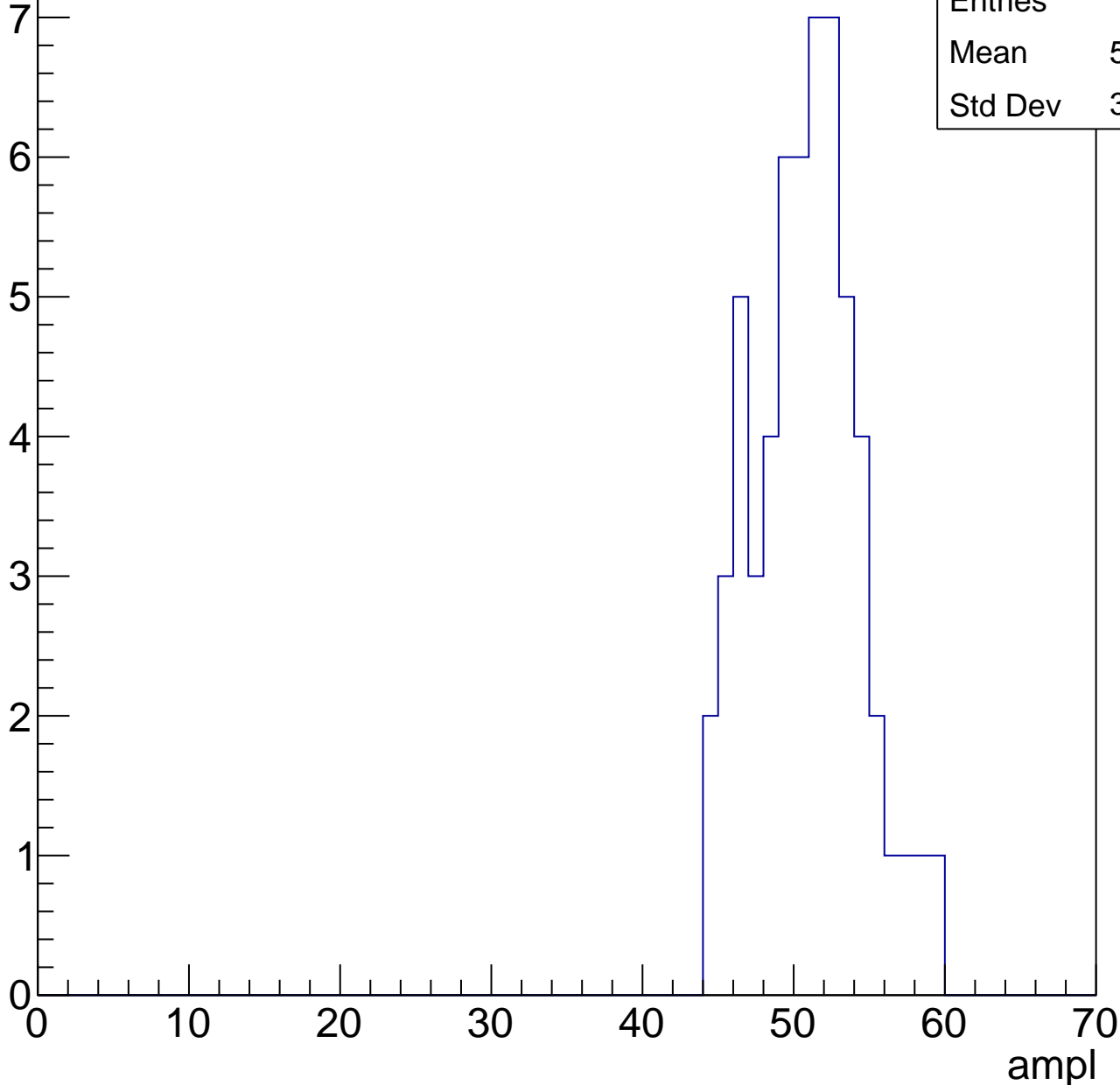


B1L101S, U21-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.38
Std Dev	3.453

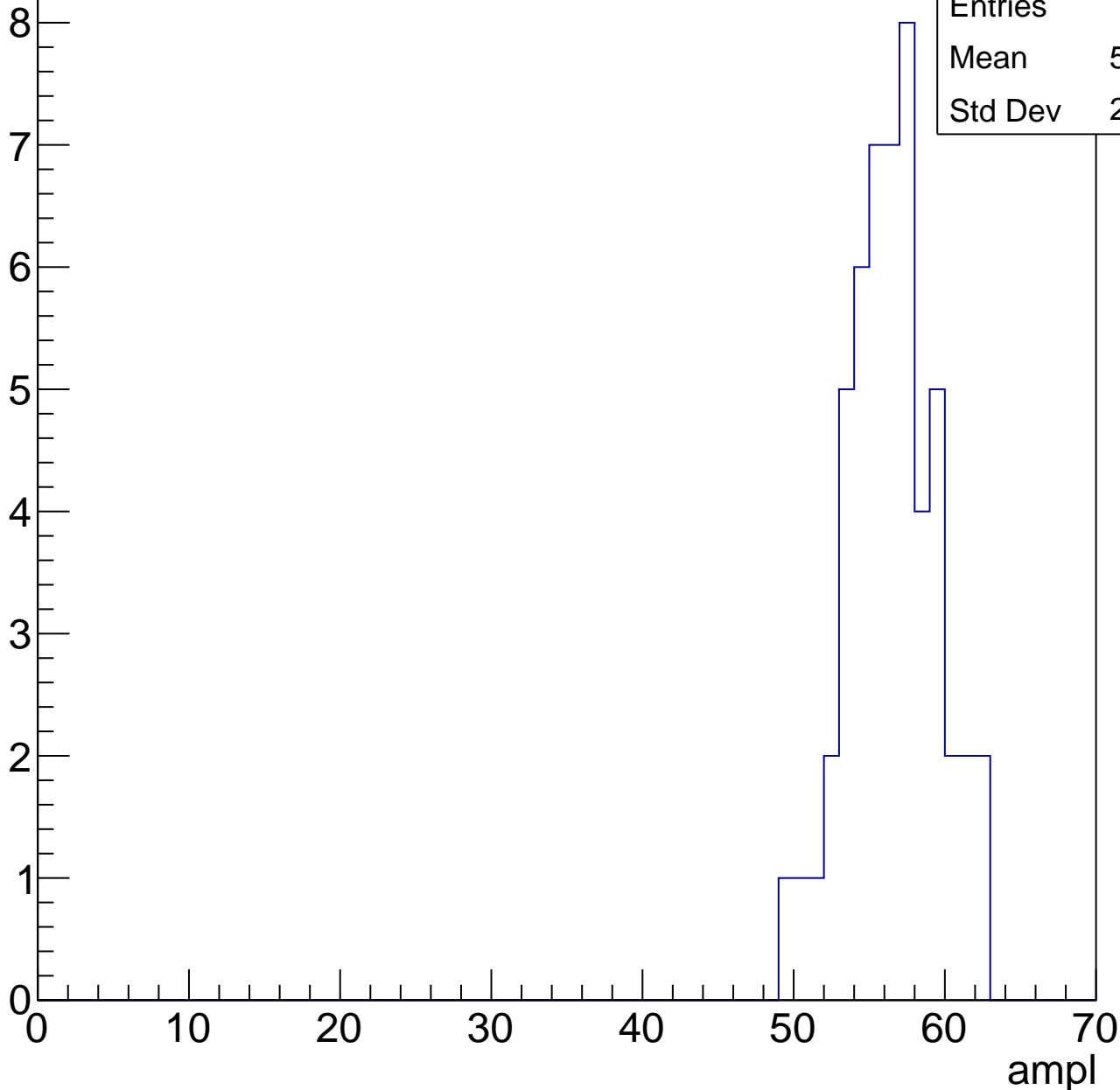


B1L101S, U21-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.02
Std Dev	2.885



B1L101S, U21-ch12, adc5

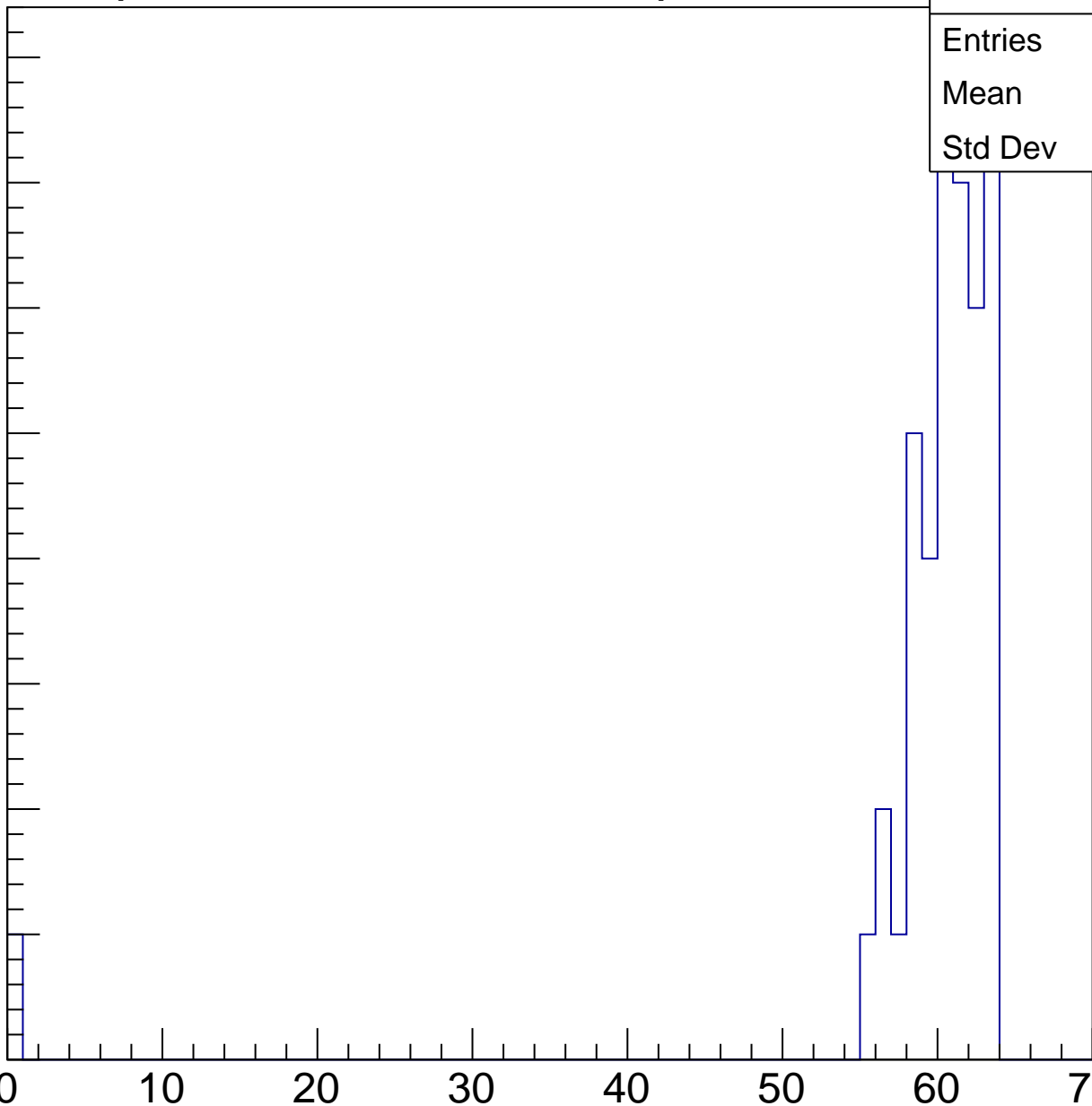
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.91
Std Dev	9.328

ampl



B1L101S, U21-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch13, adc0

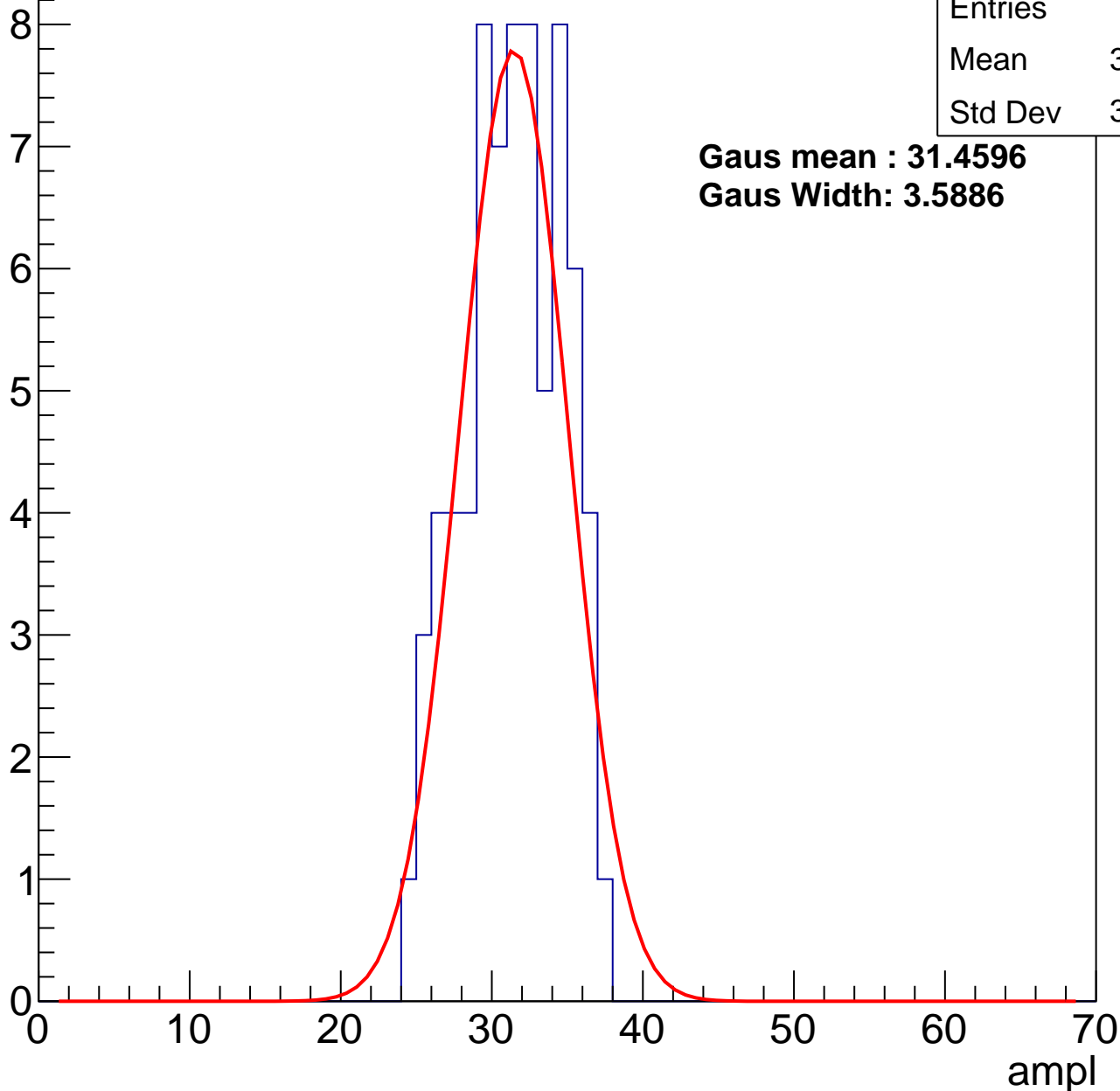
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	30.94
Std Dev	3.202

Gaus mean : 31.4596

Gaus Width: 3.5886



B1L101S, U21-ch13, adc1

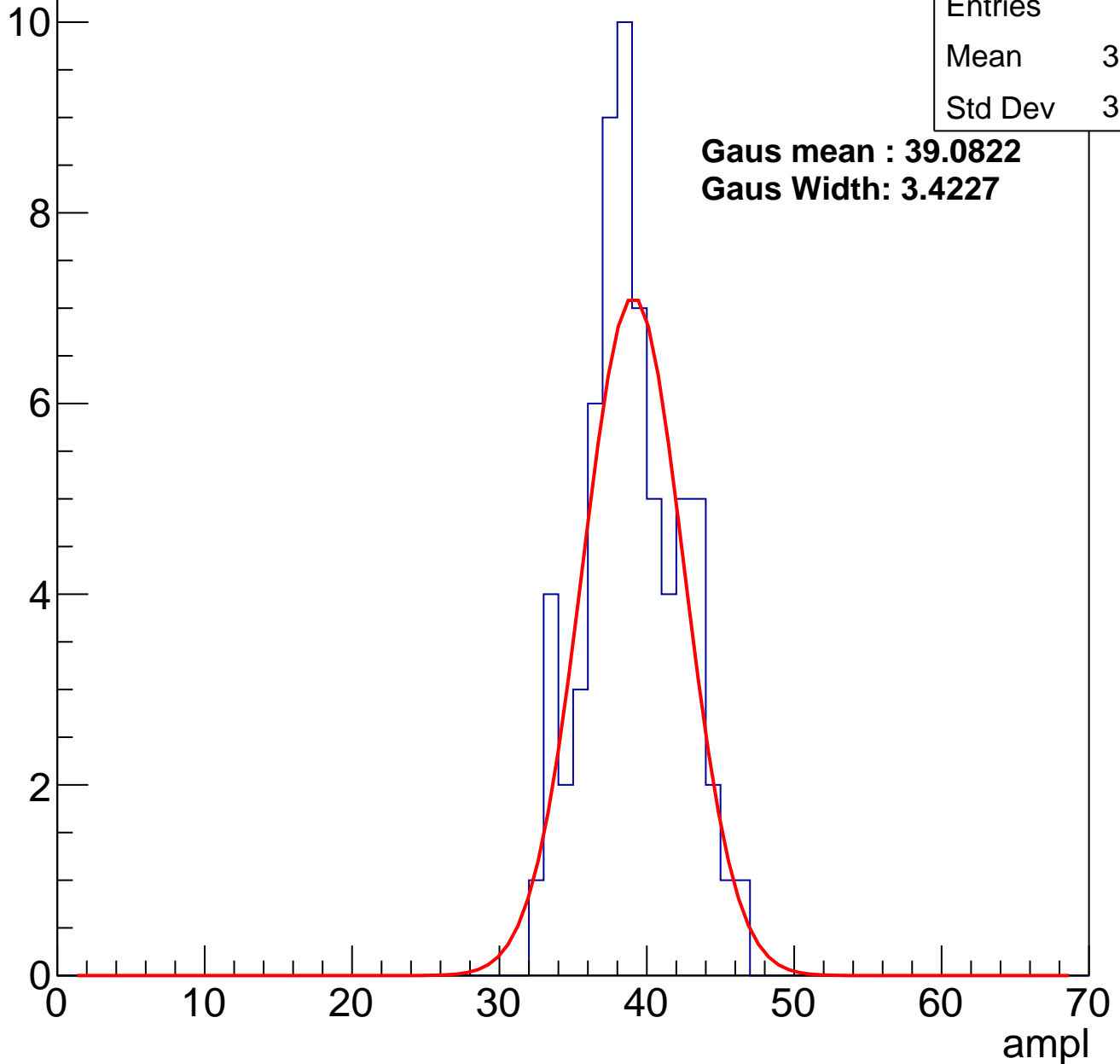
calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	38.57
Std Dev	3.186

Gaus mean : 39.0822

Gaus Width: 3.4227

Entry



B1L101S, U21-ch13, adc2

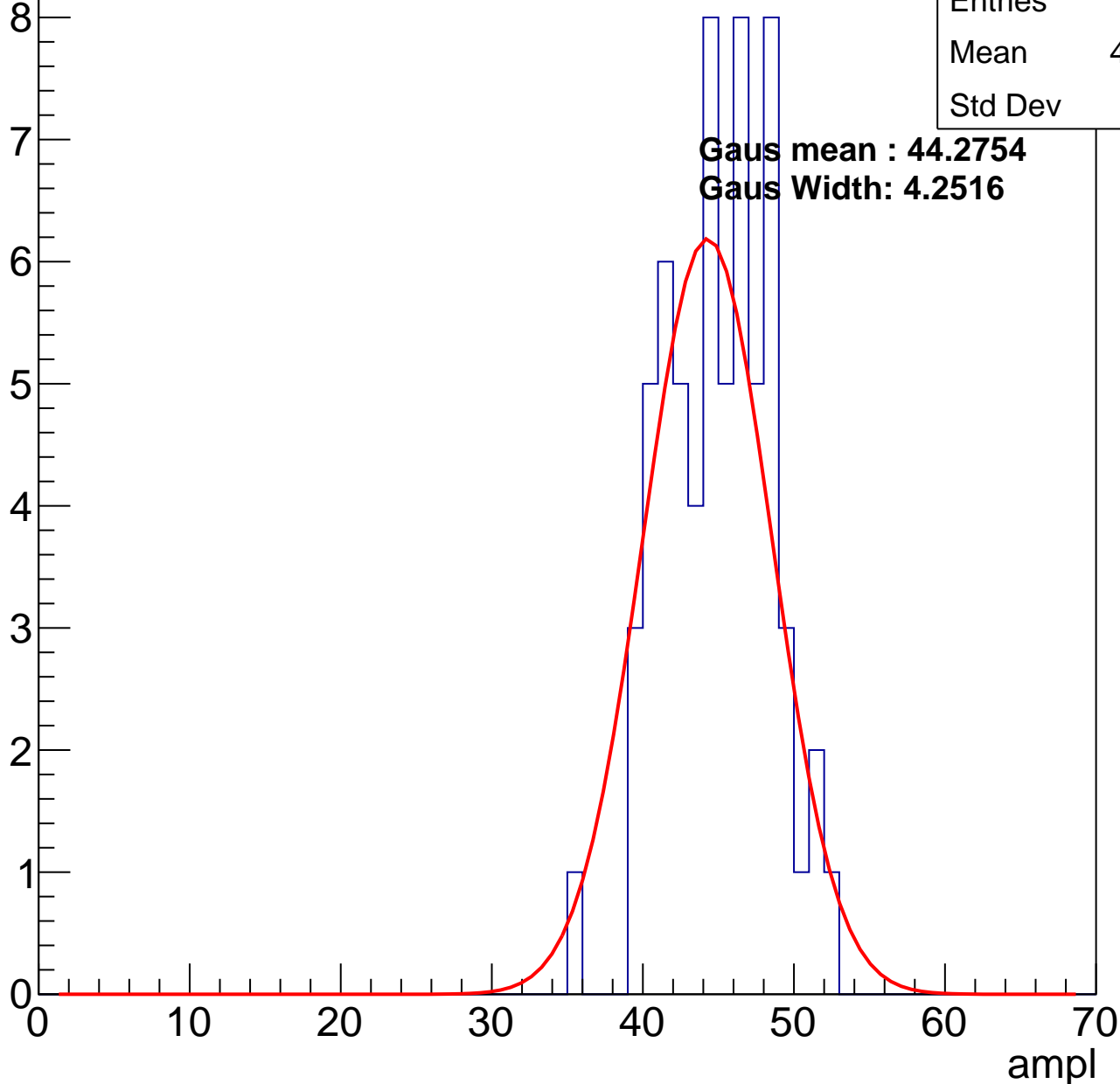
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.54
Std Dev	3.46

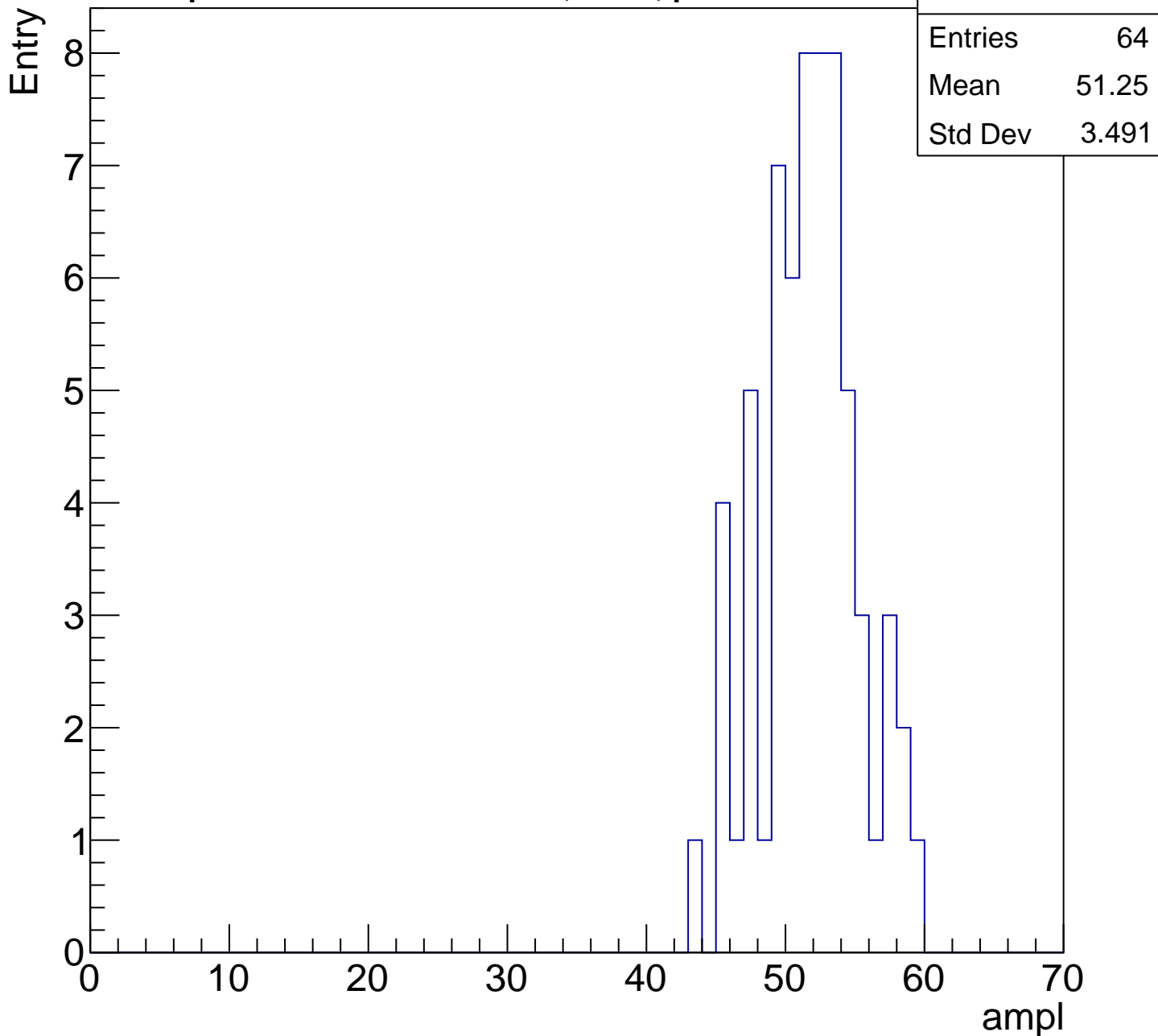
Gaus mean : 44.2754

Gaus Width: 4.2516



B1L101S, U21-ch13, adc3

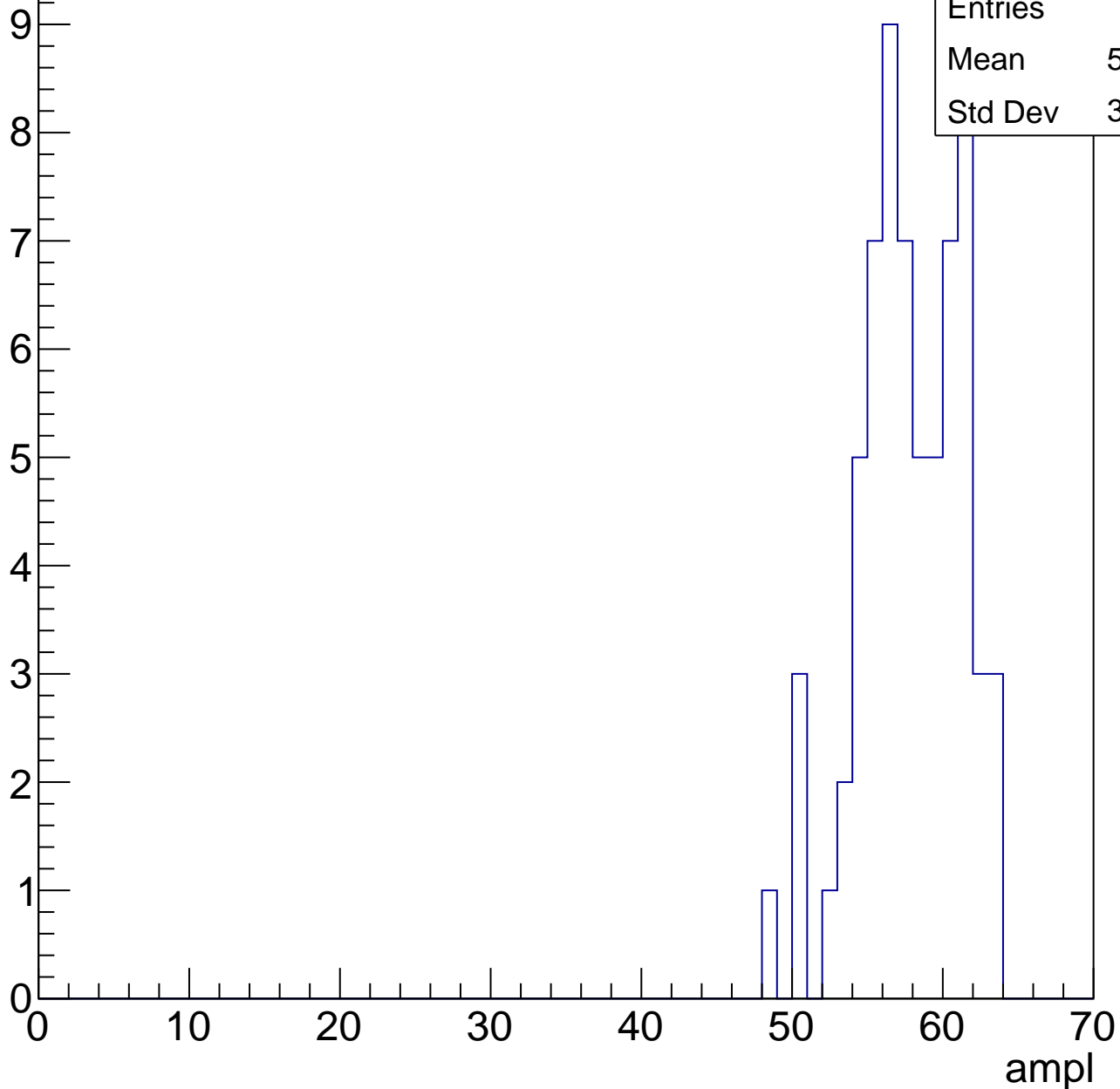
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

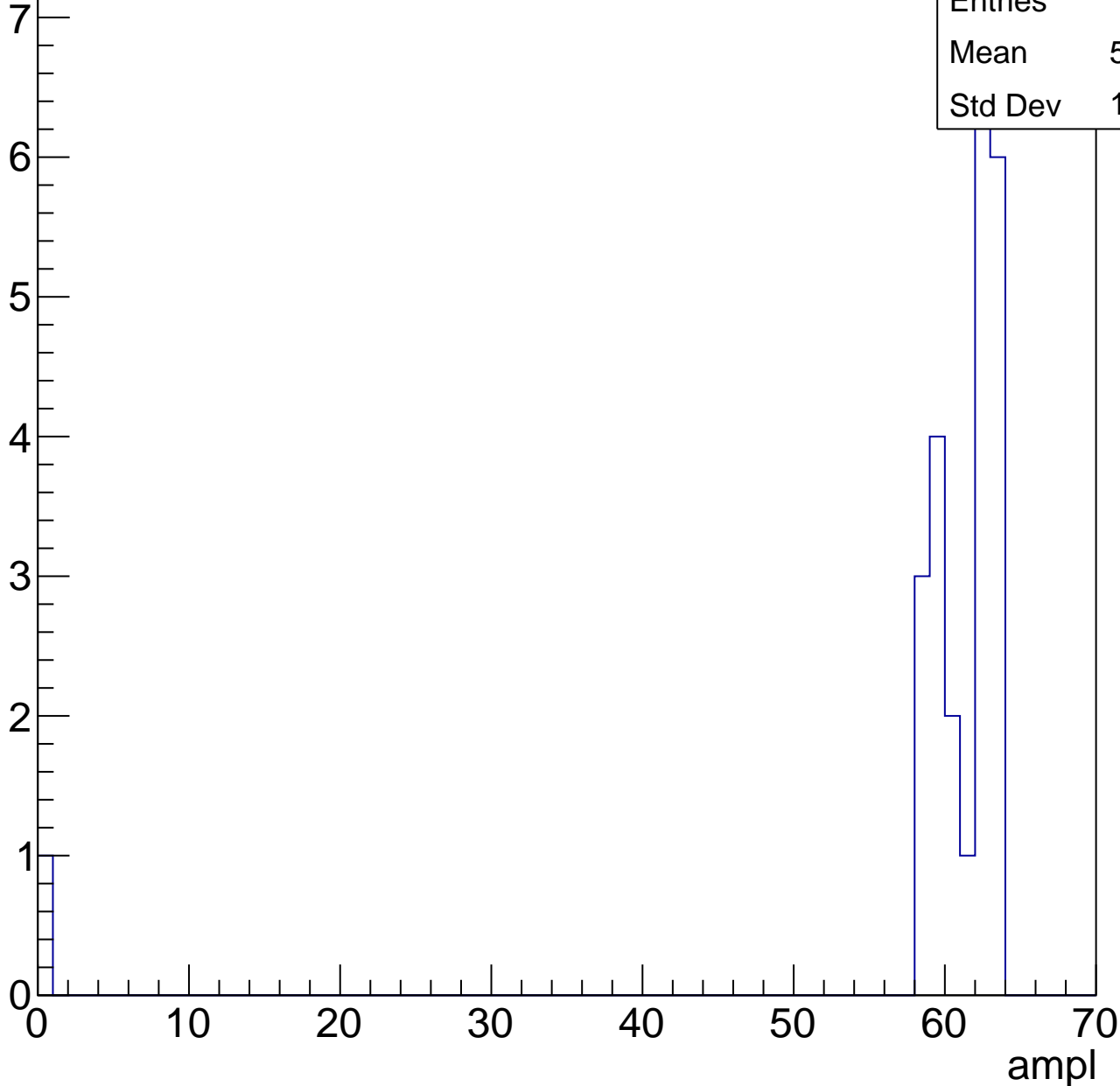


B1L101S, U21-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

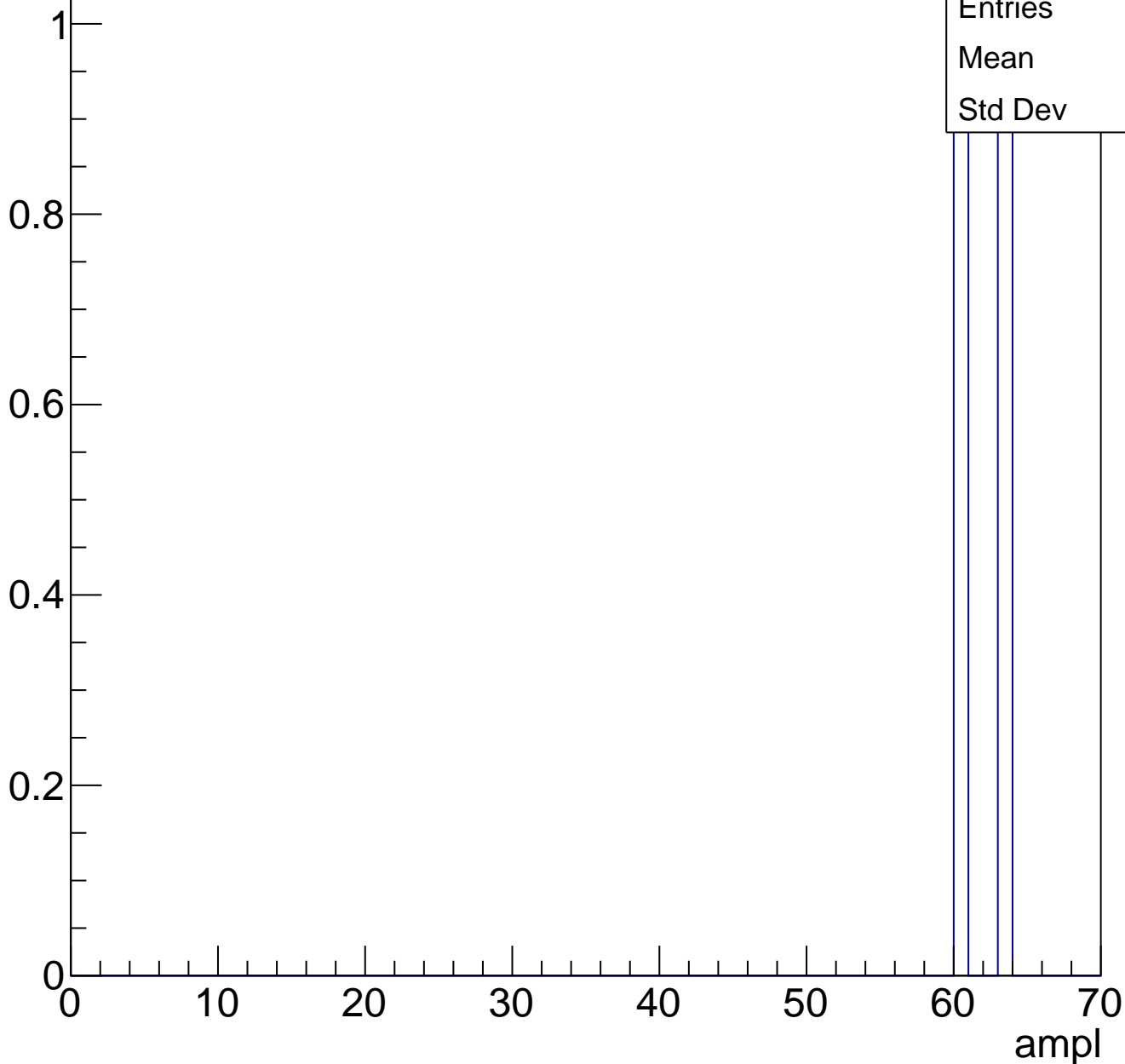
Entries	24
Mean	58.46
Std Dev	12.32



B1L101S, U21-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch14, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	97
Mean	29.87
Std Dev	4.101

Gaus mean : 30.3106

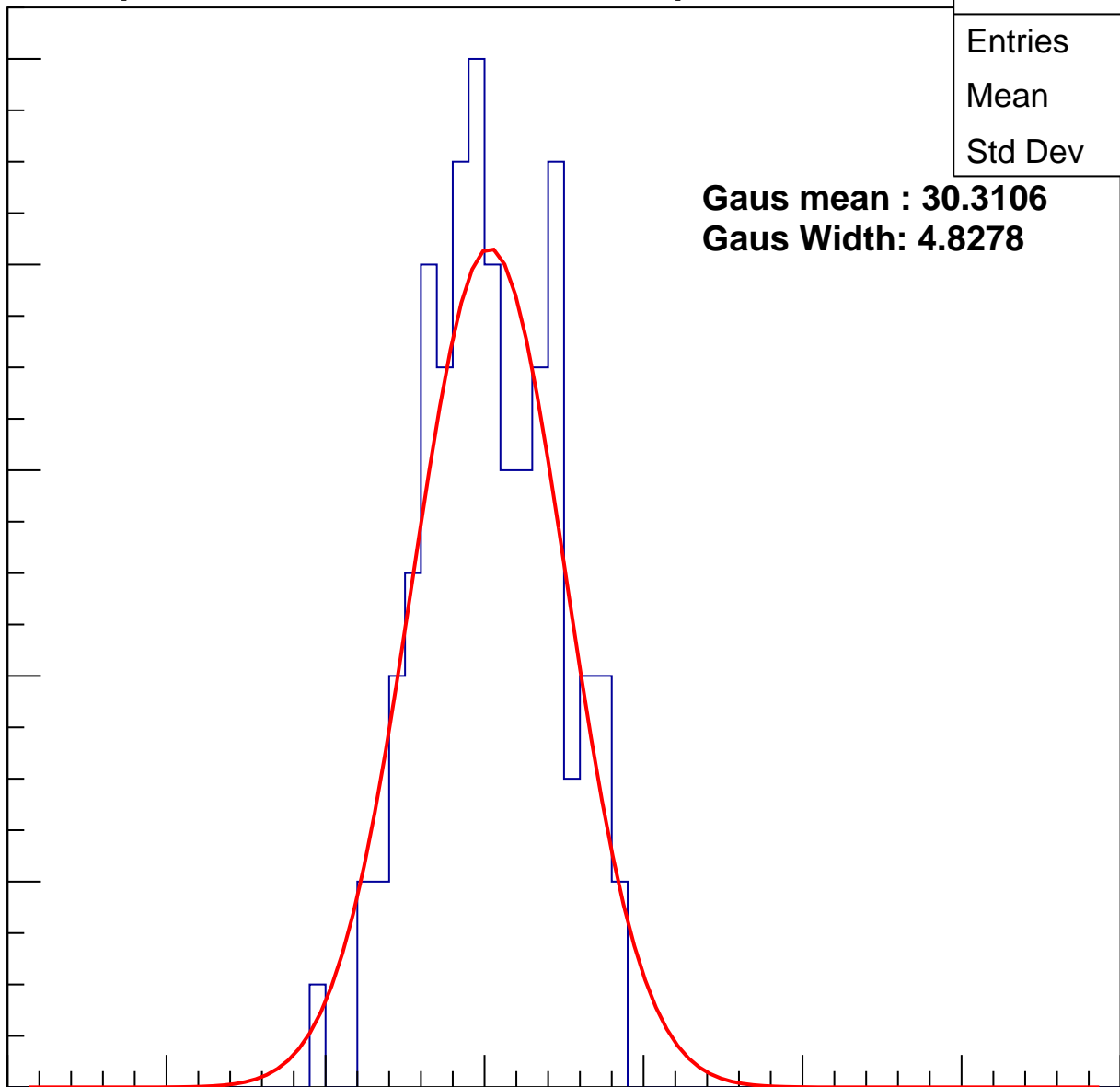
Gaus Width: 4.8278

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch14, adc1

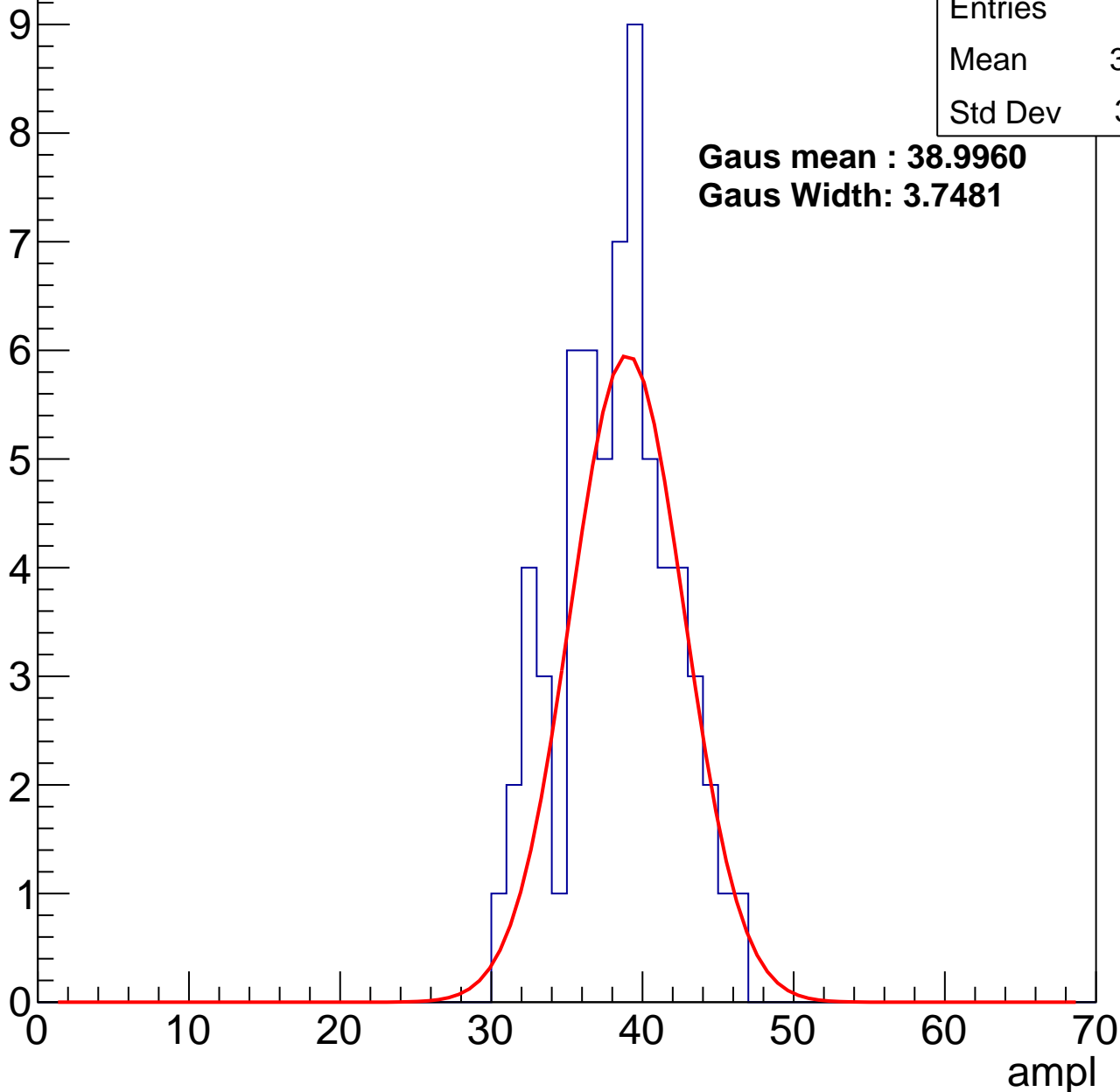
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.83
Std Dev	3.681

Gaus mean : 38.9960

Gaus Width: 3.7481

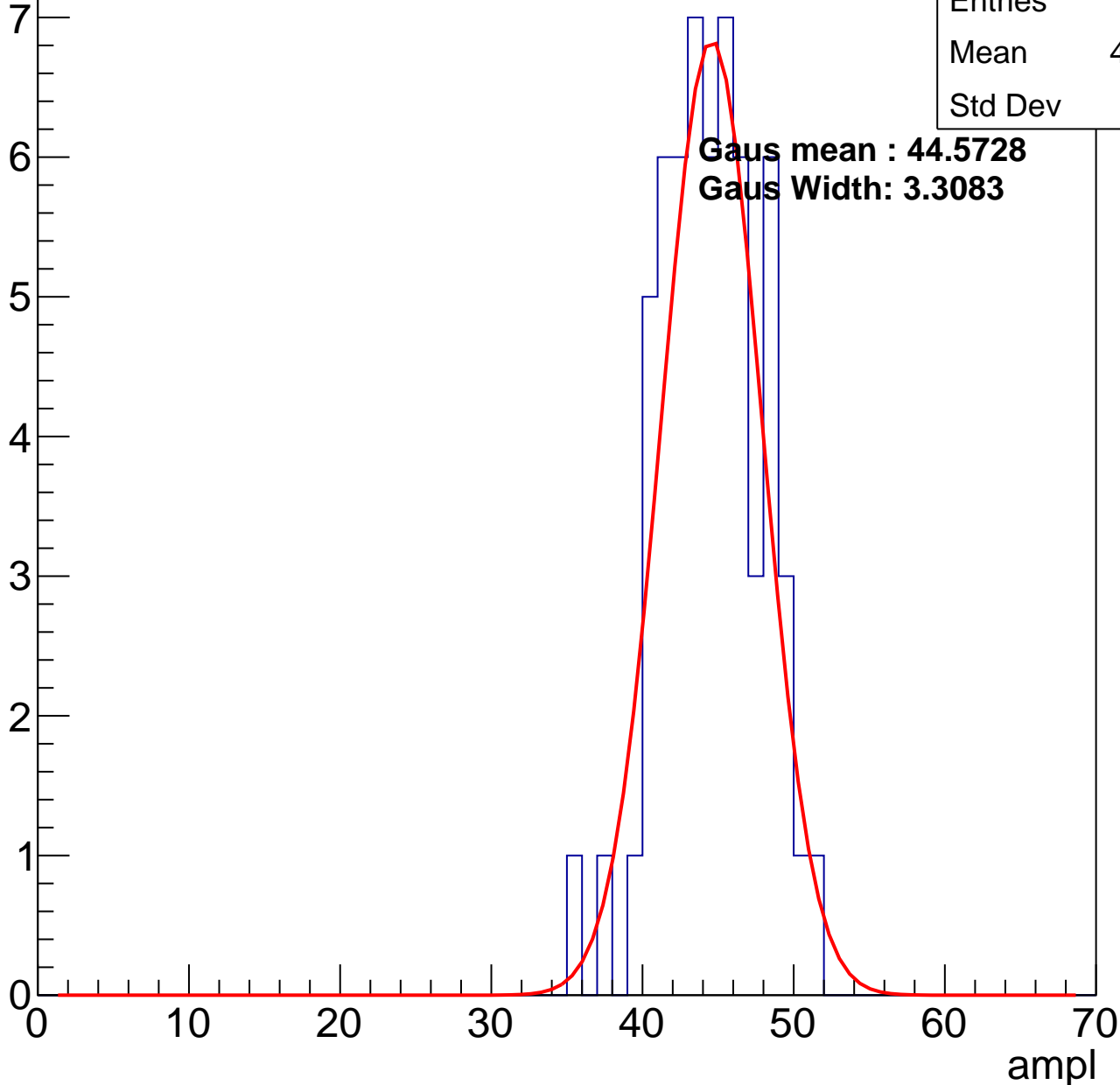


B1L101S, U21-ch14, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

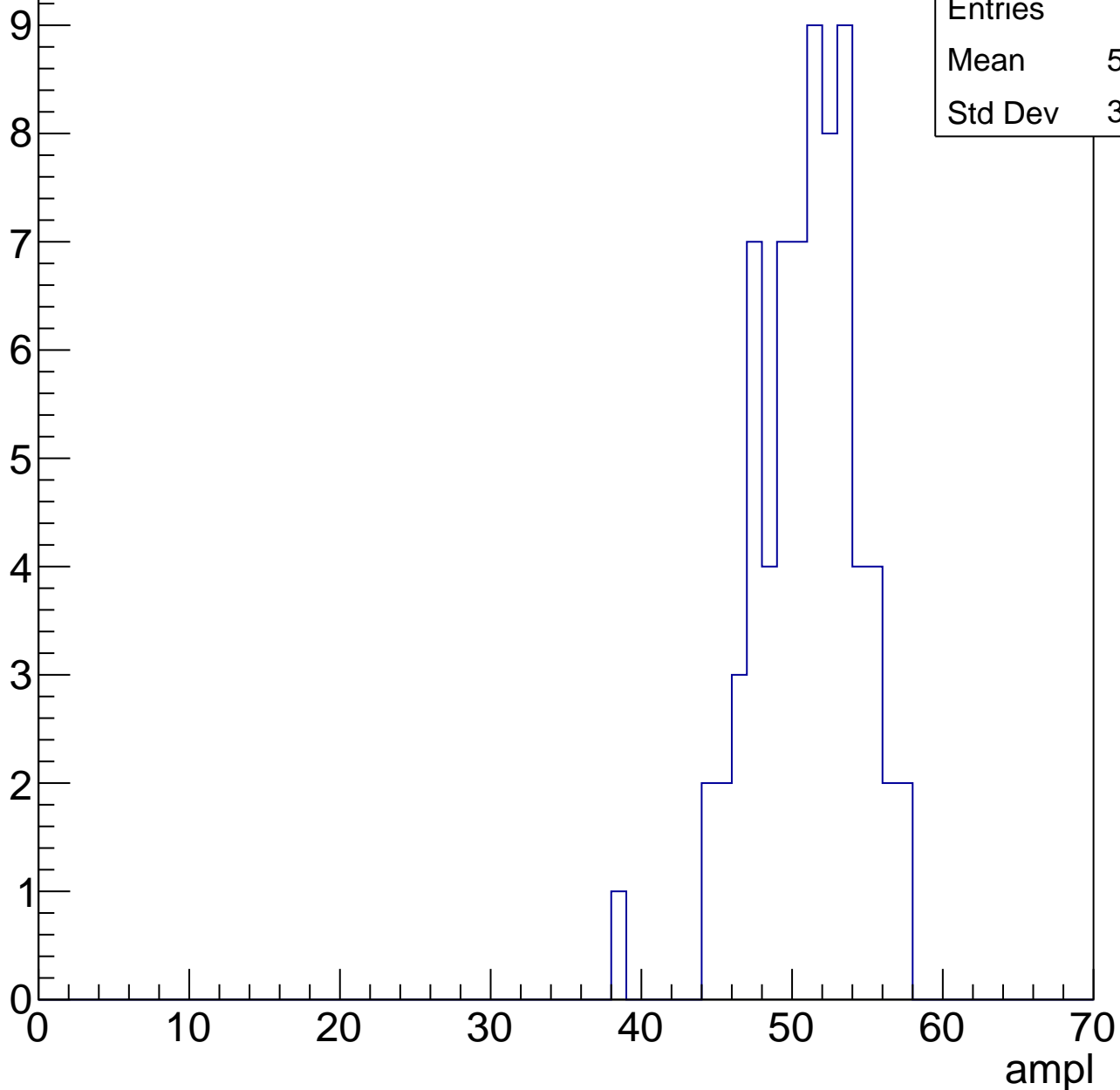
Entries	60
Mean	44.03
Std Dev	3.25



B1L101S, U21-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

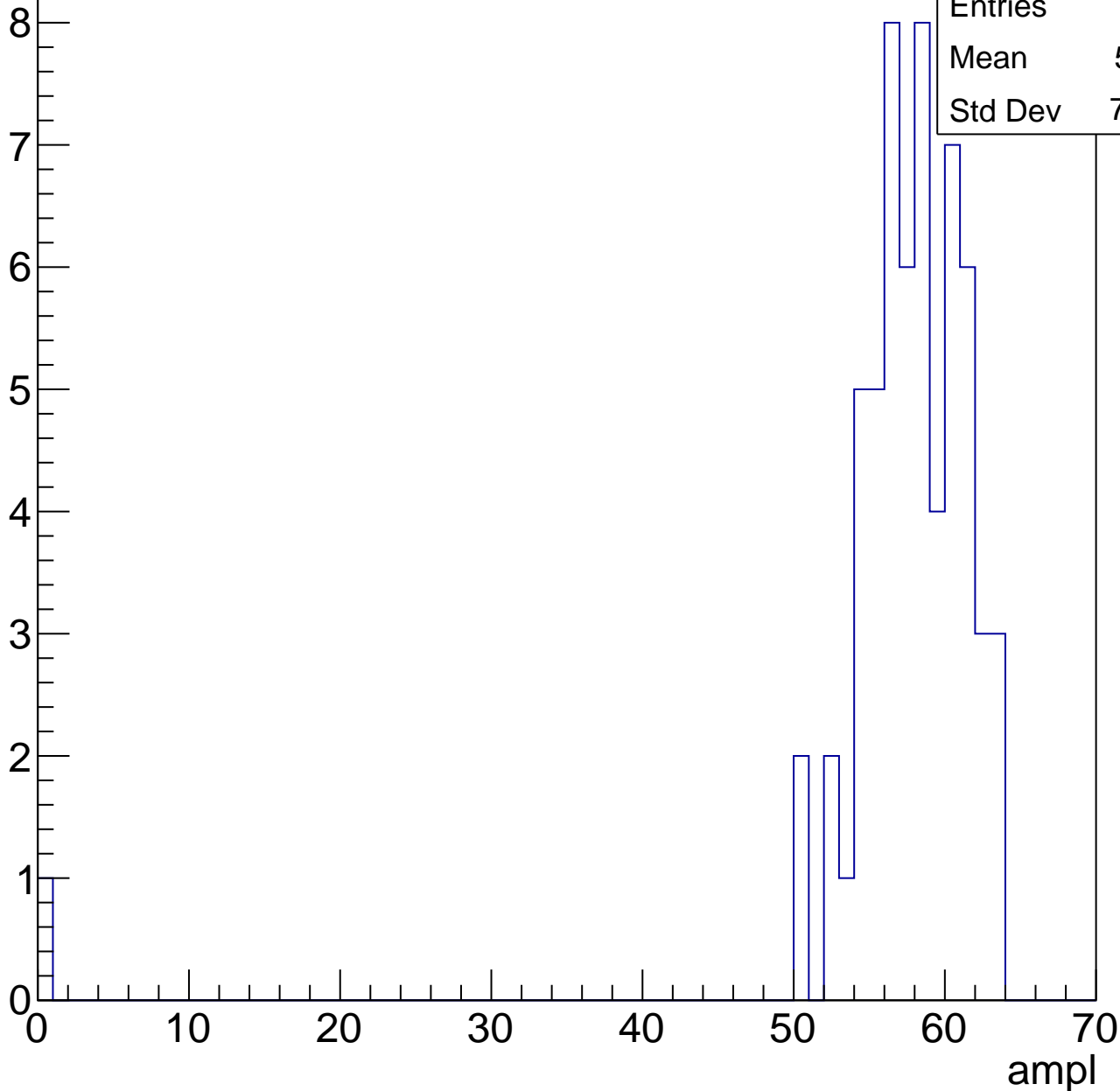


B1L101S, U21-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.61
Std Dev	7.937

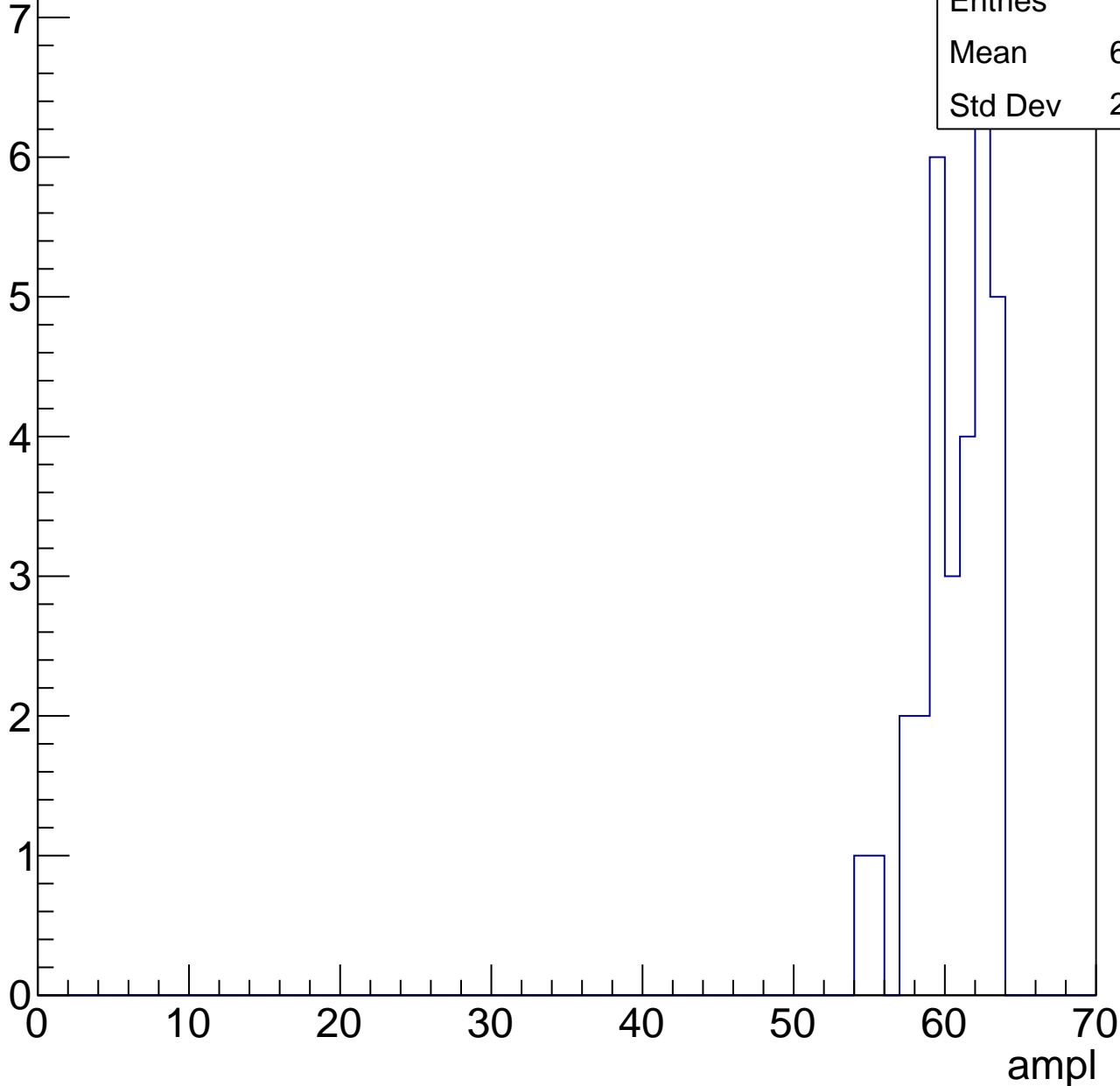


B1L101S, U21-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.19
Std Dev	2.334



B1L101S, U21-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch15, adc0

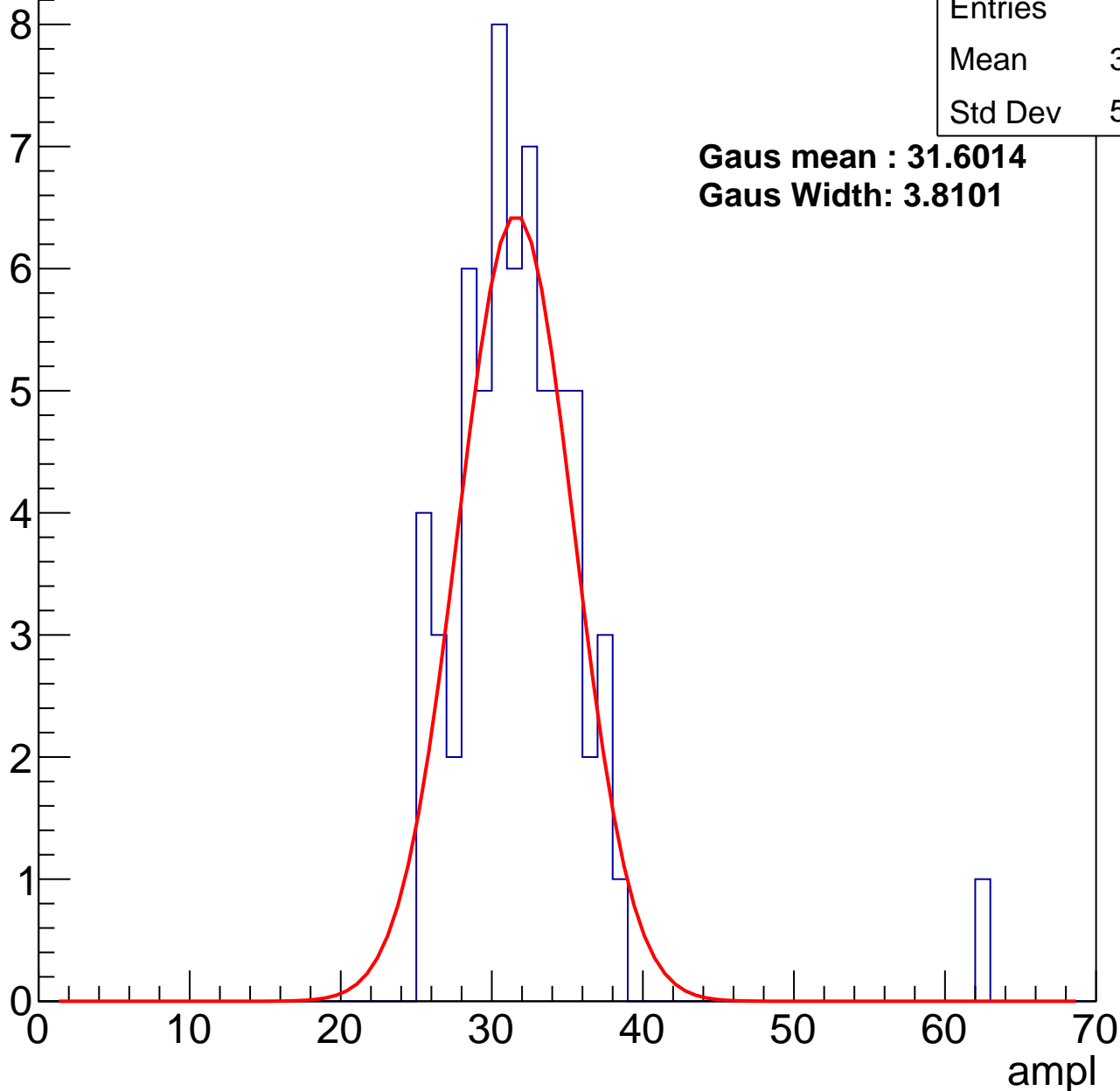
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.56
Std Dev	5.092

Gaus mean : 31.6014

Gaus Width: 3.8101



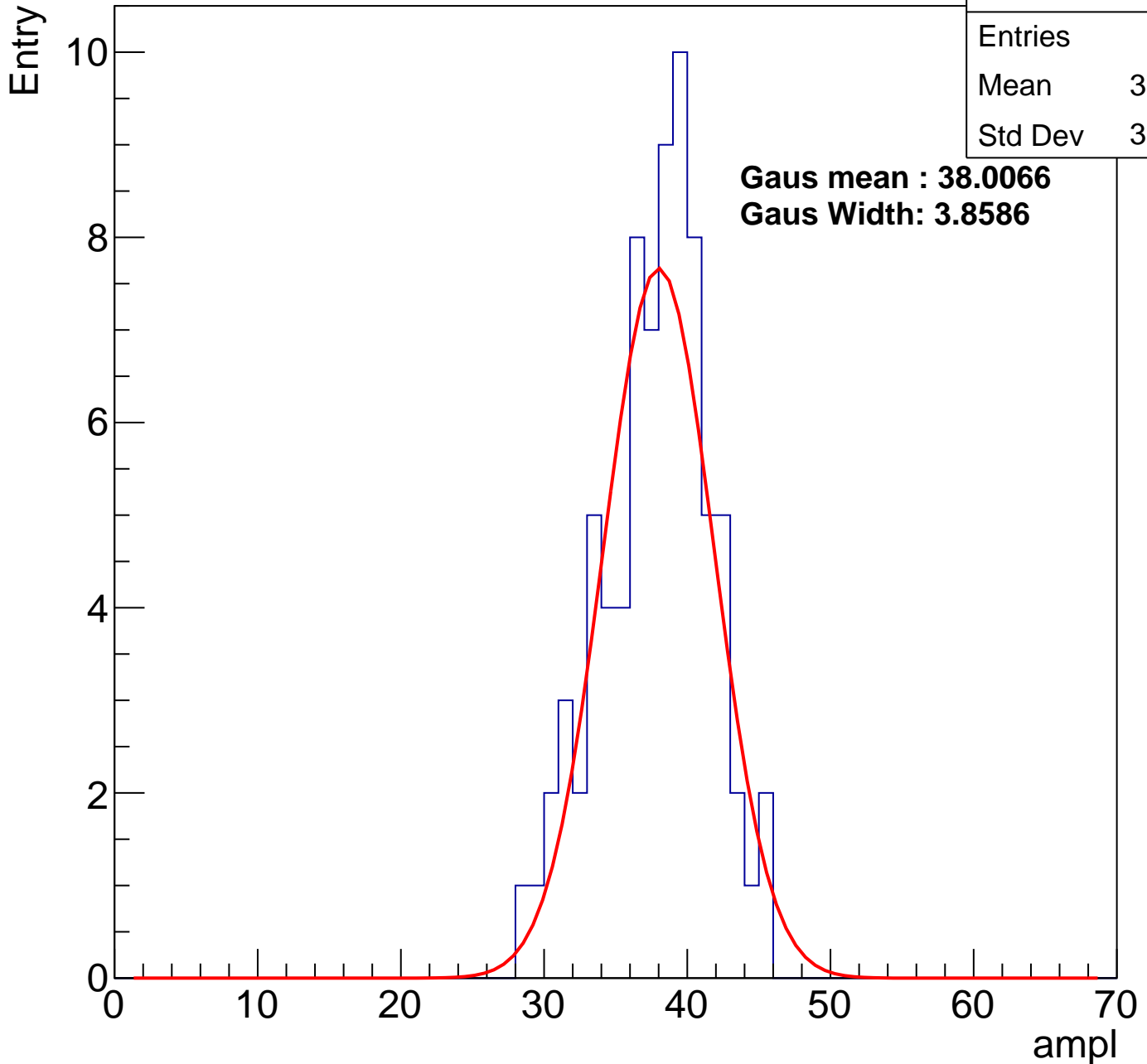
B1L101S, U21-ch15, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	37.33
Std Dev	3.744

Gaus mean : 38.0066

Gaus Width: 3.8586



B1L101S, U21-ch15, adc2

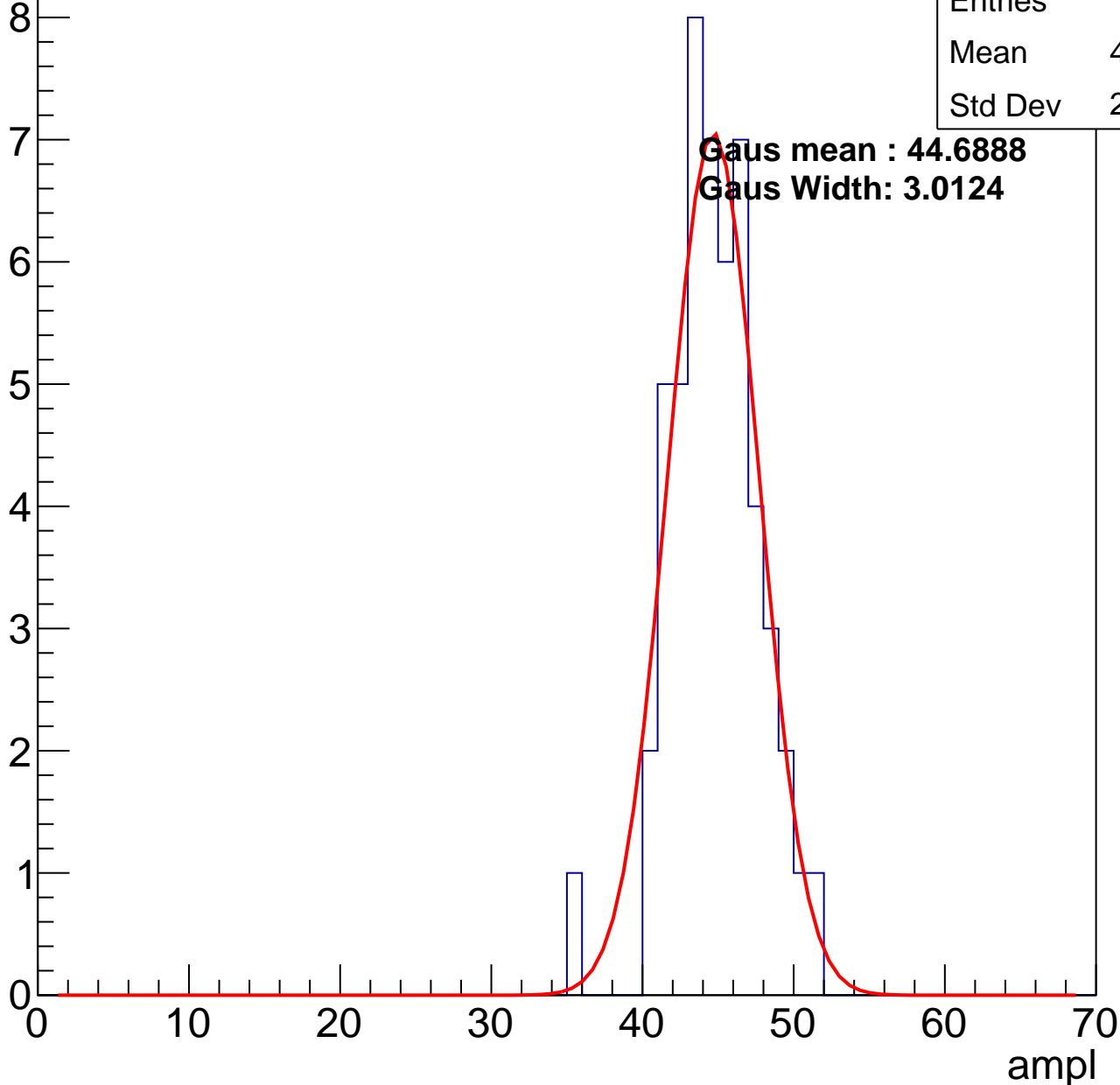
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.33
Std Dev	2.887

Gaus mean : 44.6888

Gaus Width: 3.0124

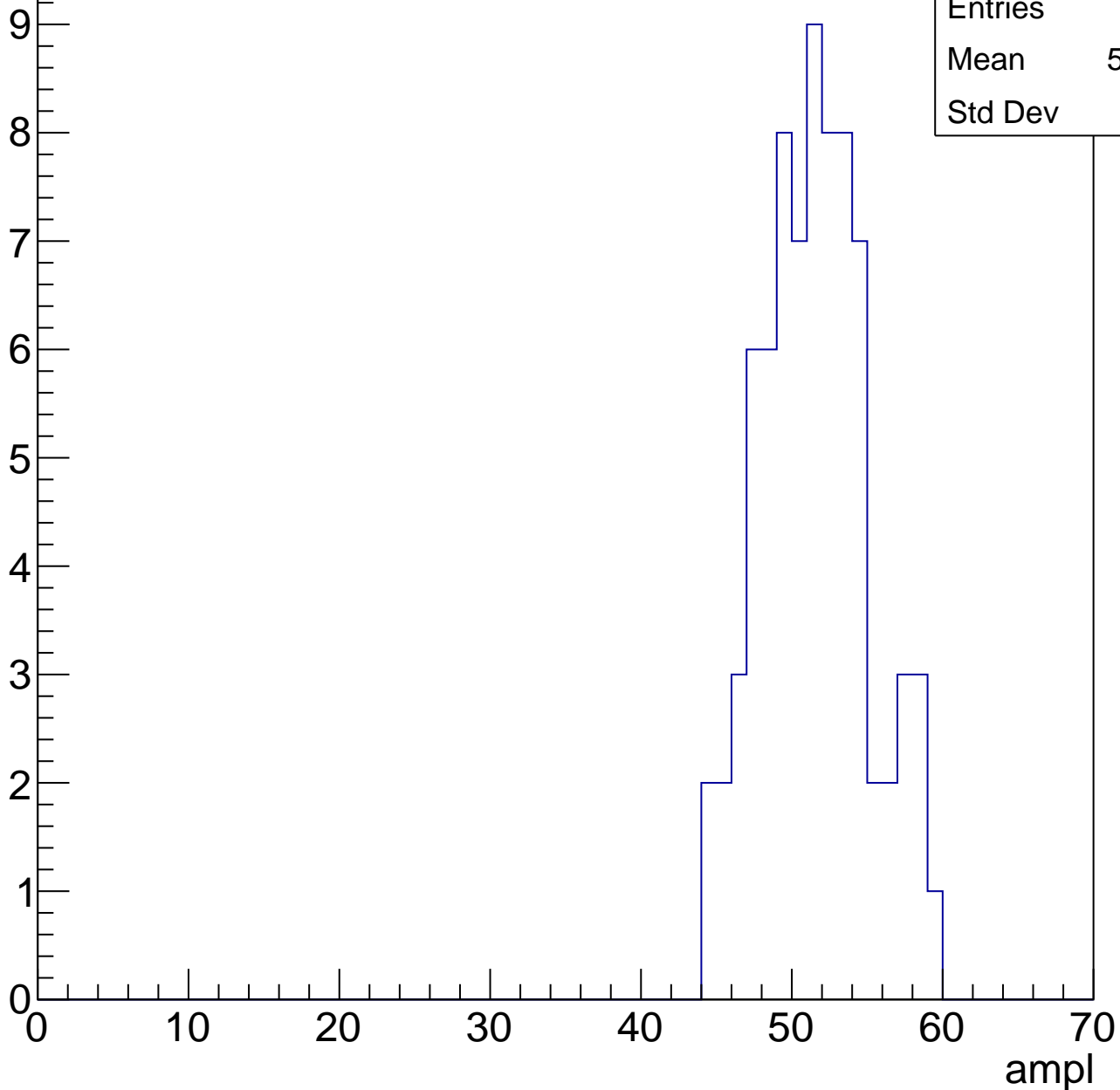


B1L101S, U21-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

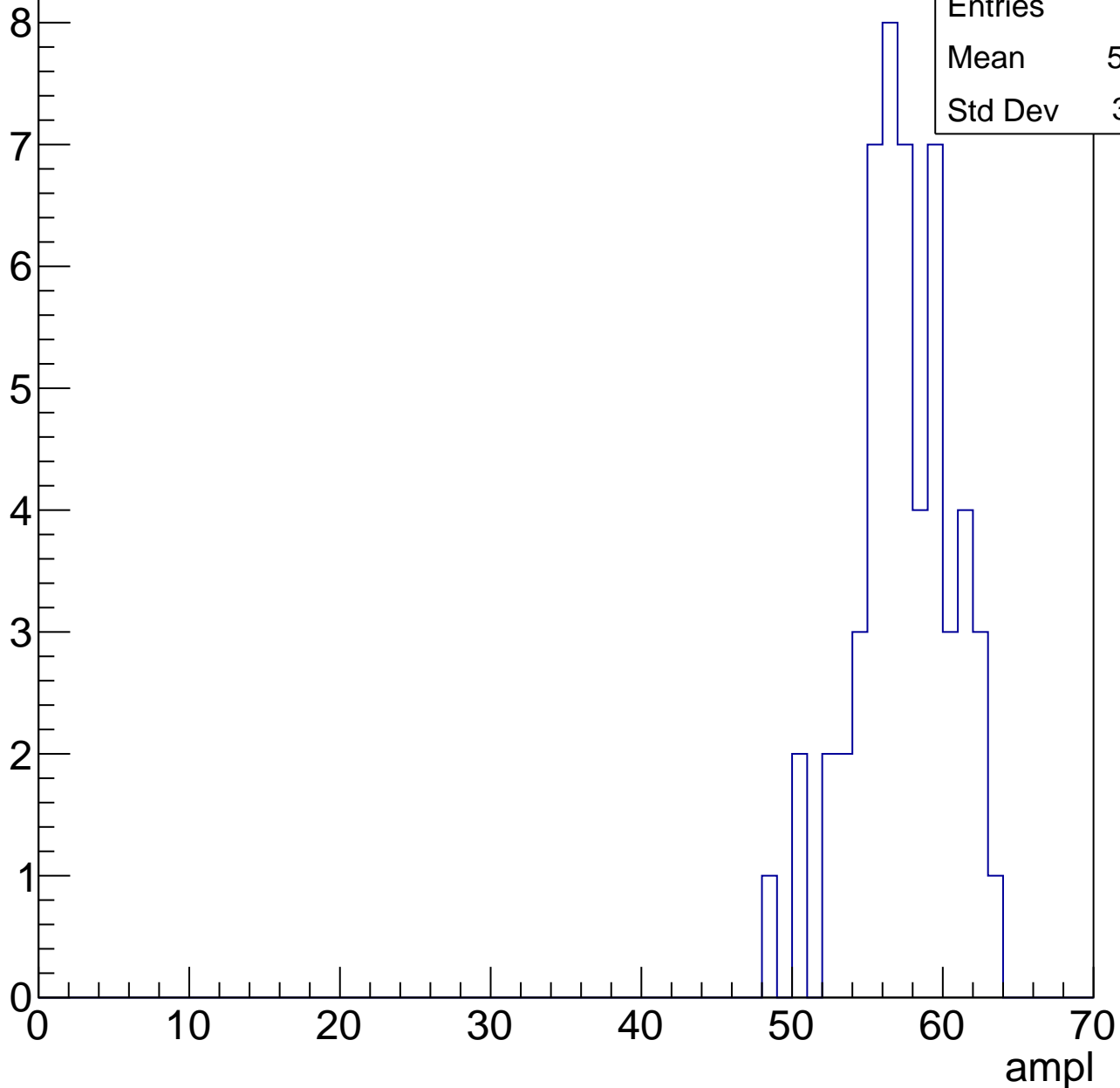
Entries	77
Mean	51.05
Std Dev	3.49



B1L101S, U21-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

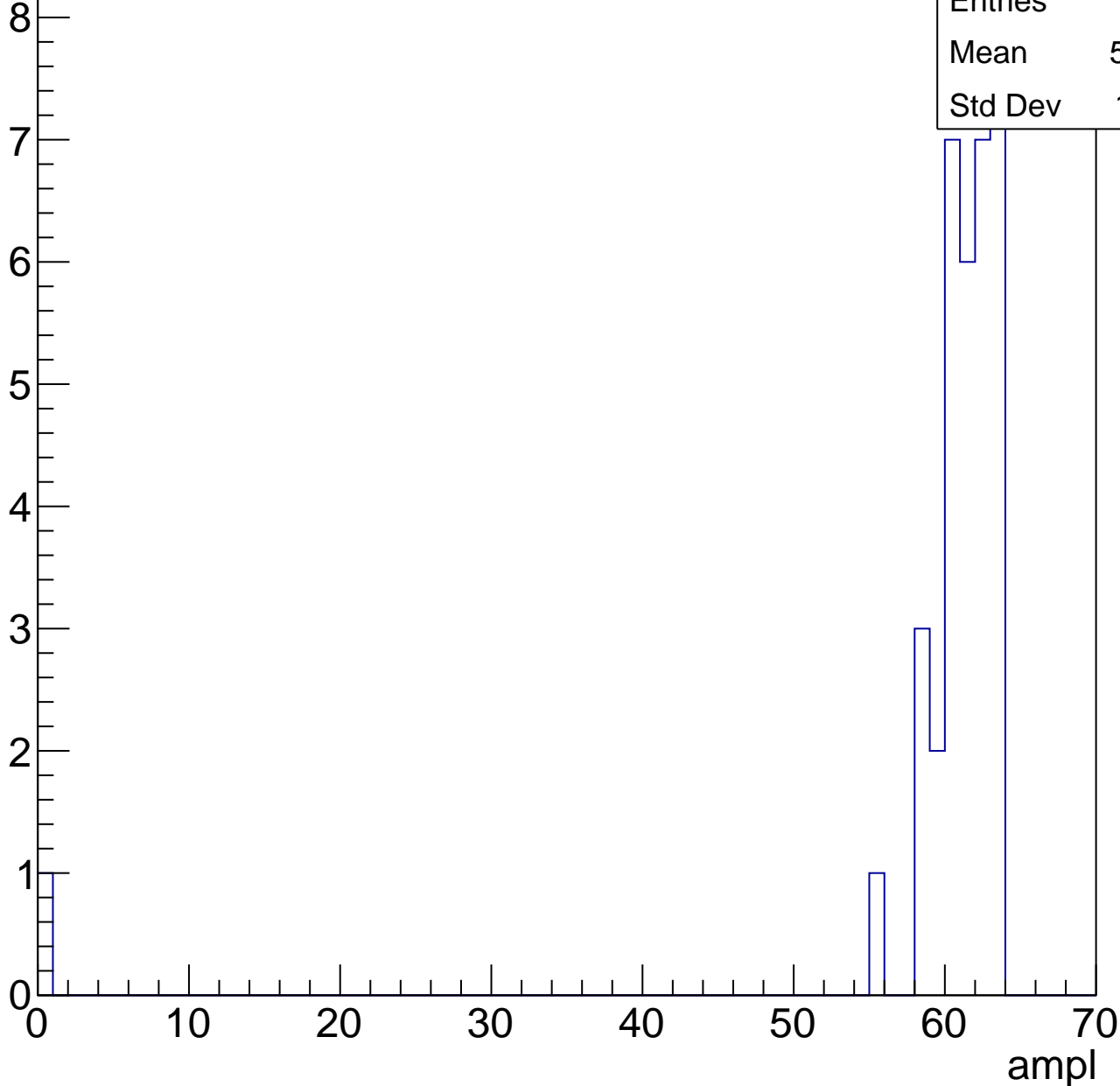


B1L101S, U21-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	59.17
Std Dev	10.31



B1L101S, U21-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch16, adc0

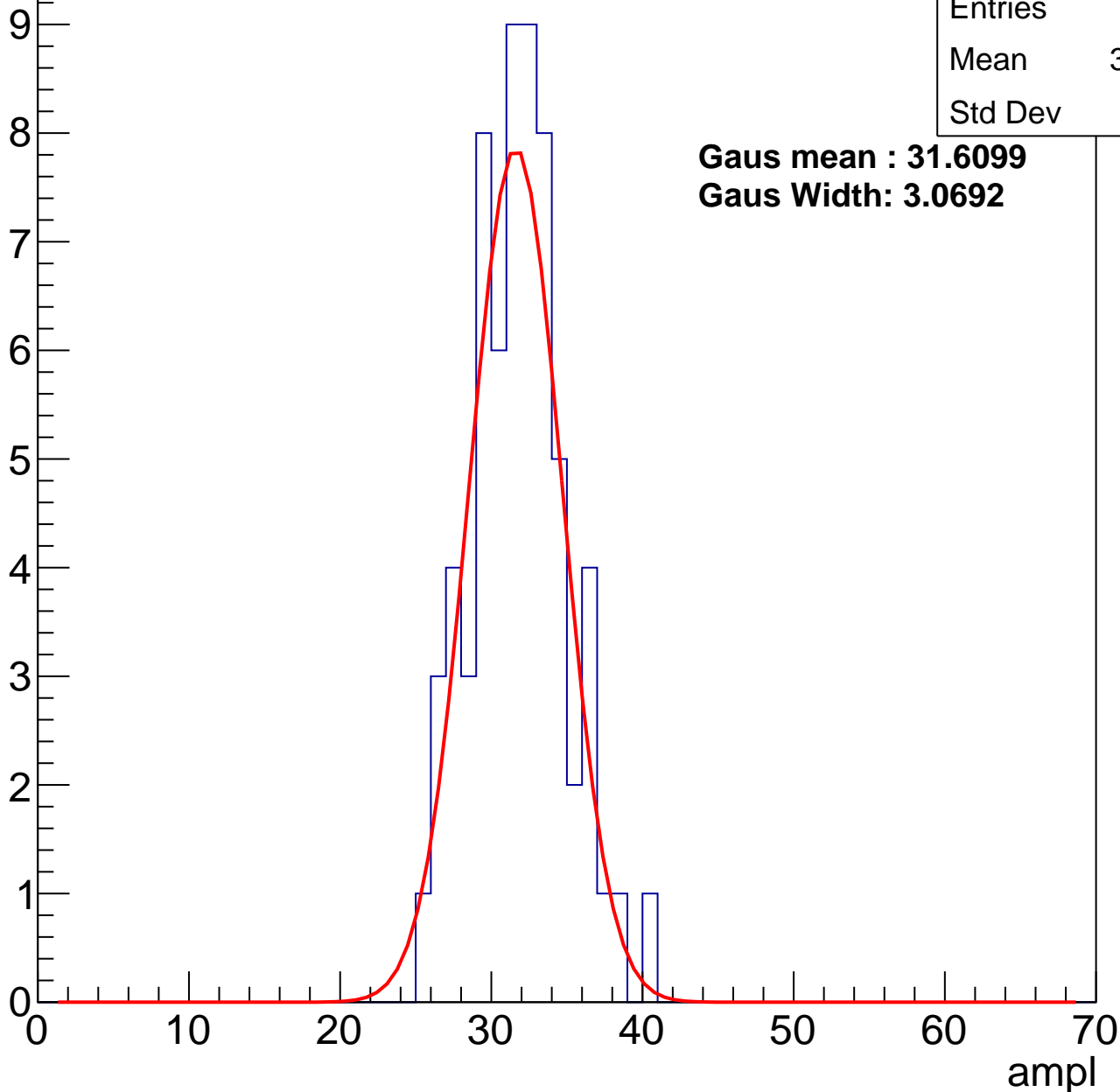
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	31.34
Std Dev	3.08

Gaus mean : 31.6099

Gaus Width: 3.0692



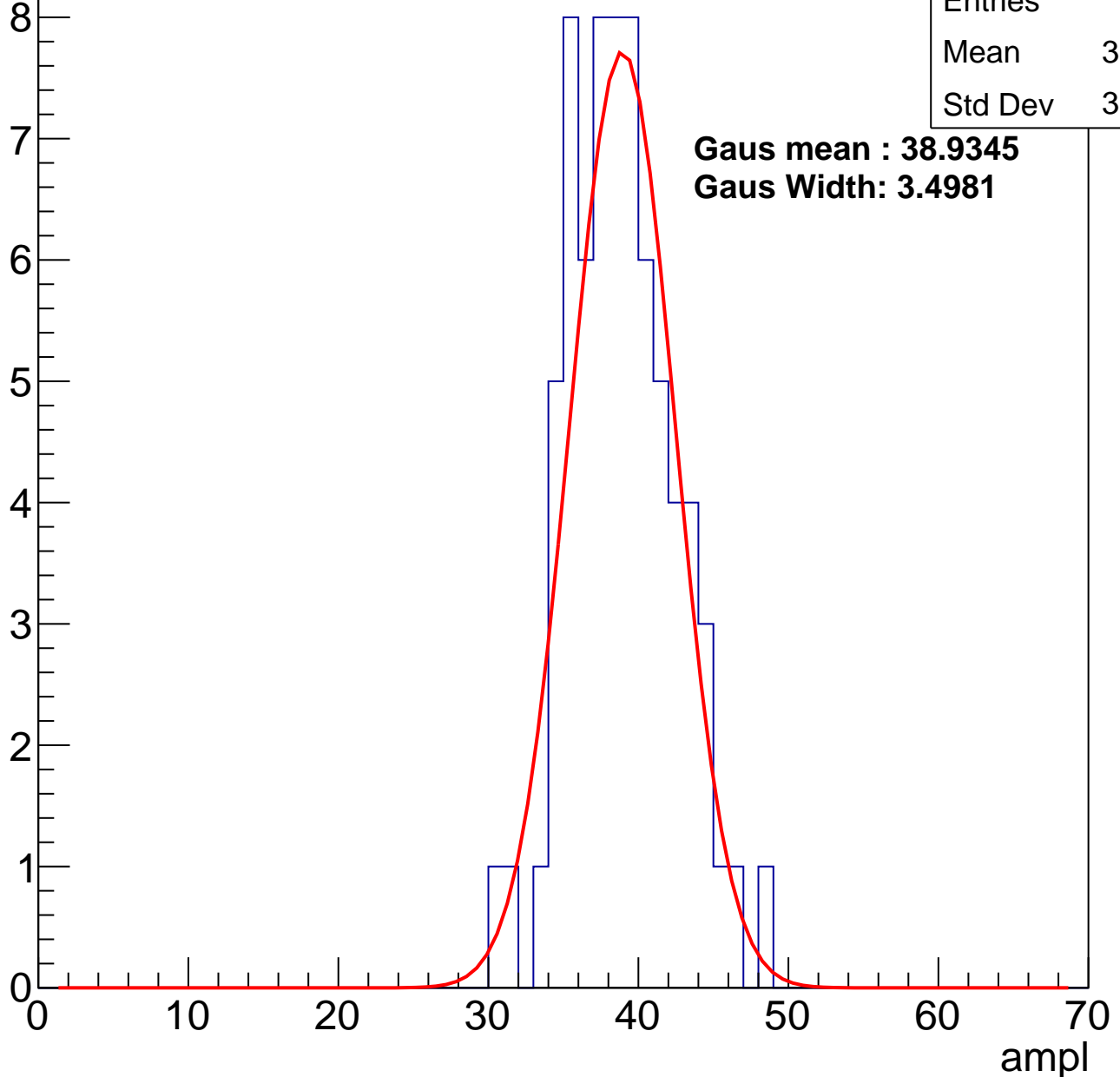
B1L101S, U21-ch16, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	38.42
Std Dev	3.503

Gaus mean : 38.9345
Gaus Width: 3.4981



B1L101S, U21-ch16, adc2

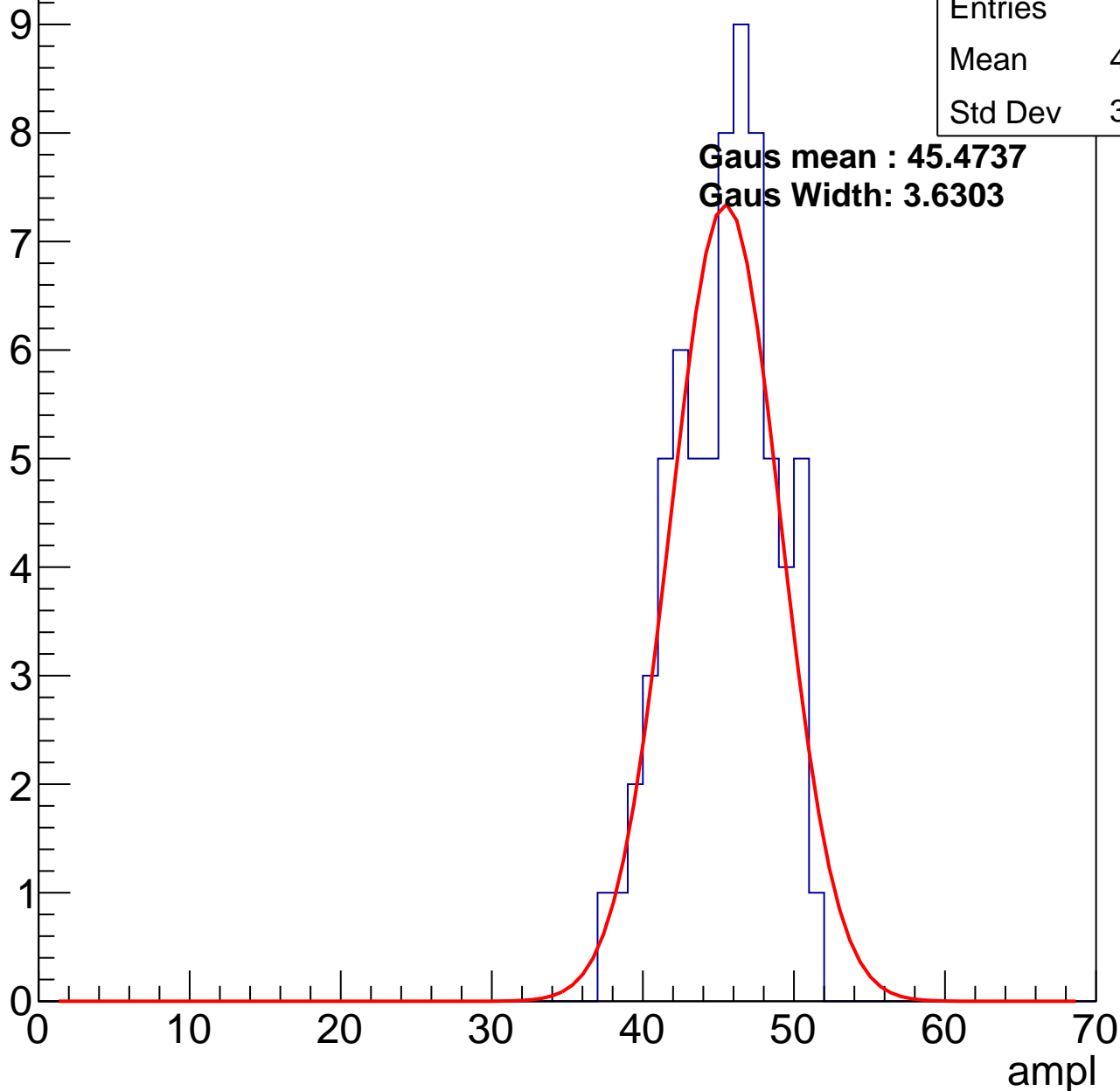
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.88
Std Dev	3.274

Gaus mean : 45.4737

Gaus Width: 3.6303

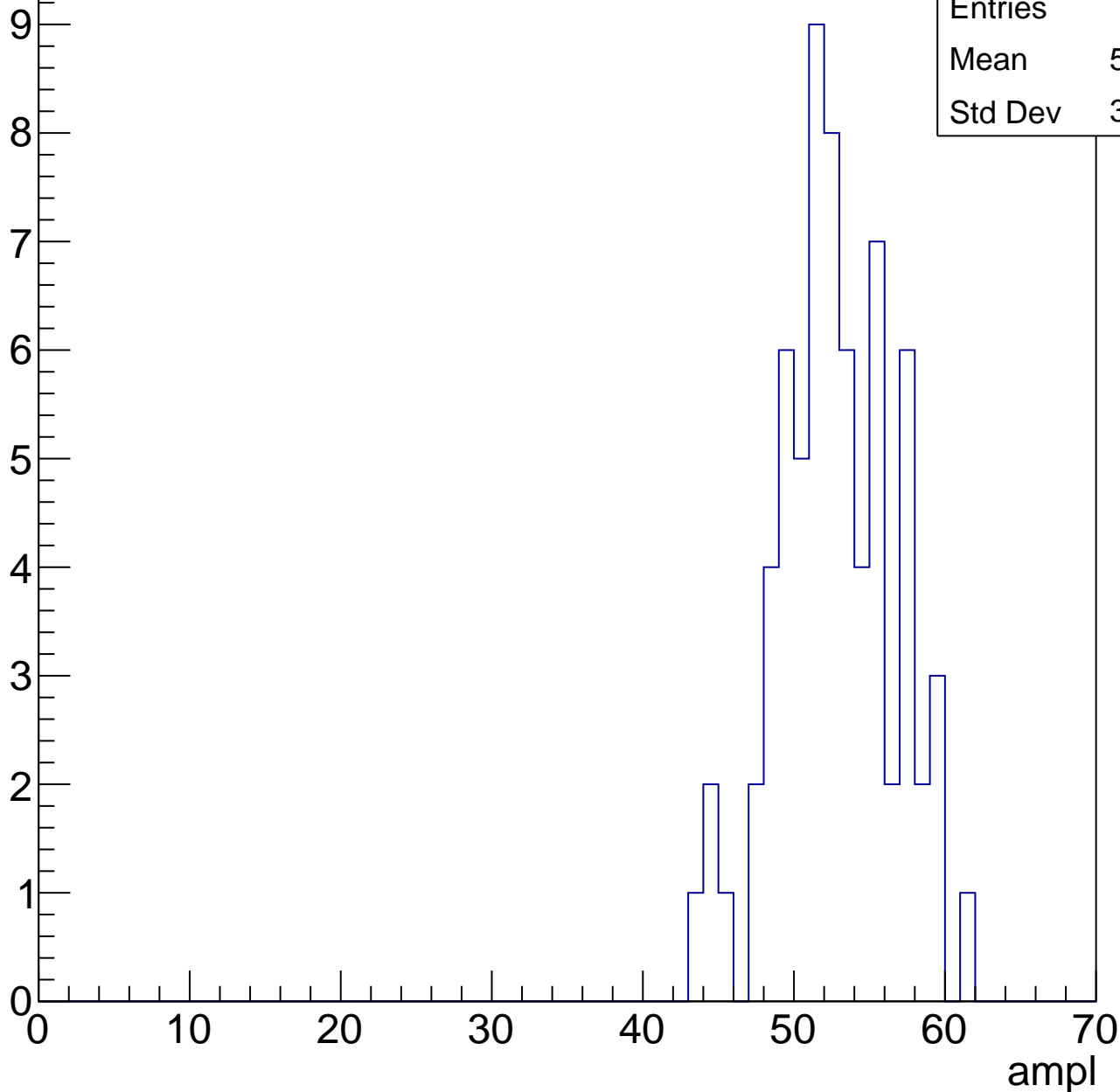


B1L101S, U21-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

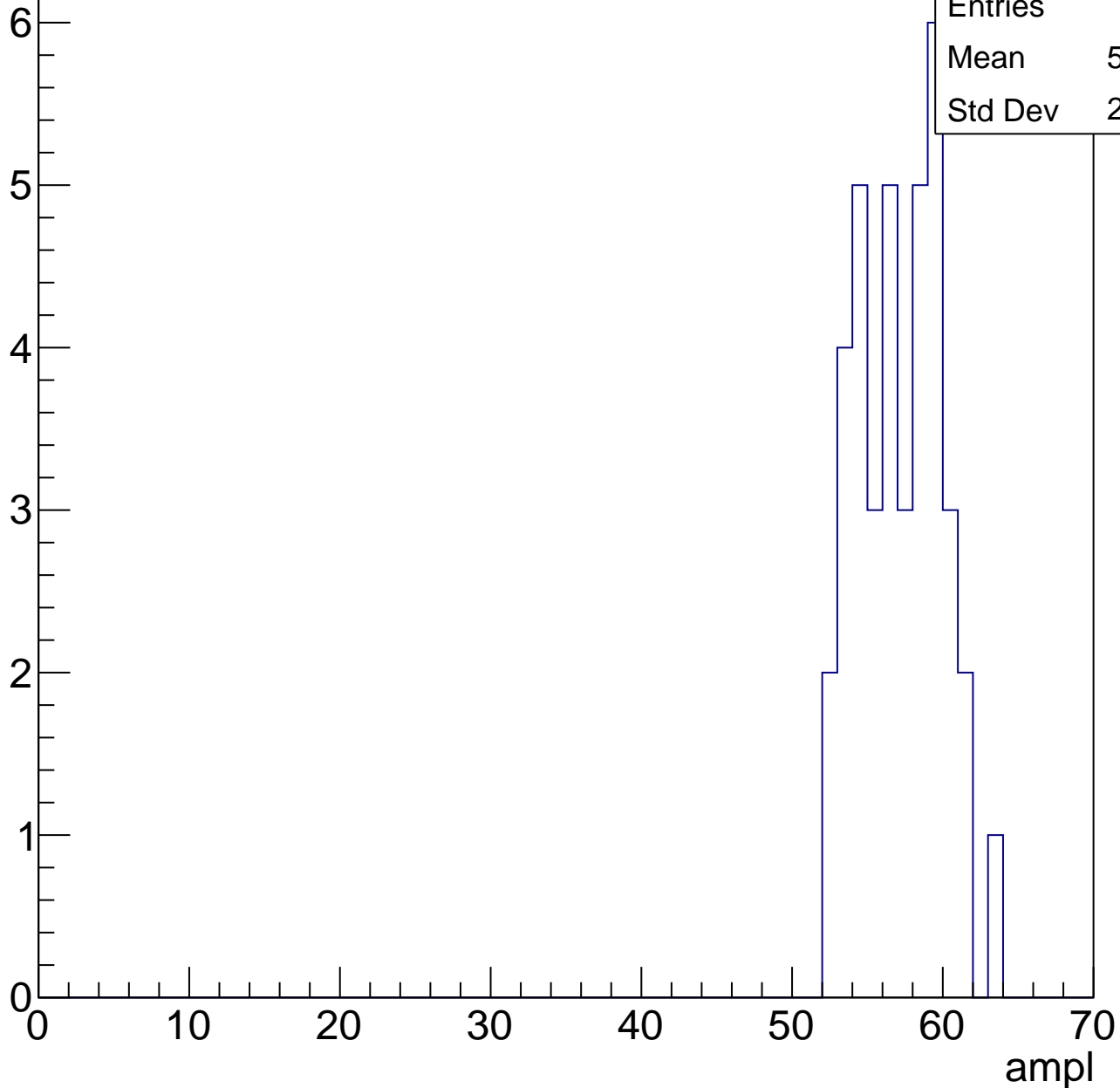
Entries	69
Mean	52.29
Std Dev	3.838



B1L101S, U21-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

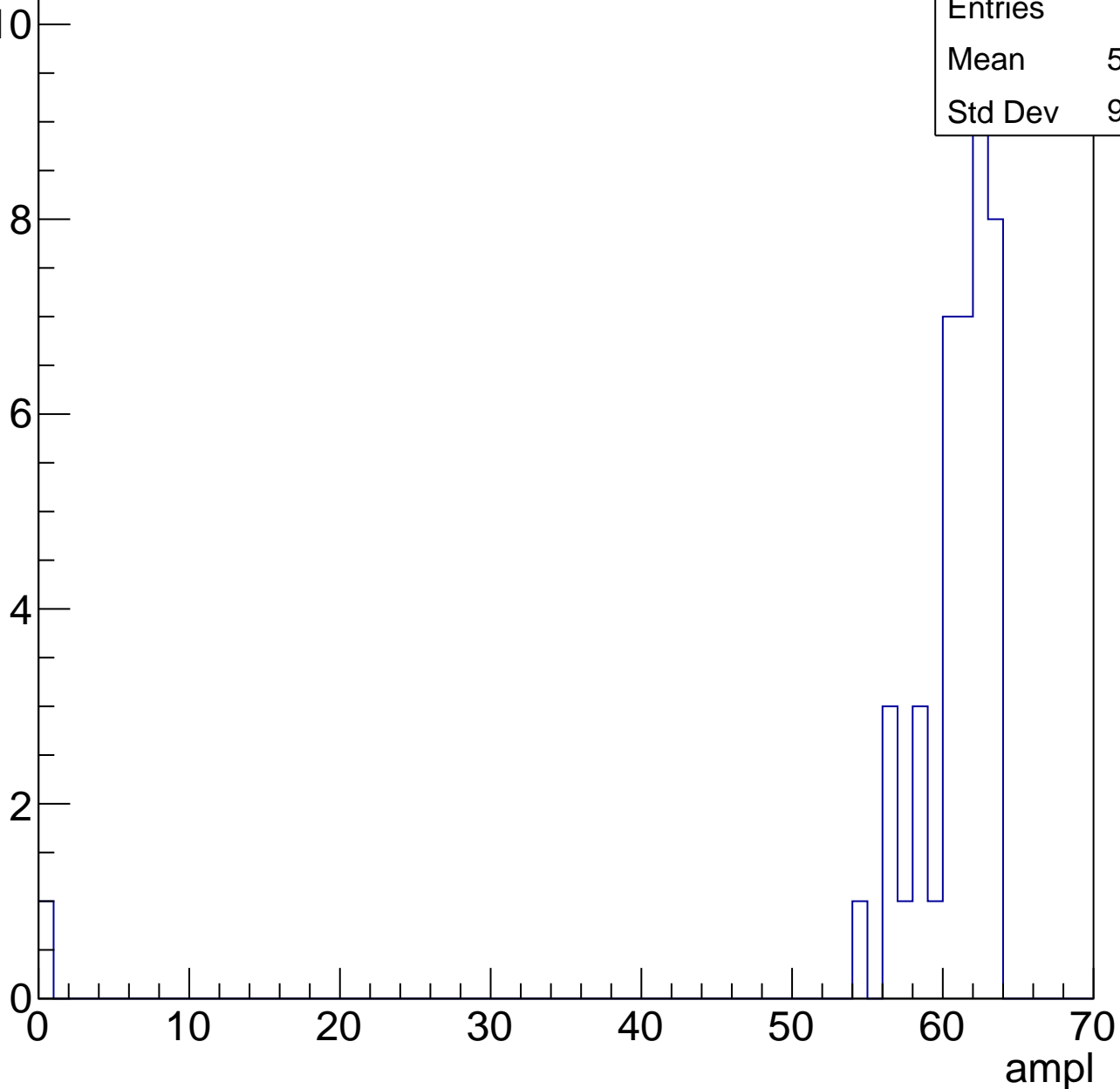


B1L101S, U21-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.12
Std Dev	9.502



B1L101S, U21-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch16, adc7

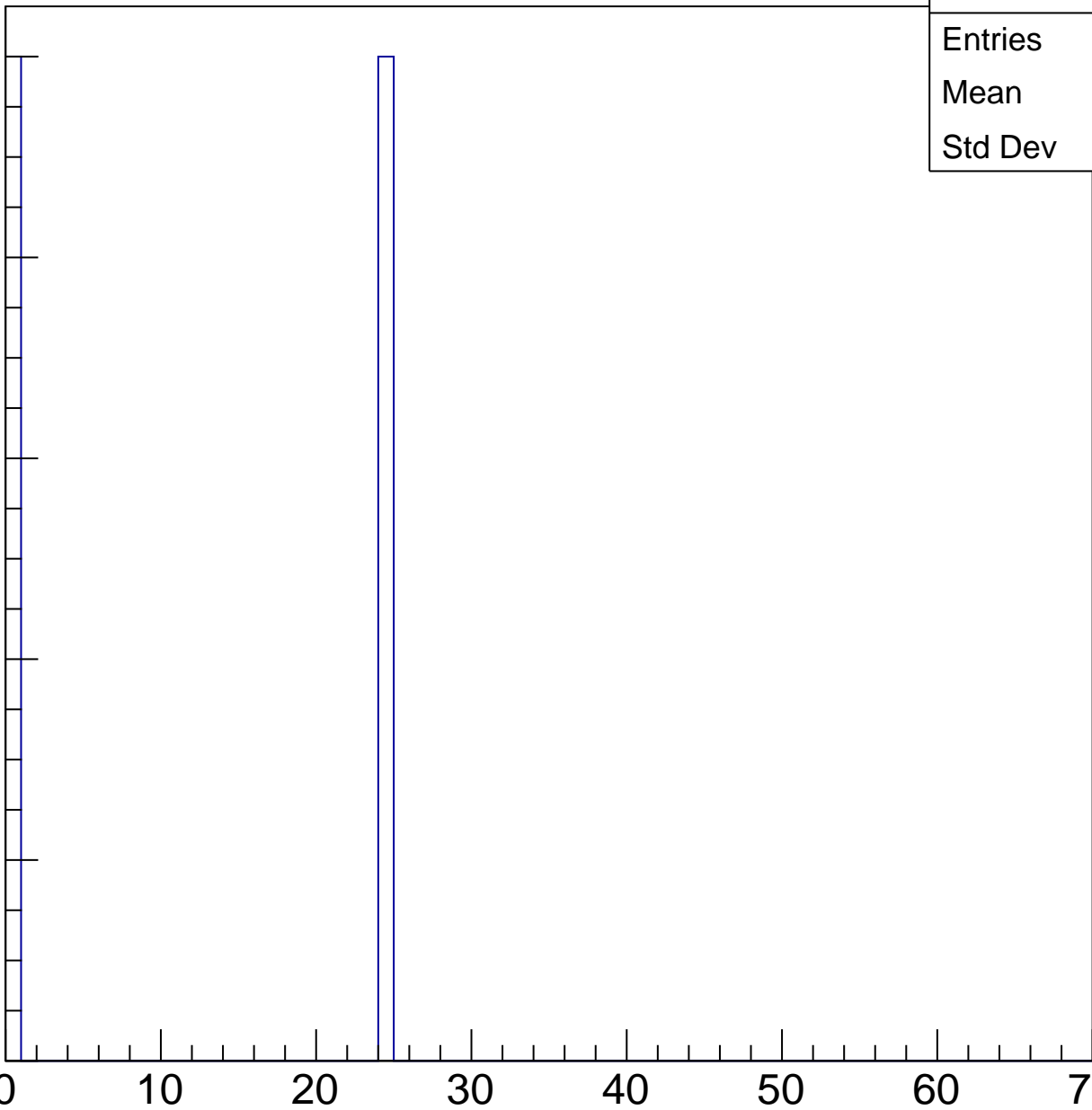
calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B1L101S, U21-ch17, adc0

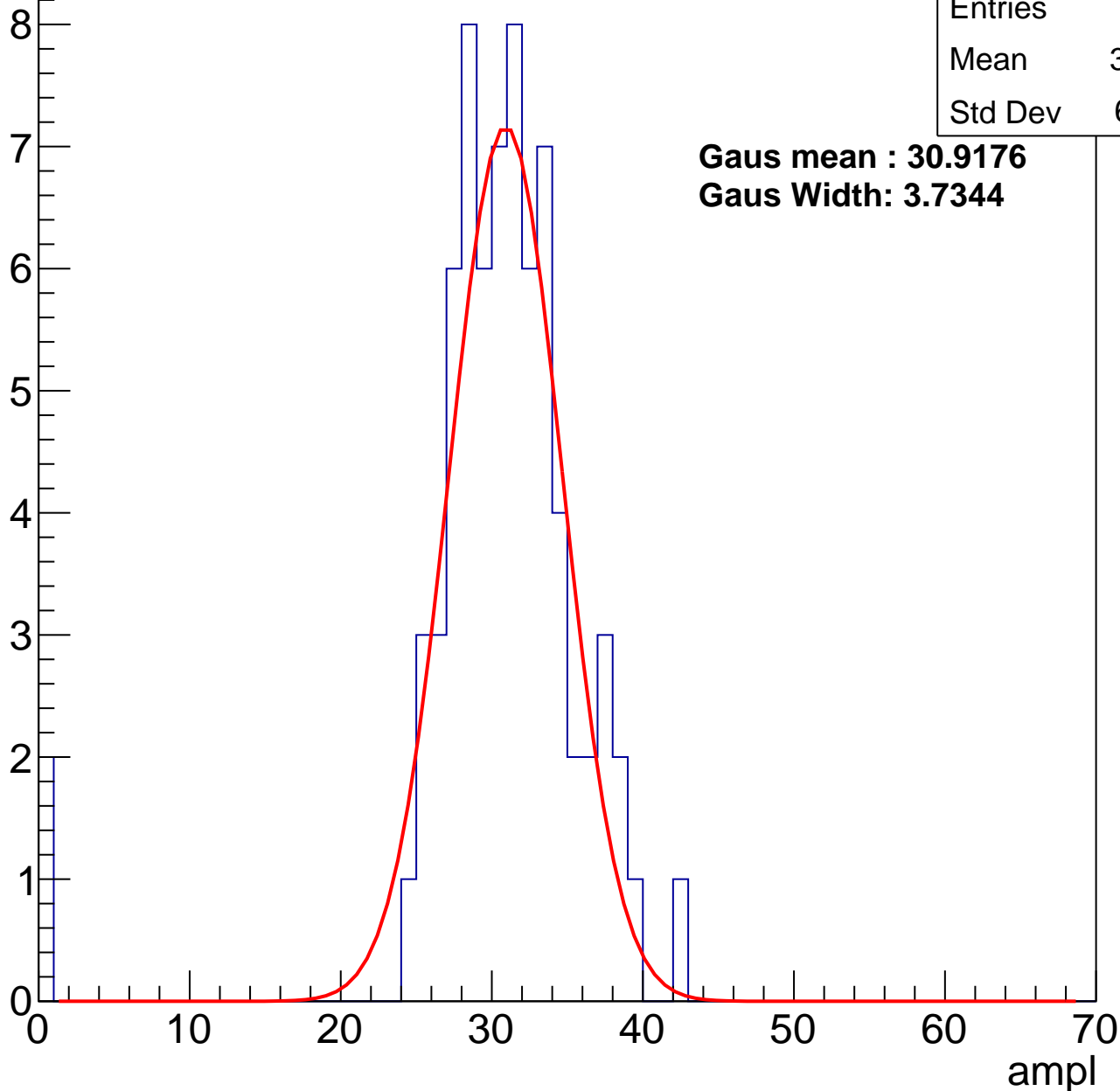
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.06
Std Dev	6.271

Gaus mean : 30.9176

Gaus Width: 3.7344



B1L101S, U21-ch17, adc1

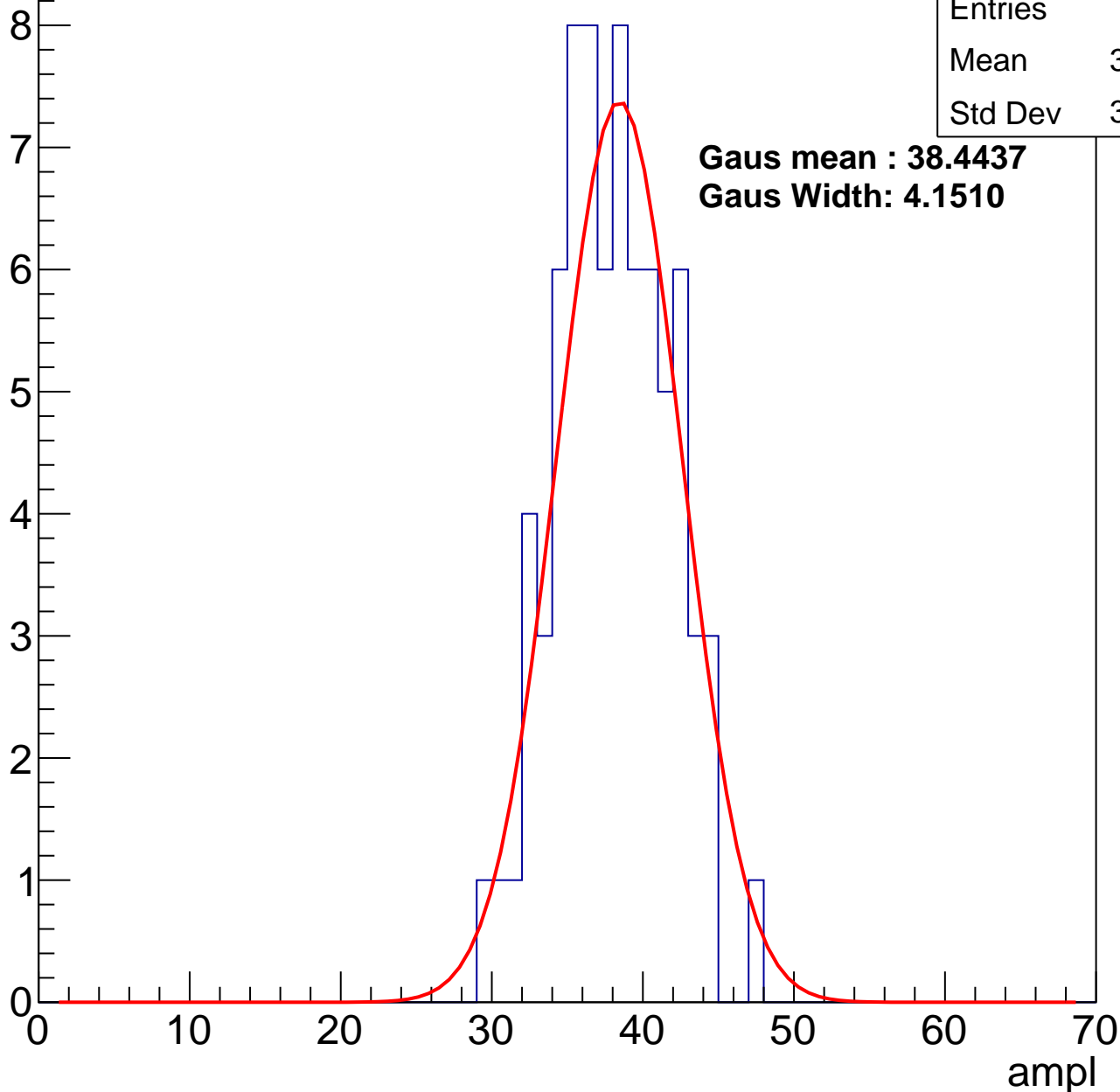
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.55
Std Dev	3.697

Gaus mean : 38.4437

Gaus Width: 4.1510



B1L101S, U21-ch17, adc2

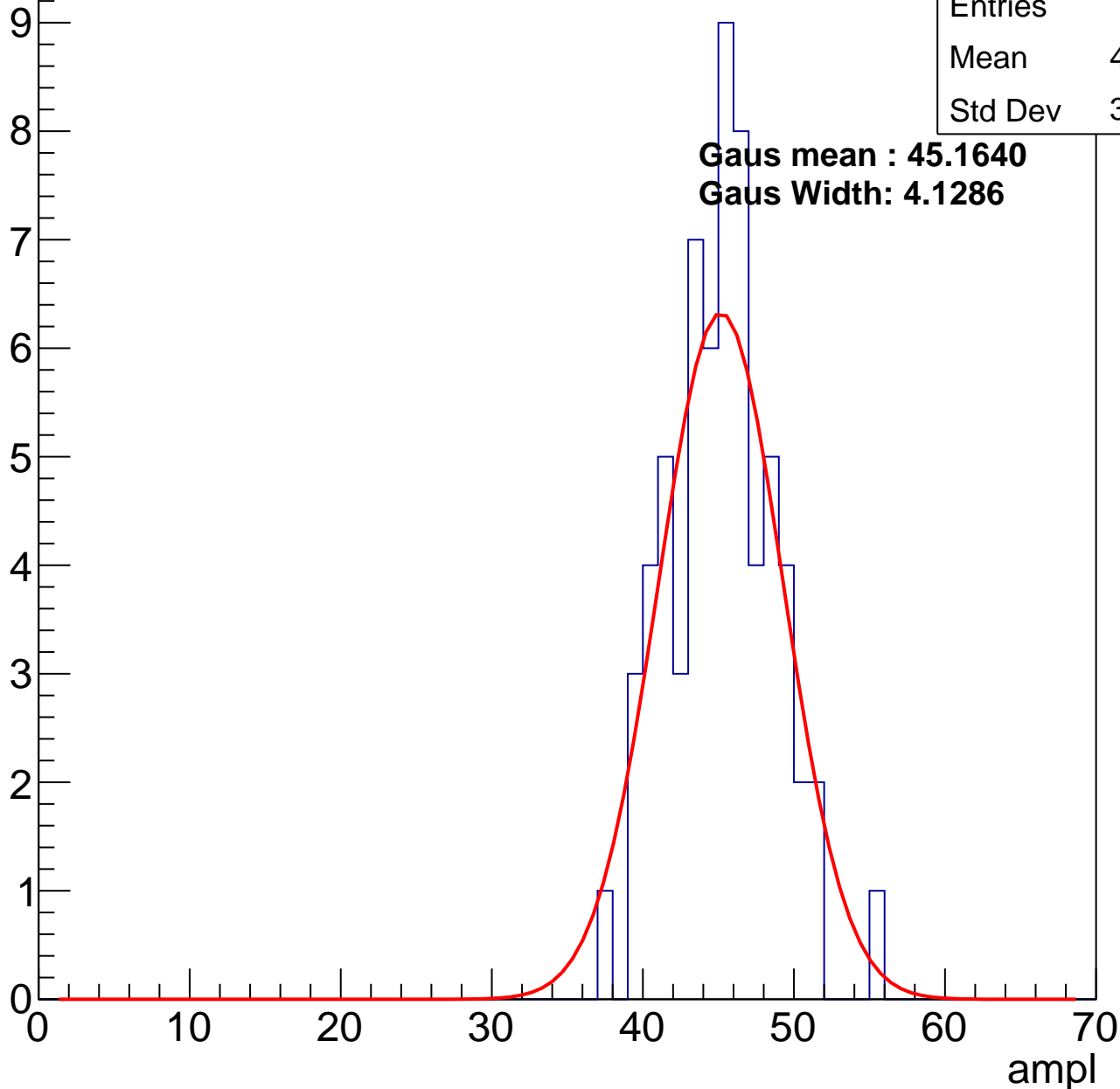
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	44.75
Std Dev	3.455

Gaus mean : 45.1640

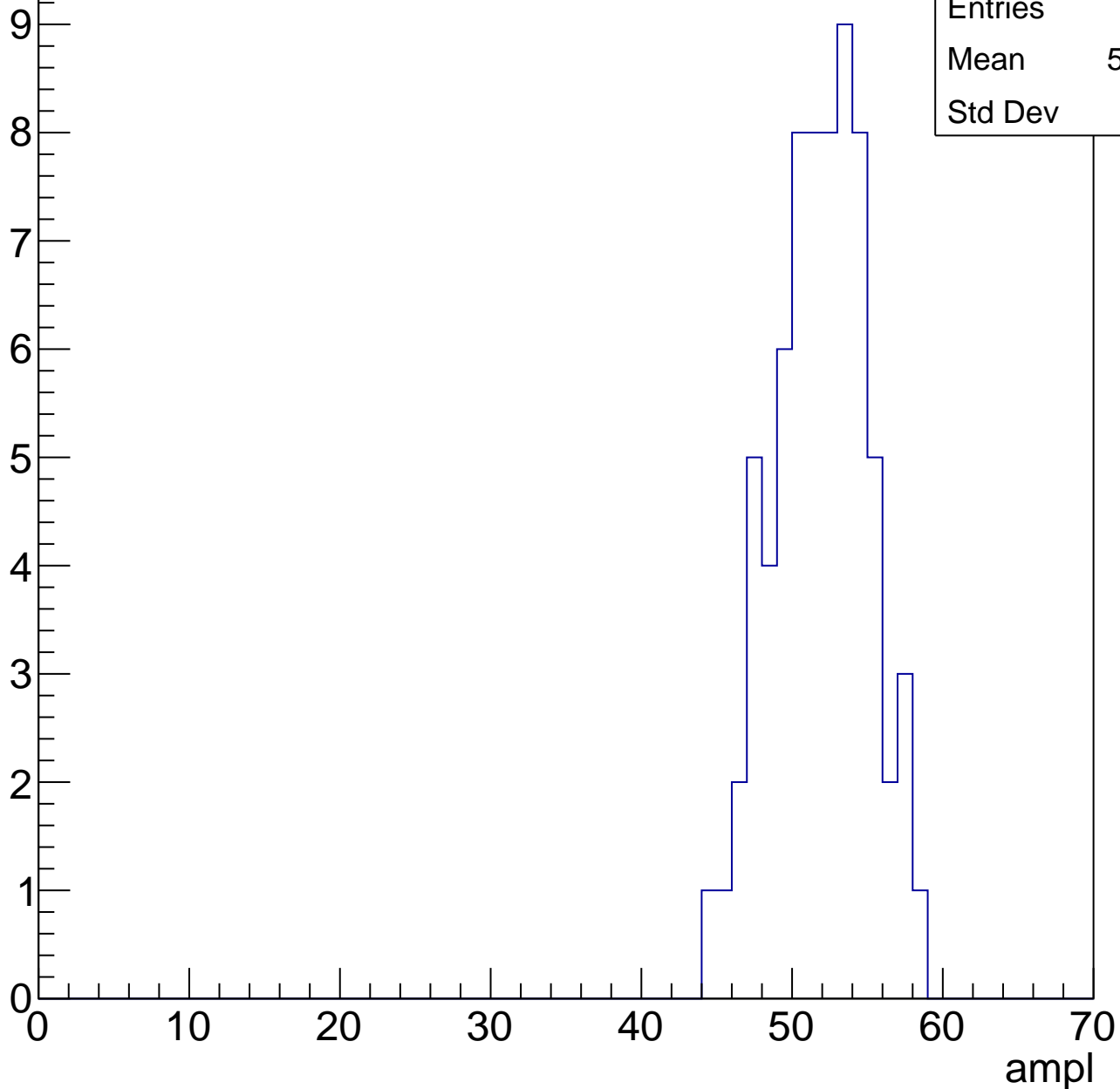
Gaus Width: 4.1286



B1L101S, U21-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

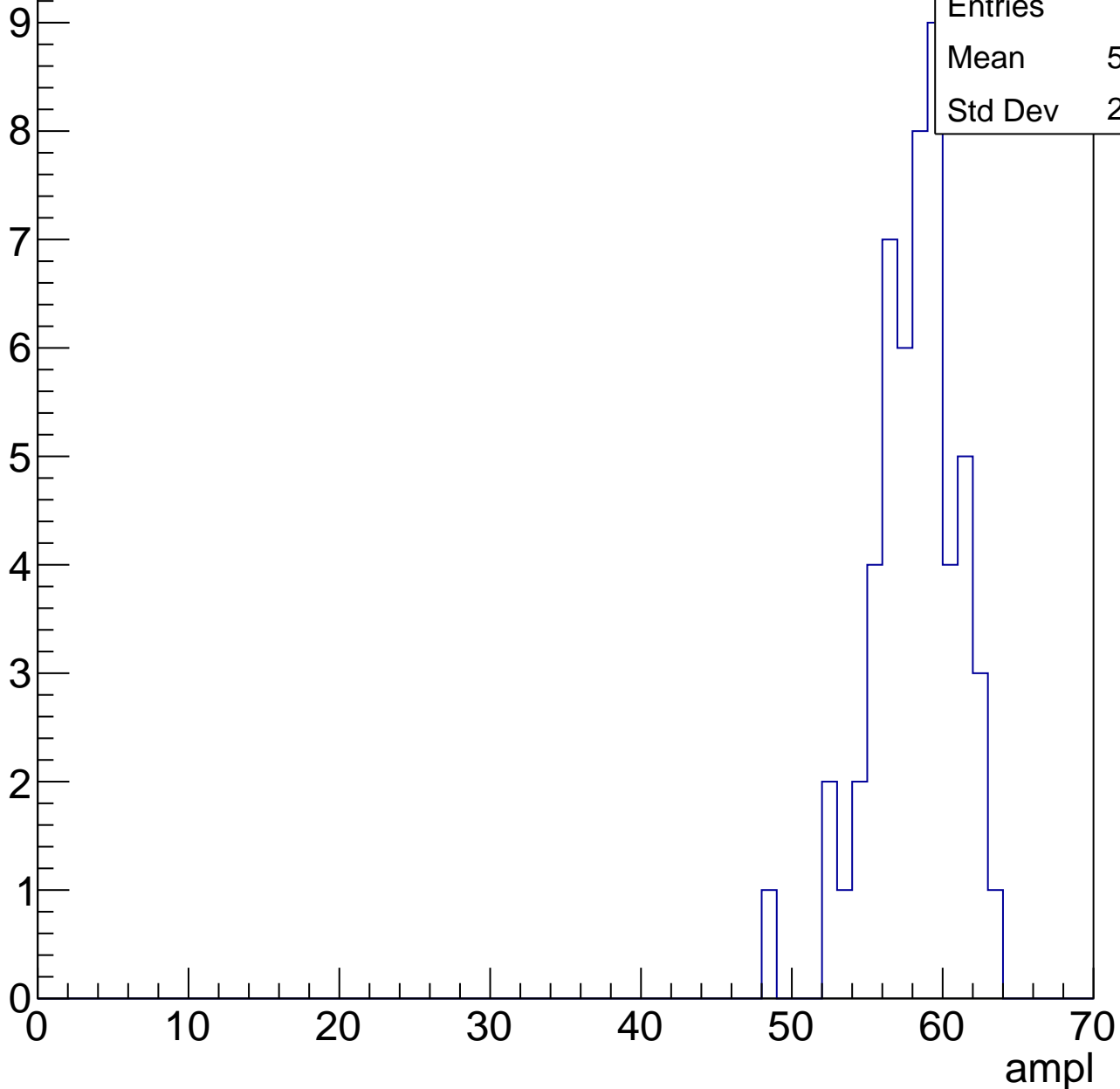


B1L101S, U21-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.66
Std Dev	2.868

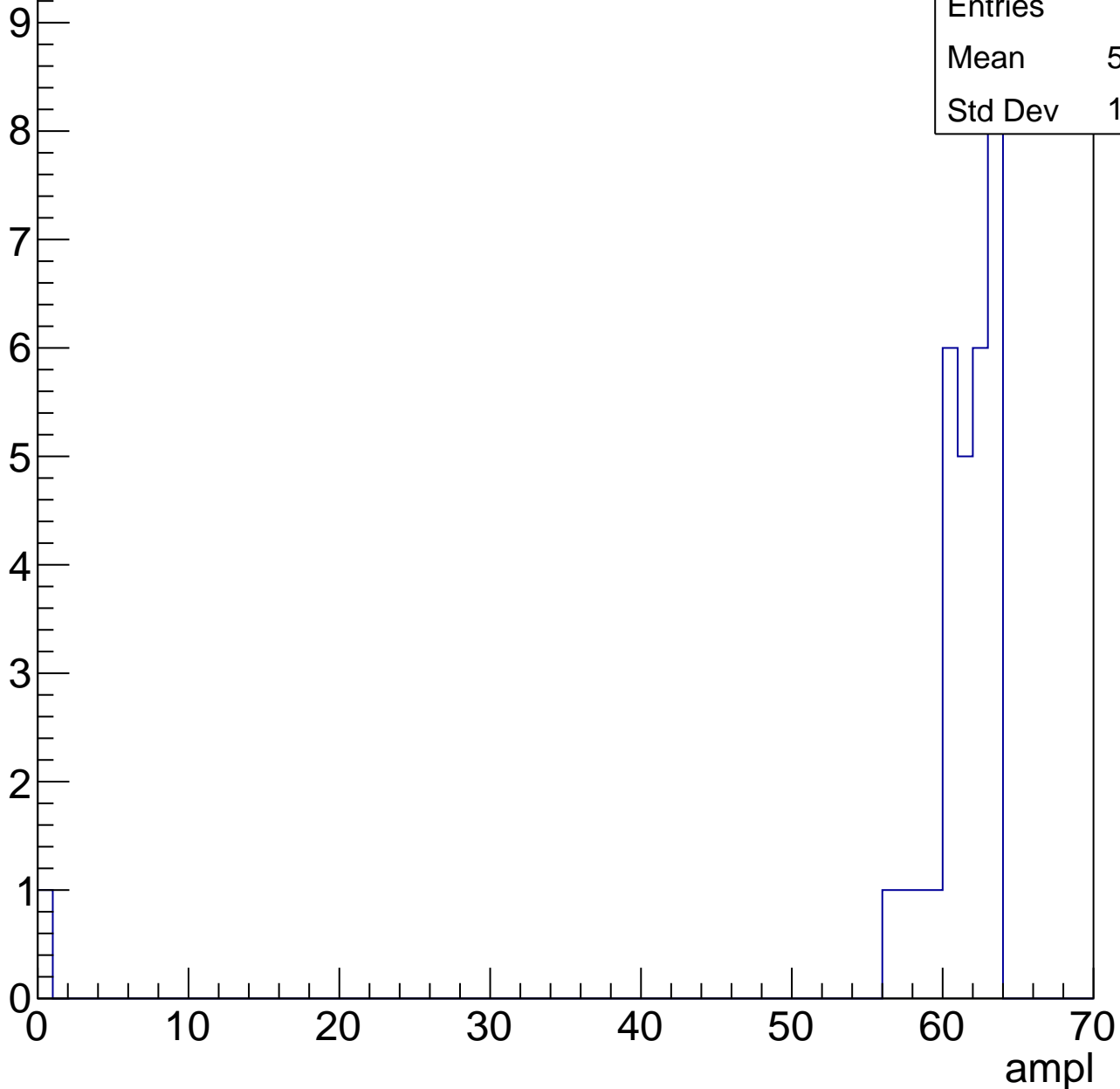


B1L101S, U21-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	59.16
Std Dev	10.95



B1L101S, U21-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U21-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch18, adc0

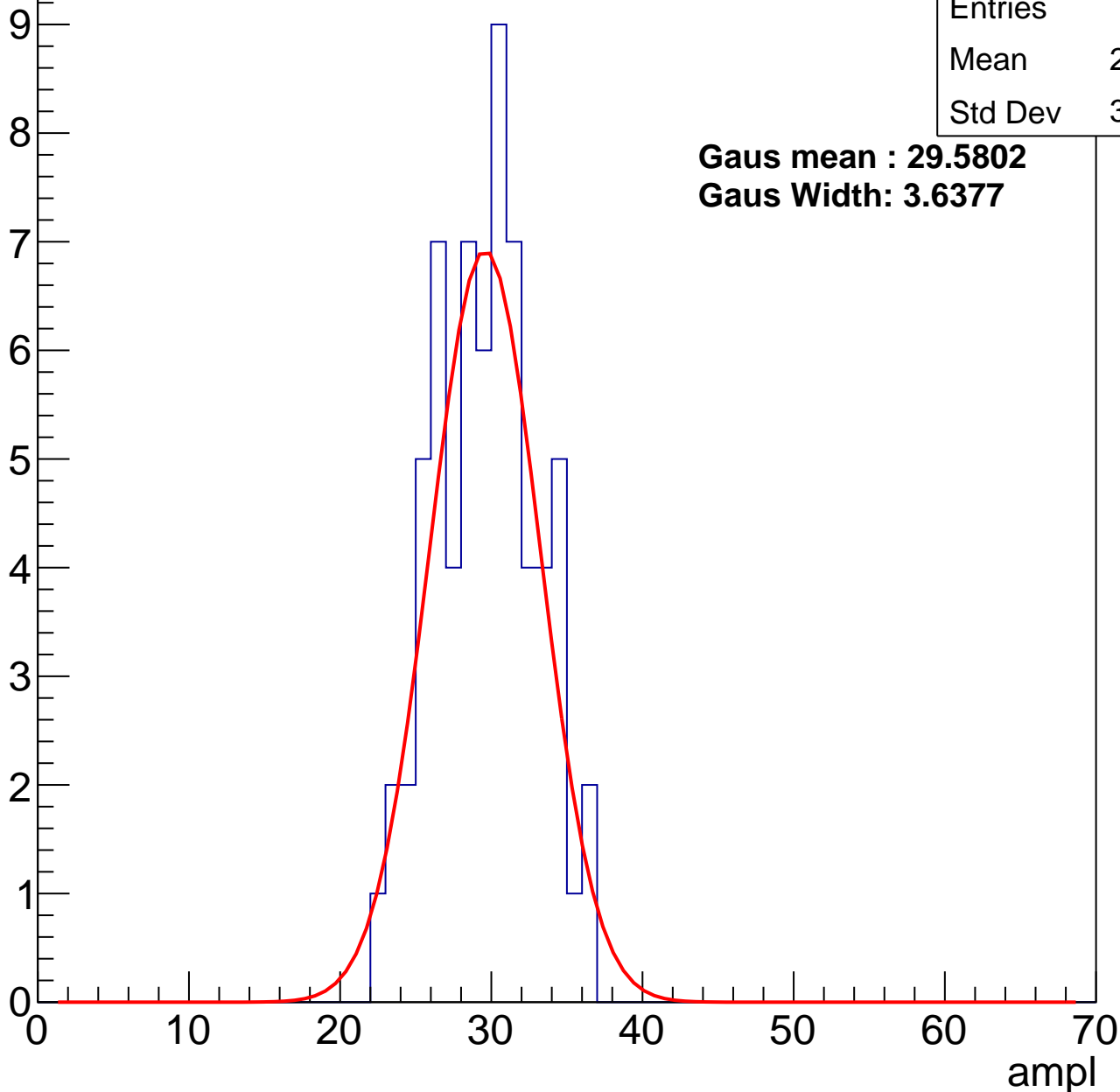
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.17
Std Dev	3.328

Gaus mean : 29.5802

Gaus Width: 3.6377



B1L101S, U21-ch18, adc1

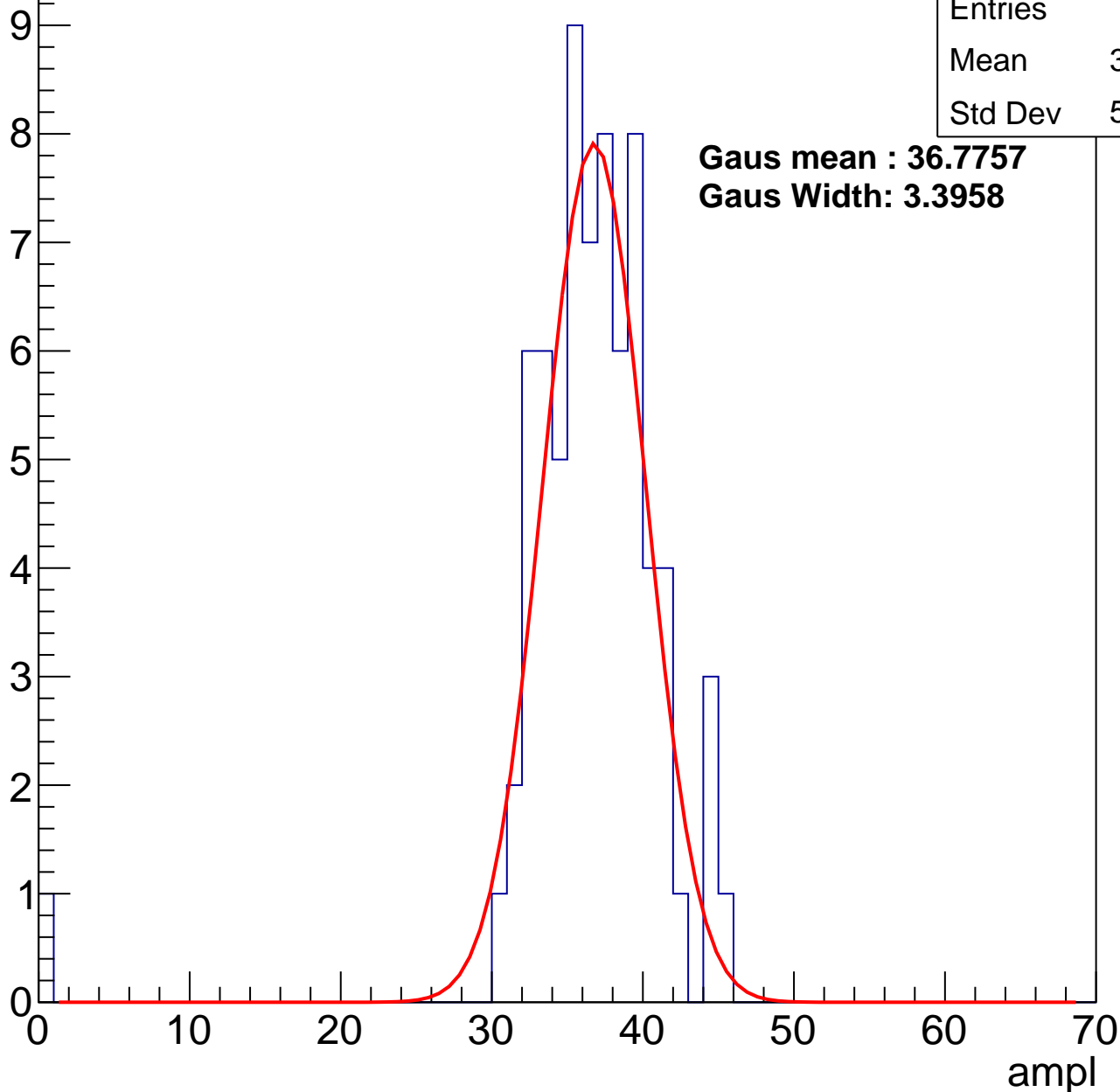
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.08
Std Dev	5.449

Gaus mean : 36.7757

Gaus Width: 3.3958



B1L101S, U21-ch18, adc2

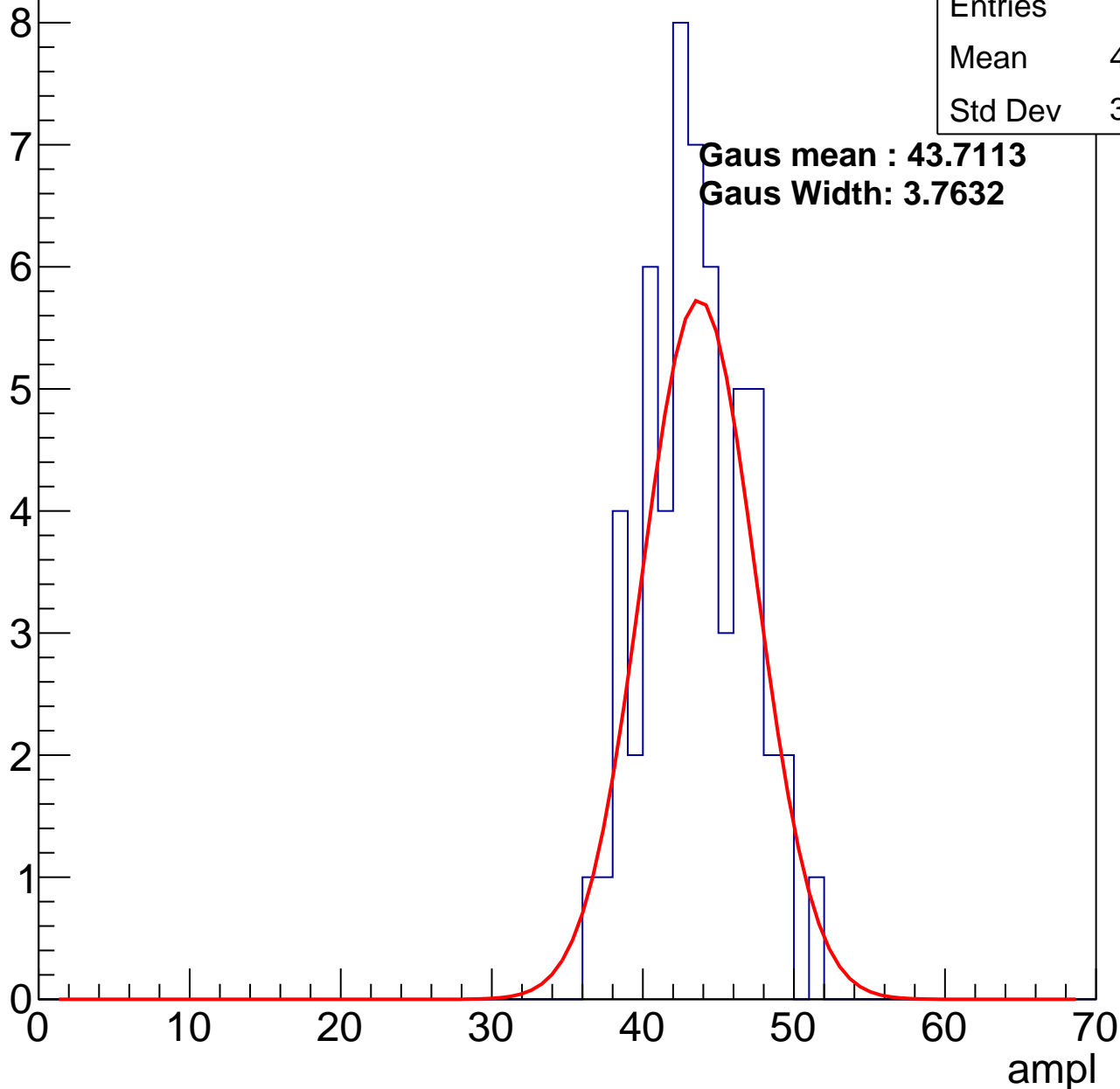
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.04
Std Dev	3.308

Gaus mean : 43.7113

Gaus Width: 3.7632

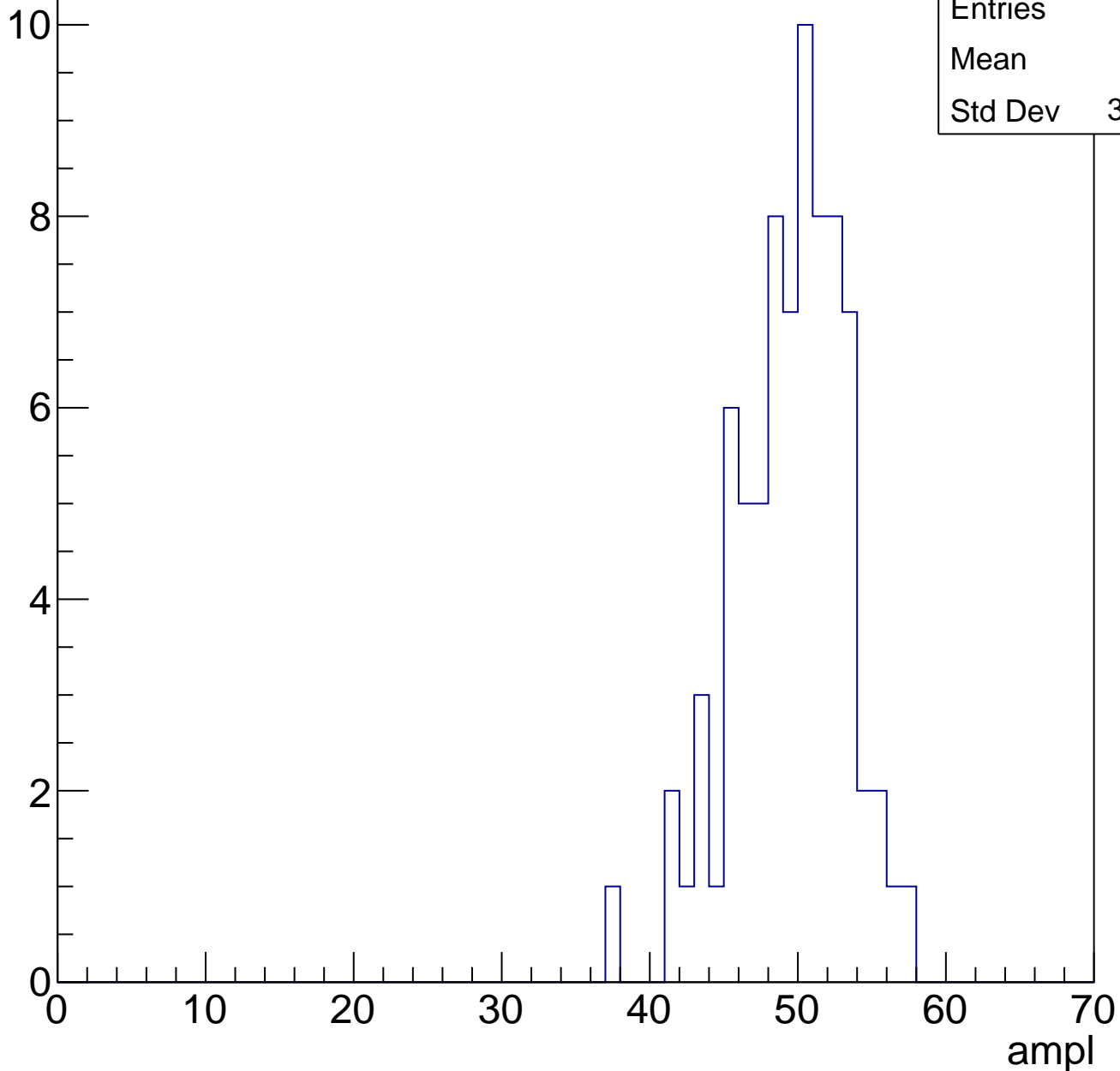


B1L101S, U21-ch18, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	49
Std Dev	3.728

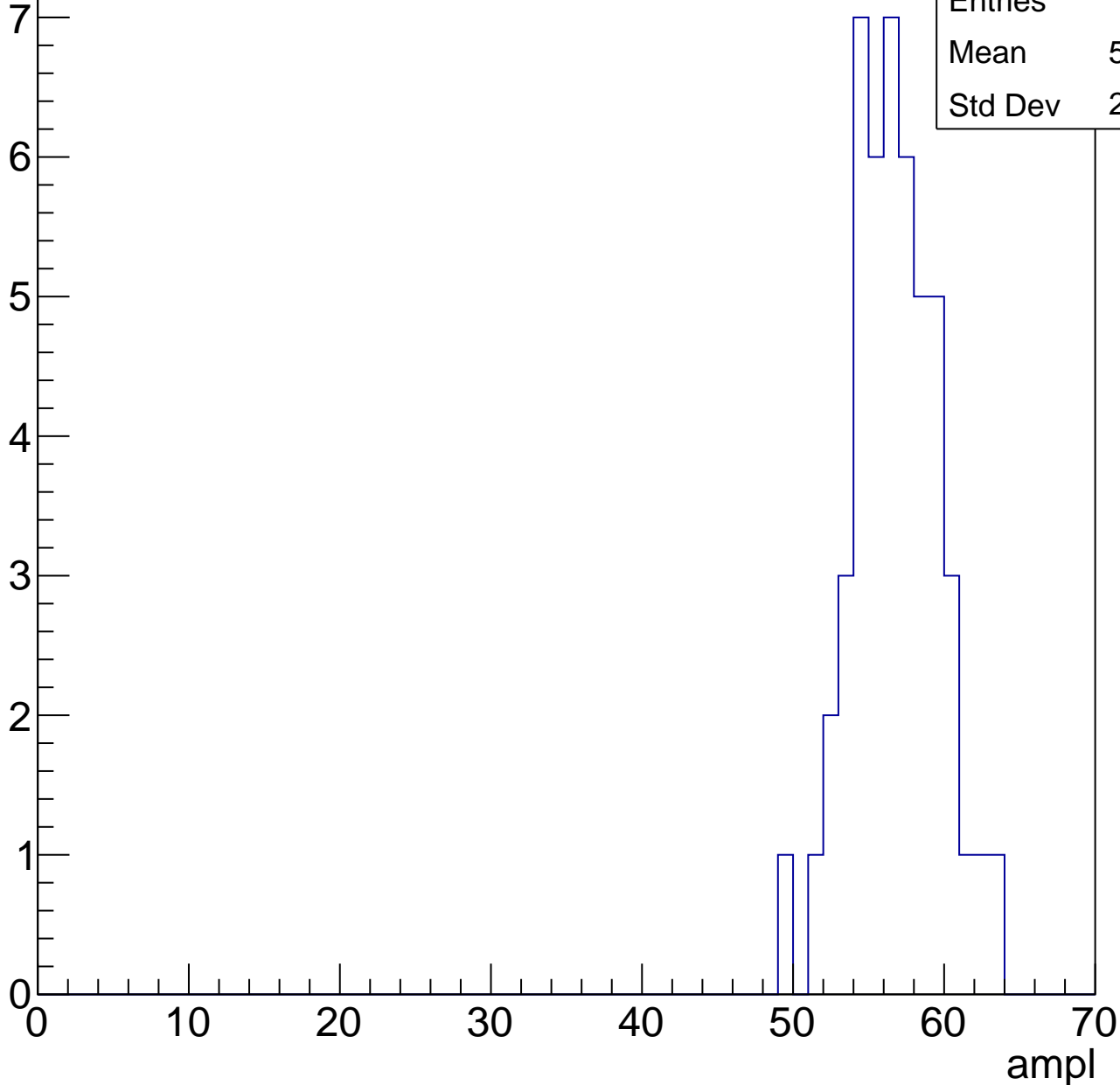


B1L101S, U21-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	56.24
Std Dev	2.832

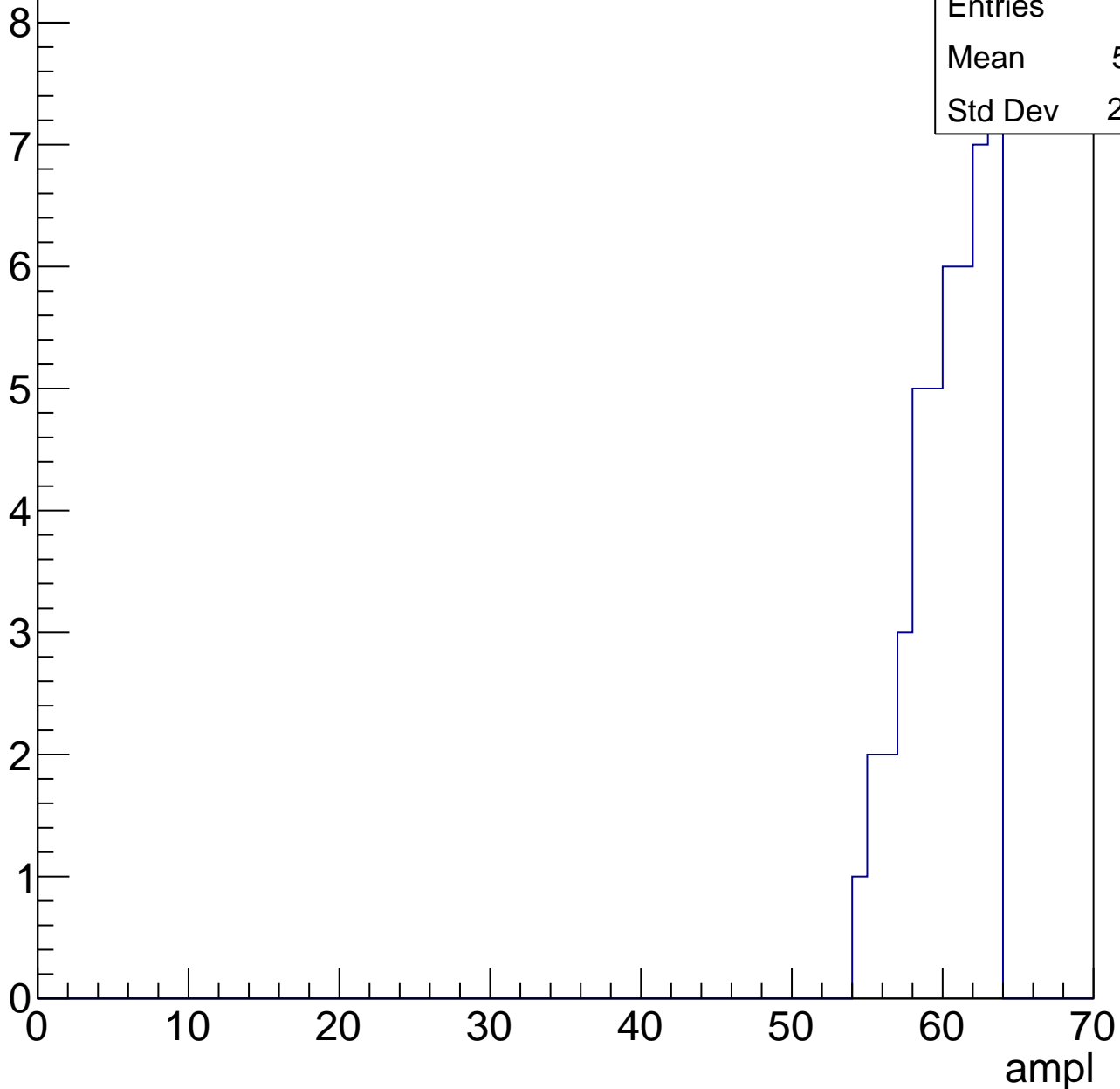


B1L101S, U21-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

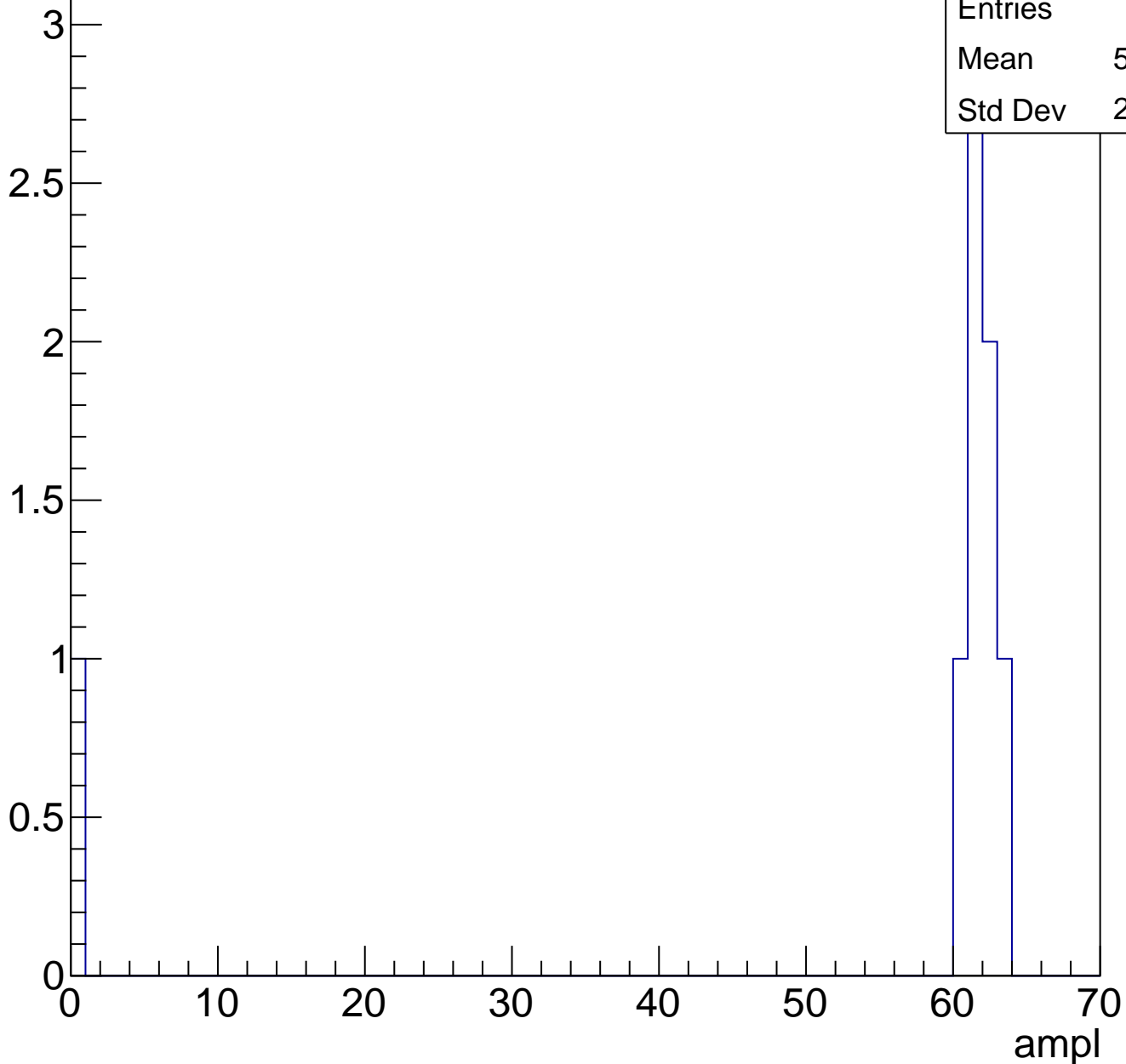
Entries	45
Mean	59.91
Std Dev	2.475



B1L101S, U21-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch19, adc0

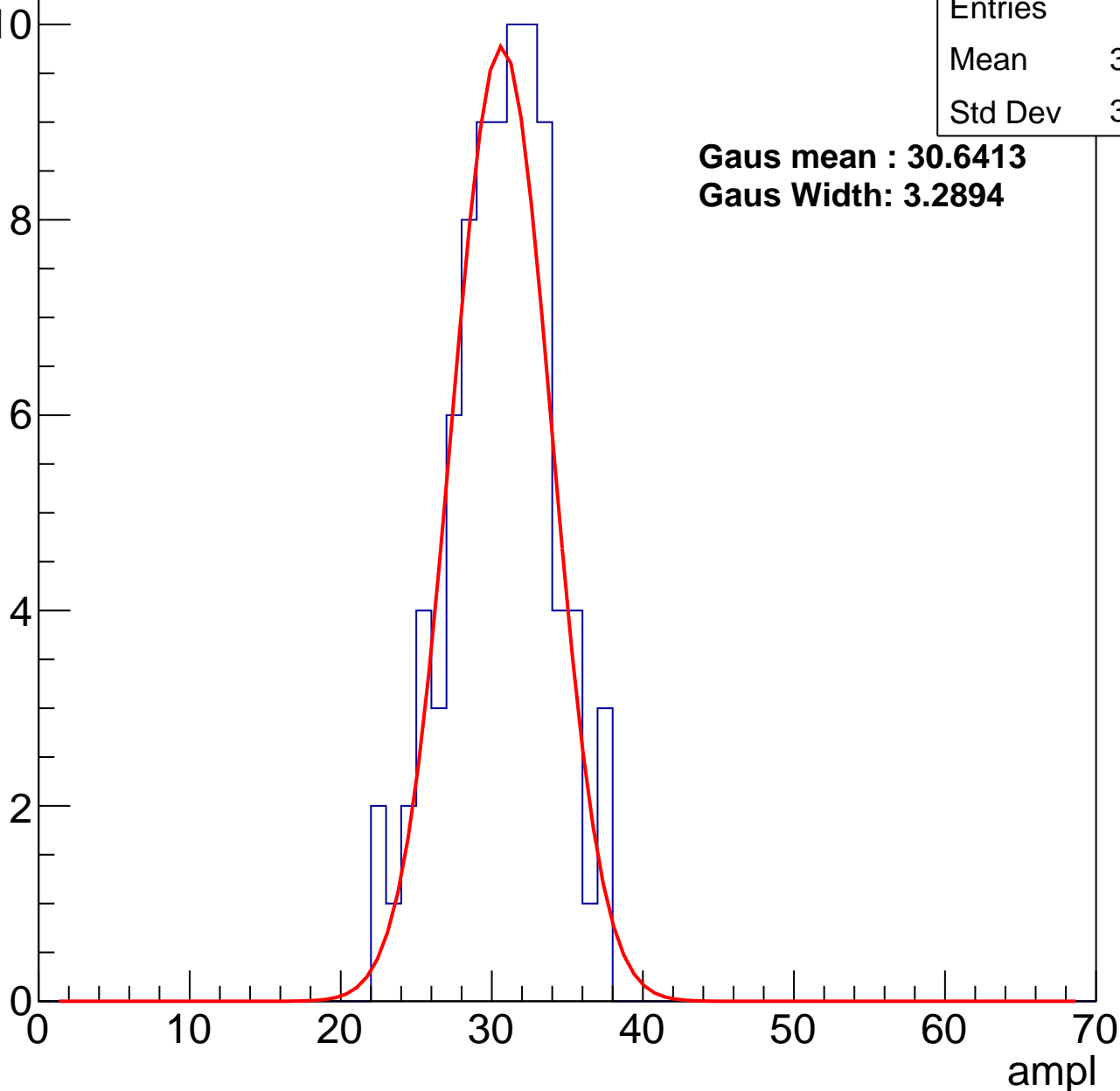
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	30.12
Std Dev	3.376

Gaus mean : 30.6413

Gaus Width: 3.2894



B1L101S, U21-ch19, adc1

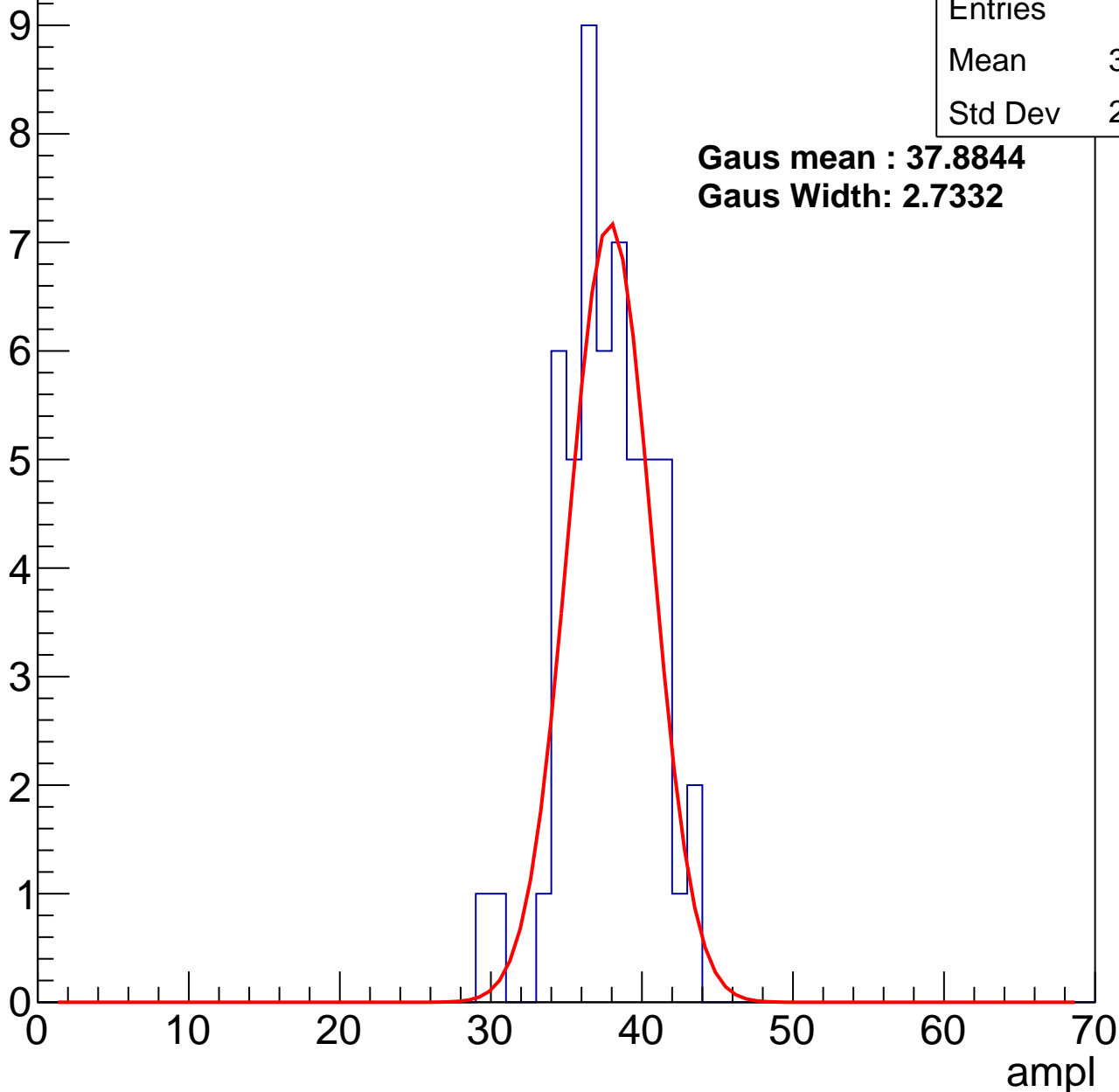
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	37.24
Std Dev	2.912

Gaus mean : 37.8844

Gaus Width: 2.7332



B1L101S, U21-ch19, adc2

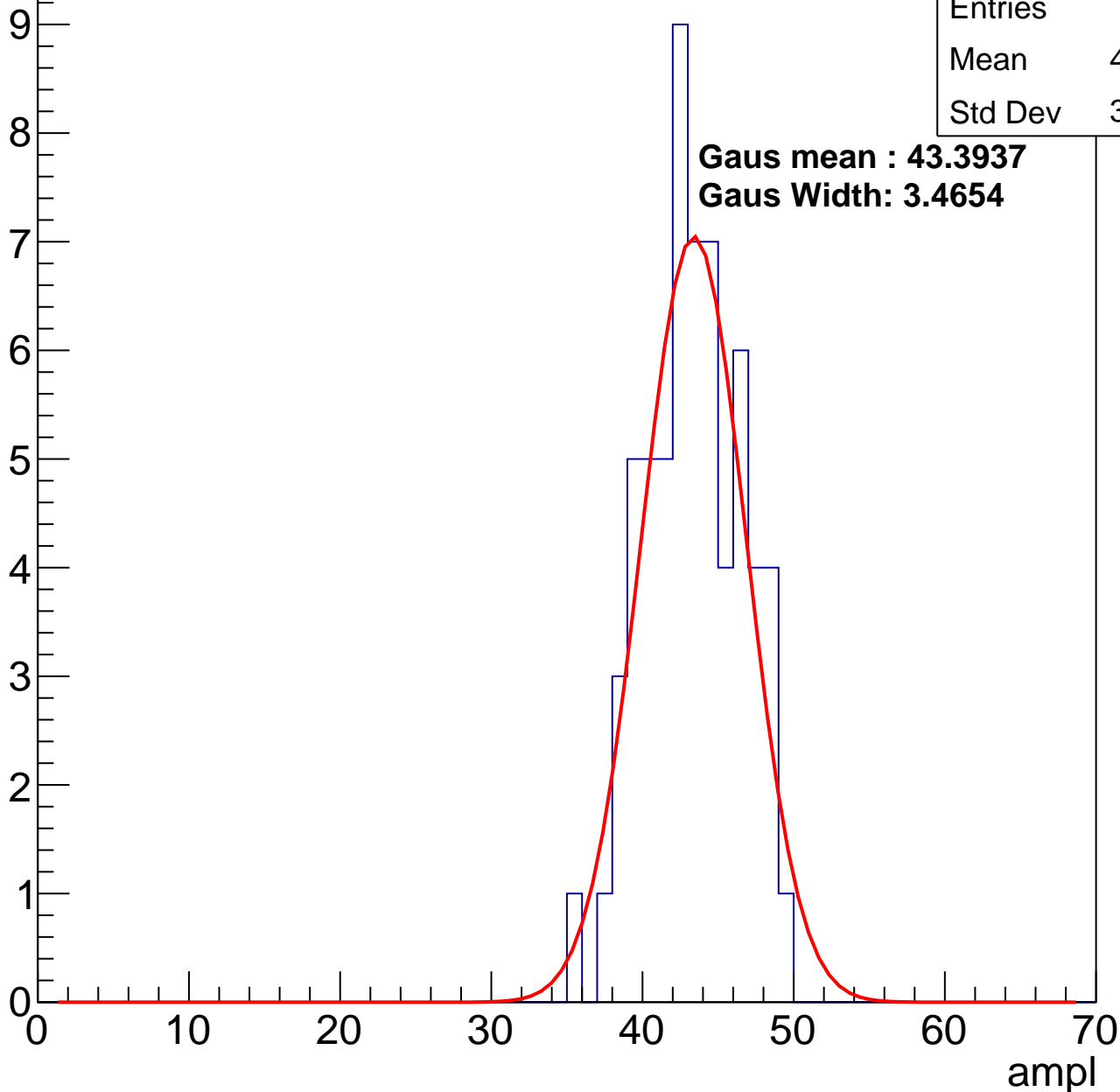
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.87
Std Dev	3.124

Gaus mean : 43.3937

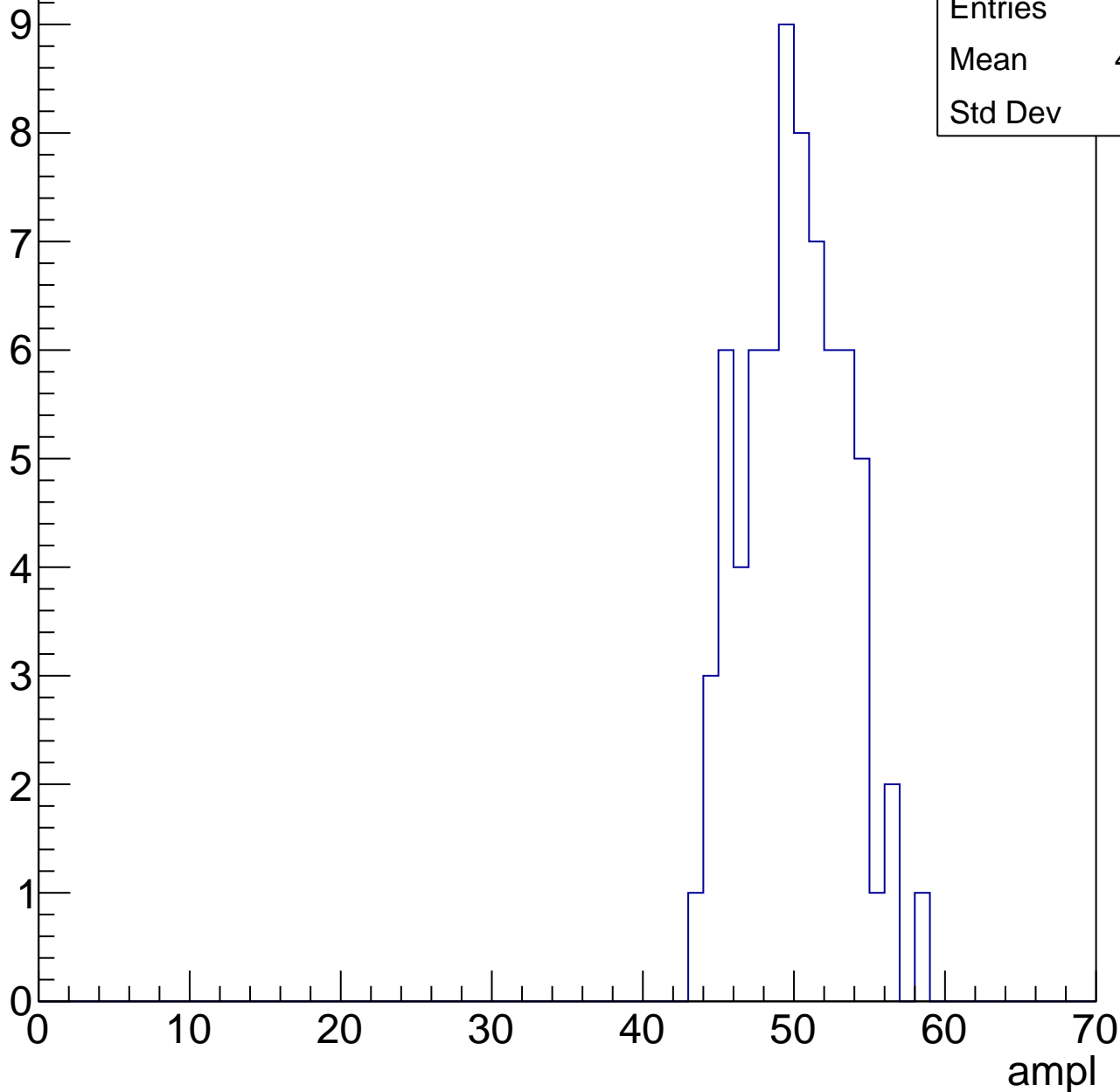
Gaus Width: 3.4654



B1L101S, U21-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

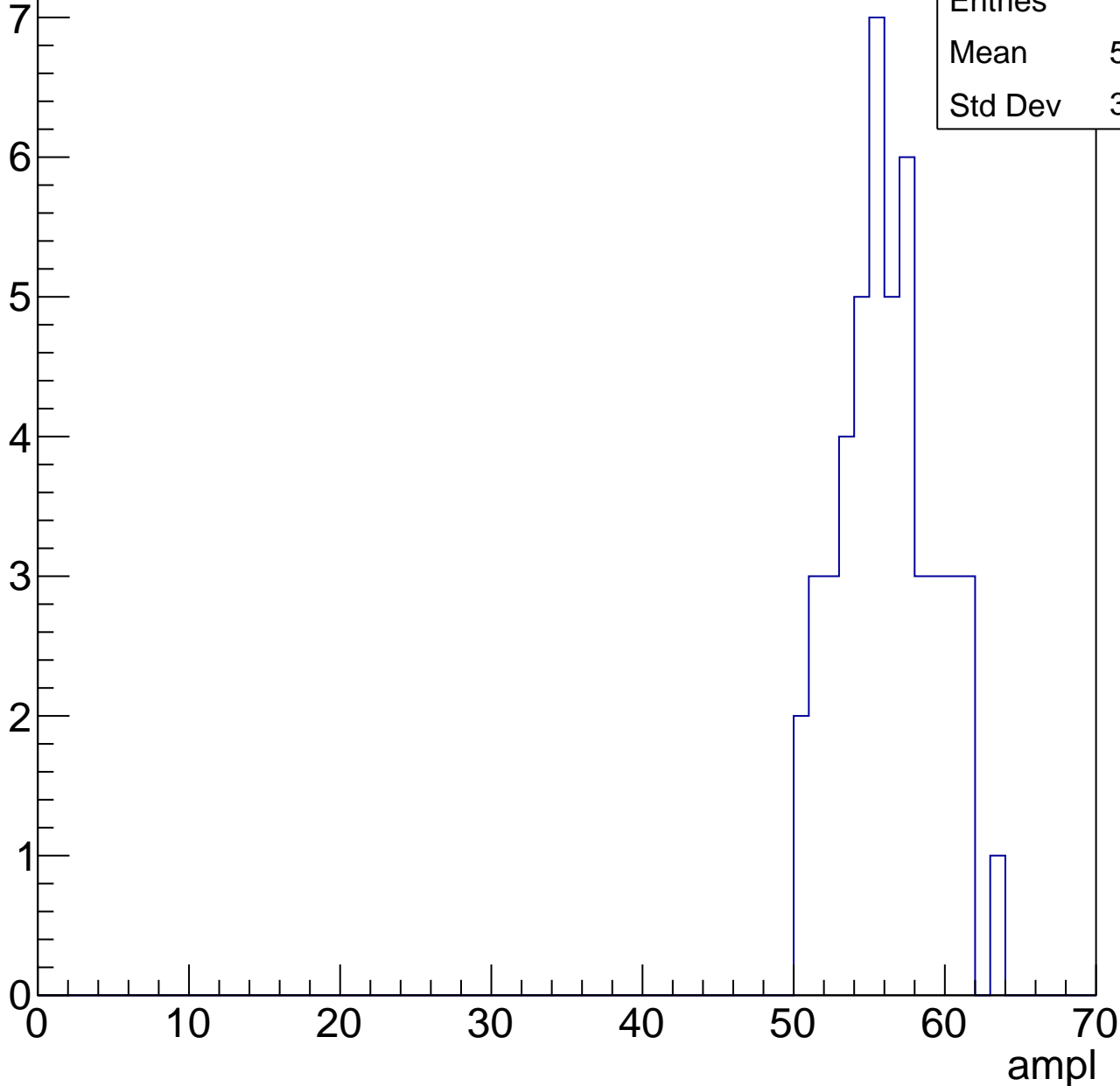


B1L101S, U21-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	55.73
Std Dev	3.134

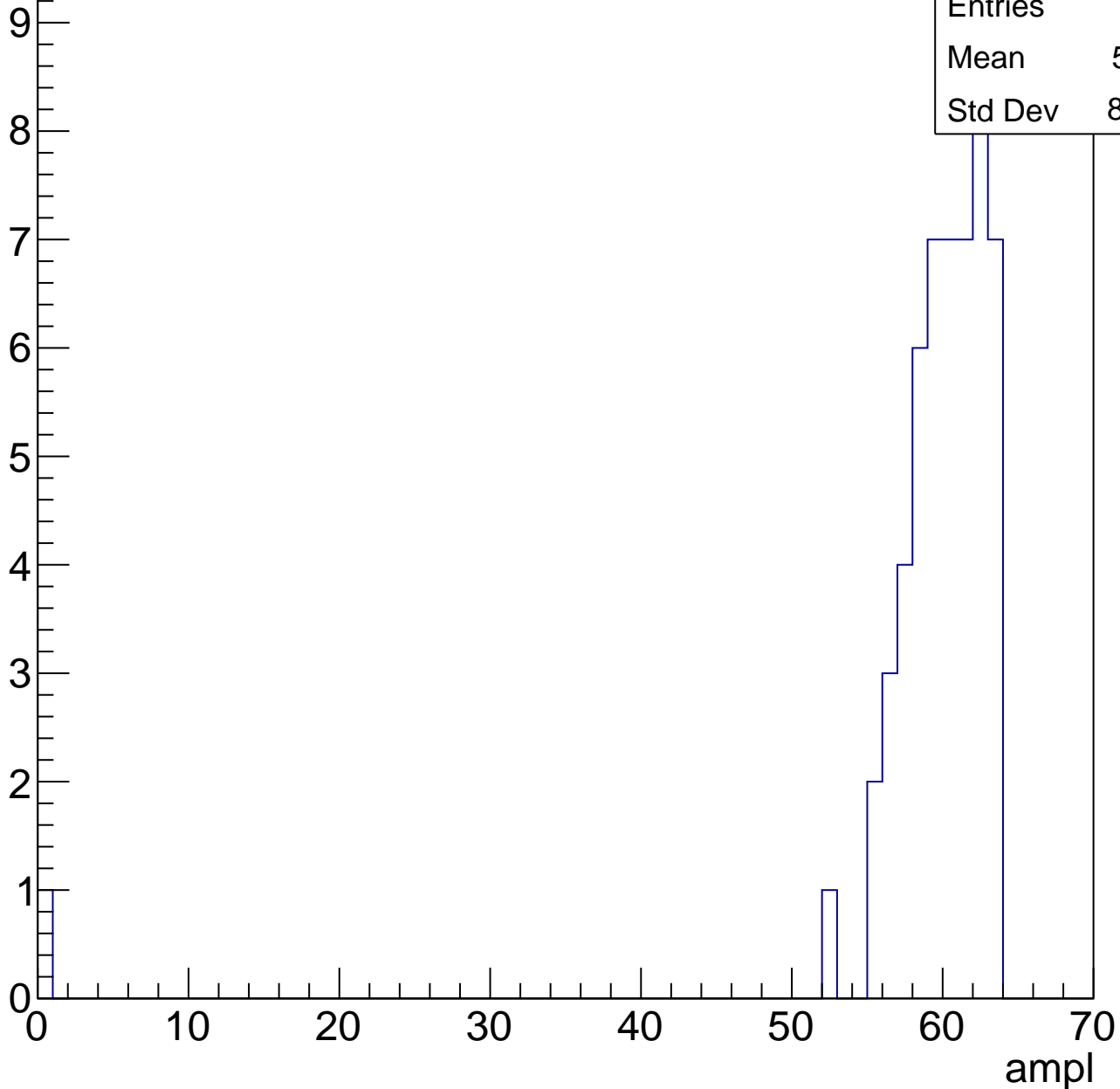


B1L101S, U21-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	58.61
Std Dev	8.423



B1L101S, U21-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch20, adc0

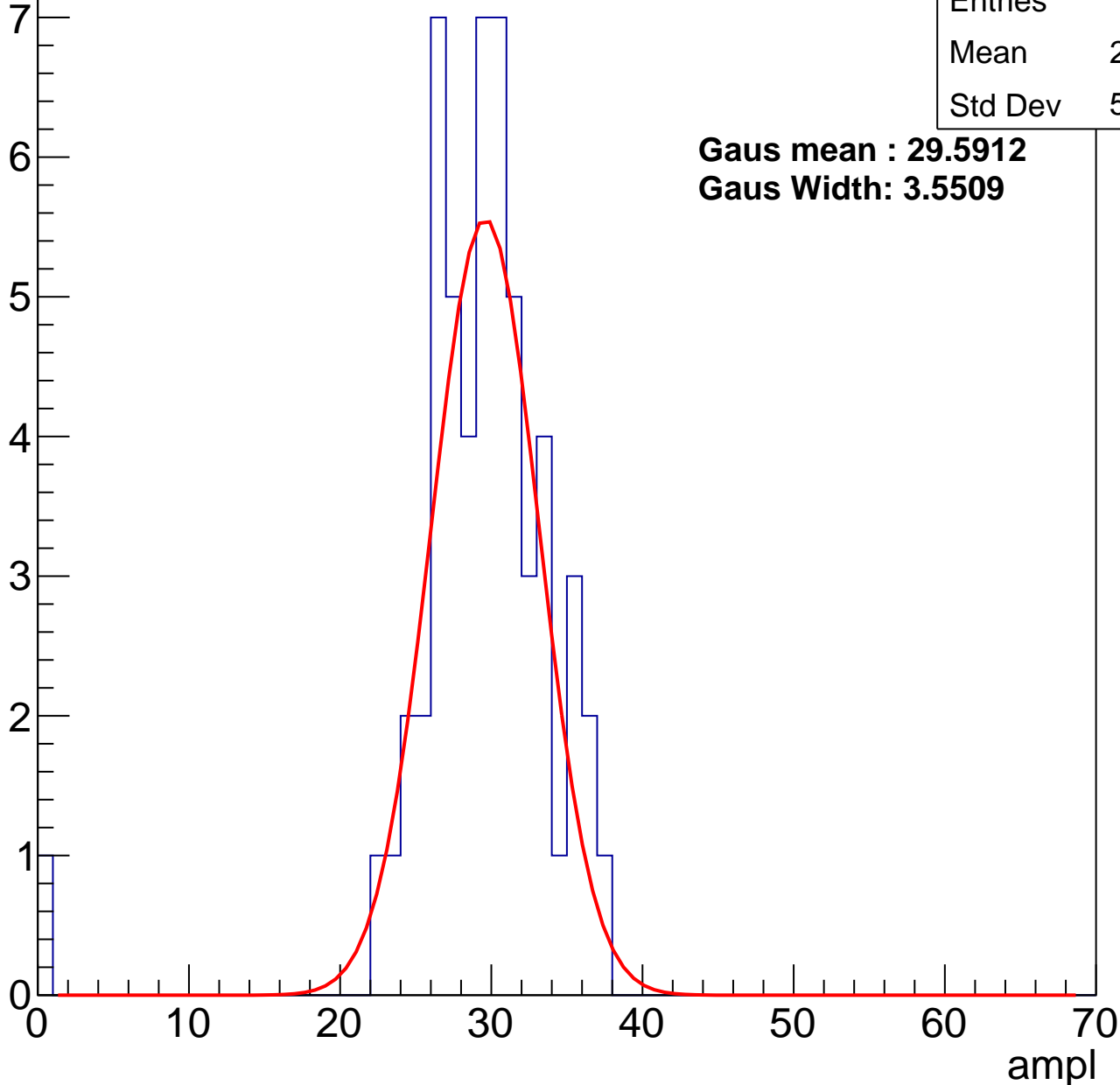
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.86
Std Dev	5.187

Gaus mean : 29.5912

Gaus Width: 3.5509



B1L101S, U21-ch20, adc1

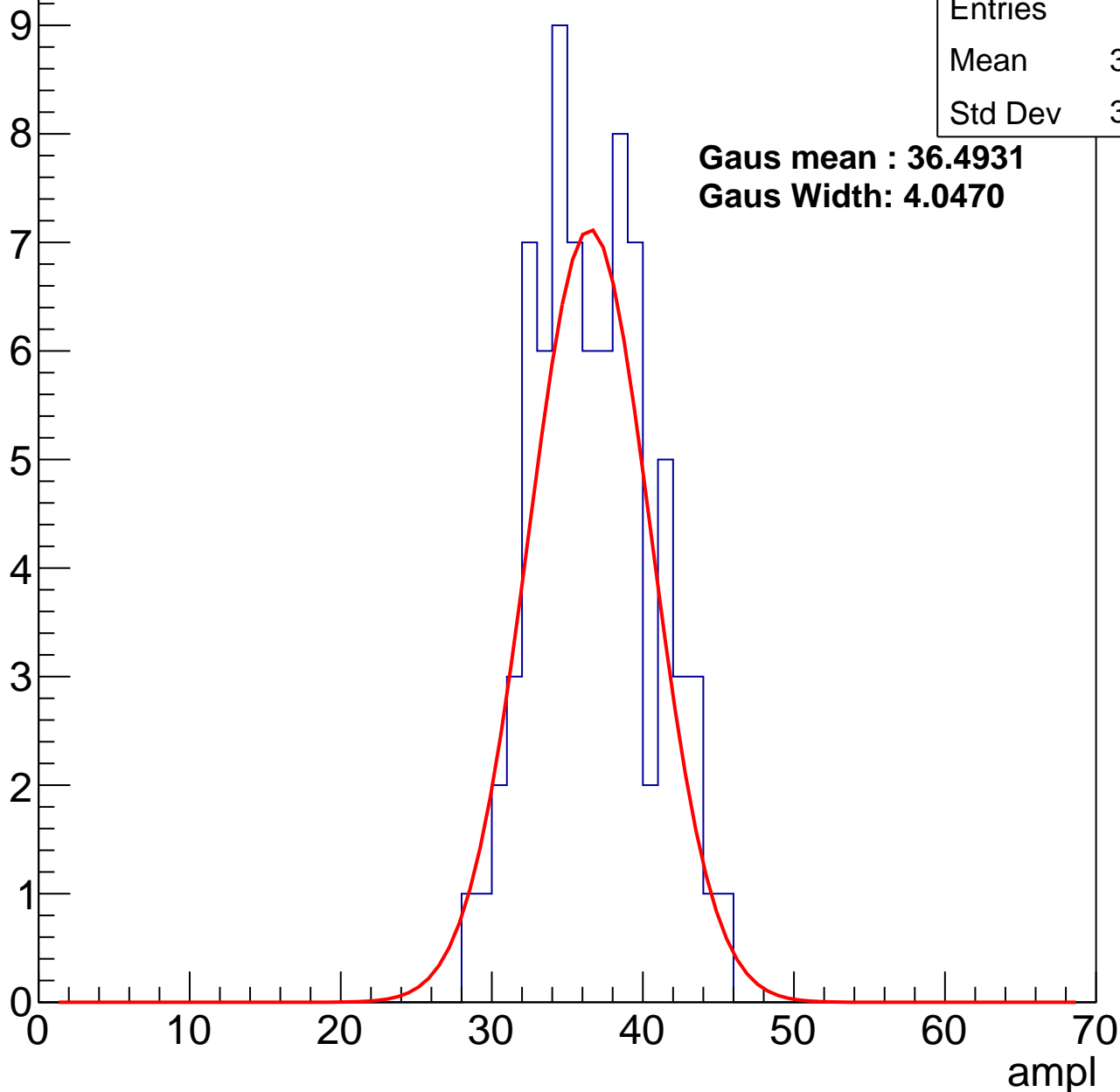
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.24
Std Dev	3.793

Gaus mean : 36.4931

Gaus Width: 4.0470



B1L101S, U21-ch20, adc2

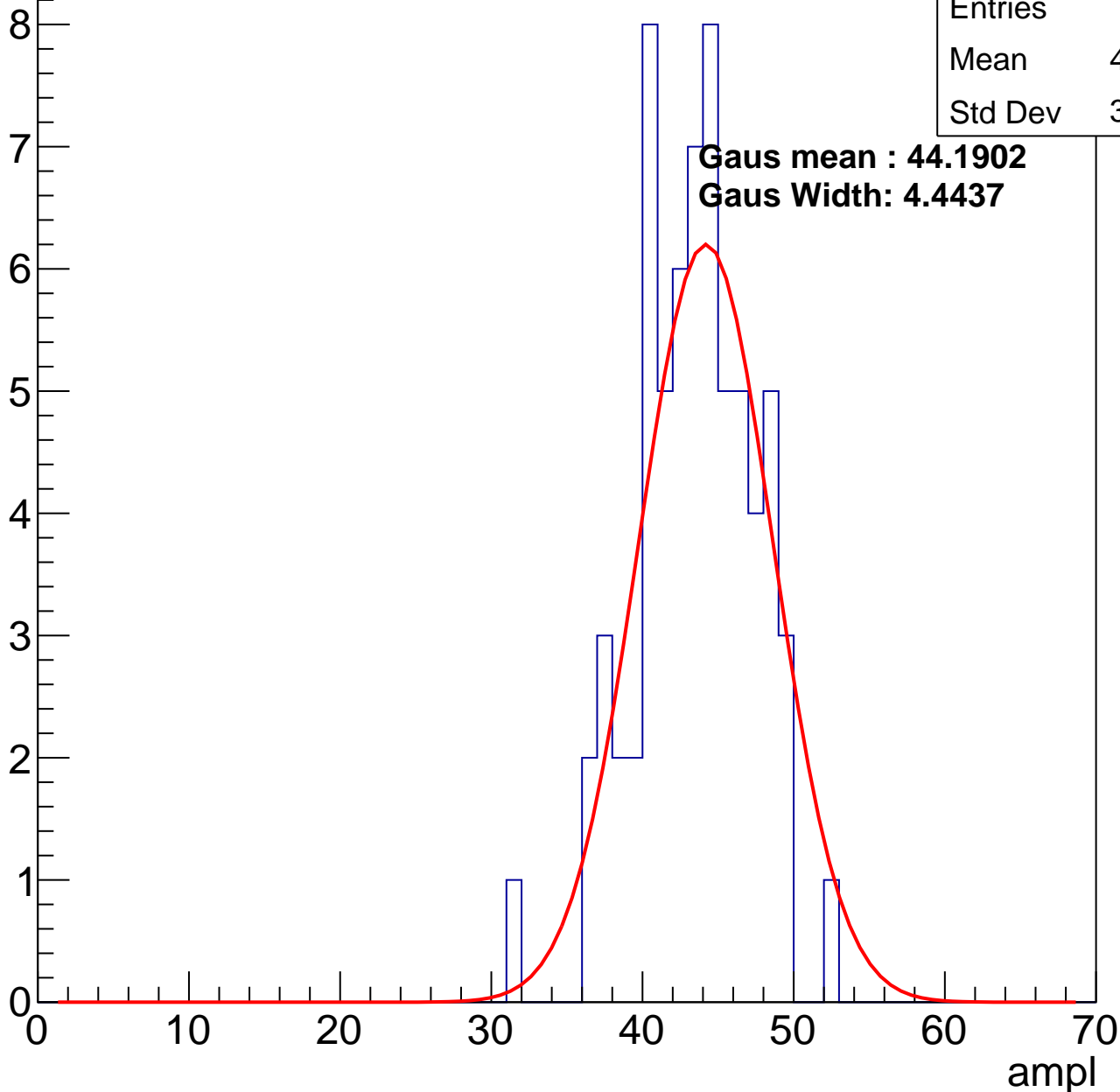
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.99
Std Dev	3.826

Gaus mean : 44.1902

Gaus Width: 4.4437

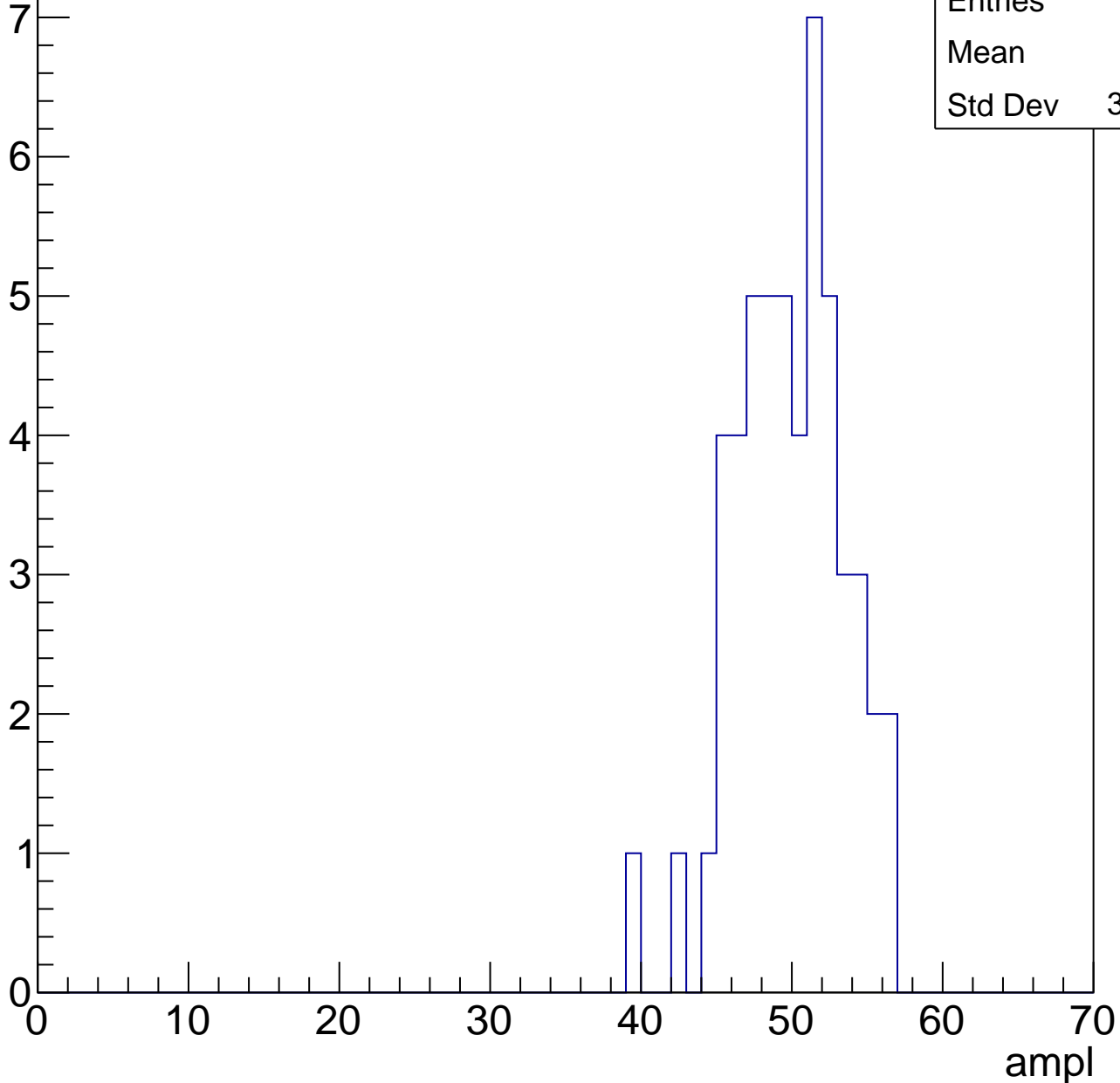


B1L101S, U21-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	49.4
Std Dev	3.564

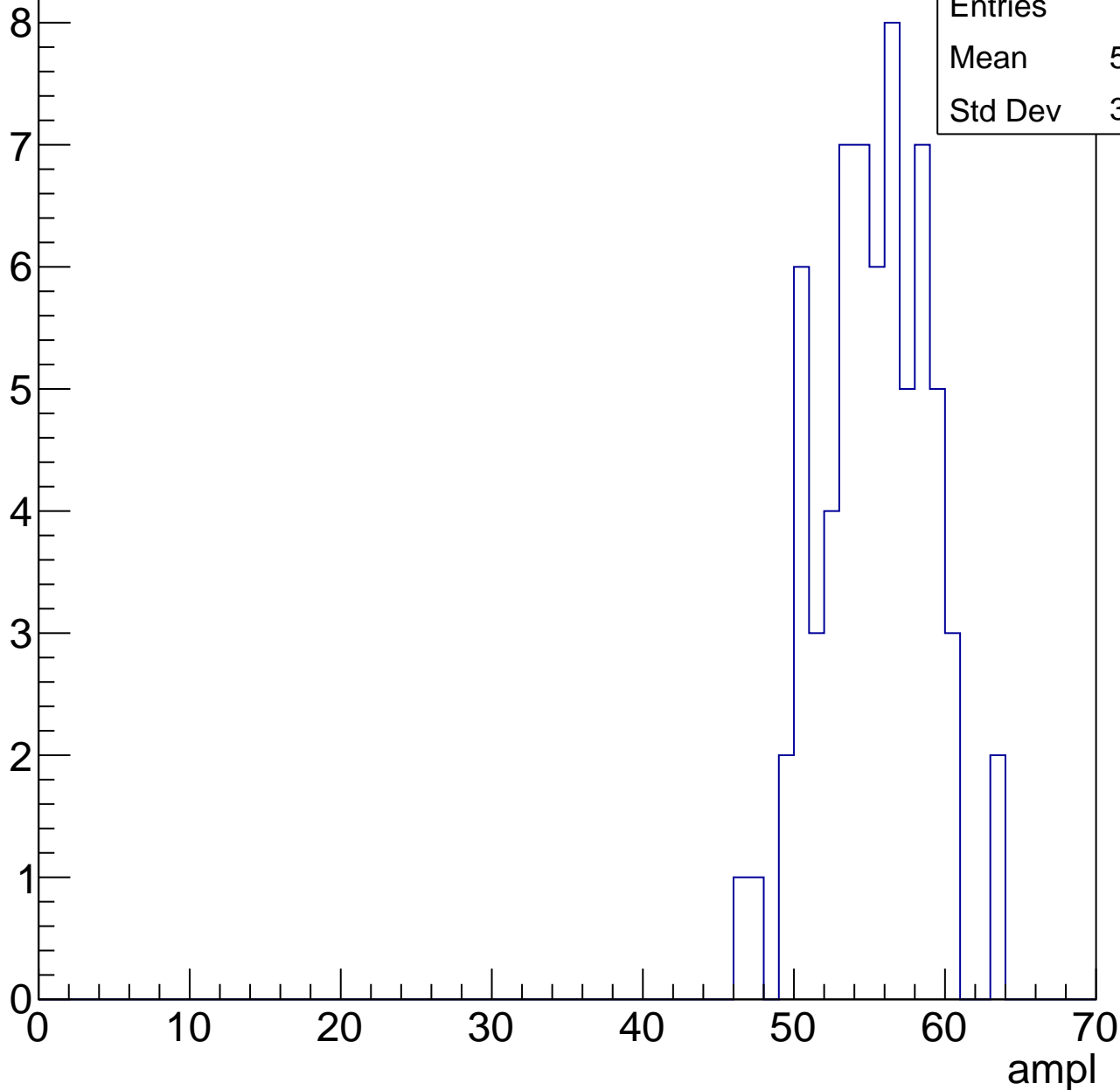


B1L101S, U21-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	54.79
Std Dev	3.577

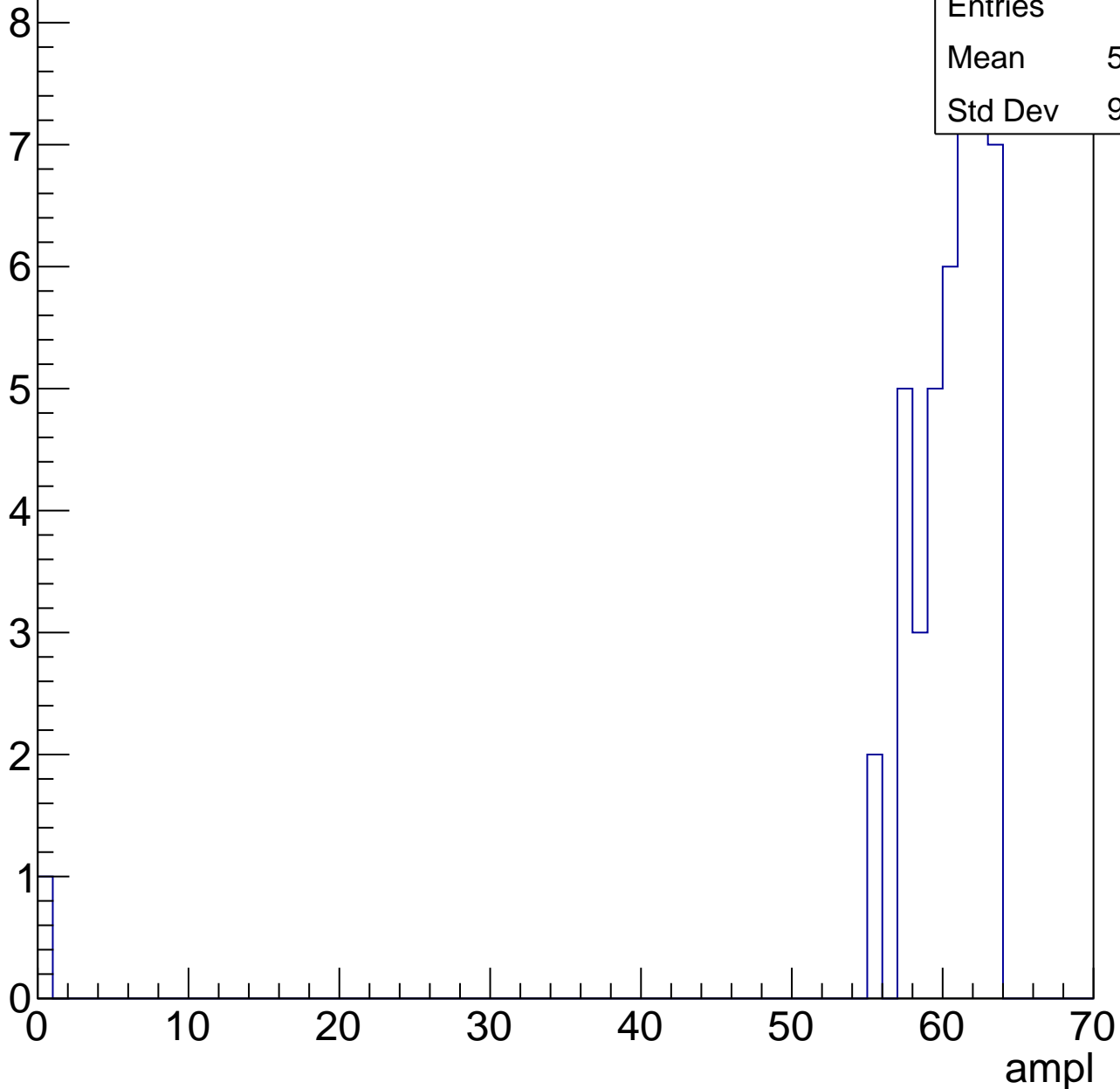


B1L101S, U21-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.87
Std Dev	9.137



B1L101S, U21-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

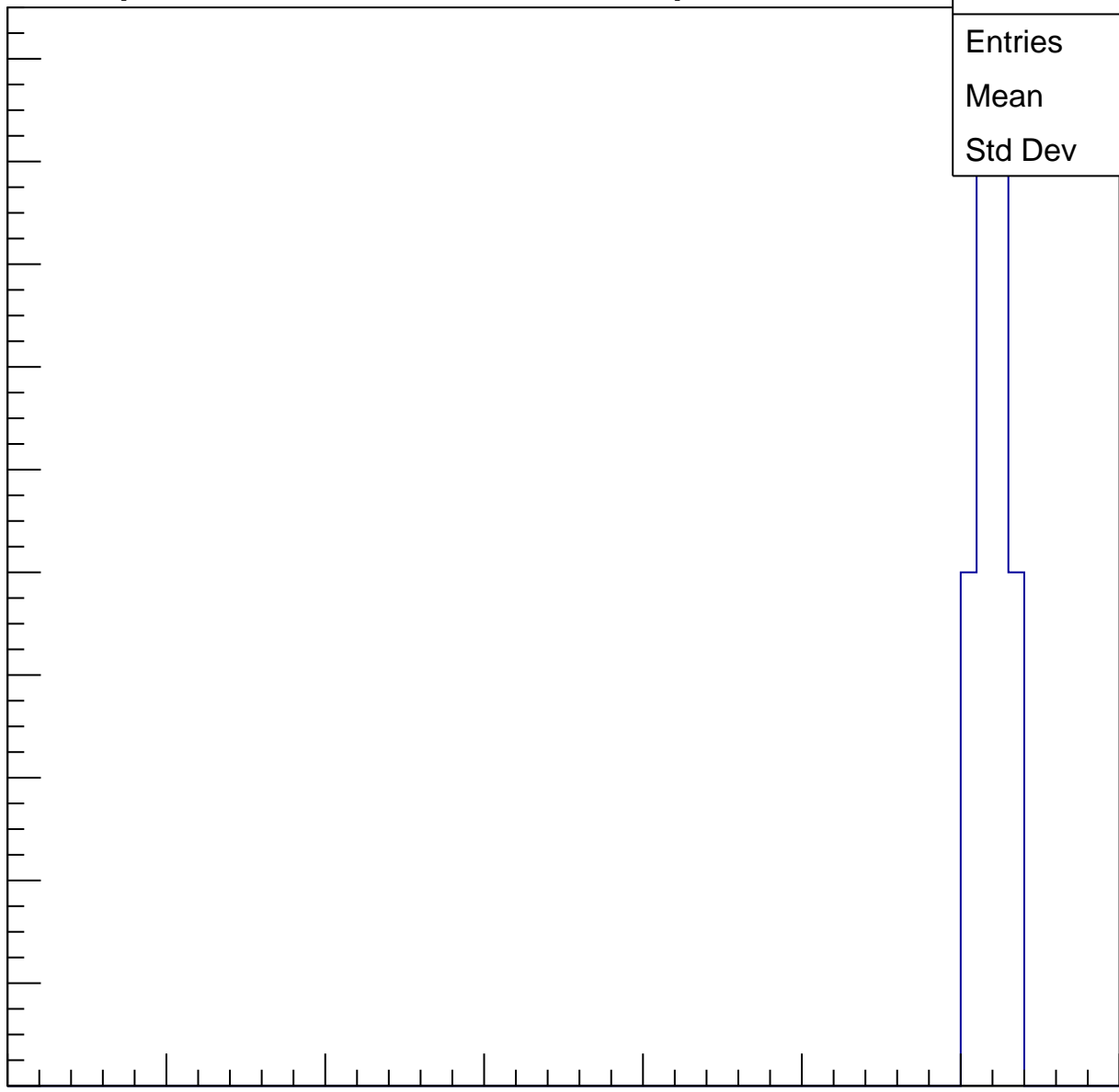
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch21, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	32.83
Std Dev	3.417

Gaus mean : 33.7542

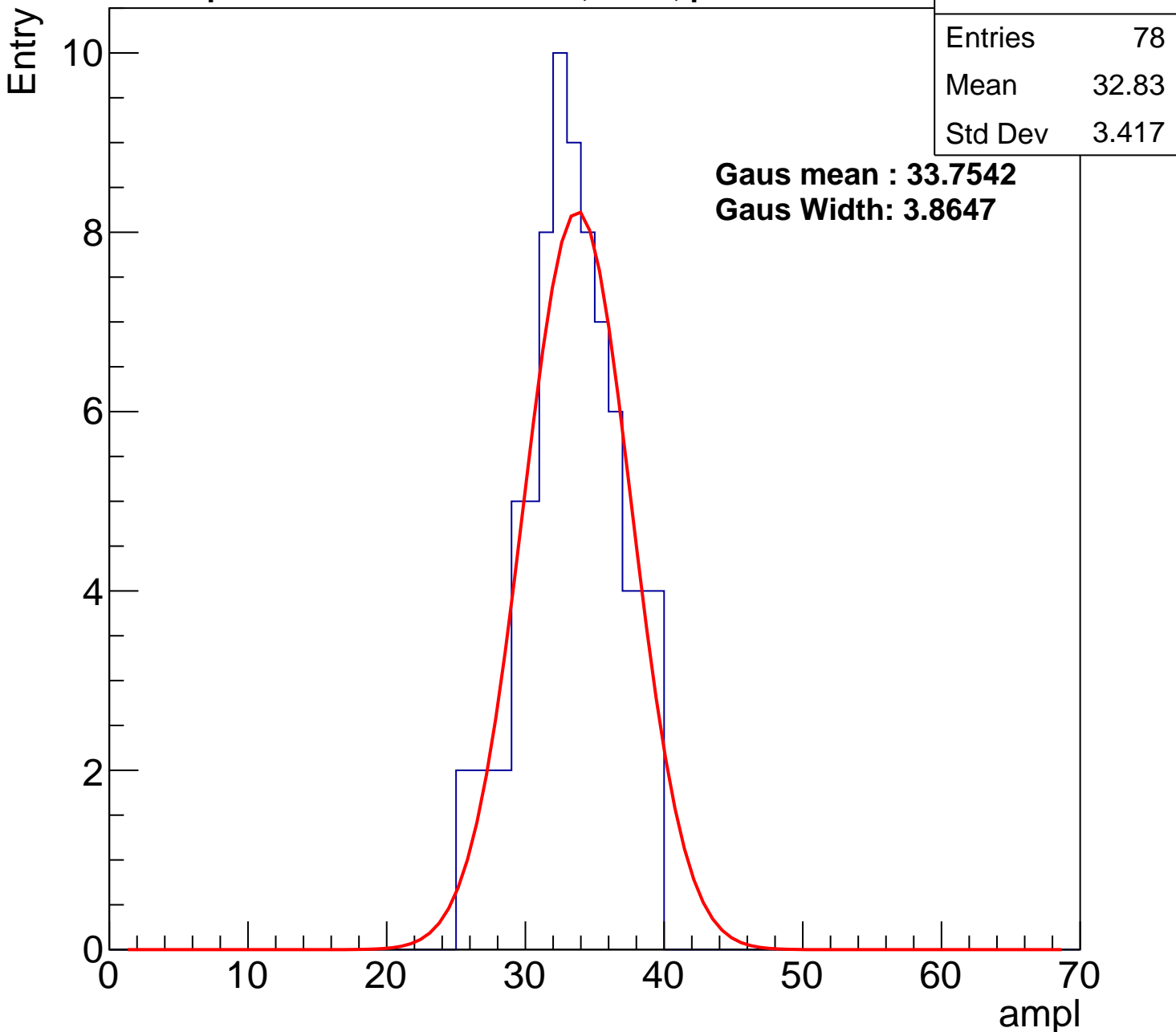
Gaus Width: 3.8647

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch21, adc1

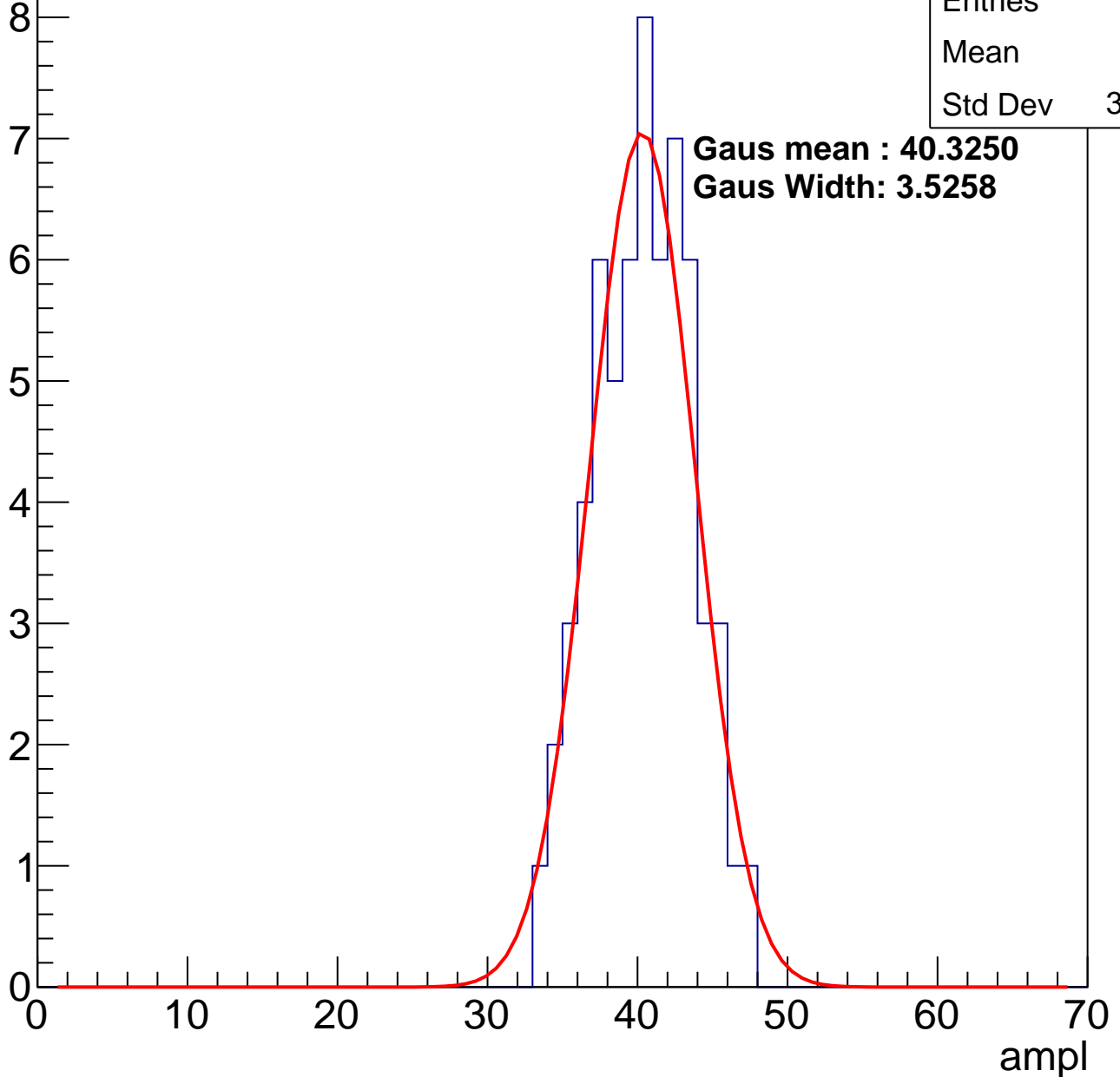
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	39.9
Std Dev	3.201

Gaus mean : 40.3250

Gaus Width: 3.5258



B1L101S, U21-ch21, adc2

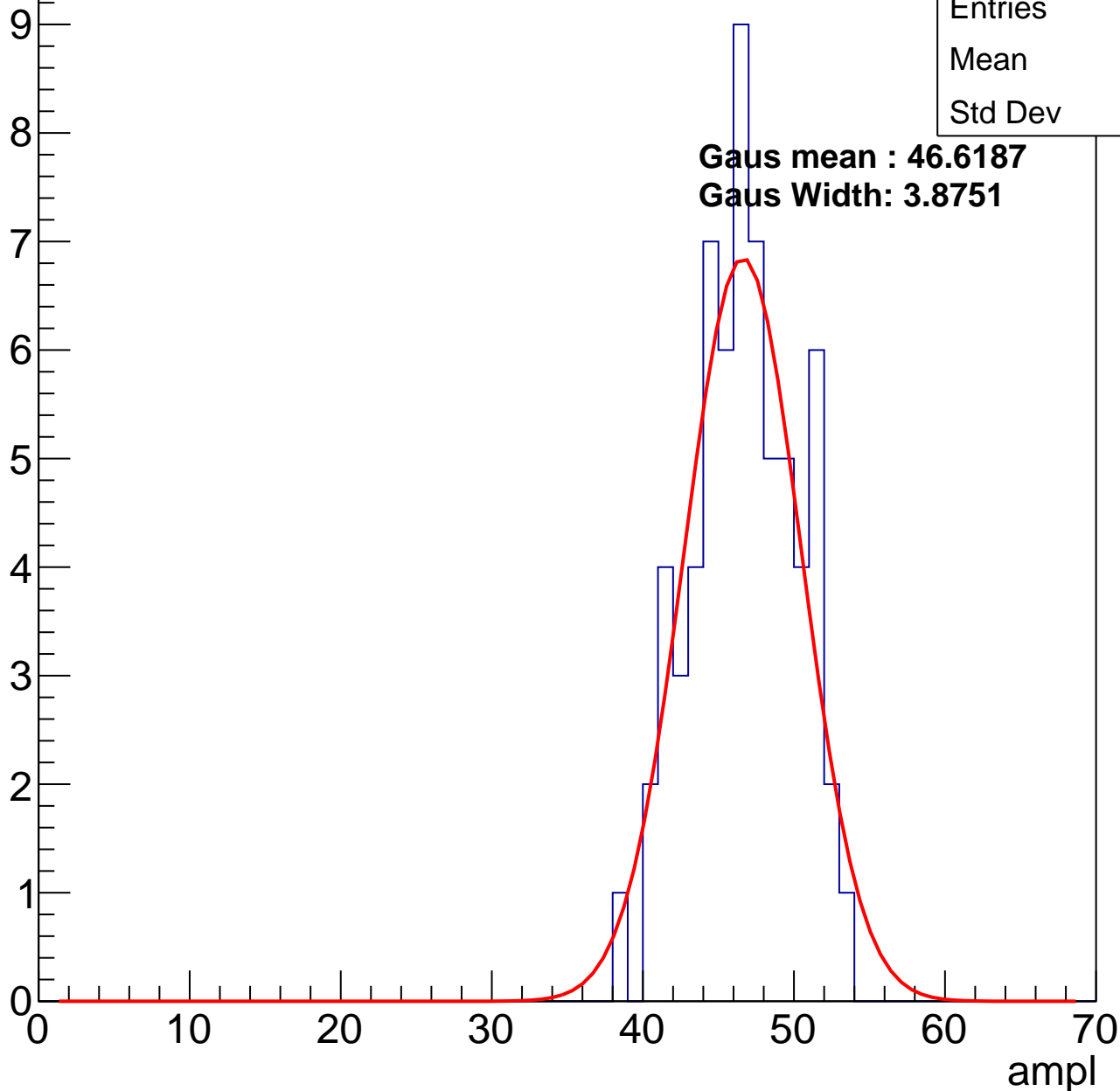
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	46.2
Std Dev	3.39

Gaus mean : 46.6187

Gaus Width: 3.8751

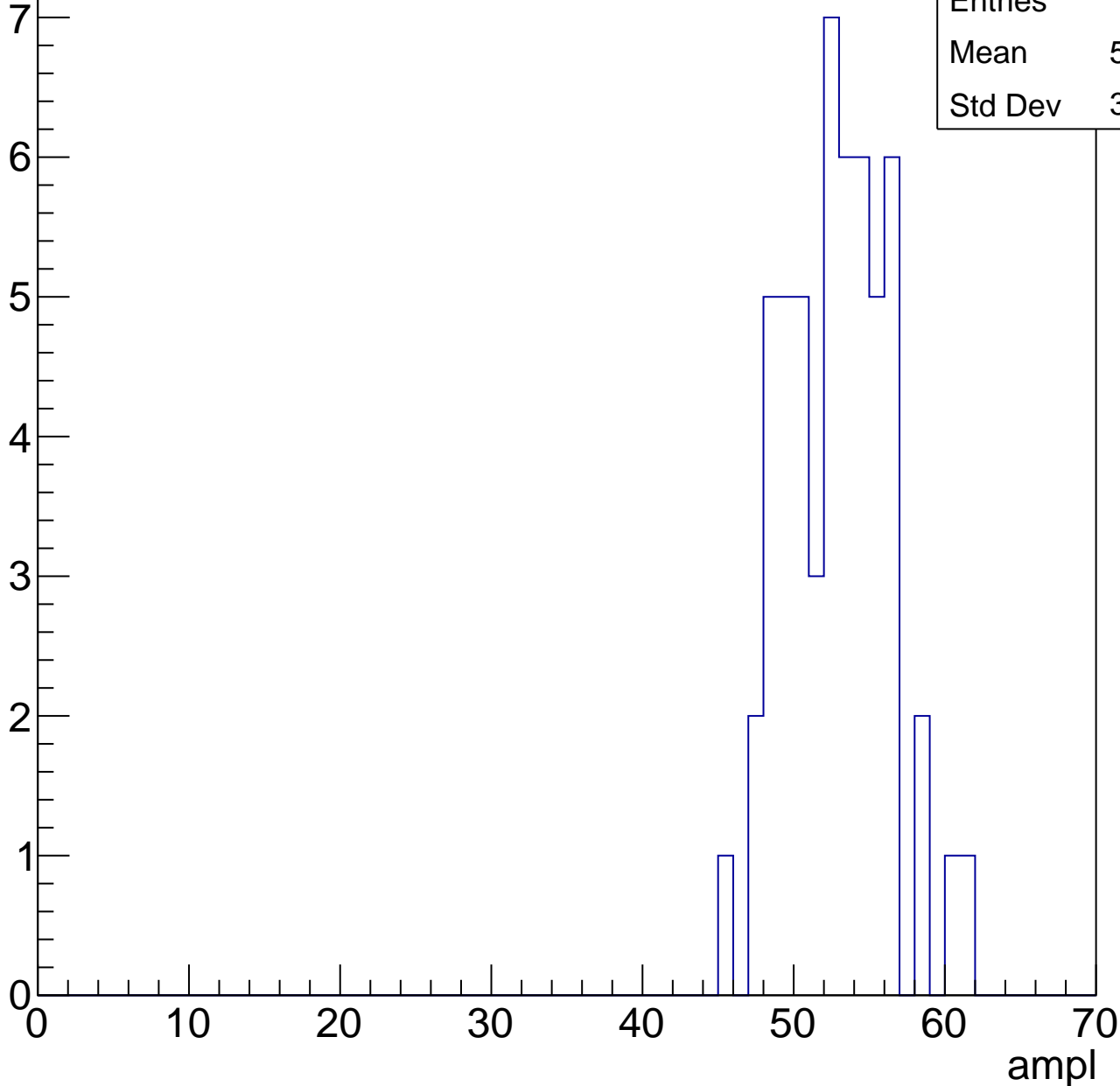


B1L101S, U21-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

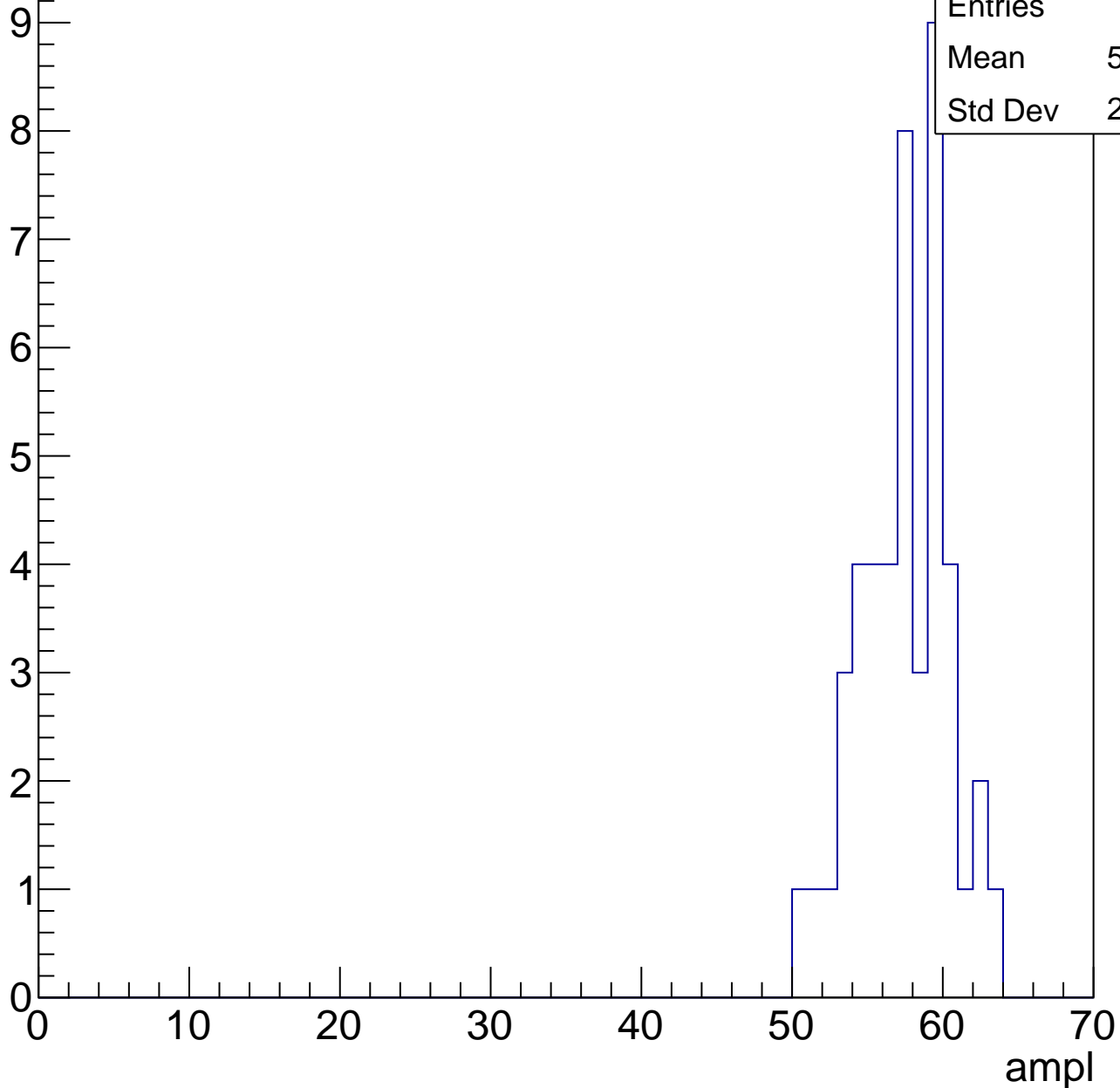
Entries	55
Mean	52.38
Std Dev	3.376



B1L101S, U21-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

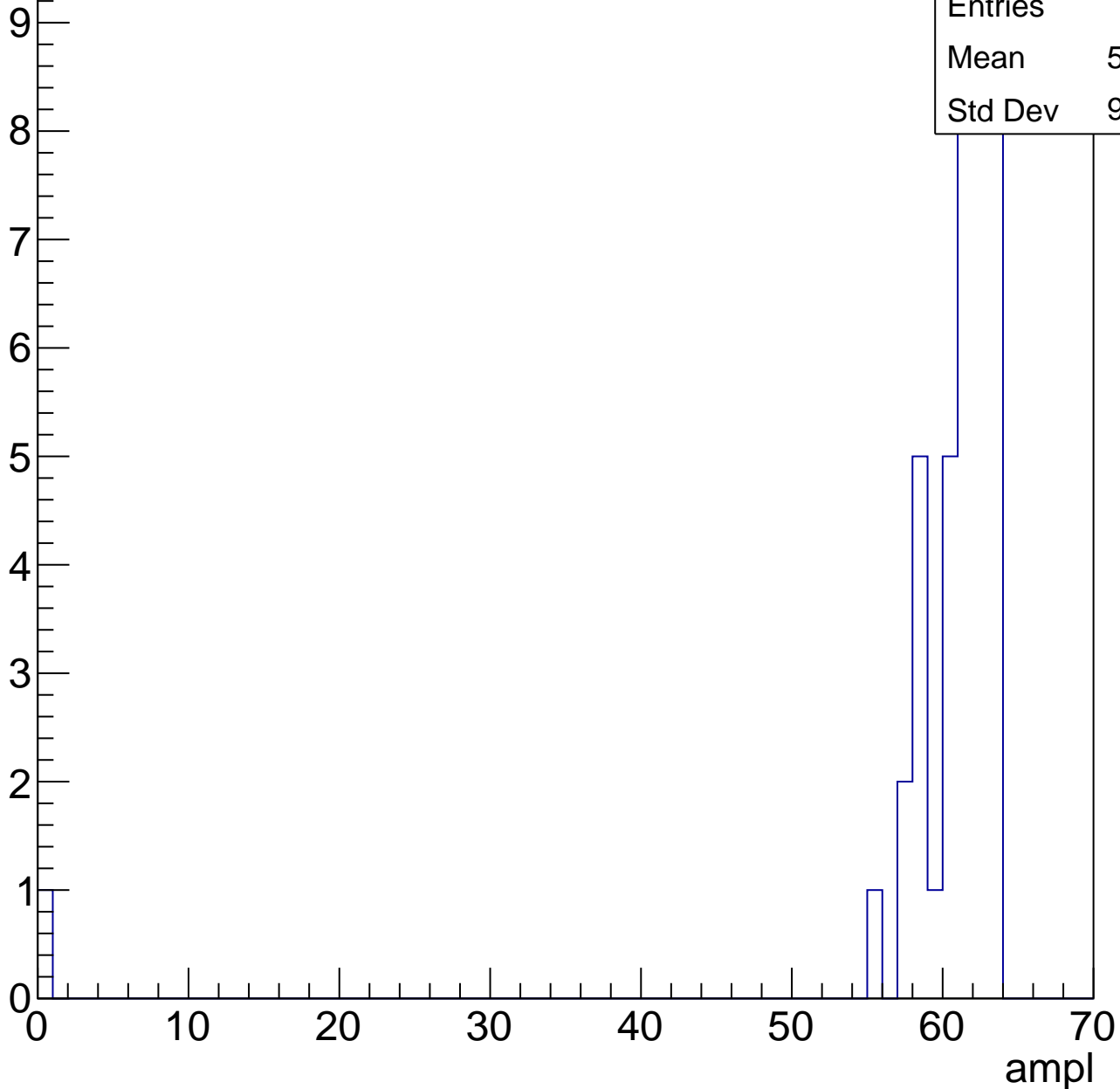


B1L101S, U21-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.23
Std Dev	9.697



B1L101S, U21-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch22, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.83
Std Dev	2.853

Gaus mean : 30.0559

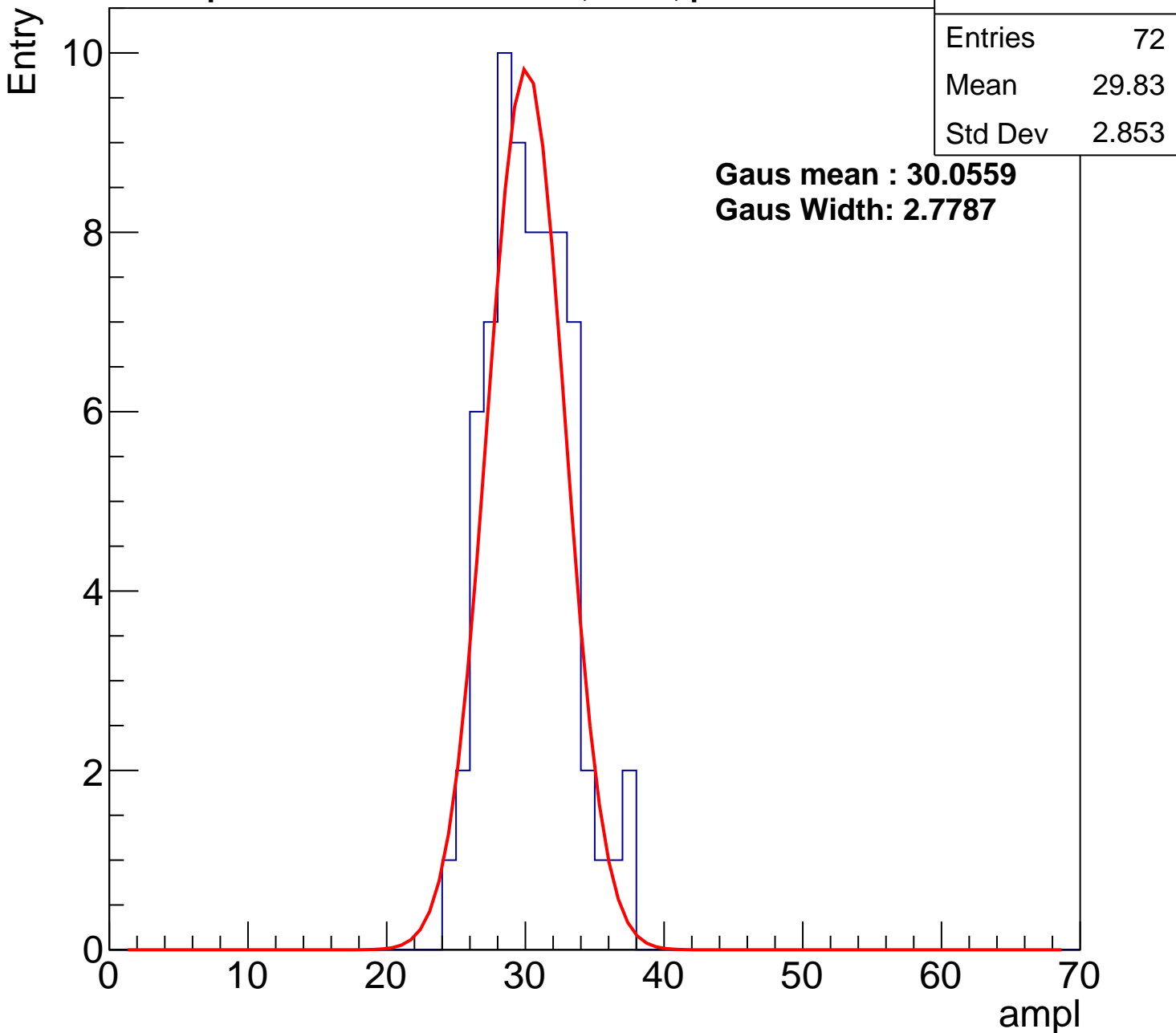
Gaus Width: 2.7787

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch22, adc1

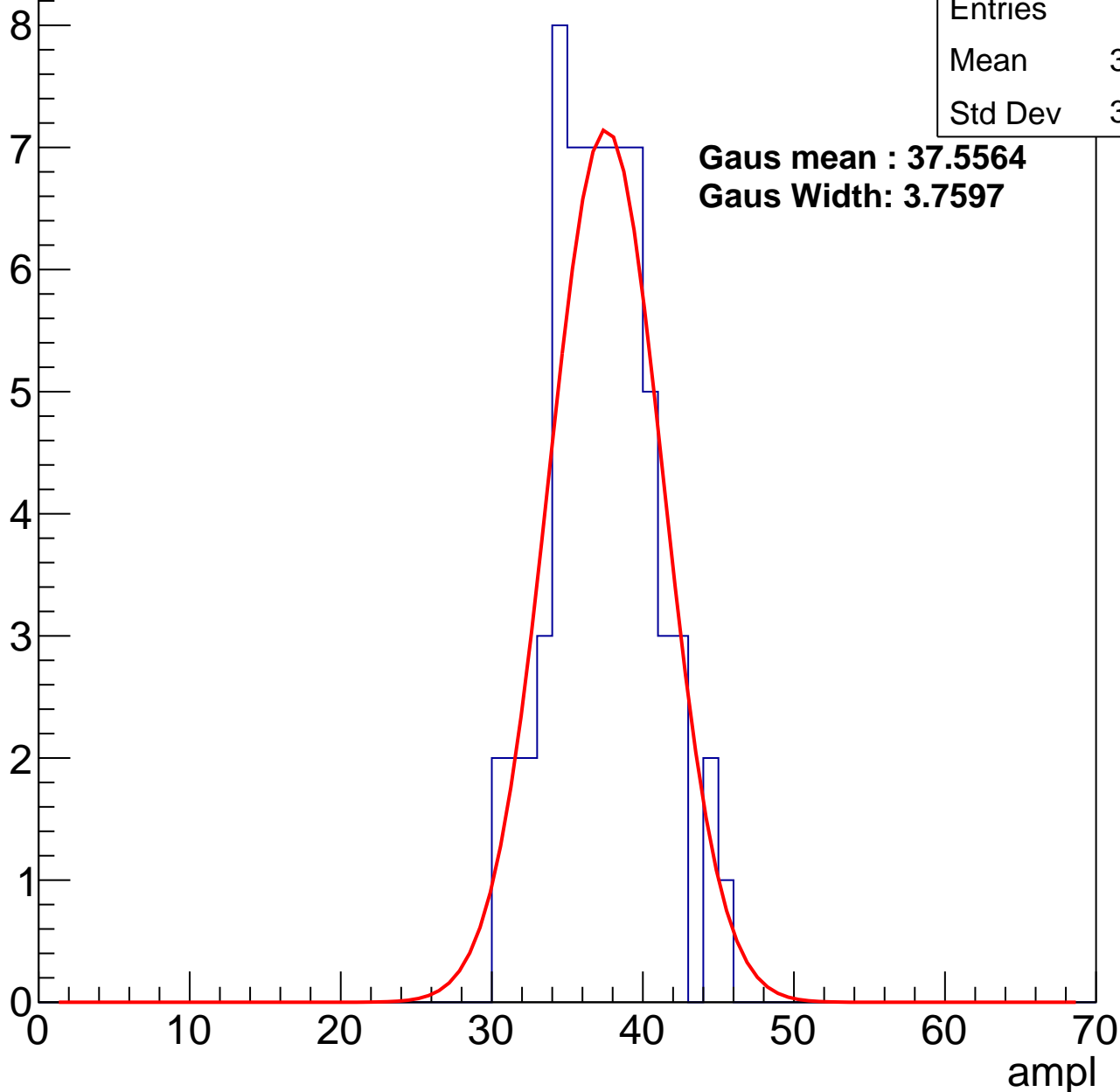
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.88
Std Dev	3.346

Gaus mean : 37.5564

Gaus Width: 3.7597



B1L101S, U21-ch22, adc2

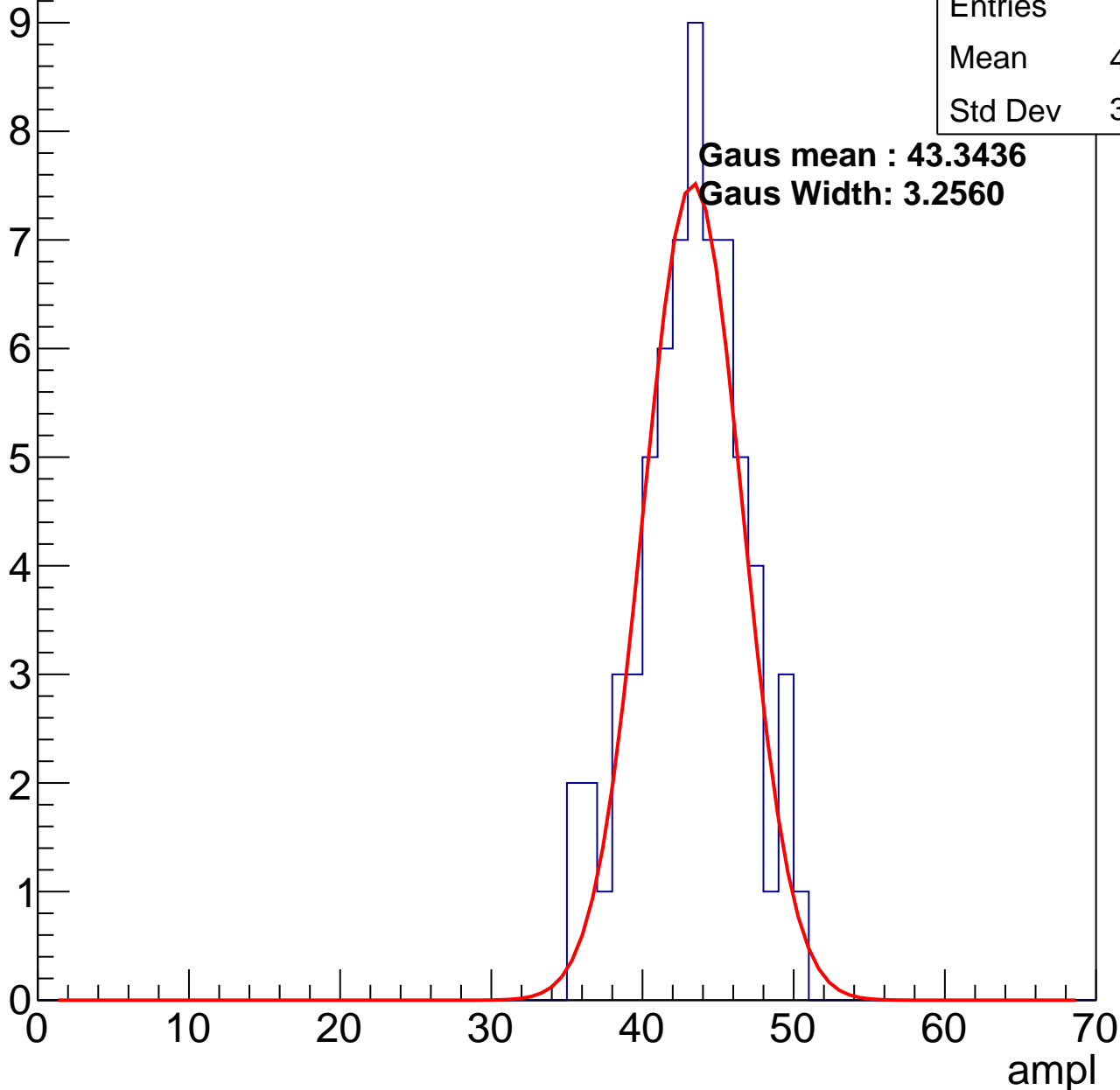
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.77
Std Dev	3.446

Gaus mean : 43.3436

Gaus Width: 3.2560

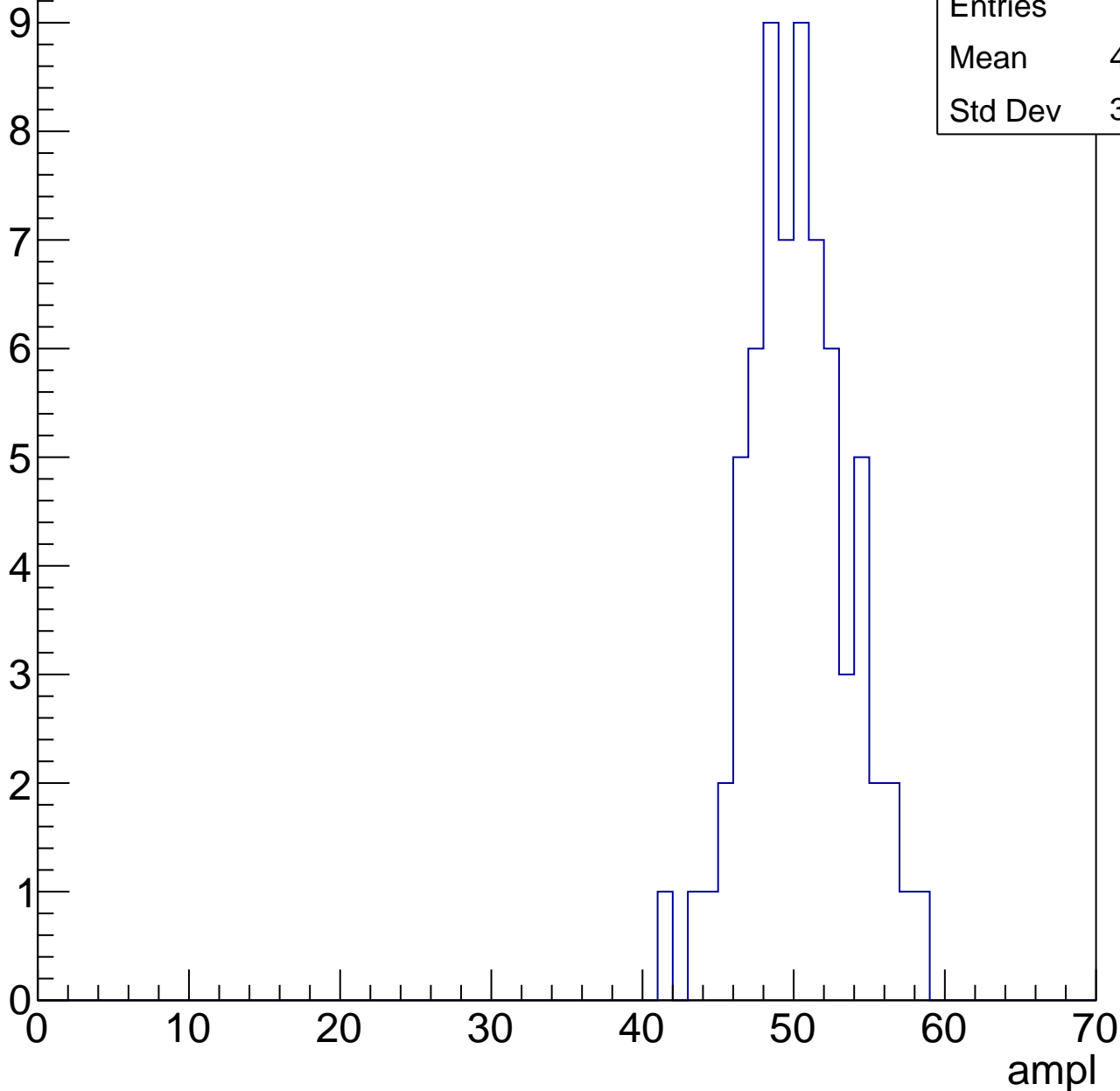


B1L101S, U21-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.85
Std Dev	3.353

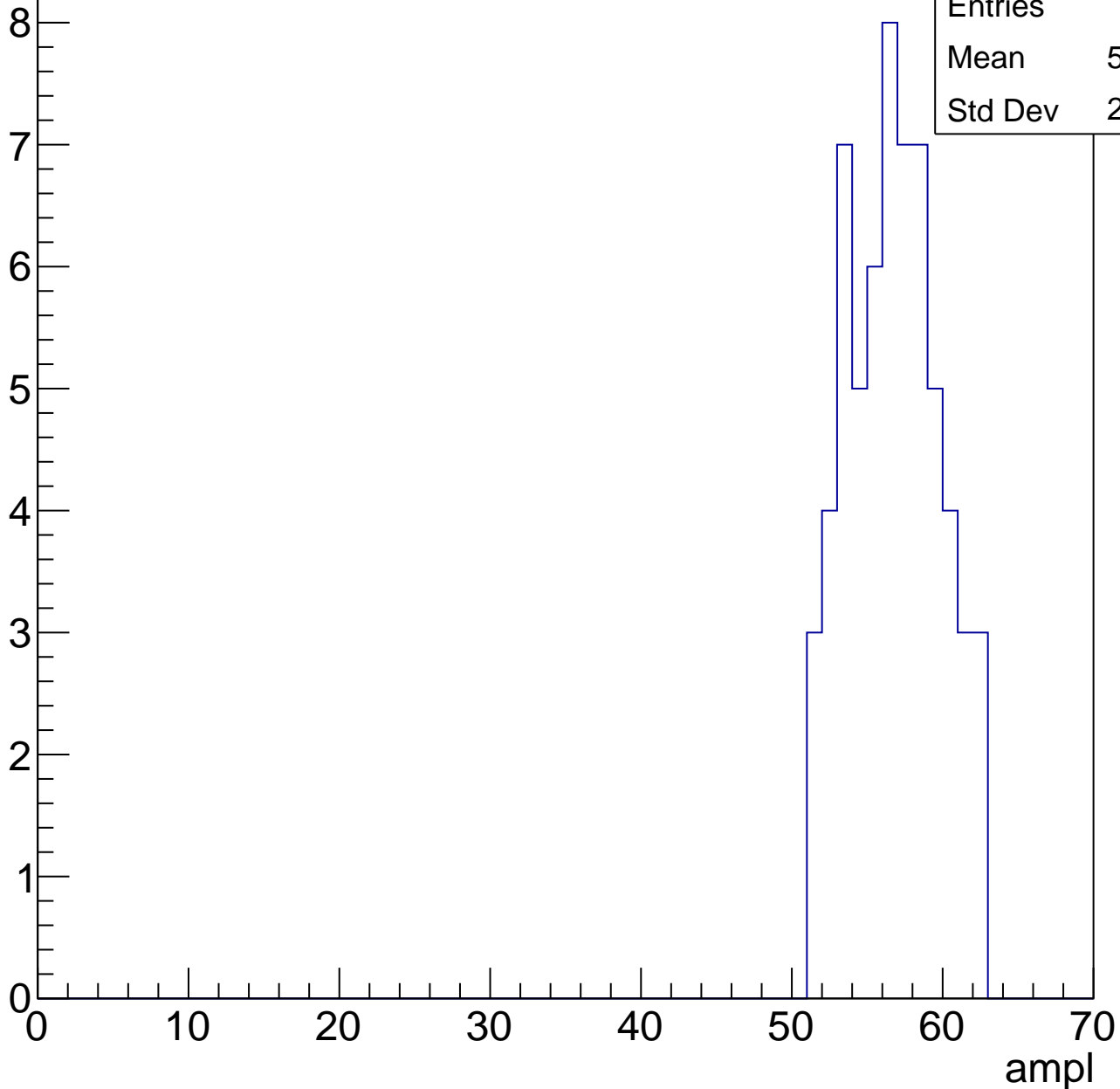


B1L101S, U21-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.27
Std Dev	2.979

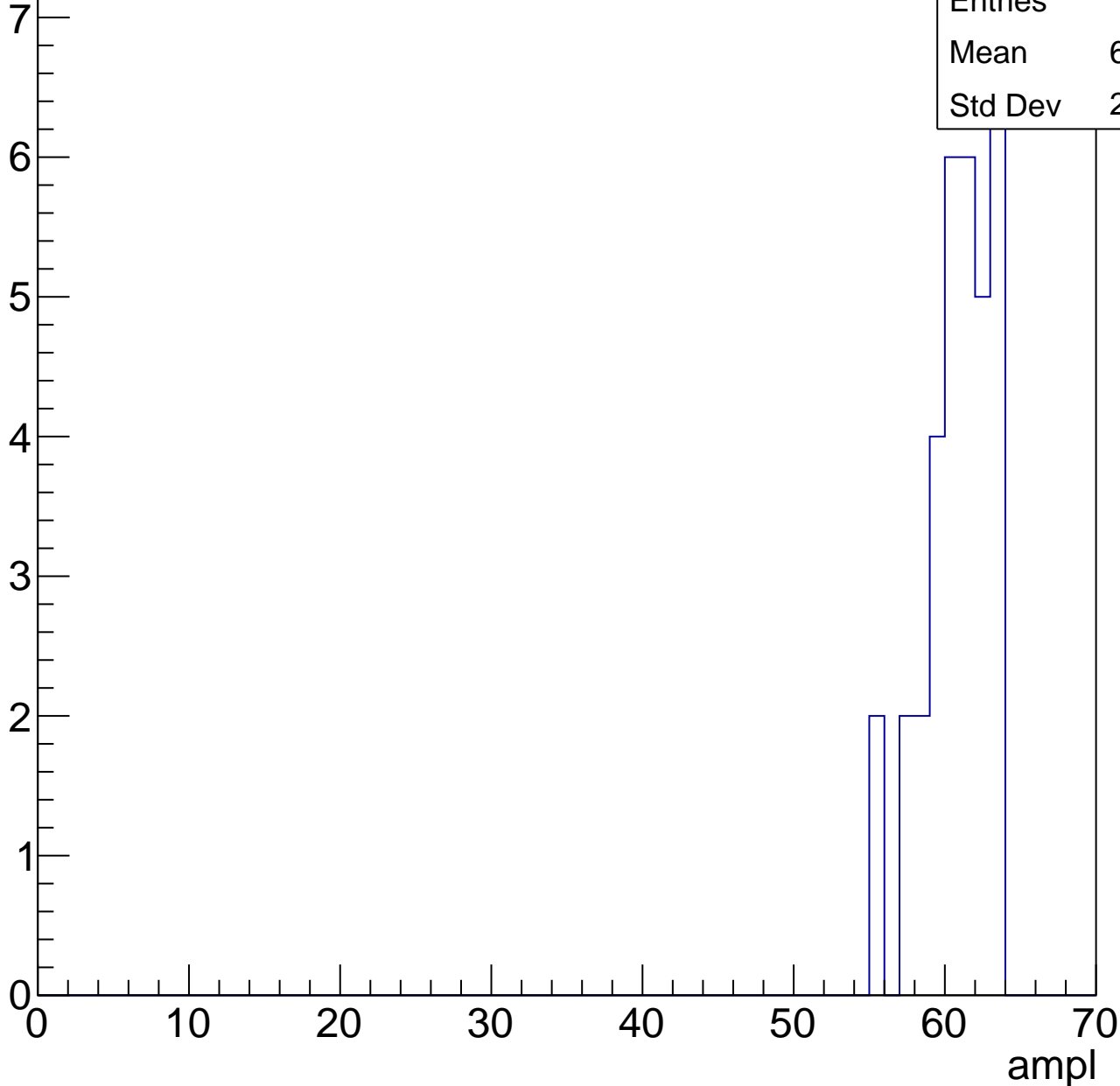


B1L101S, U21-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

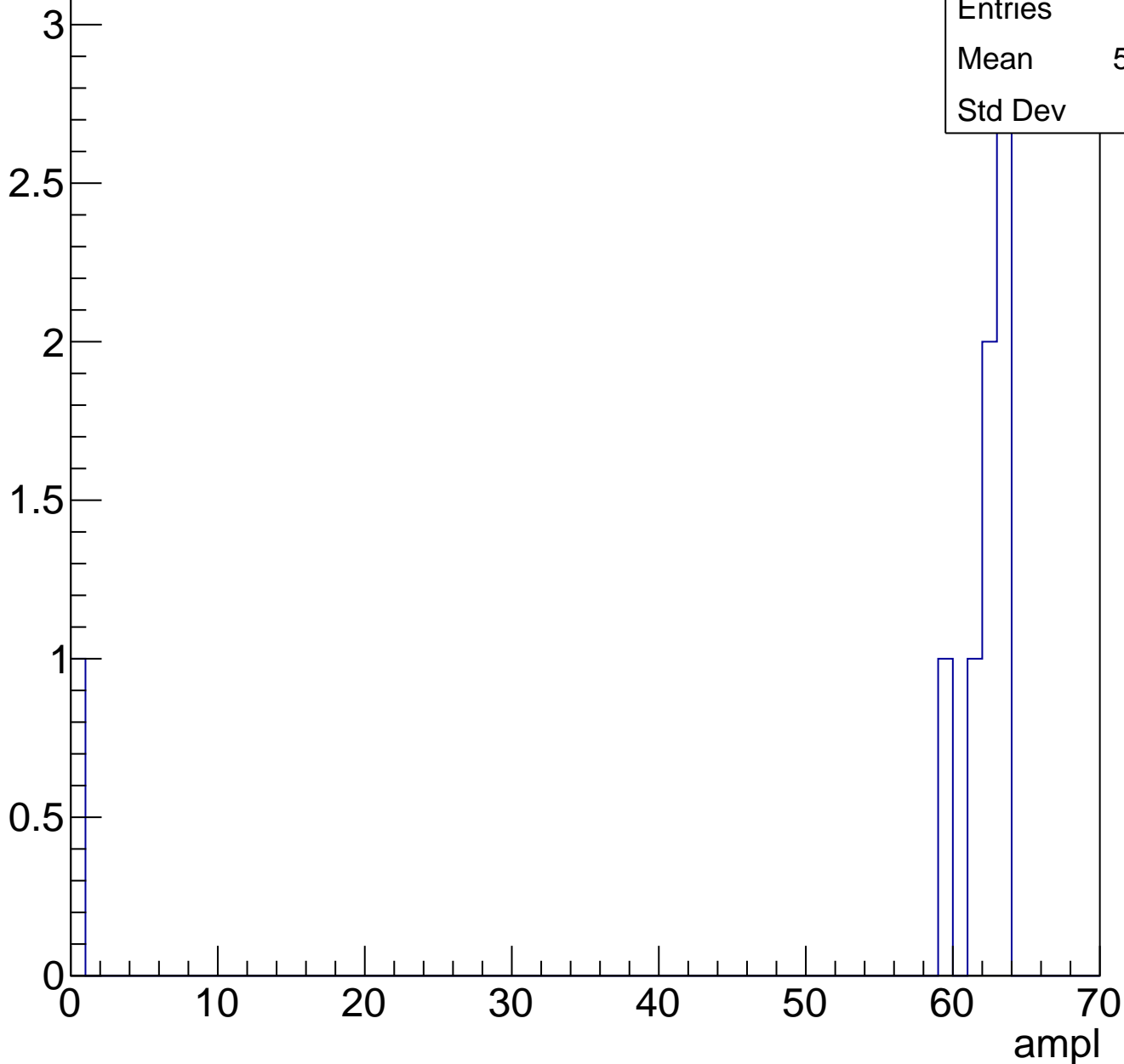
Entries	34
Mean	60.38
Std Dev	2.196



B1L101S, U21-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L101S, U21-ch23, adc0

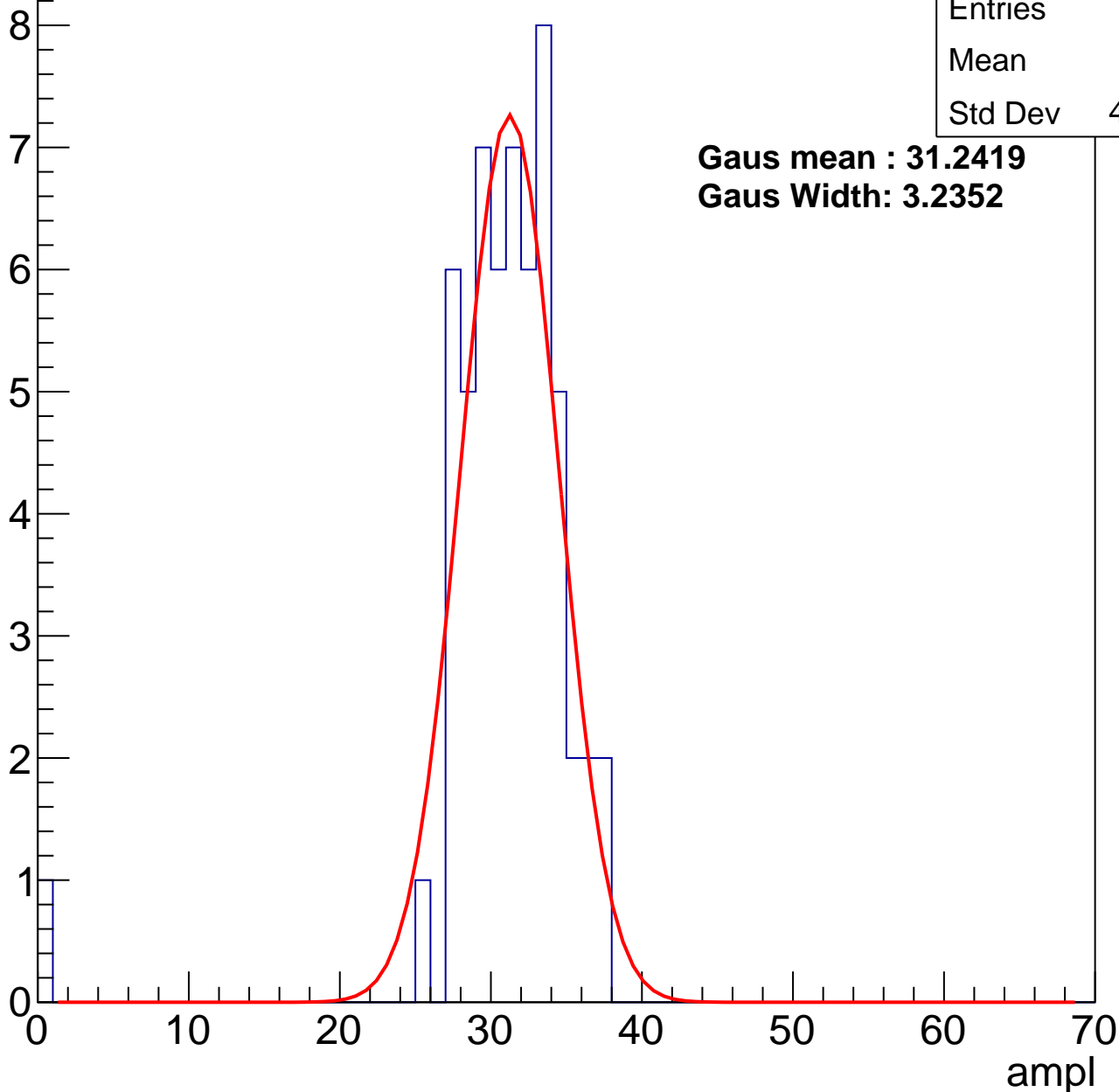
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	30.5
Std Dev	4.903

Gaus mean : 31.2419

Gaus Width: 3.2352



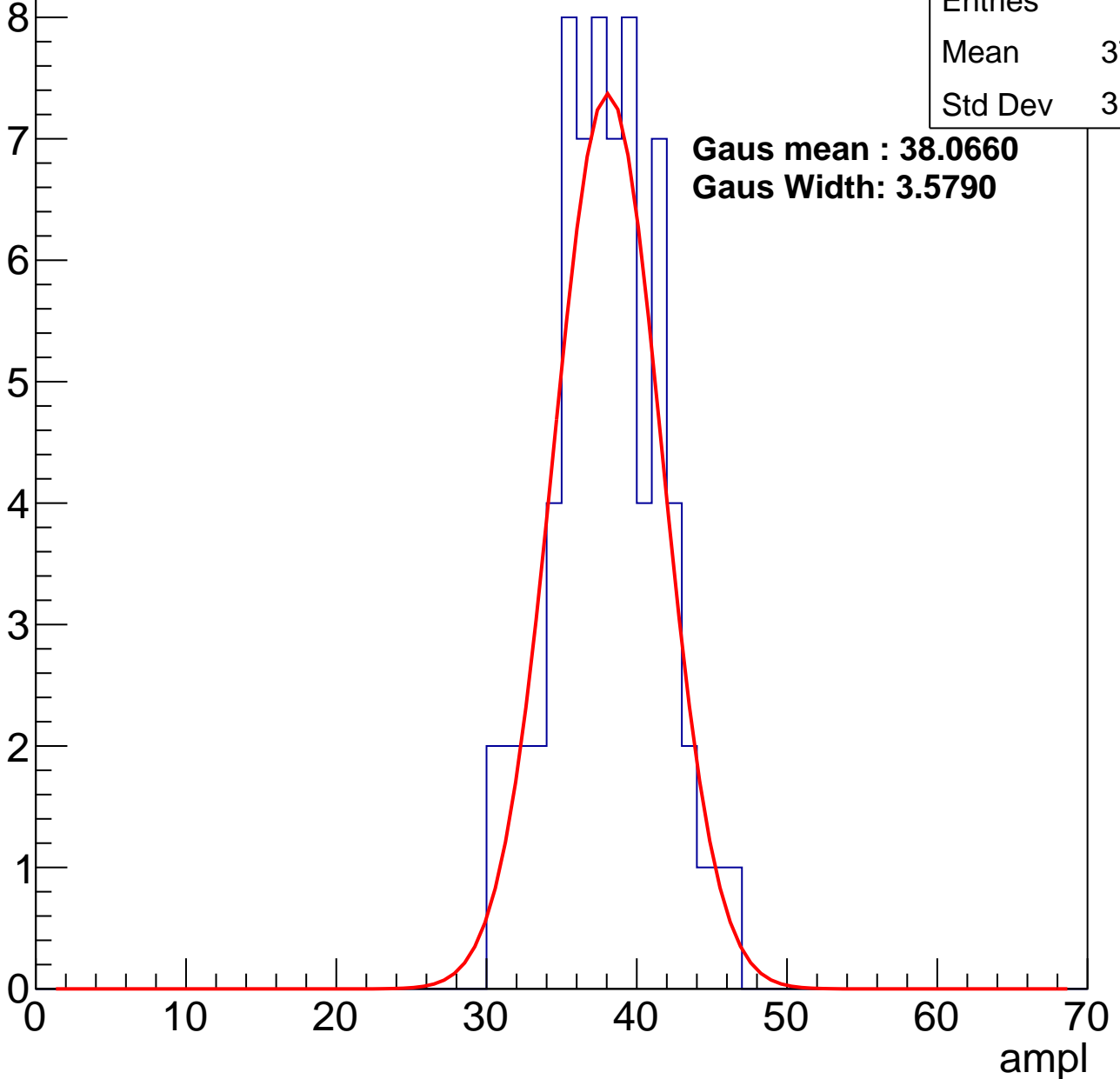
B1L101S, U21-ch23, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.57
Std Dev	3.499

Gaus mean : 38.0660
Gaus Width: 3.5790



B1L101S, U21-ch23, adc2

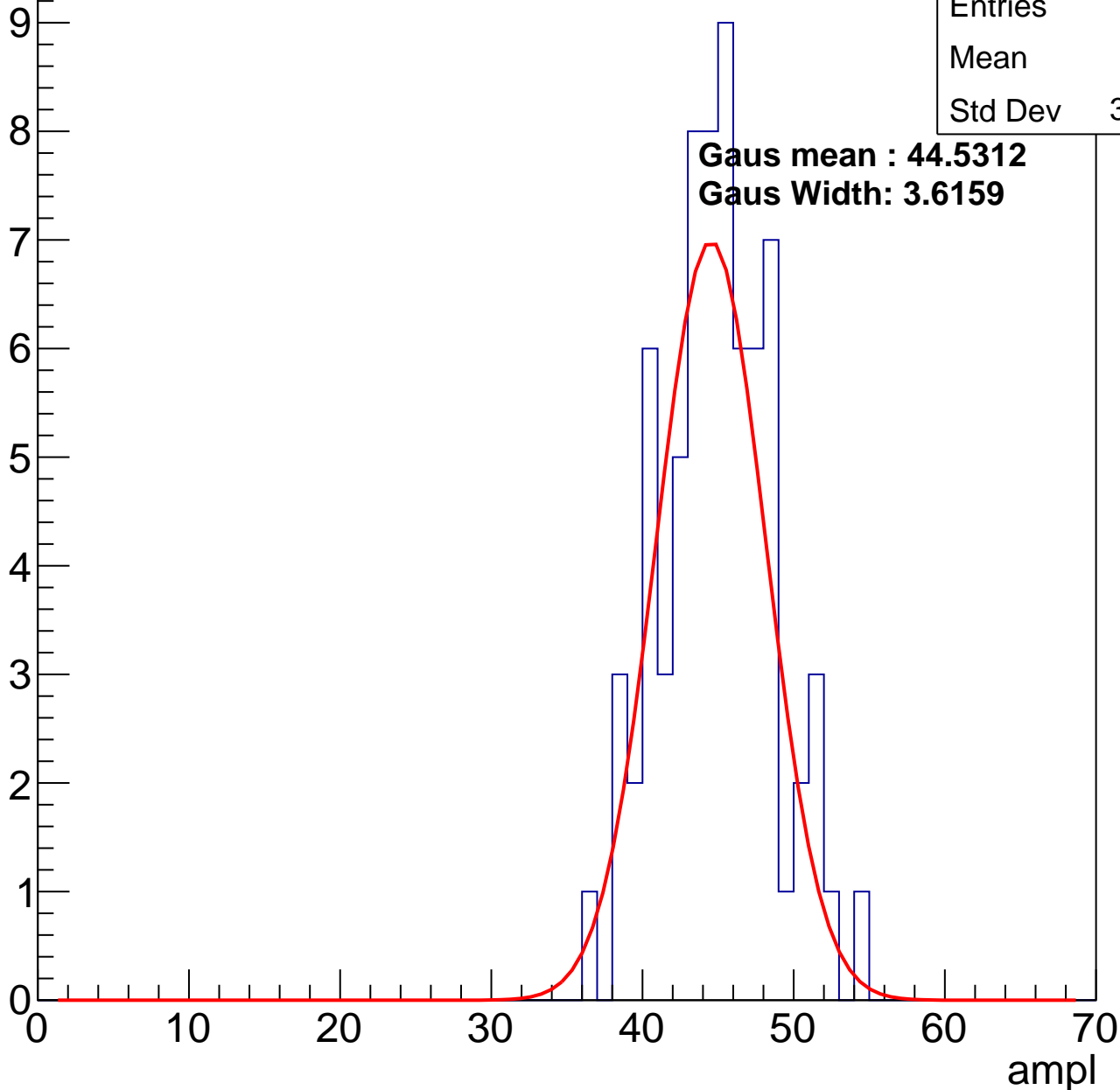
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	44.5
Std Dev	3.655

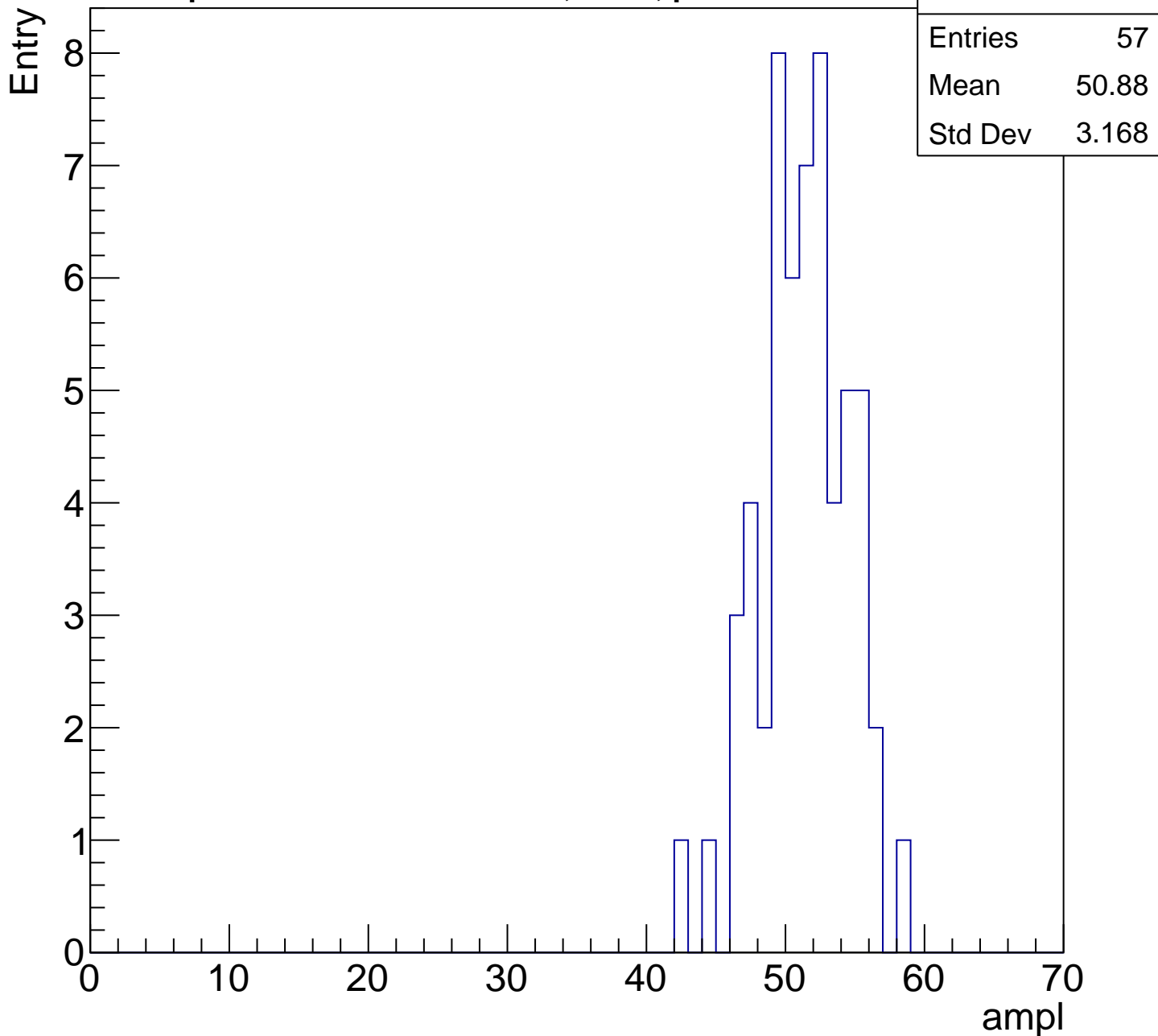
Gaus mean : 44.5312

Gaus Width: 3.6159



B1L101S, U21-ch23, adc3

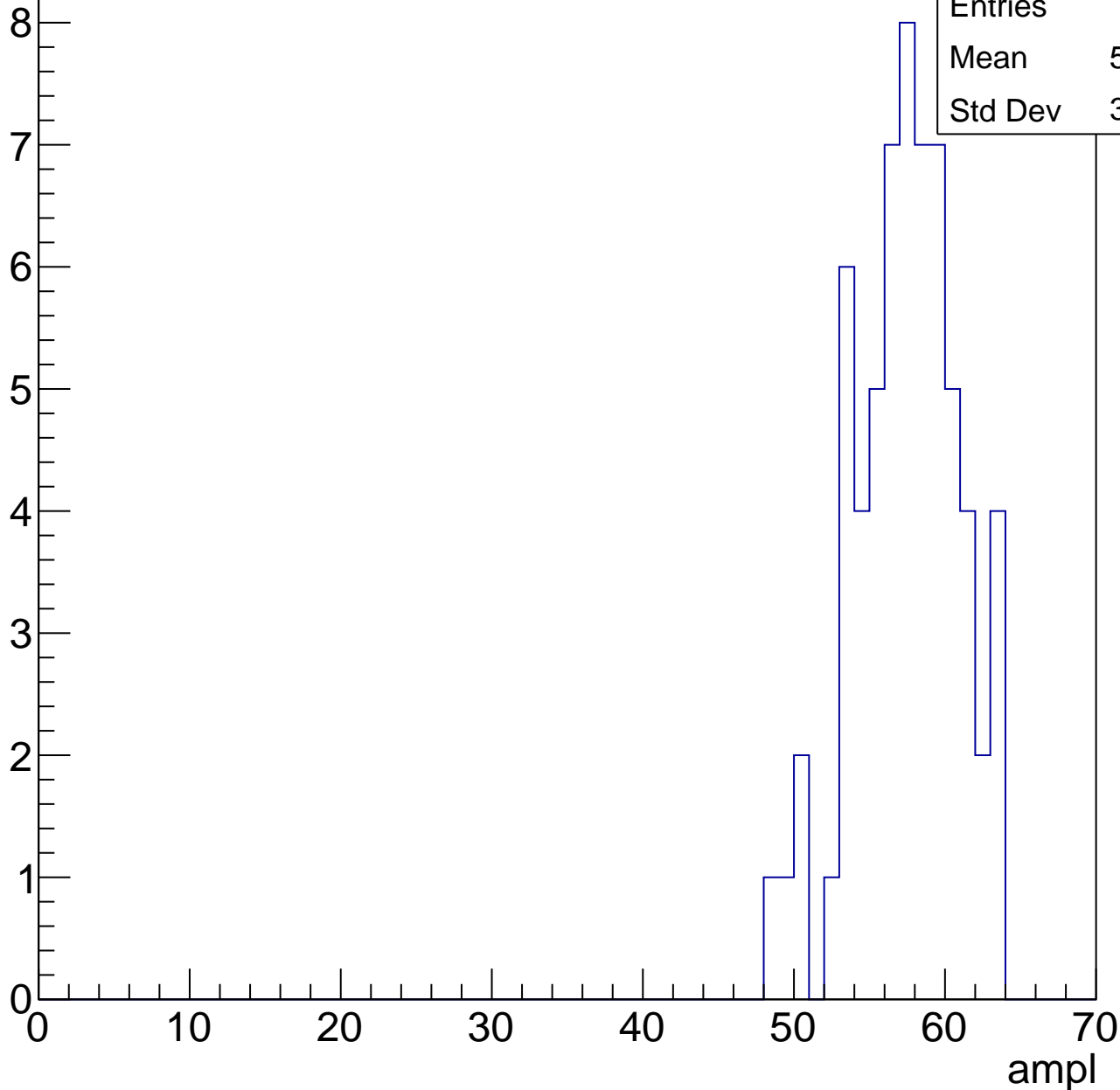
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

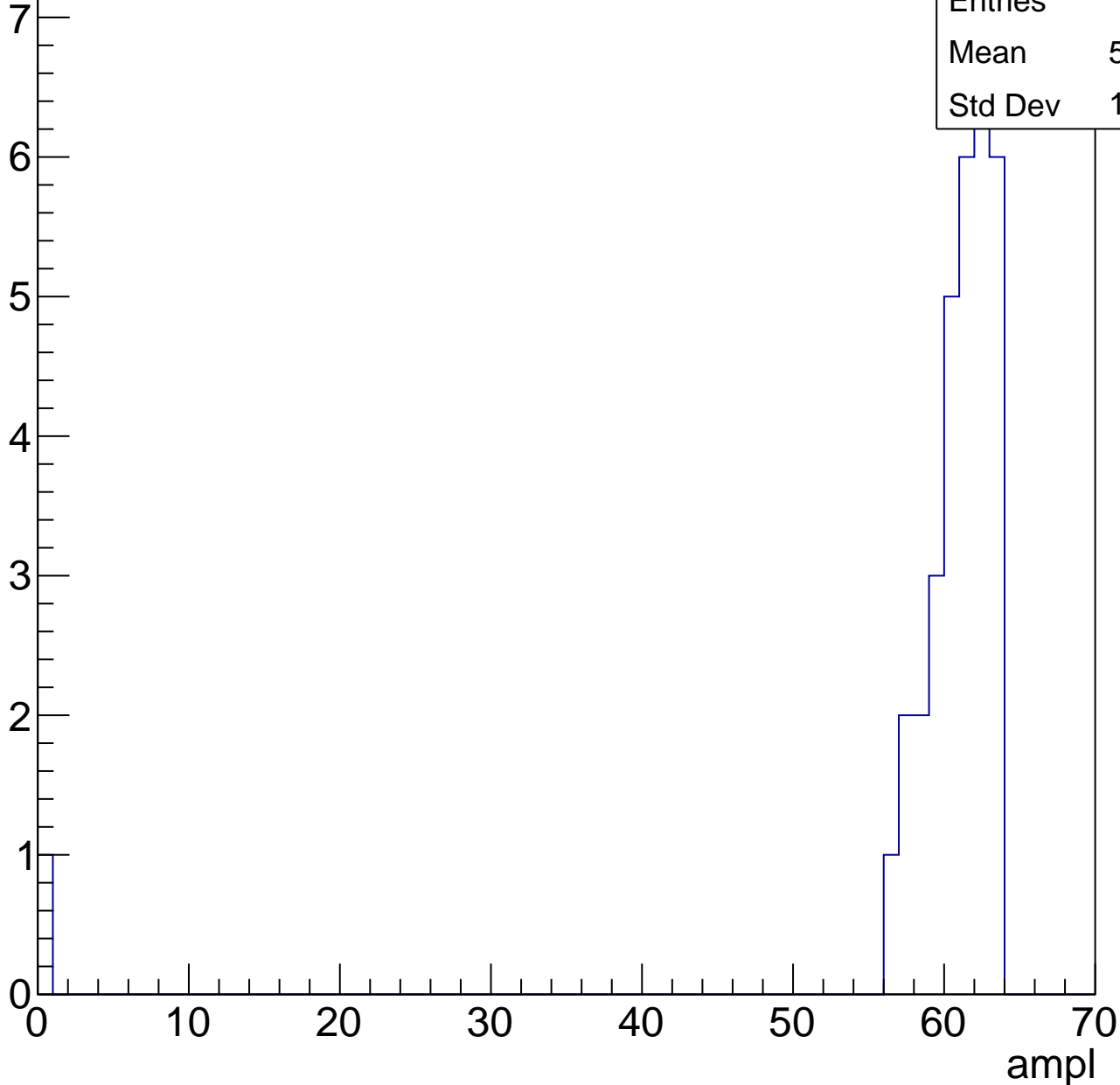


B1L101S, U21-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.82
Std Dev	10.57



B1L101S, U21-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch24, adc0

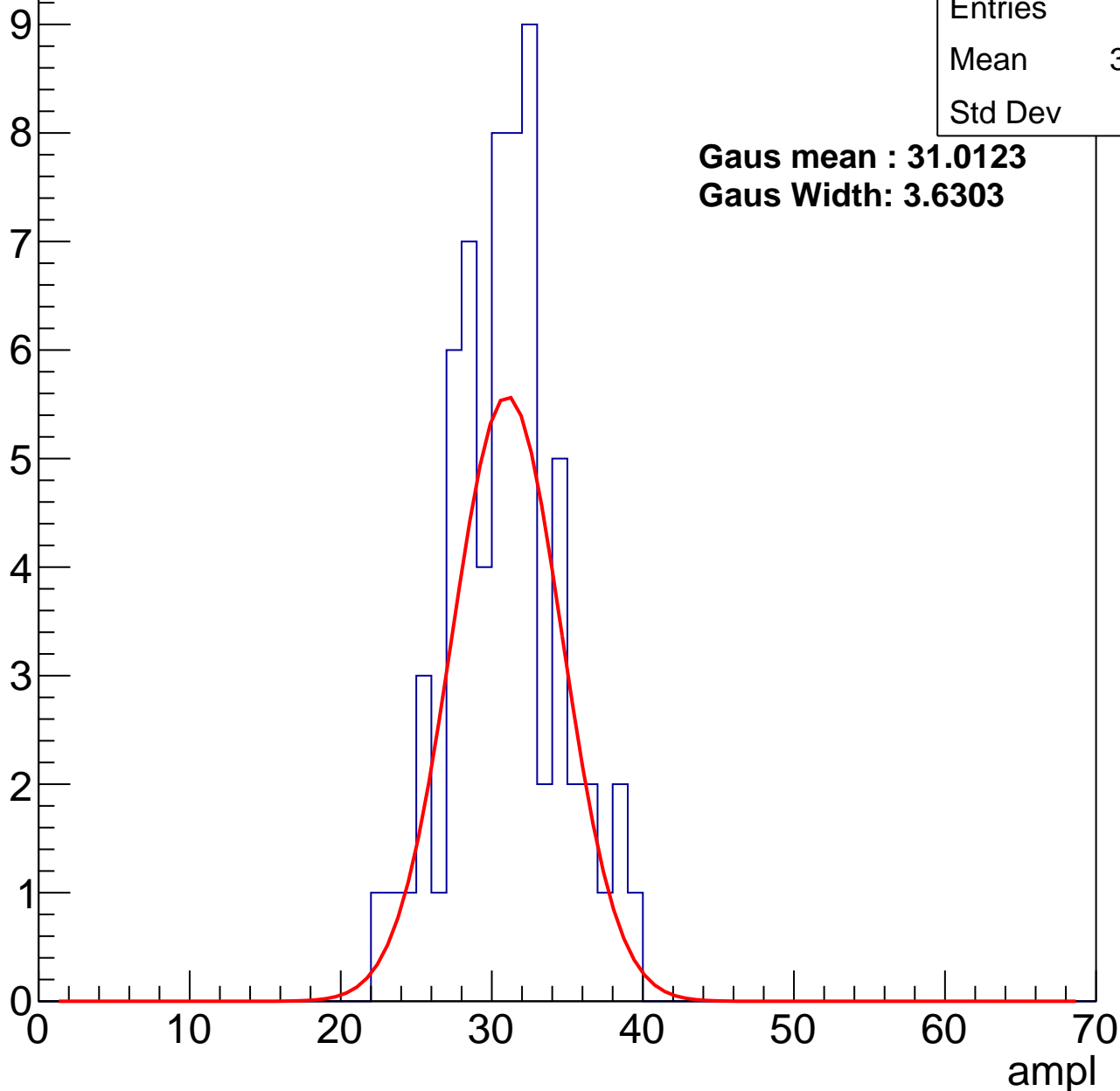
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.47
Std Dev	3.61

Gaus mean : 31.0123

Gaus Width: 3.6303



B1L101S, U21-ch24, adc1

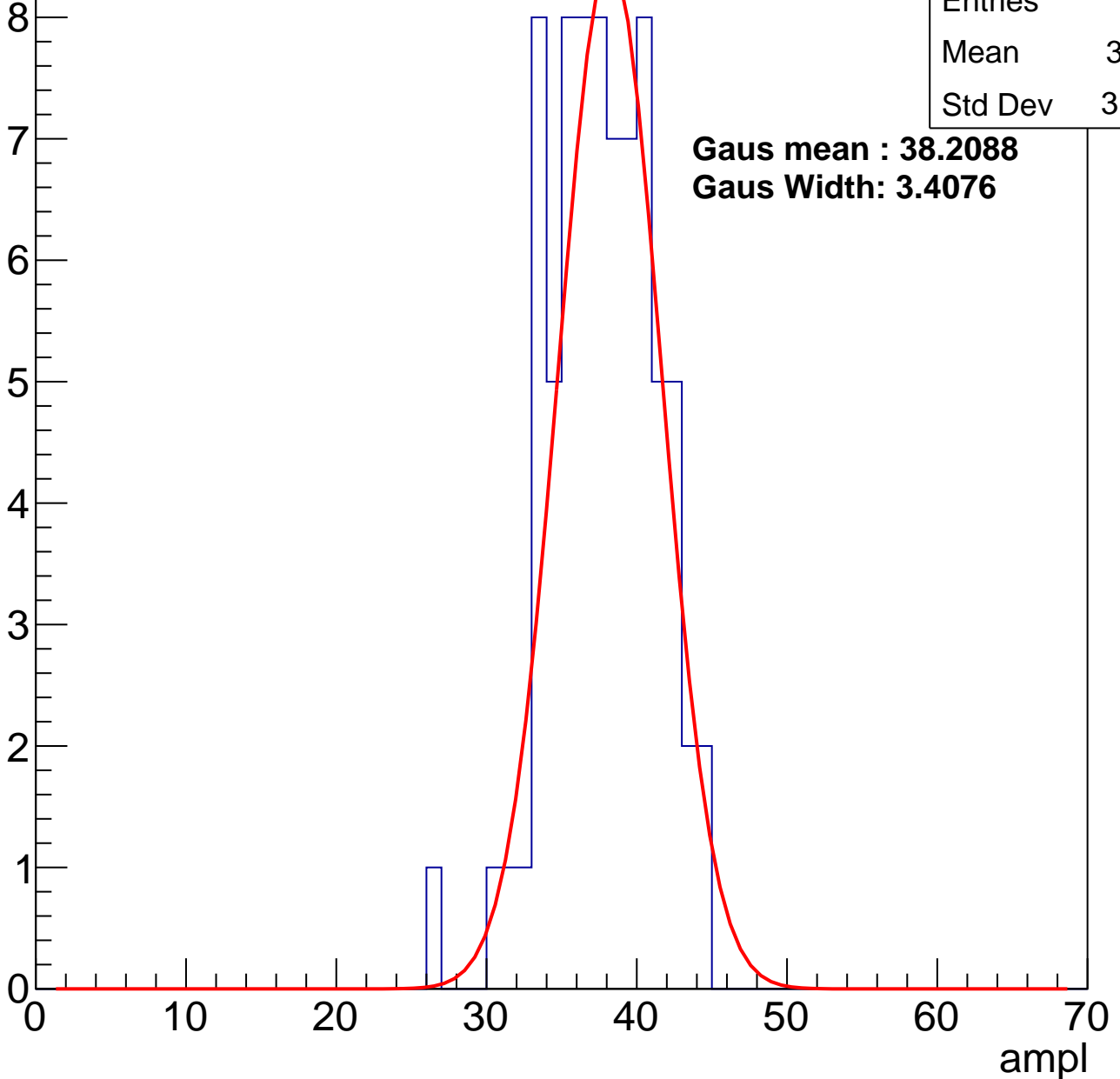
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	37.21
Std Dev	3.462

Gaus mean : 38.2088

Gaus Width: 3.4076



B1L101S, U21-ch24, adc2

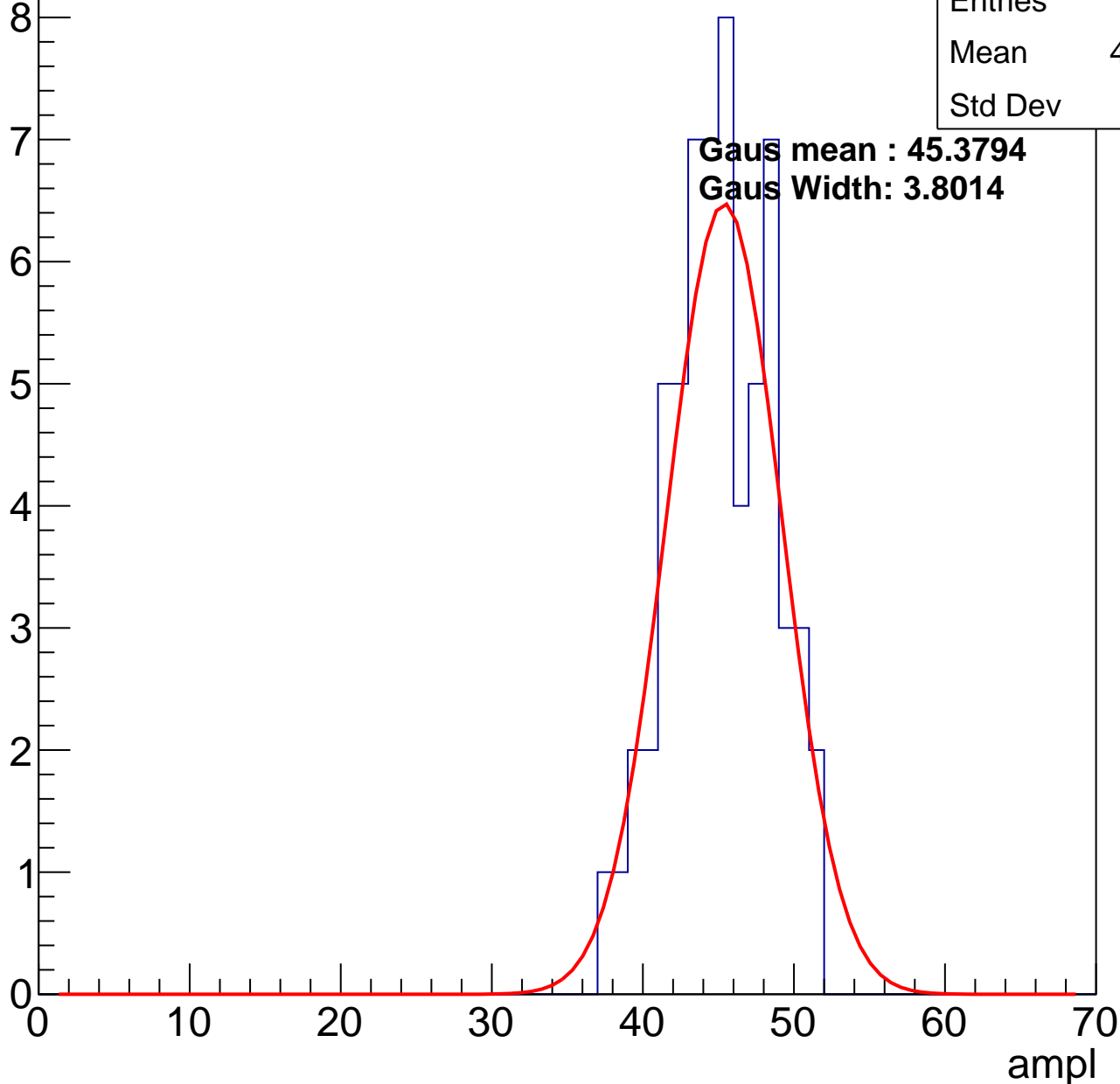
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.69
Std Dev	3.29

Gaus mean : 45.3794

Gaus Width: 3.8014

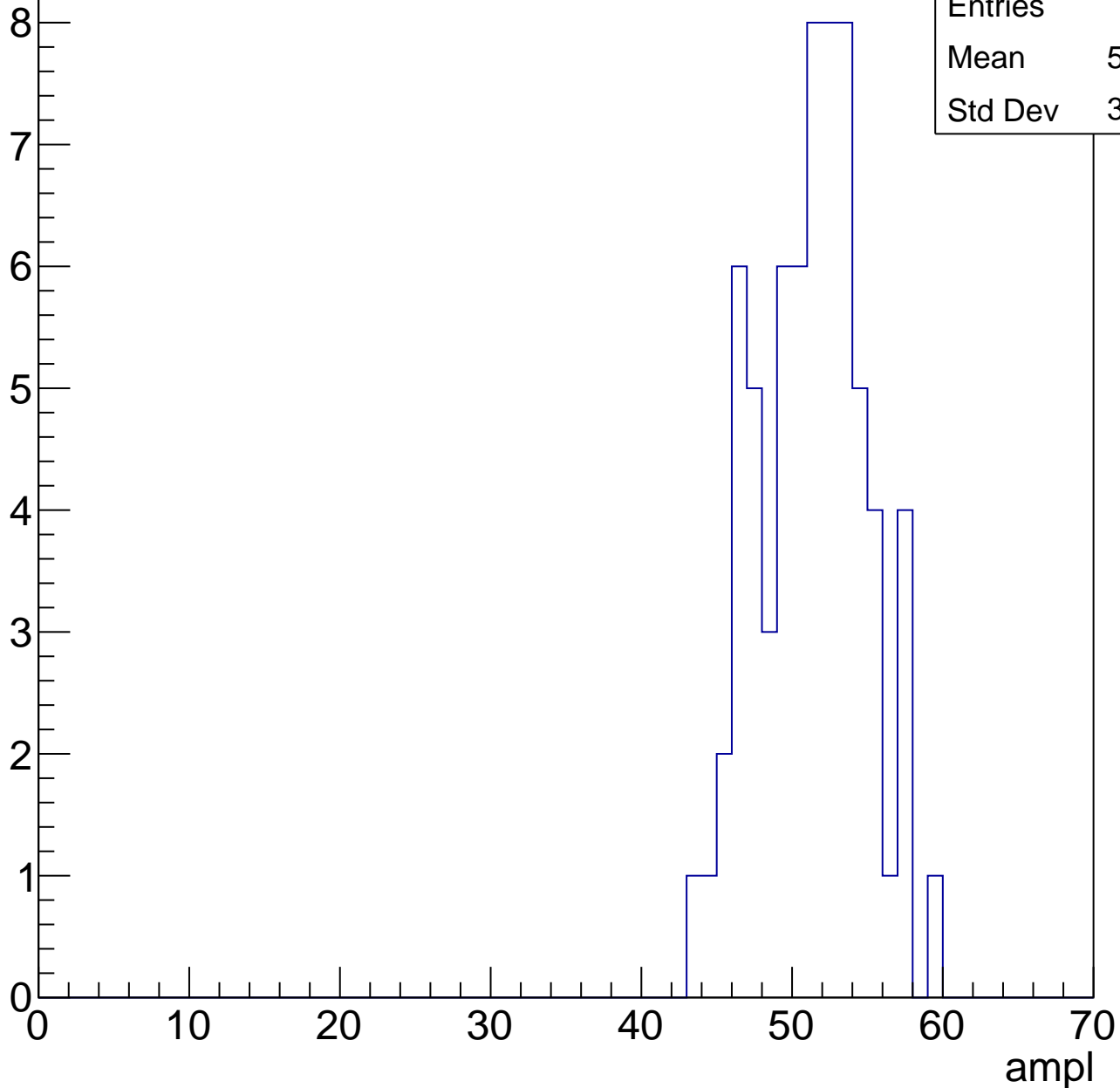


B1L101S, U21-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.83
Std Dev	3.514

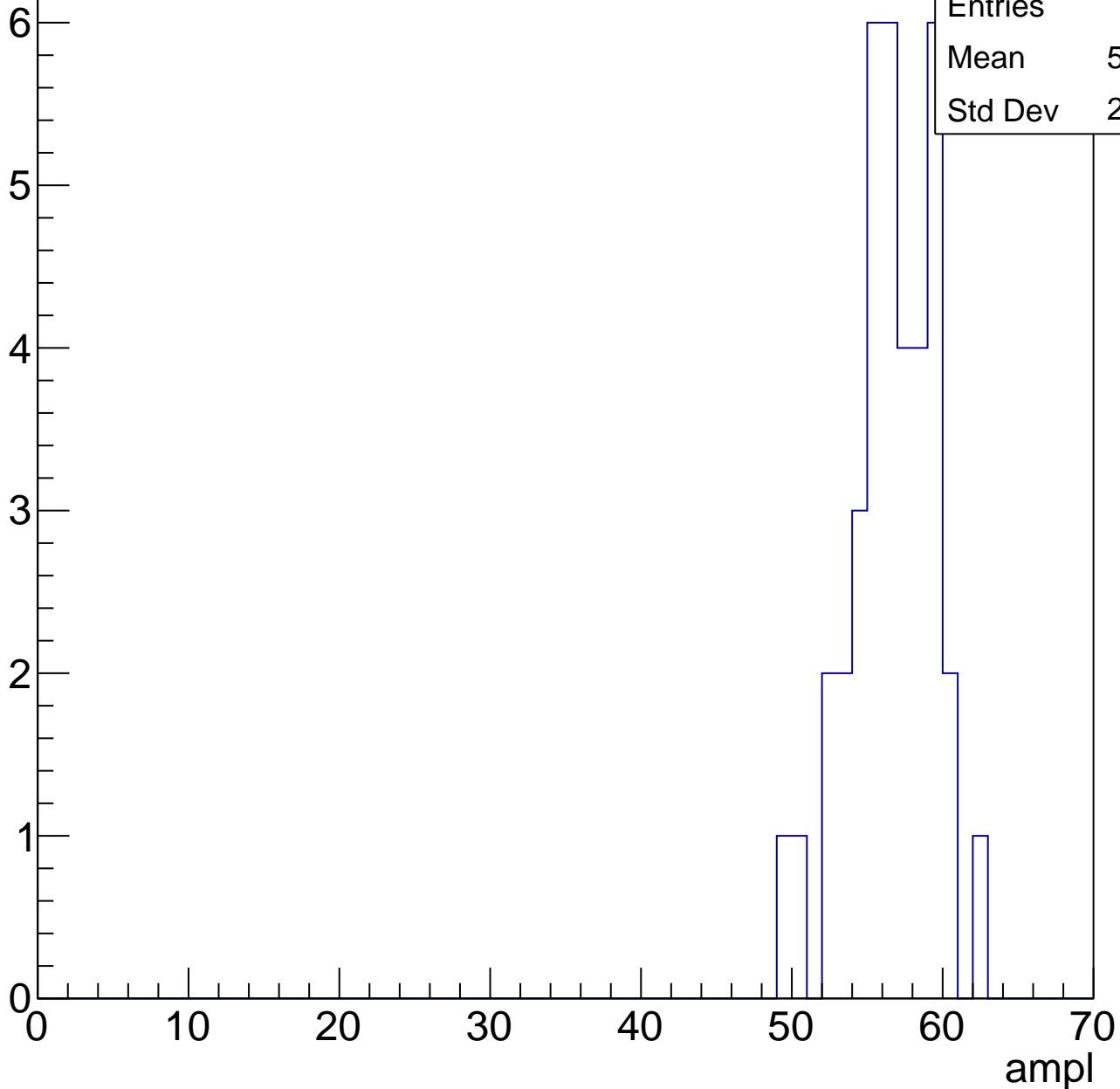


B1L101S, U21-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	56.13
Std Dev	2.783

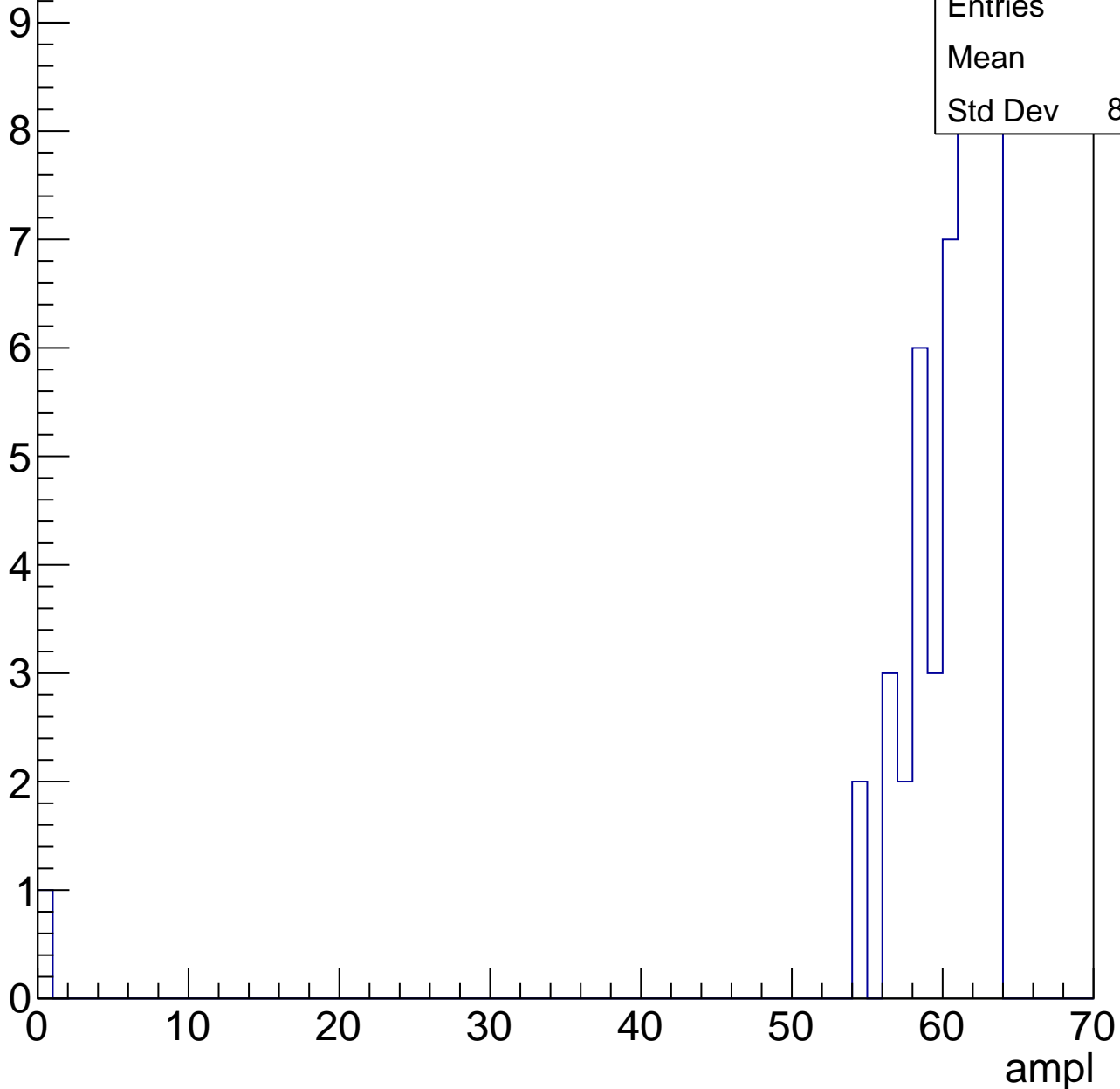


B1L101S, U21-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

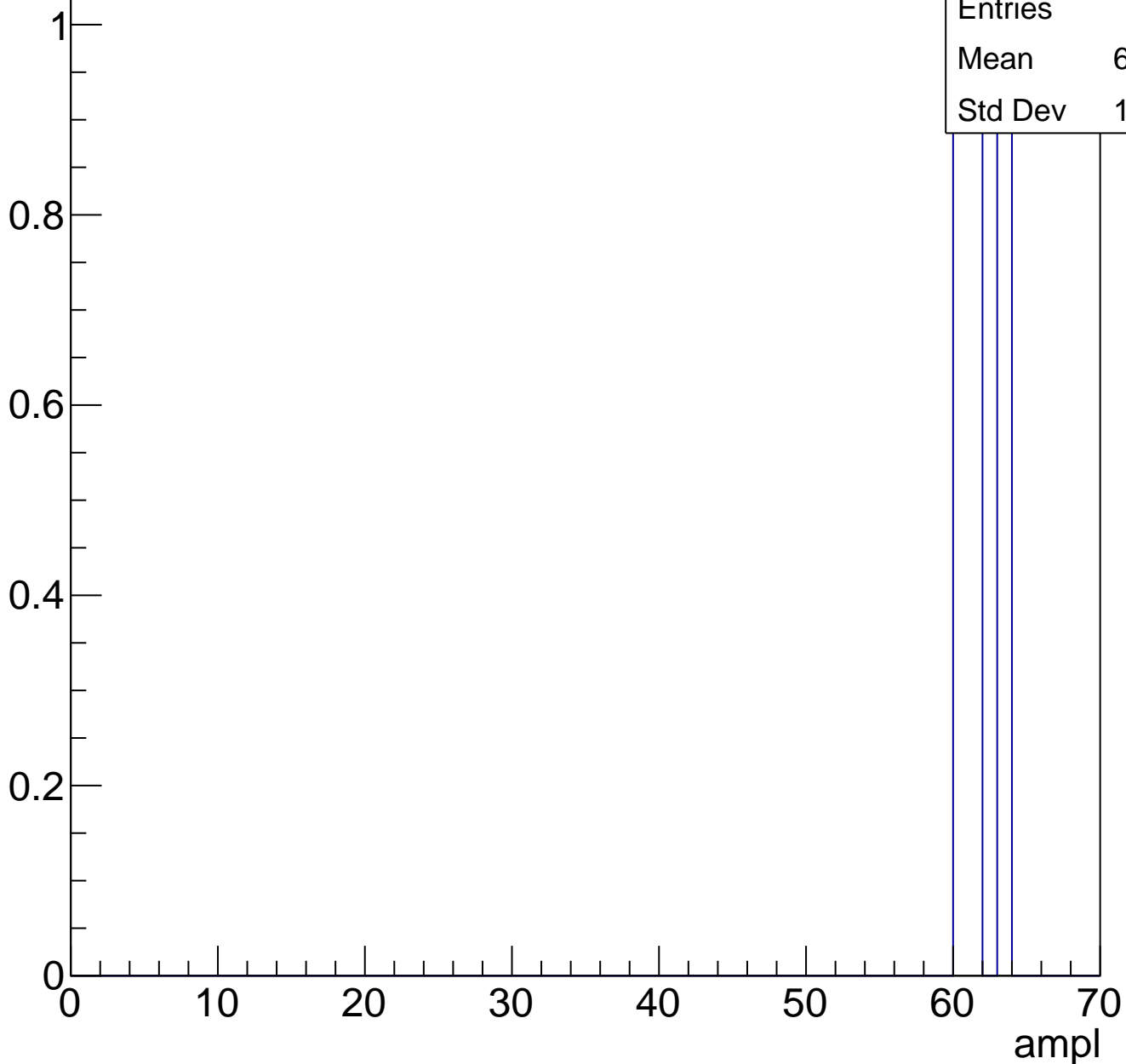
Entries	51
Mean	59
Std Dev	8.677



B1L101S, U21-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch25, adc0

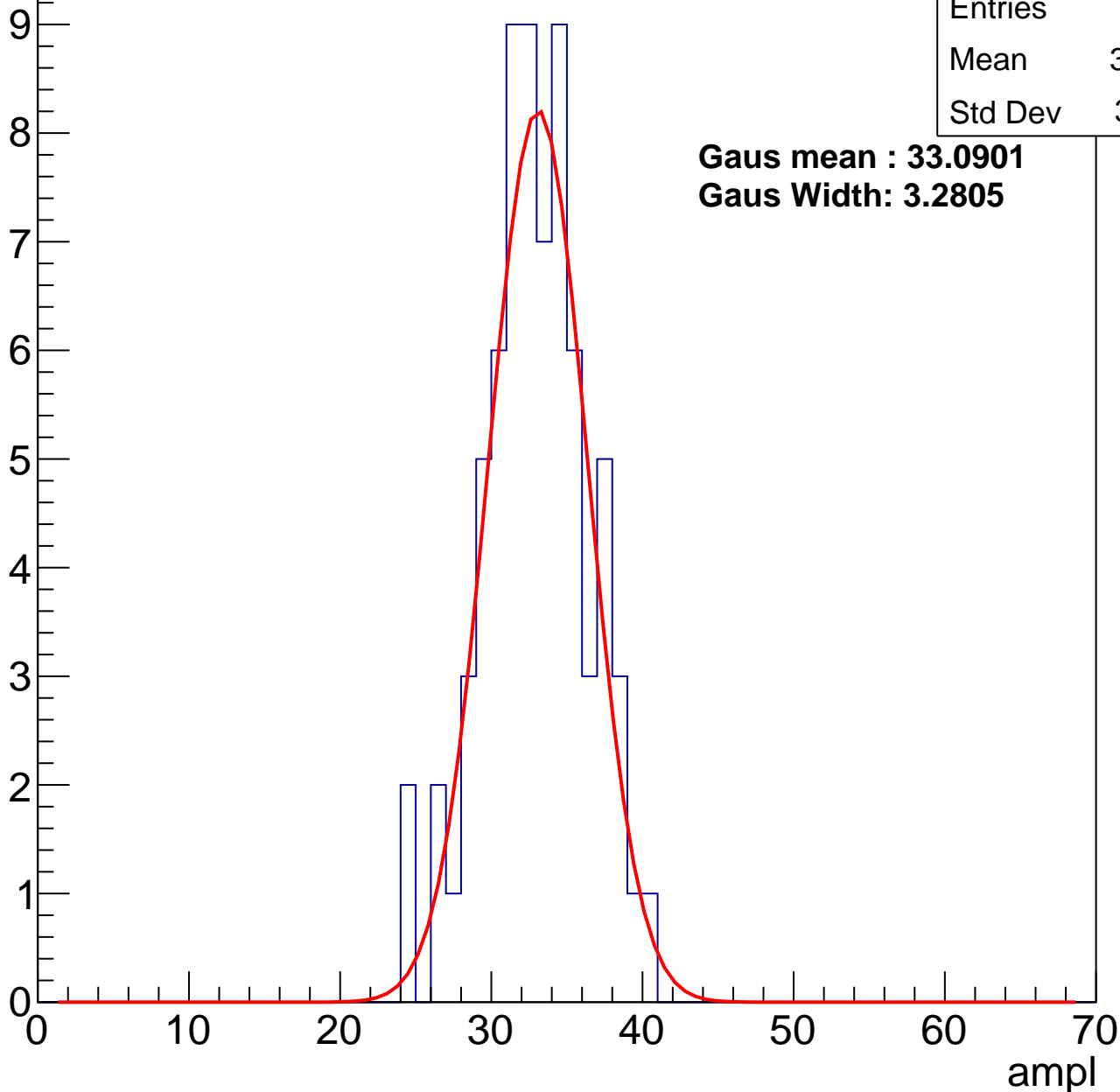
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	32.44
Std Dev	3.391

Gaus mean : 33.0901

Gaus Width: 3.2805



B1L101S, U21-ch25, adc1

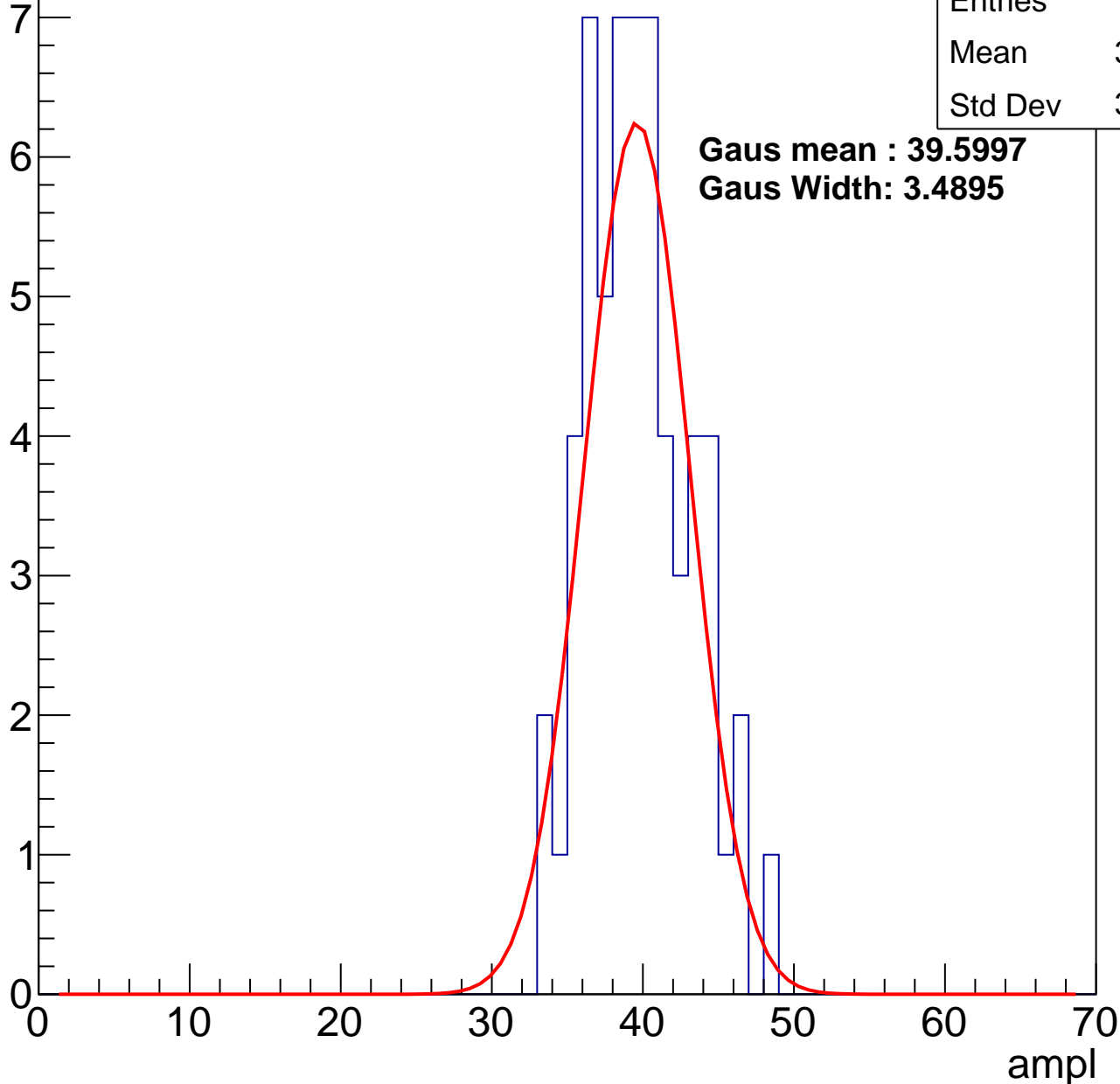
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	39.31
Std Dev	3.381

Gaus mean : 39.5997

Gaus Width: 3.4895



B1L101S, U21-ch25, adc2

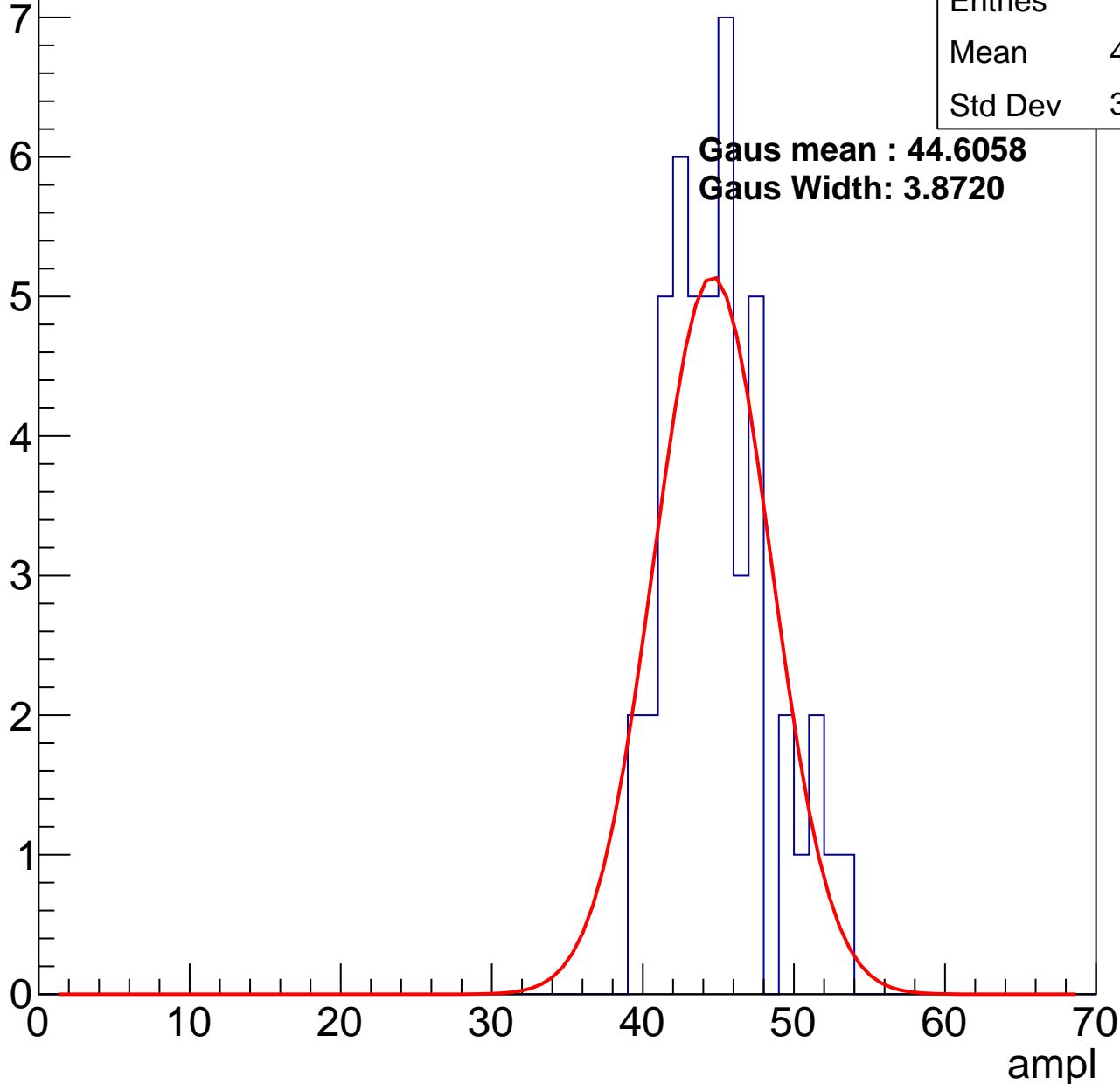
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	44.53
Std Dev	3.376

Gaus mean : 44.6058

Gaus Width: 3.8720

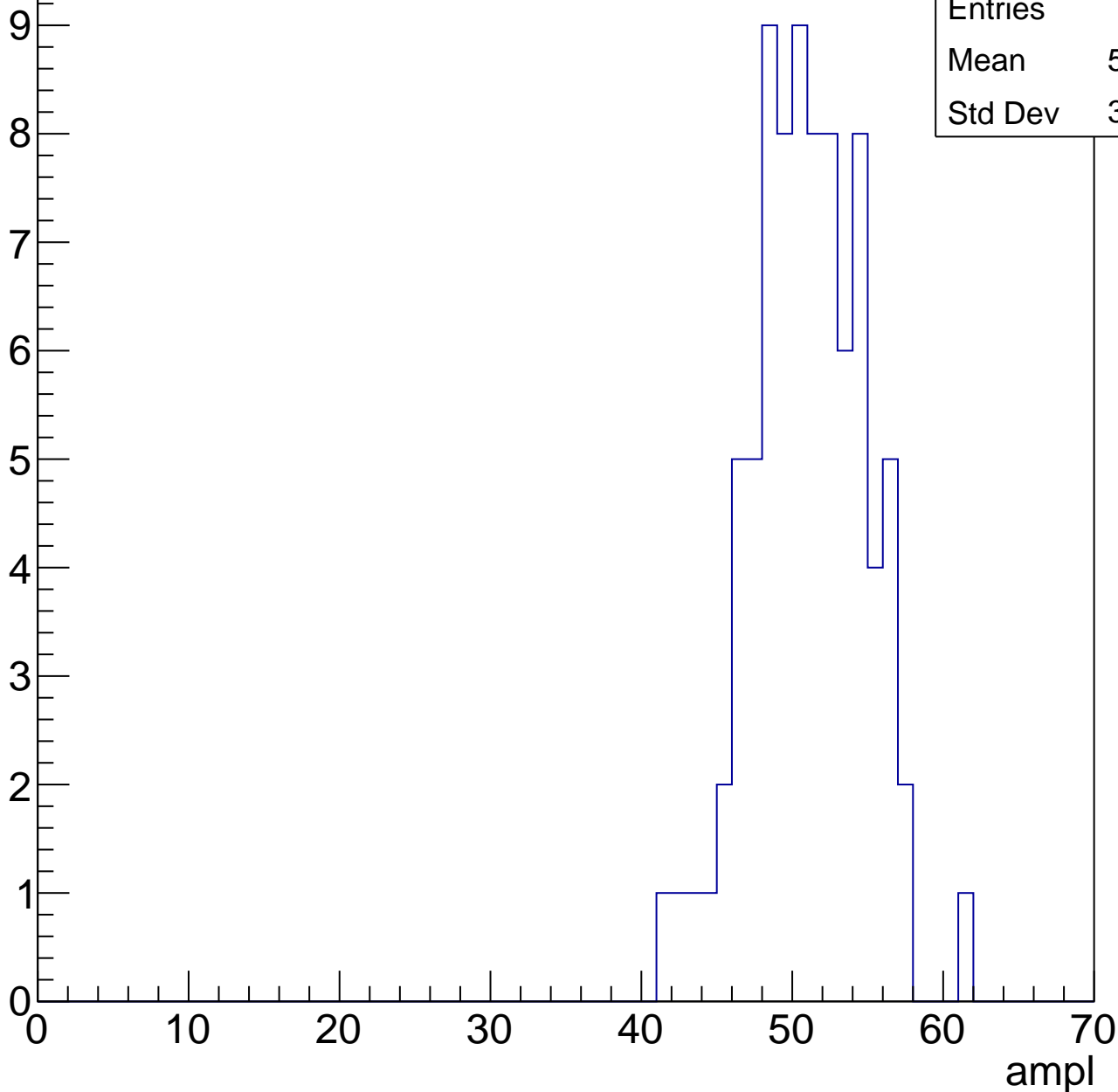


B1L101S, U21-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	50.57
Std Dev	3.688

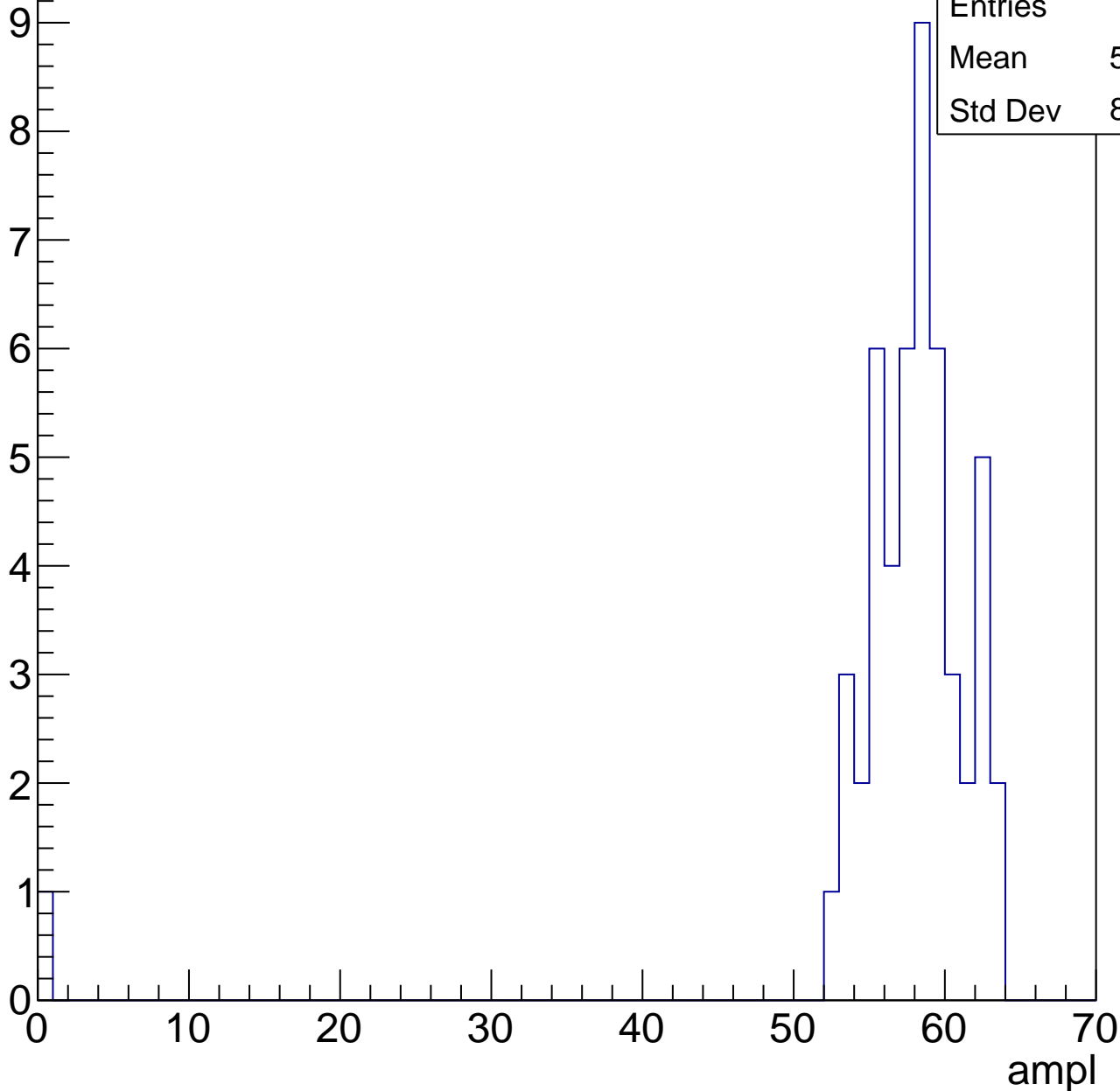


B1L101S, U21-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	56.58
Std Dev	8.542



B1L101S, U21-ch25, adc5

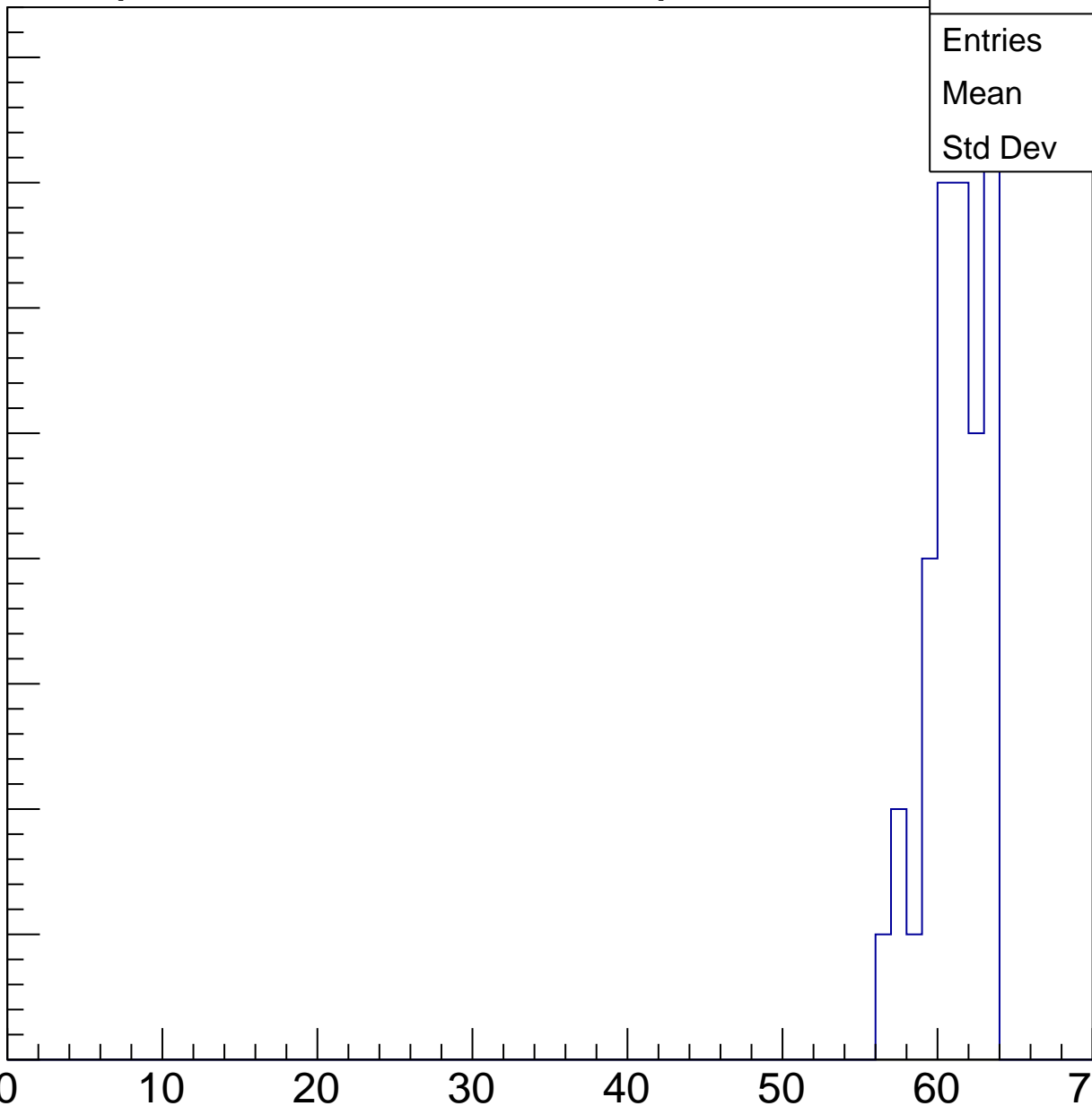
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	35
Mean	60.71
Std Dev	1.876

ampl



B1L101S, U21-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch26, adc0

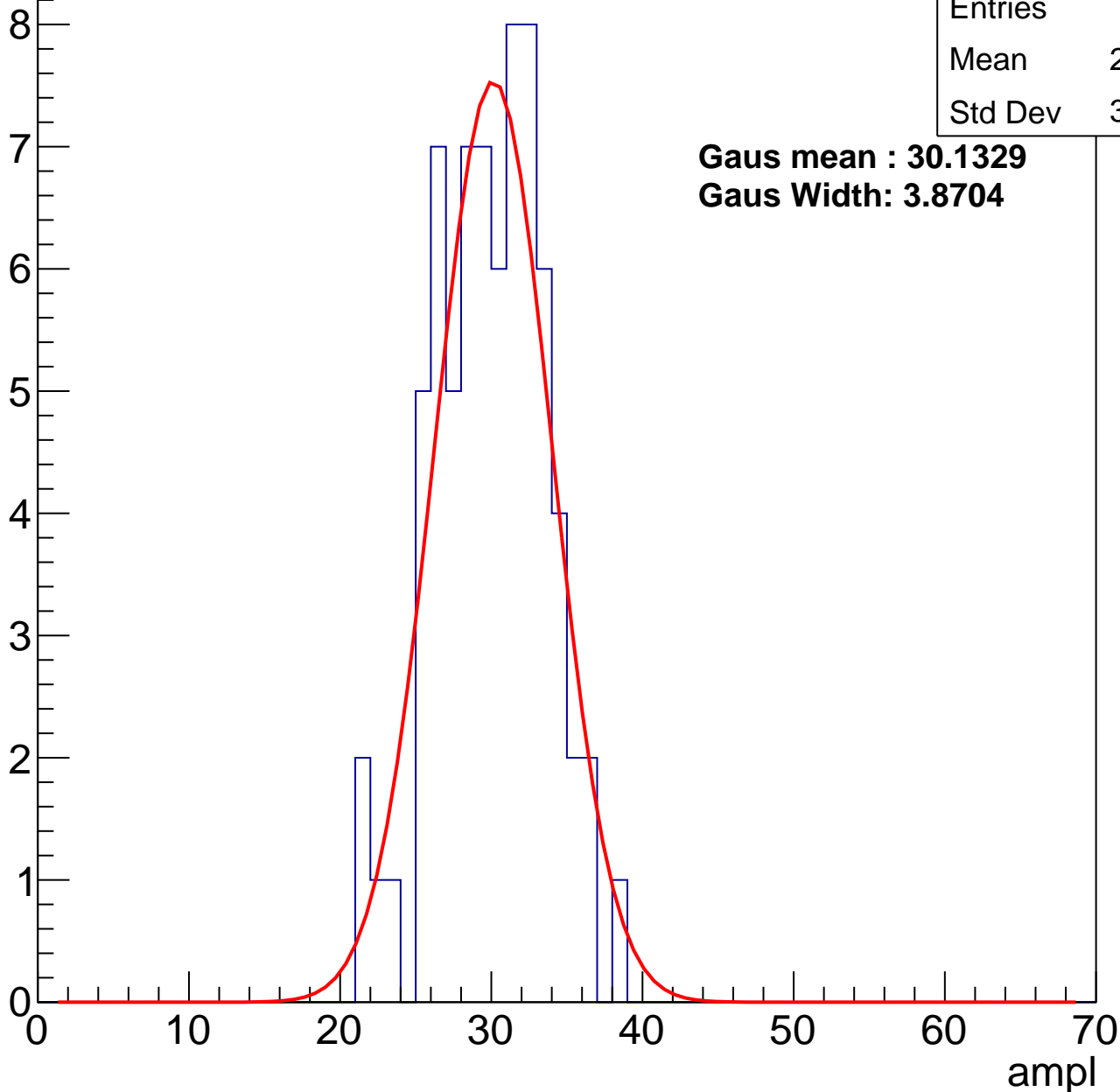
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.53
Std Dev	3.574

Gaus mean : 30.1329

Gaus Width: 3.8704



B1L101S, U21-ch26, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	37.22
Std Dev	3.299

Gaus mean : 37.5660

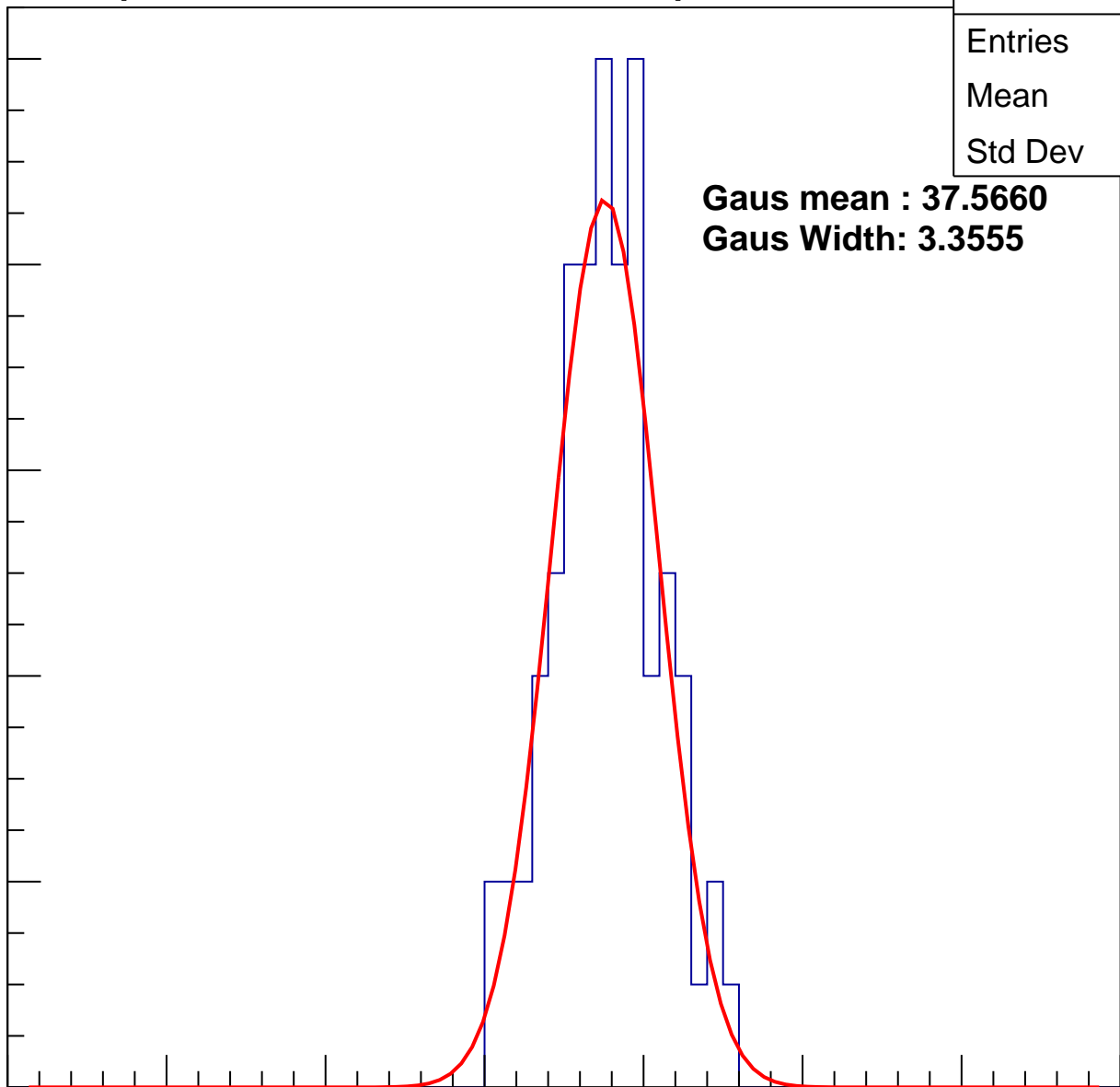
Gaus Width: 3.3555

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch26, adc2

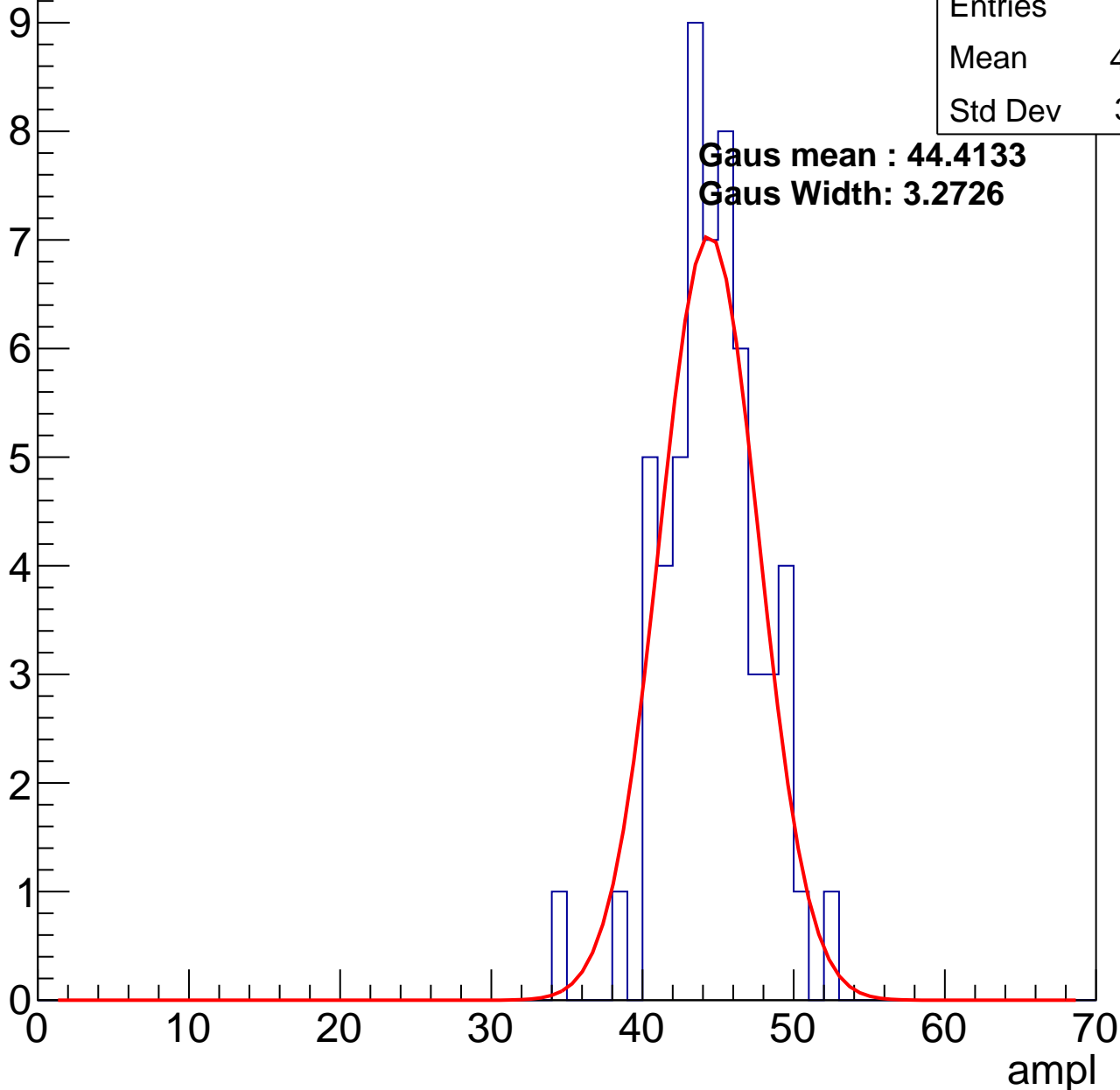
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.14
Std Dev	3.181

Gaus mean : 44.4133

Gaus Width: 3.2726

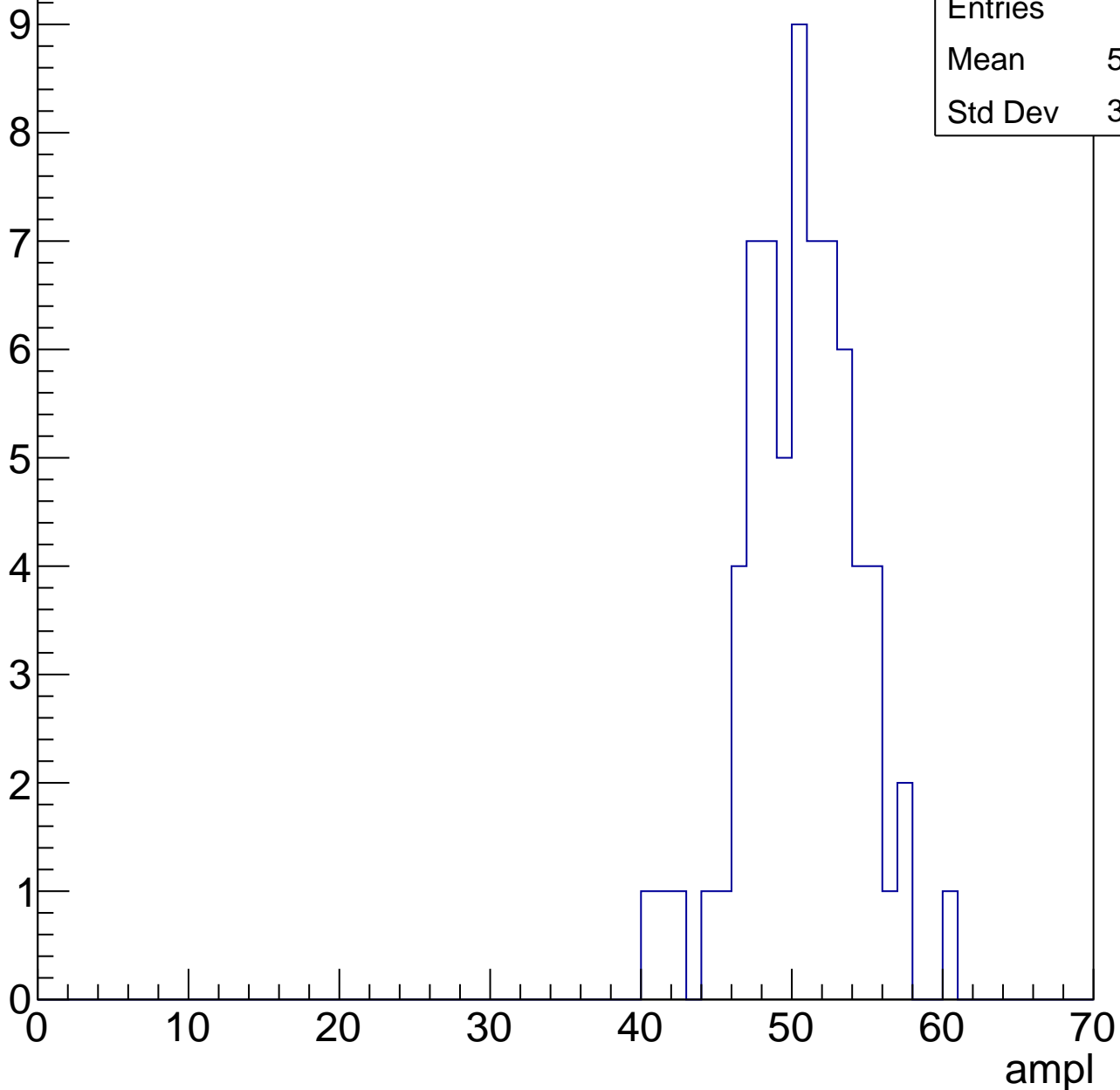


B1L101S, U21-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.16
Std Dev	3.713

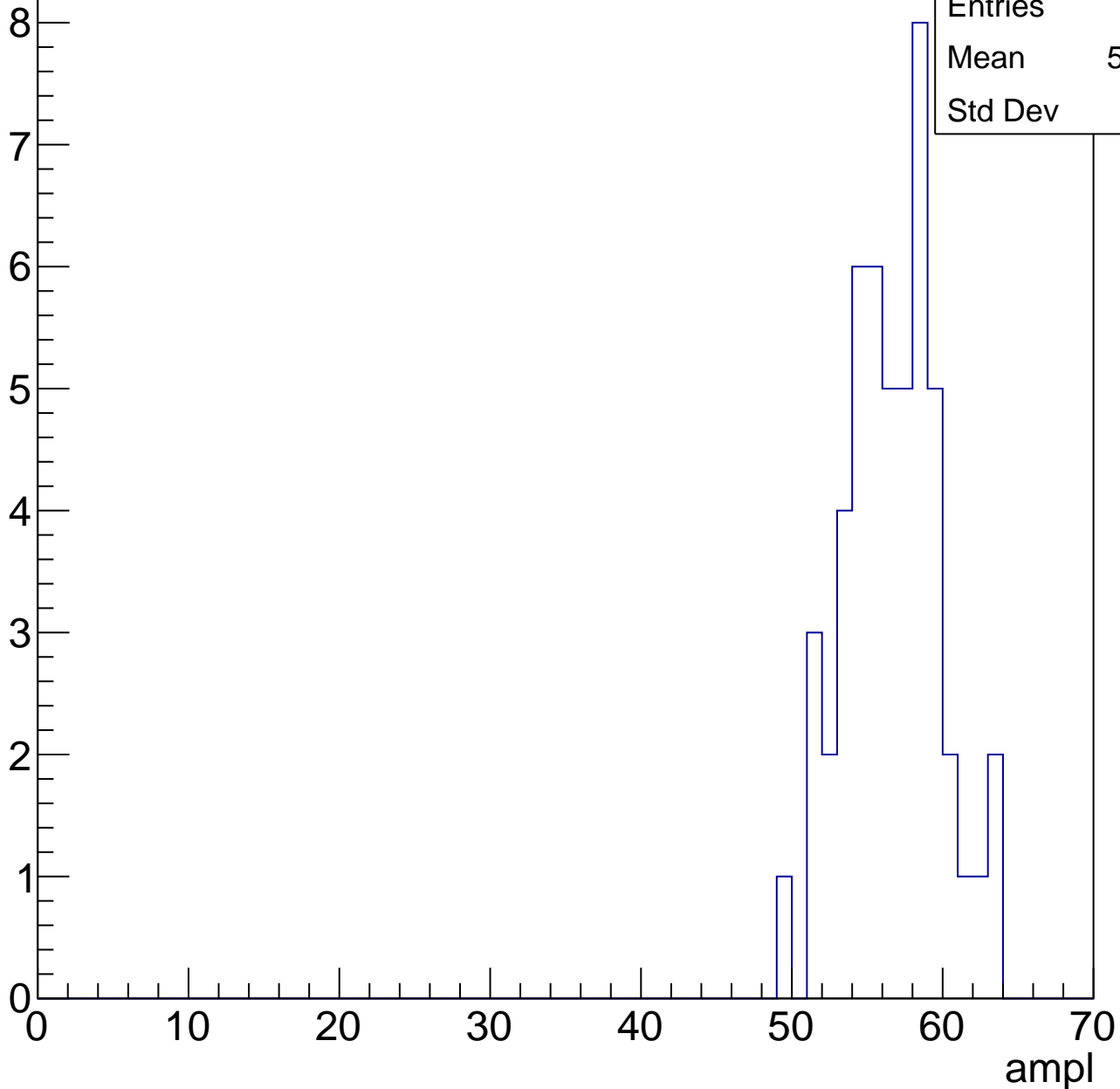


B1L101S, U21-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

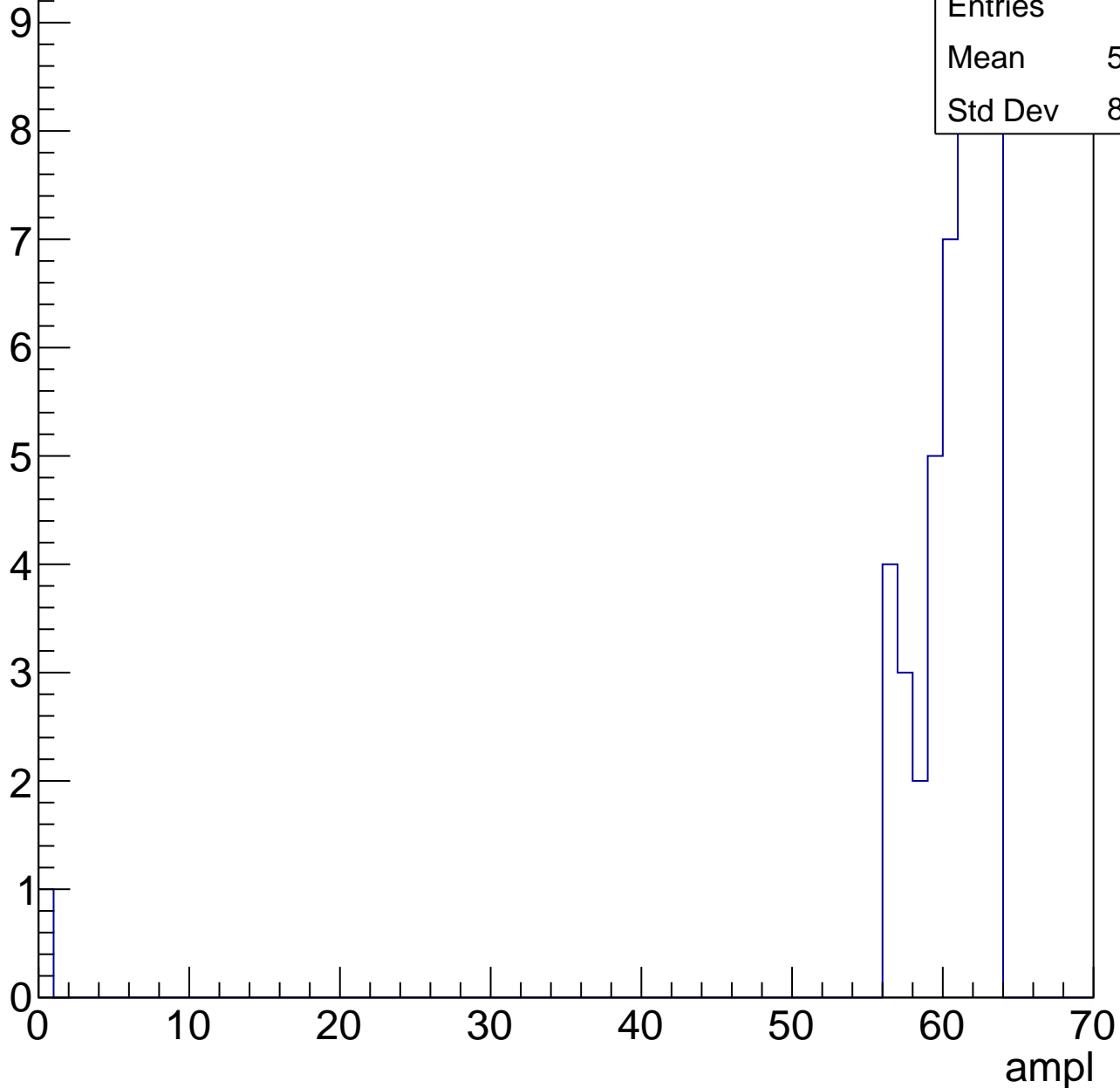
Entries	51
Mean	56.18
Std Dev	3.11



B1L101S, U21-ch26, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch27, adc0

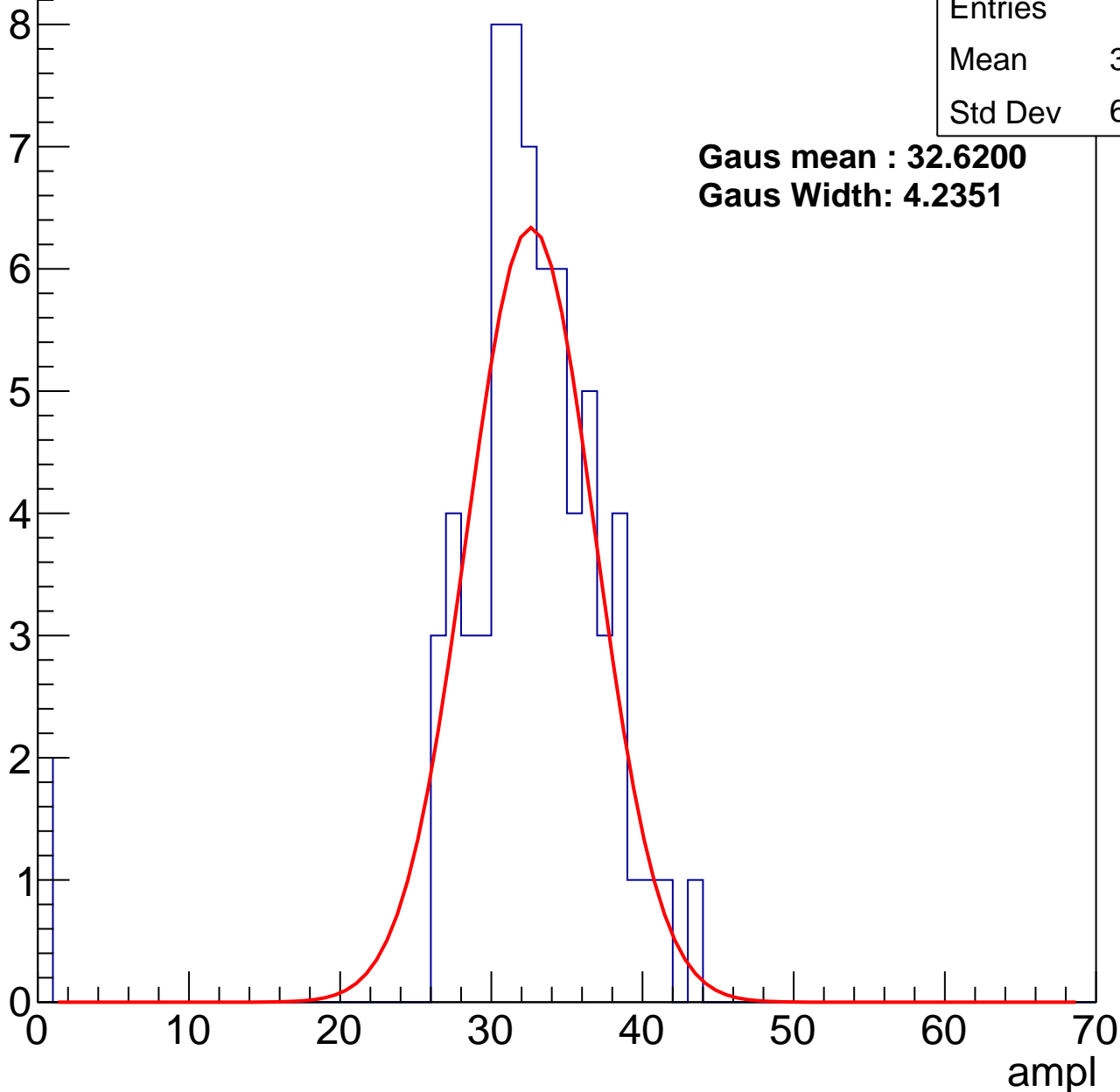
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	31.67
Std Dev	6.594

Gaus mean : 32.6200

Gaus Width: 4.2351



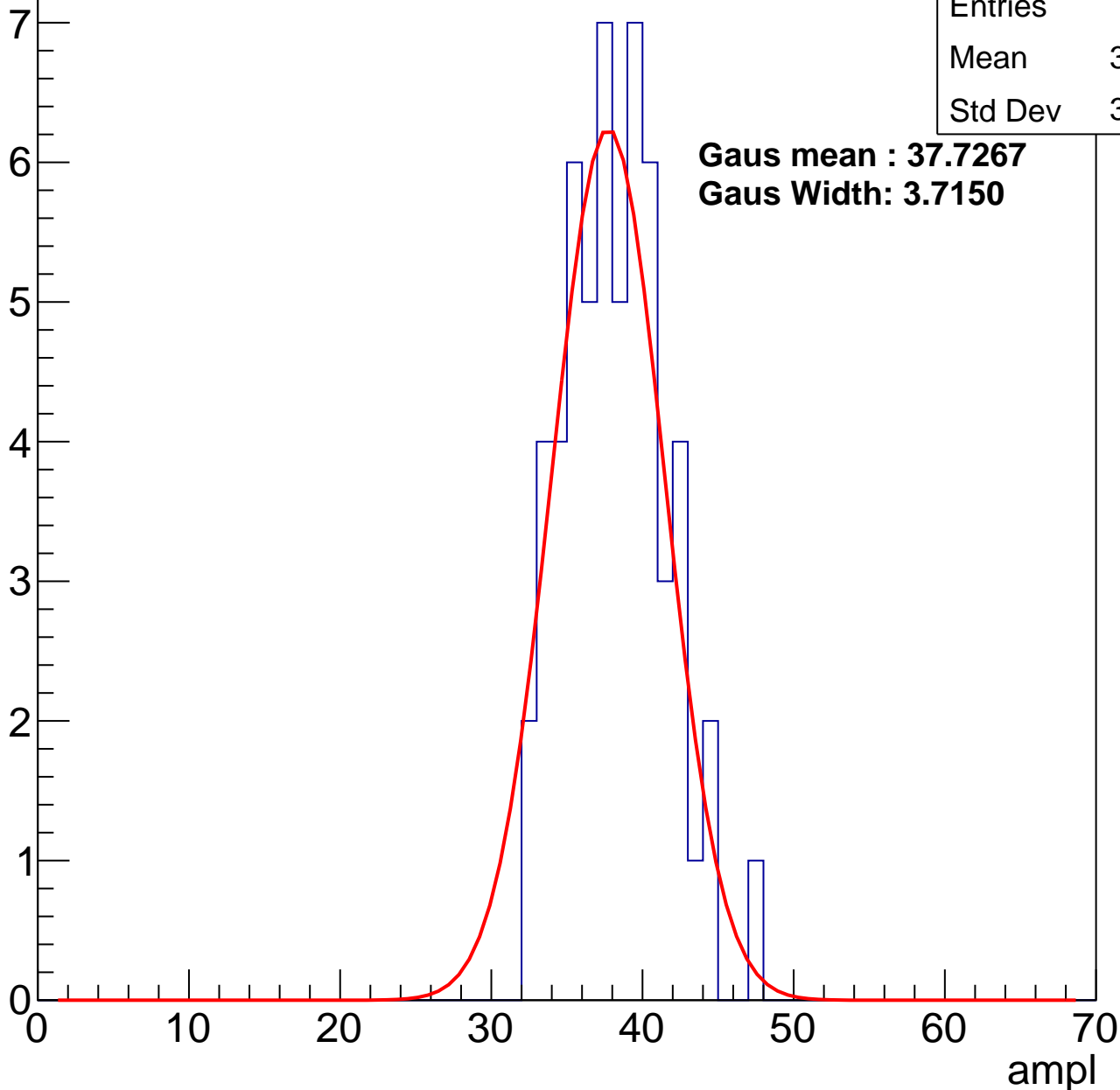
B1L101S, U21-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.77
Std Dev	3.282

Gaus mean : 37.7267
Gaus Width: 3.7150



B1L101S, U21-ch27, adc2

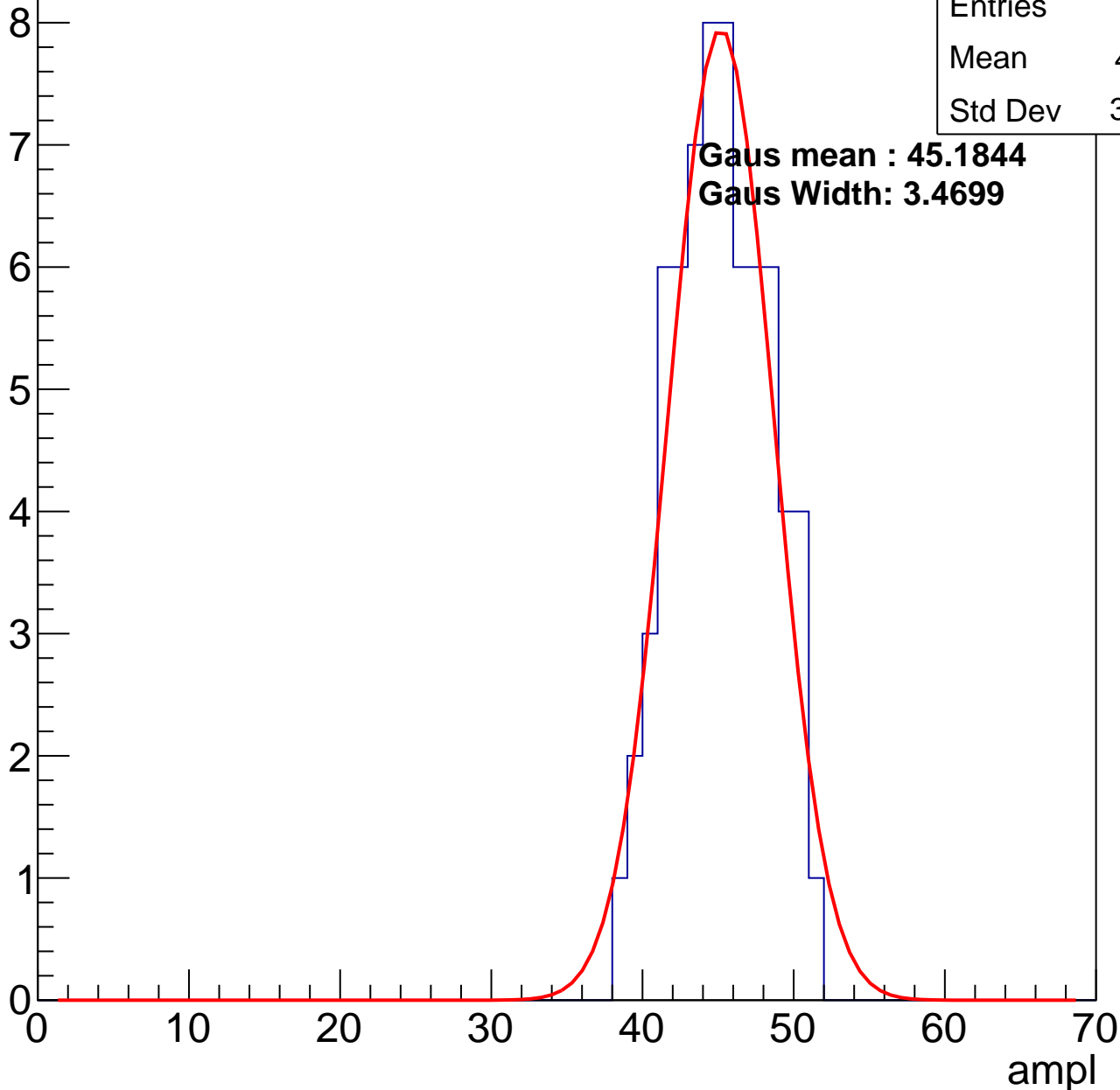
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.71
Std Dev	3.116

Gaus mean : 45.1844

Gaus Width: 3.4699



B1L101S, U21-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	52.08
Std Dev	3.854

Entry

10

8

6

4

2

0

0

10

20

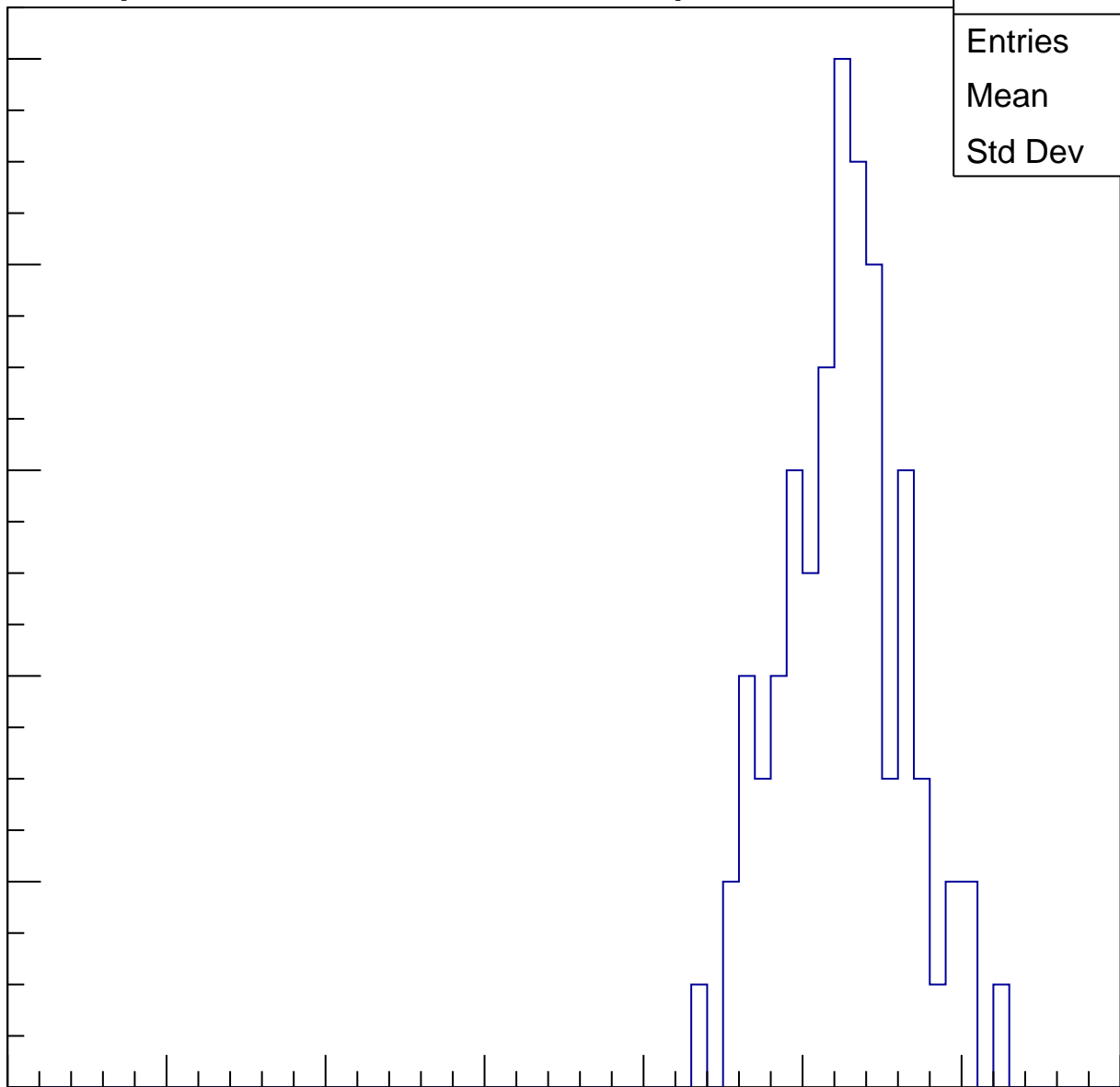
30

40

50

60

ampl

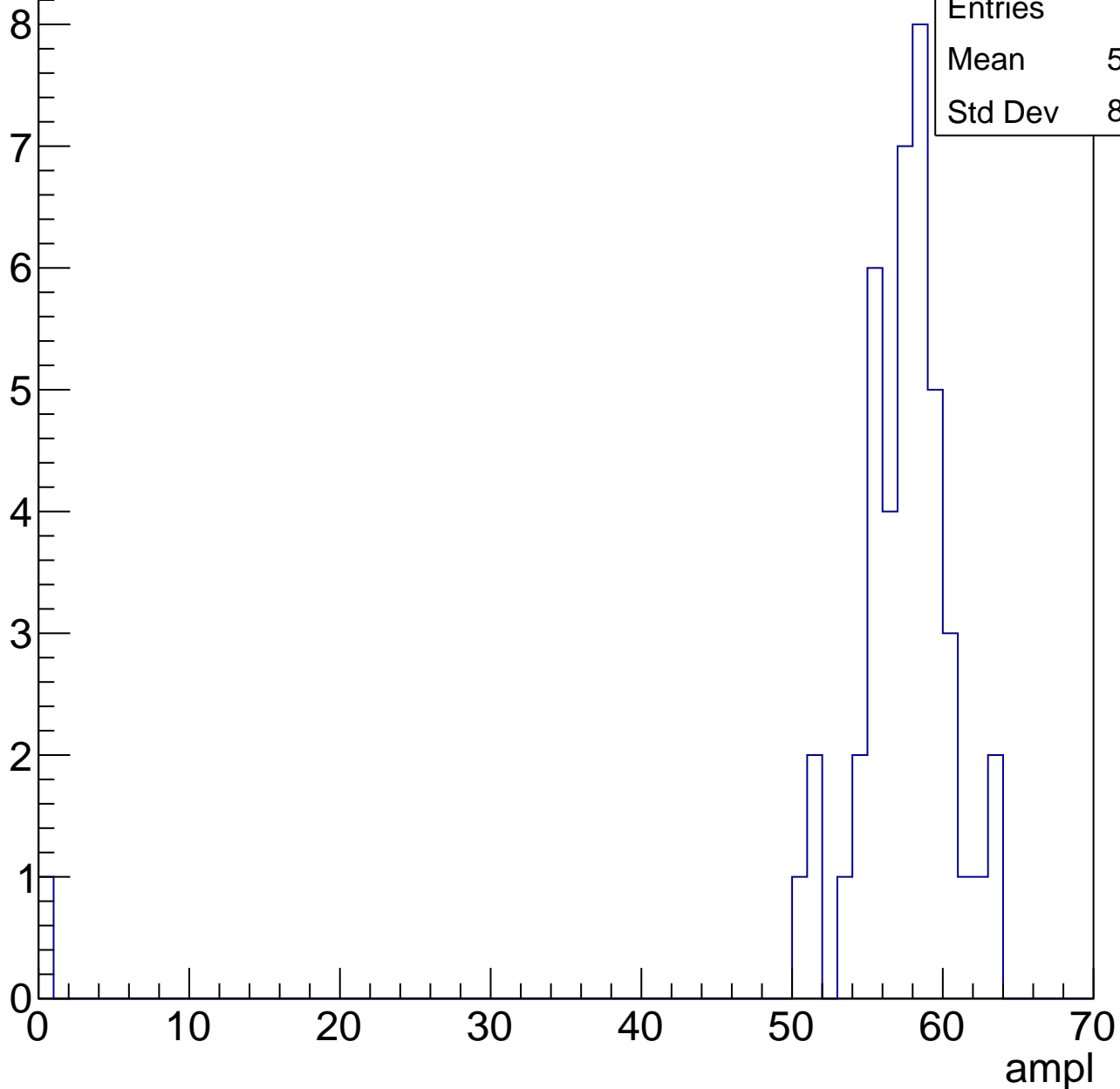


B1L101S, U21-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	55.77
Std Dev	8.962

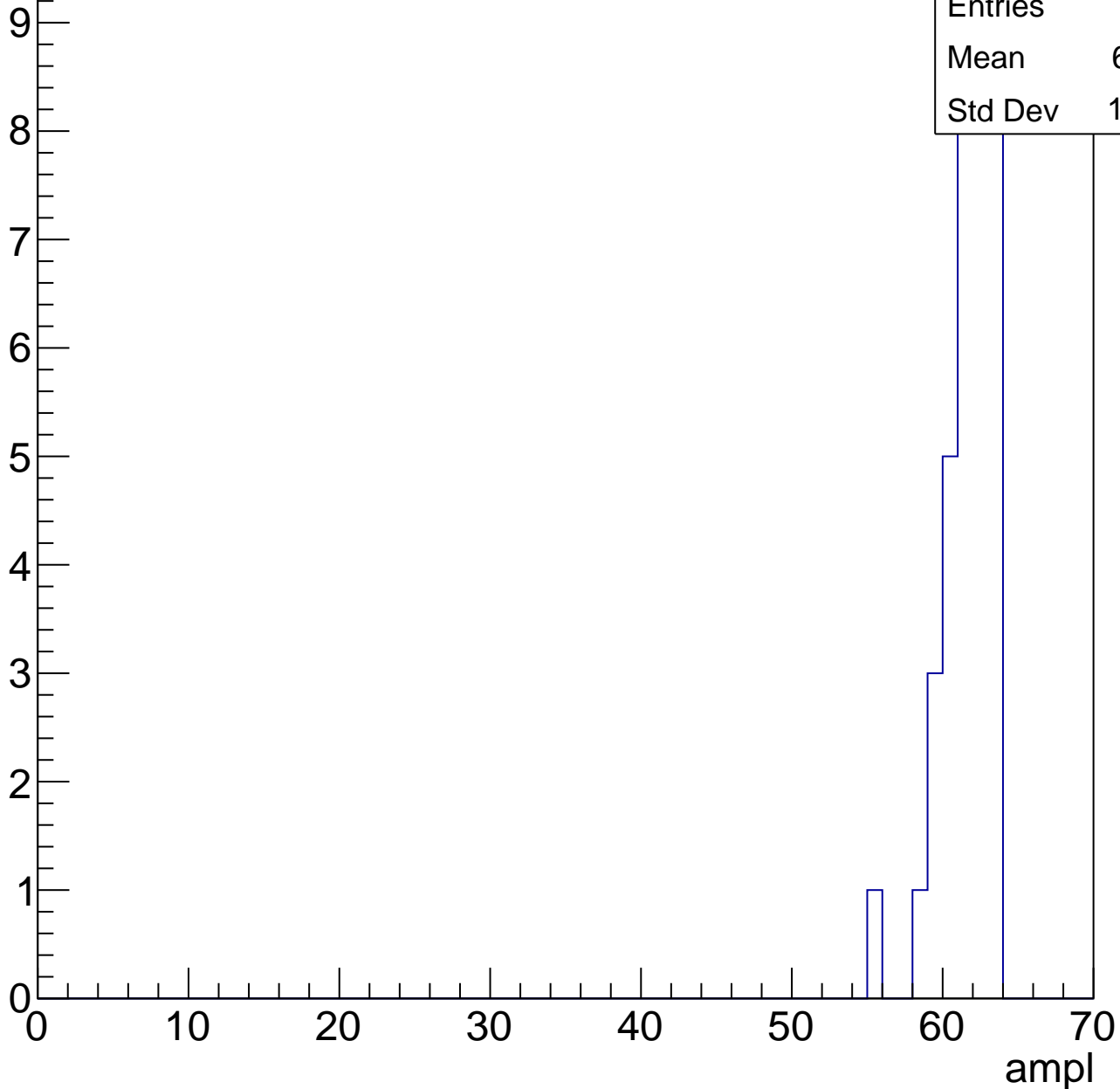


B1L101S, U21-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	61.11
Std Dev	1.703



B1L101S, U21-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	12.5
Std Dev	12.5

B1L101S, U21-ch28, adc0

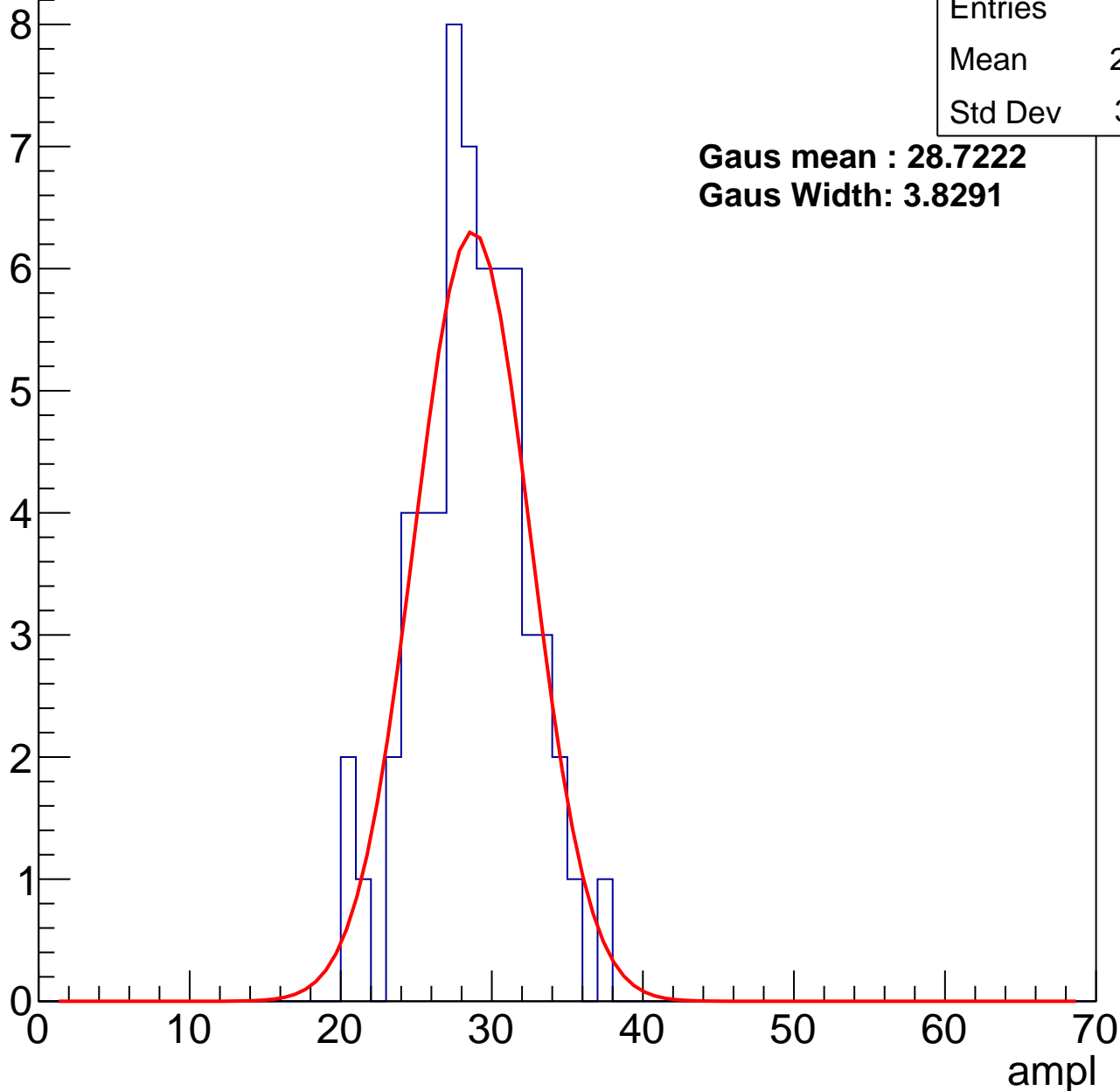
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.23
Std Dev	3.551

Gaus mean : 28.7222

Gaus Width: 3.8291



B1L101S, U21-ch28, adc1

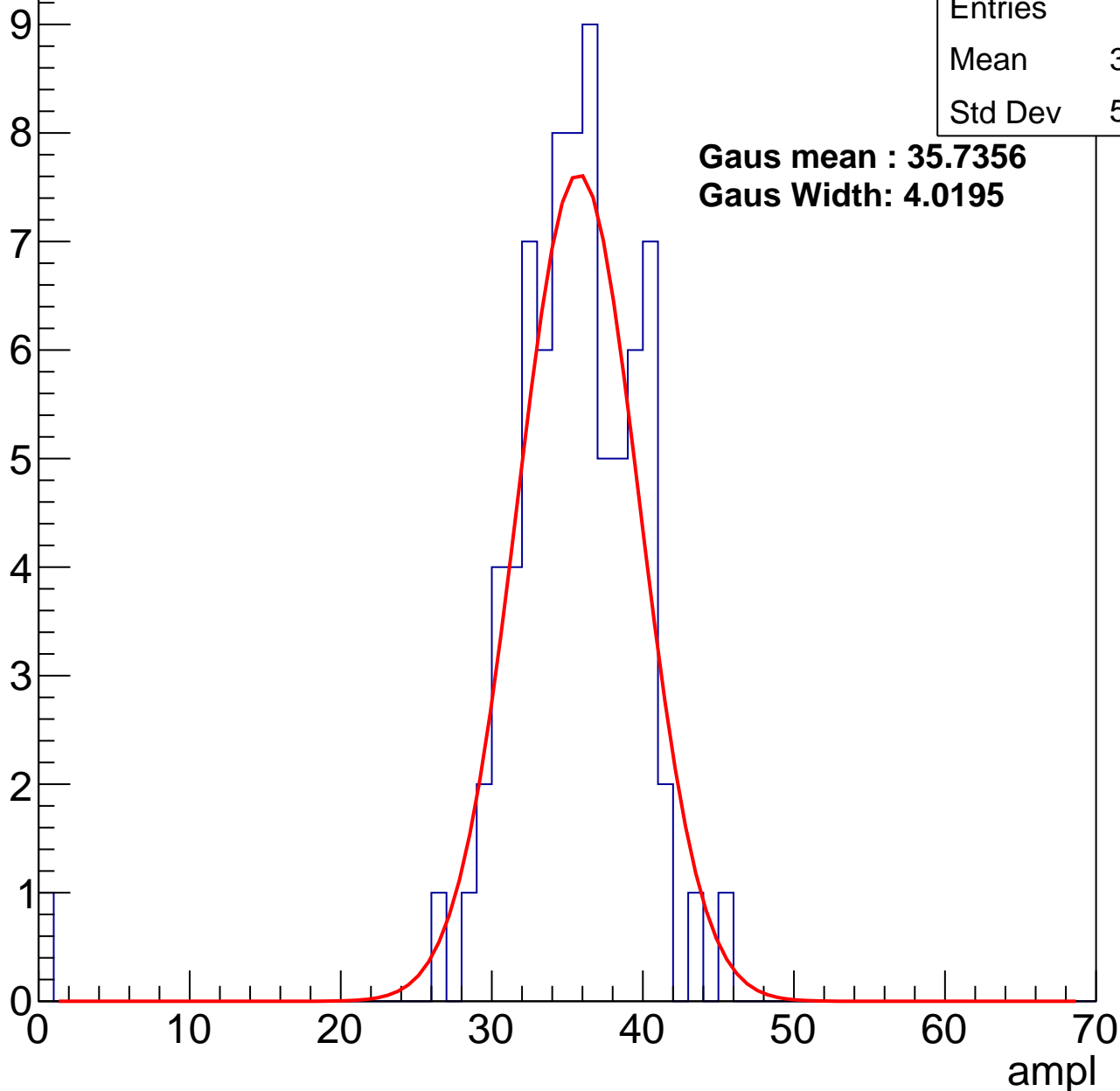
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	34.78
Std Dev	5.387

Gaus mean : 35.7356

Gaus Width: 4.0195



B1L101S, U21-ch28, adc2

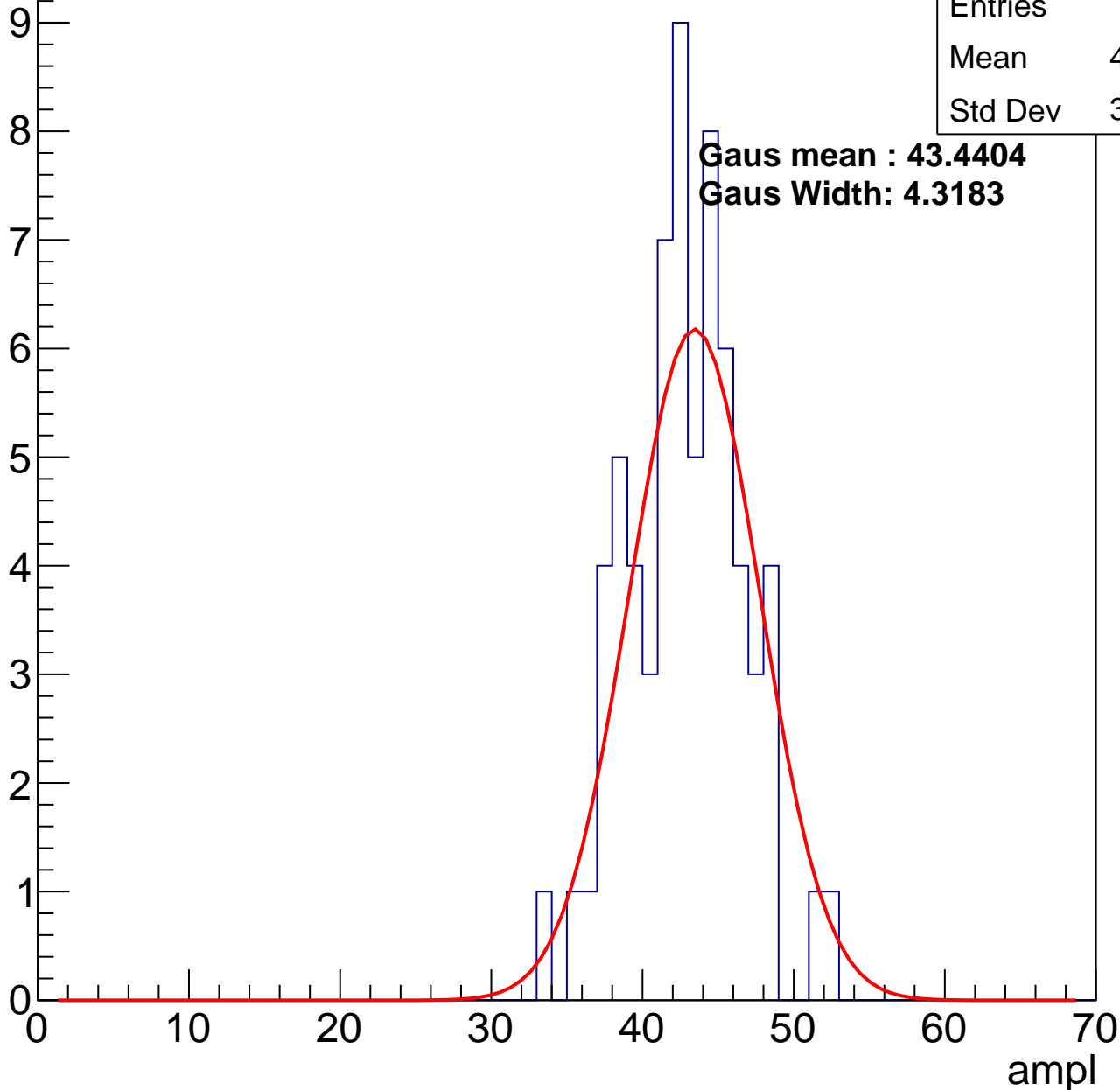
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.39
Std Dev	3.757

Gaus mean : 43.4404

Gaus Width: 4.3183



B1L101S, U21-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	48.3
Std Dev	3.313

Entry

10

8

6

4

2

0

0

10

20

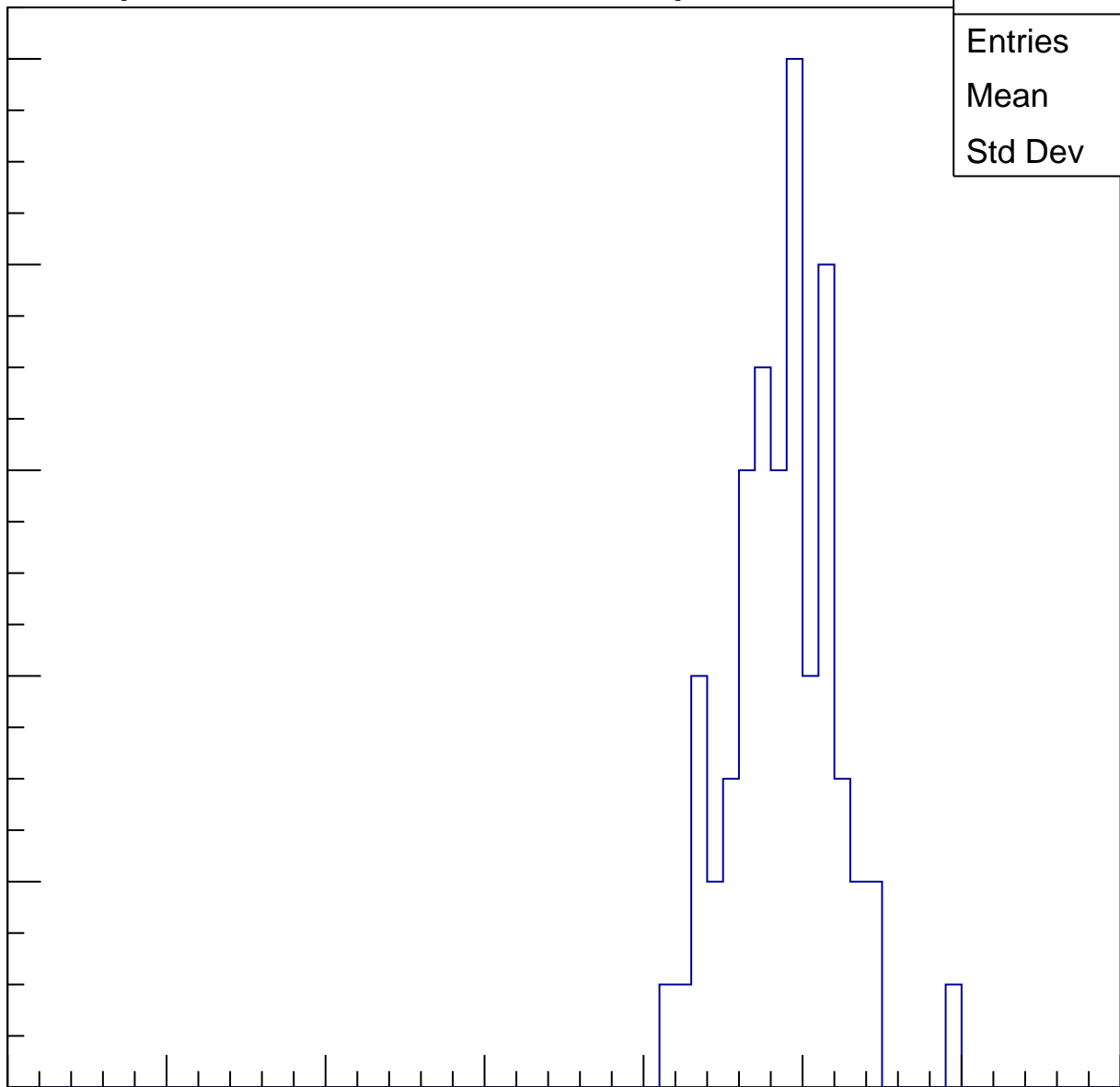
30

40

50

60

ampl

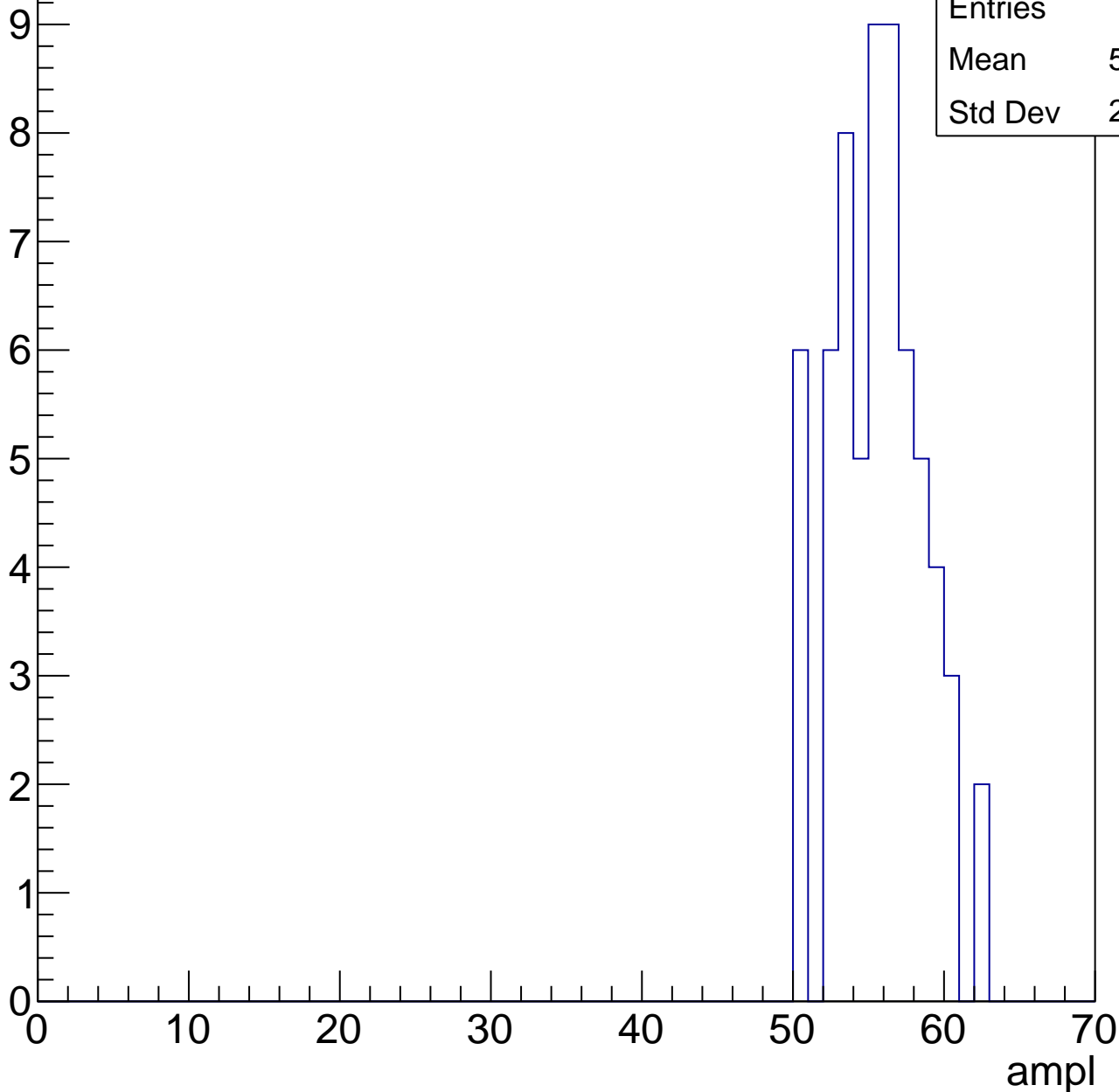


B1L101S, U21-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	55.19
Std Dev	2.965

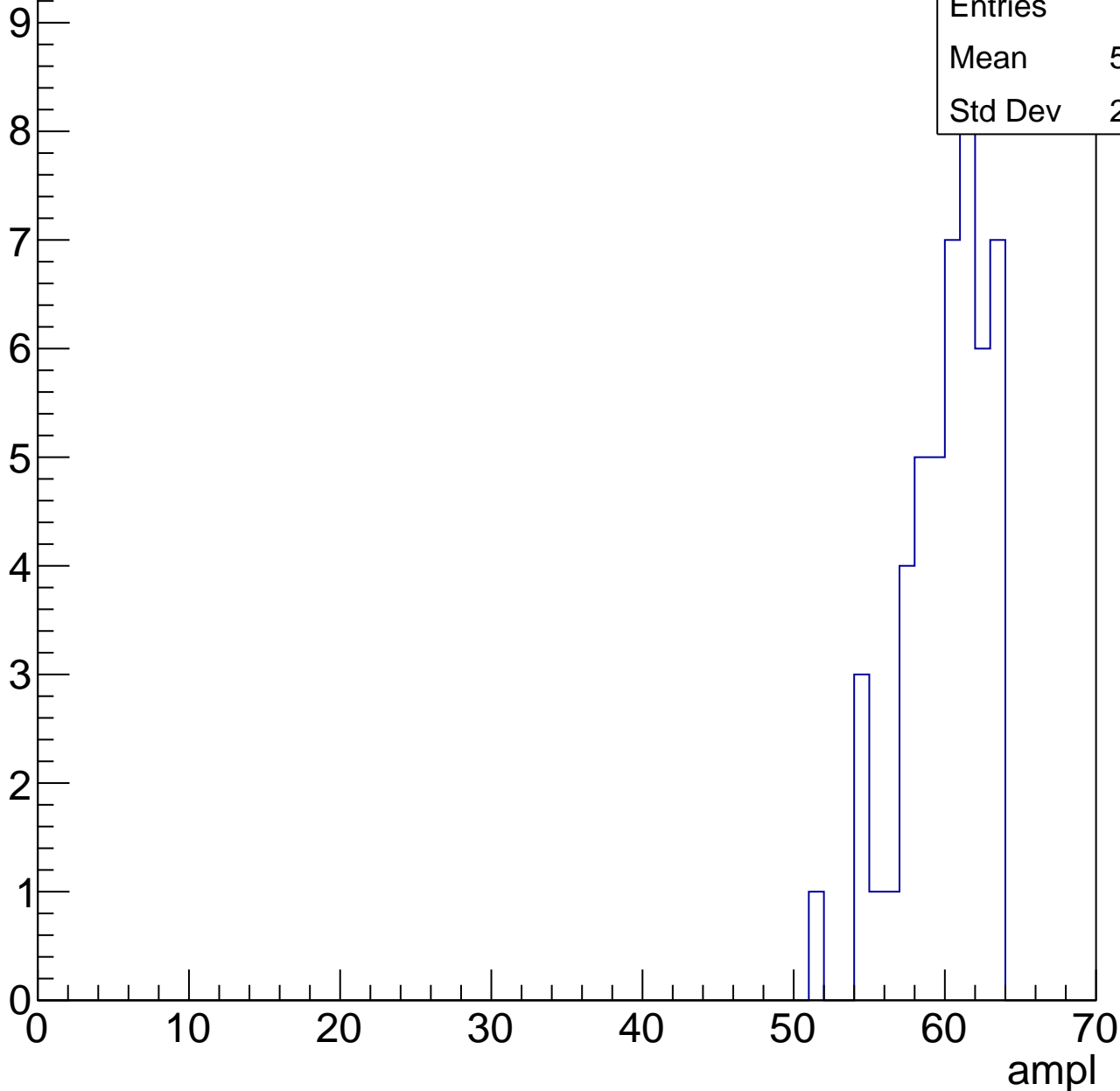


B1L101S, U21-ch28, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

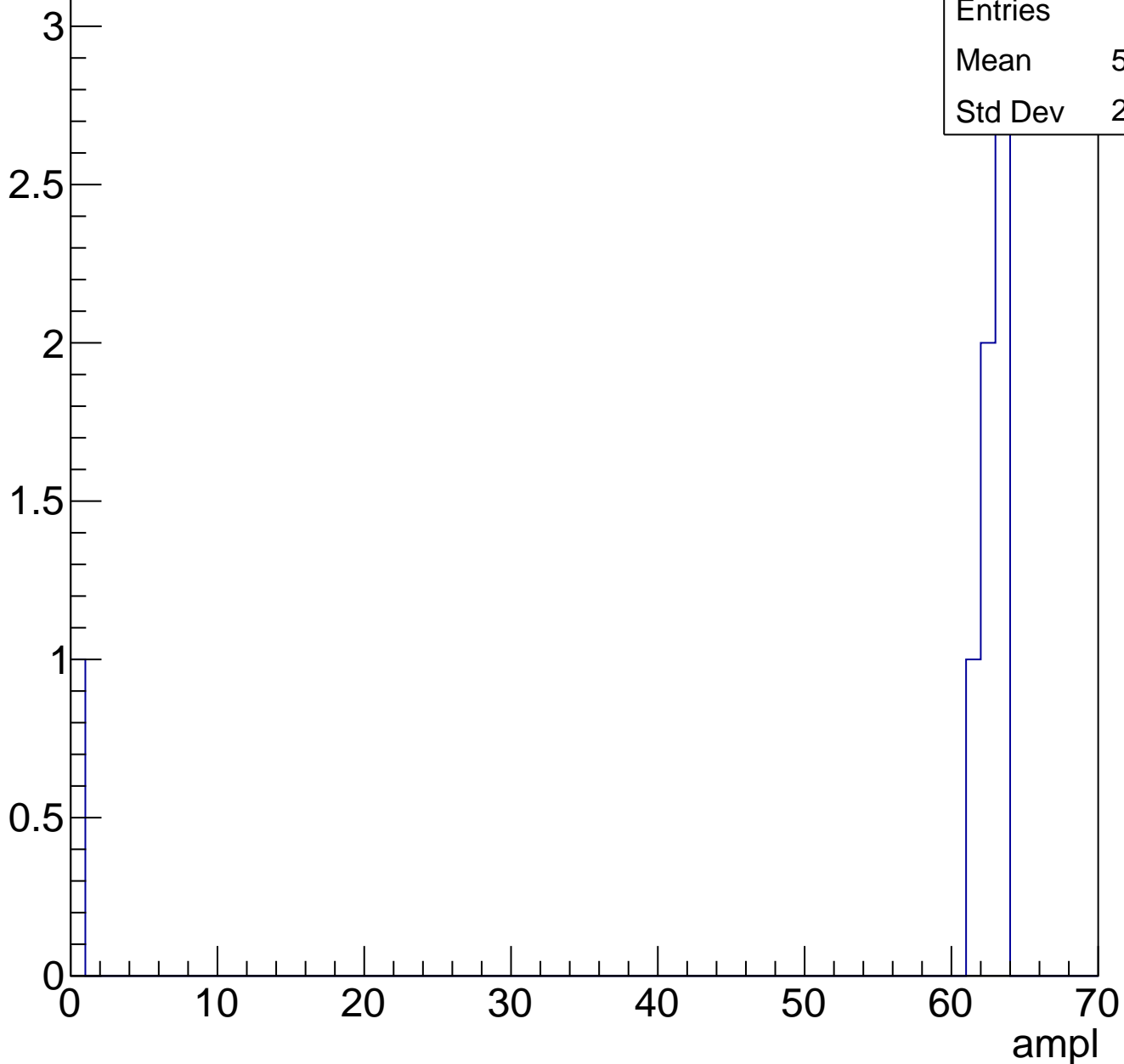
Entries	49
Mean	59.57
Std Dev	2.777



B1L101S, U21-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch29, adc0

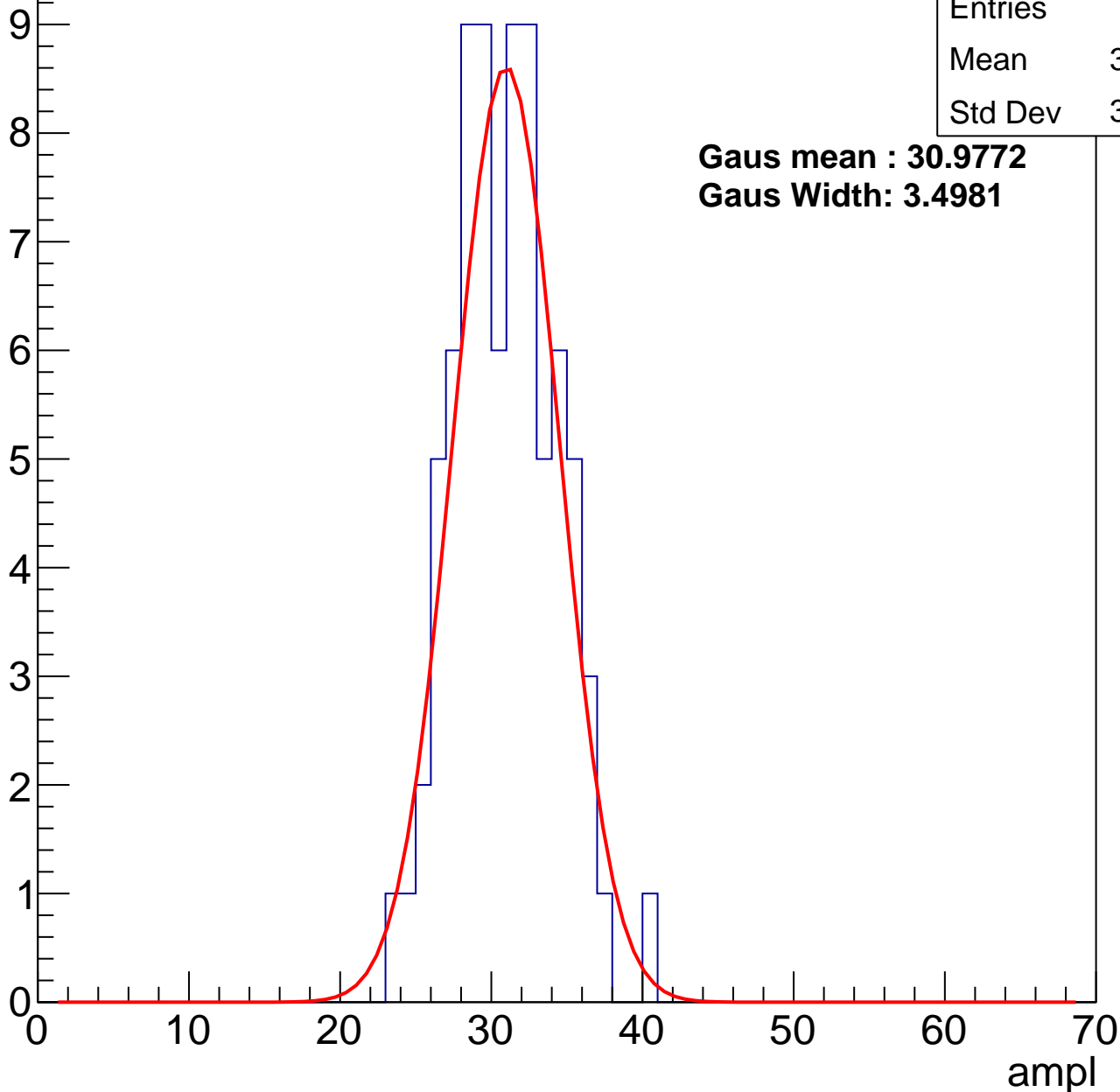
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.49
Std Dev	3.327

Gaus mean : 30.9772

Gaus Width: 3.4981



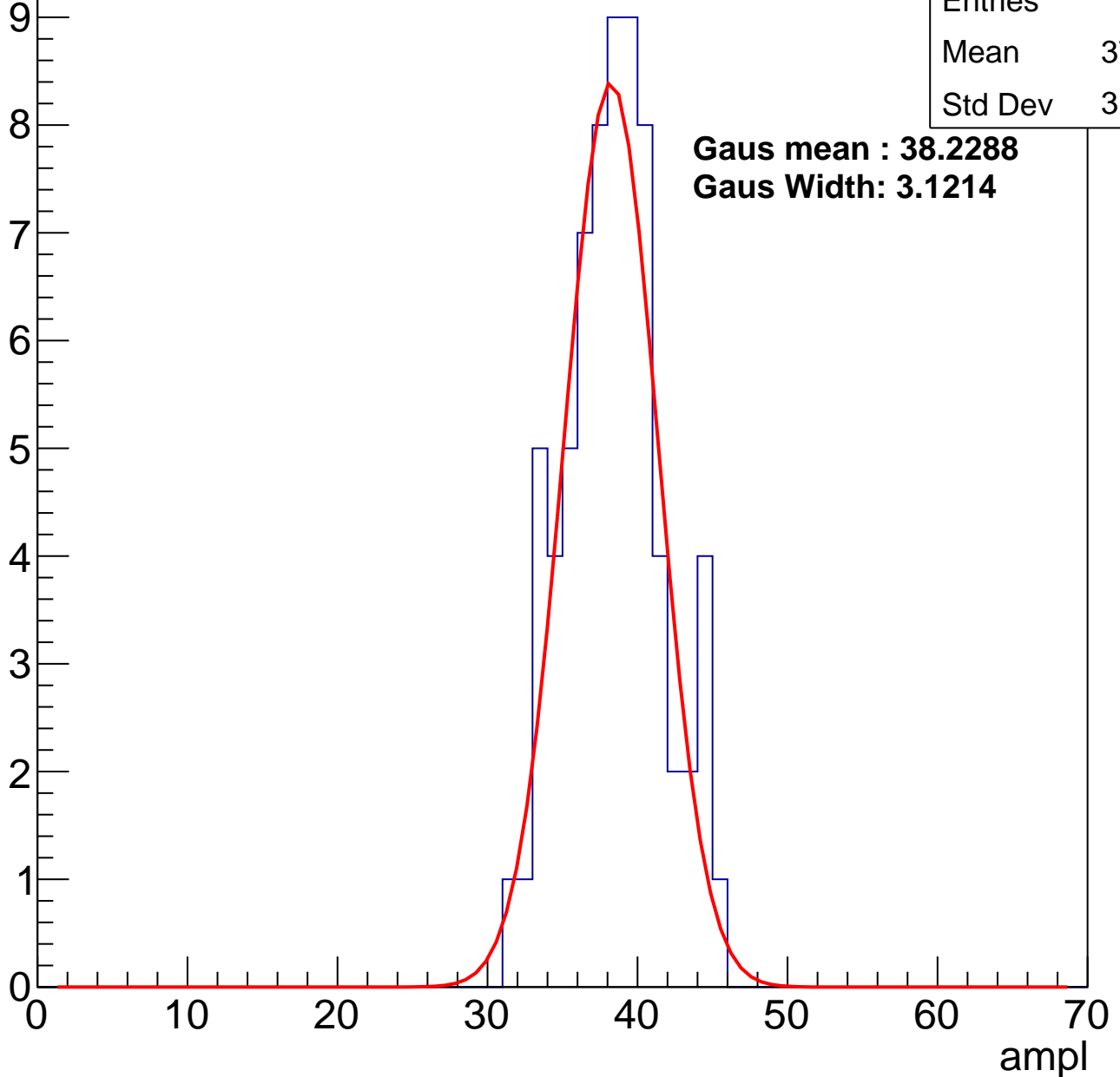
B1L101S, U21-ch29, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.93
Std Dev	3.177

Gaus mean : 38.2288
Gaus Width: 3.1214



B1L101S, U21-ch29, adc2

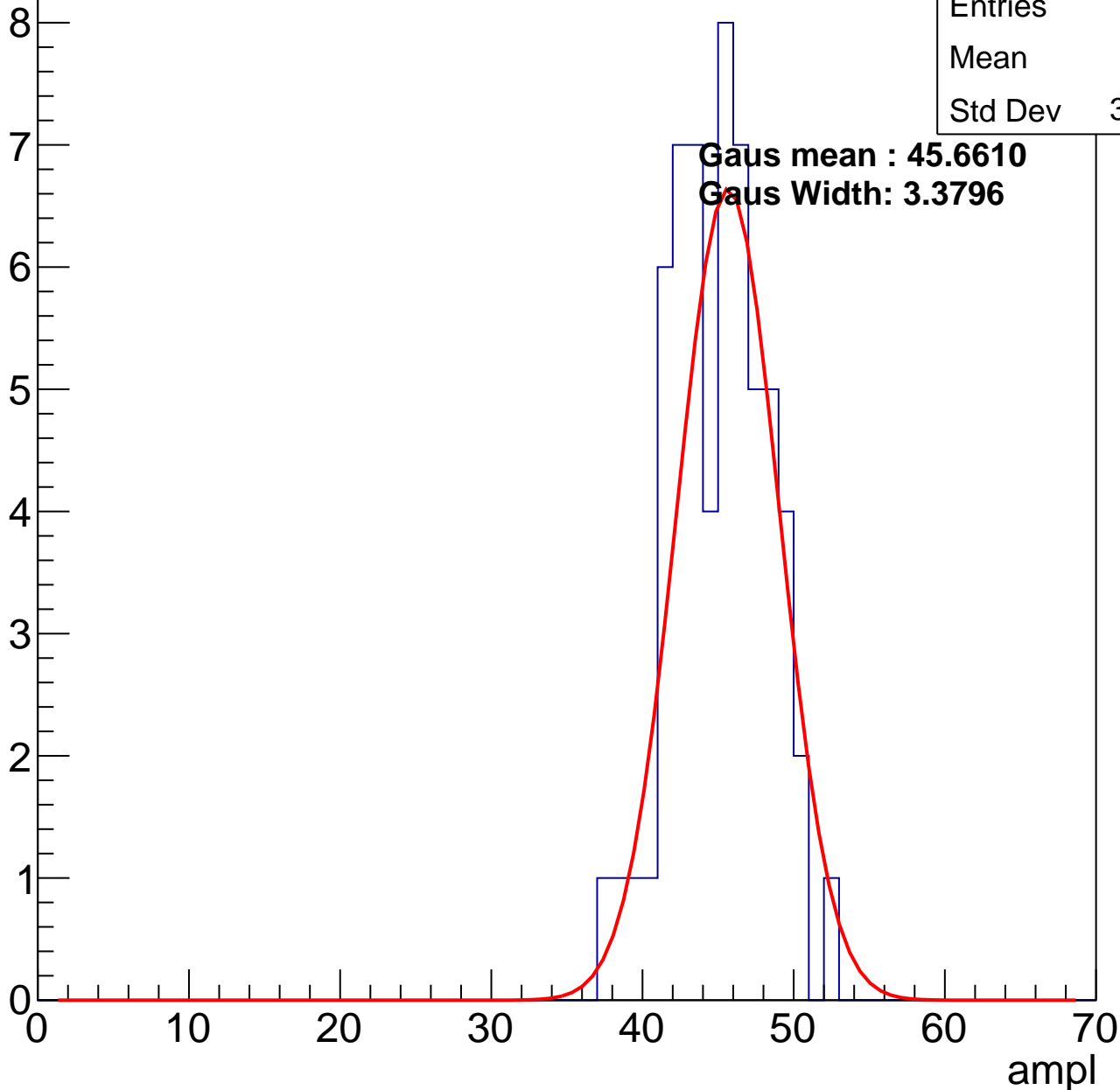
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.6
Std Dev	3.137

Gaus mean : 45.6610

Gaus Width: 3.3796

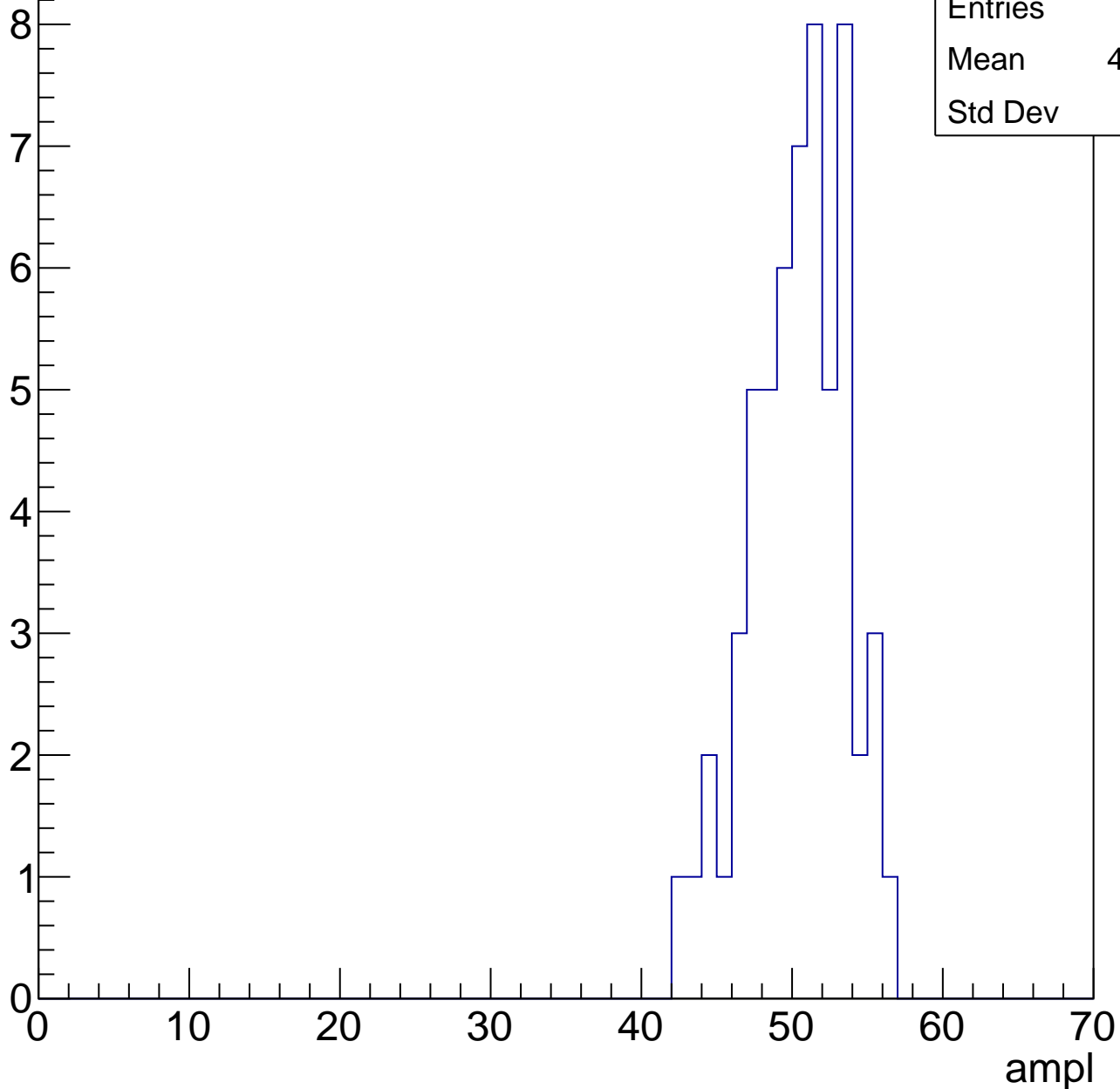


B1L101S, U21-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	49.93
Std Dev	3.14

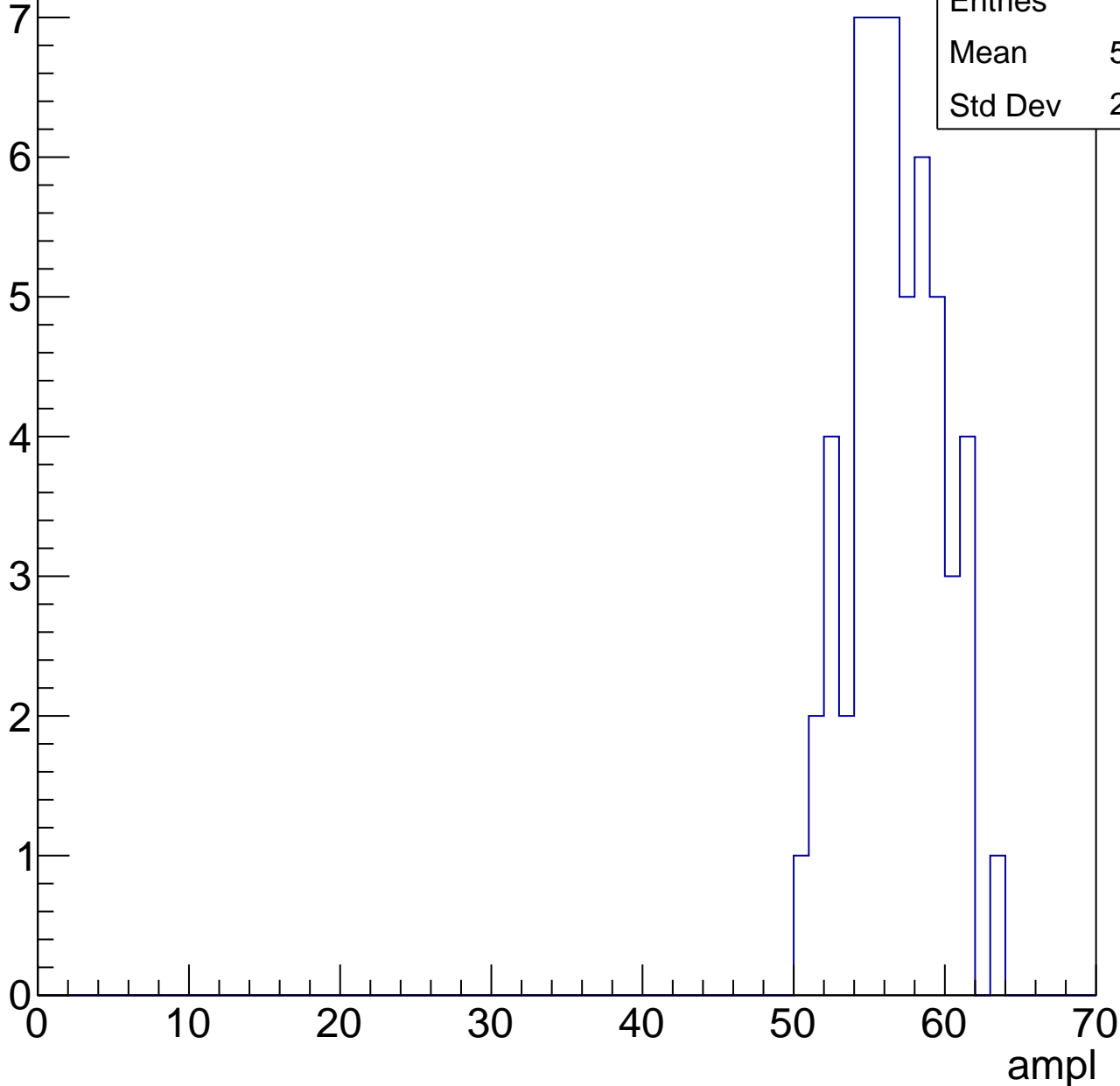


B1L101S, U21-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.22
Std Dev	2.954



B1L101S, U21-ch29, adc5

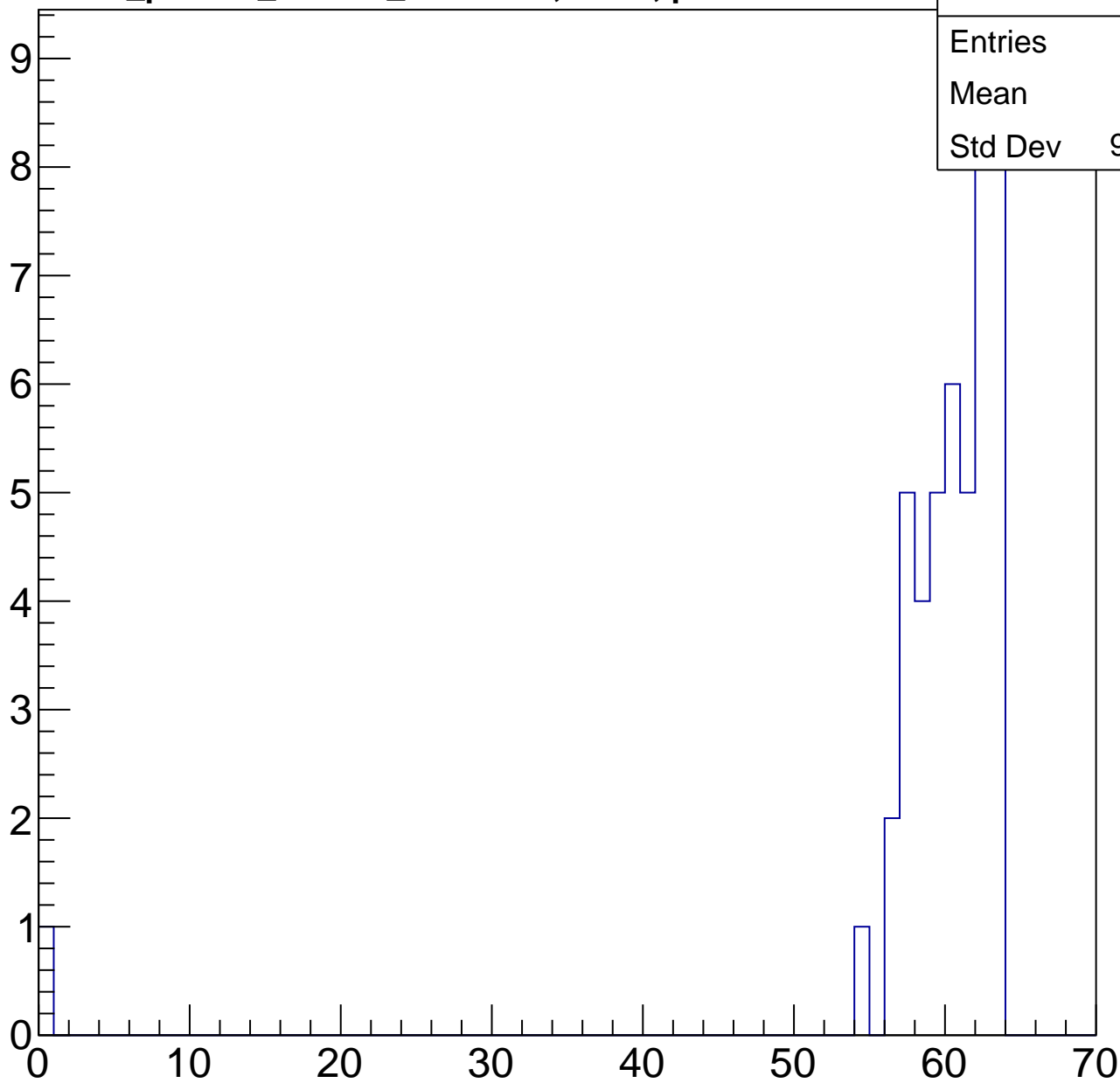
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.8
Std Dev	9.066

ampl



B1L101S, U21-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch30, adc0

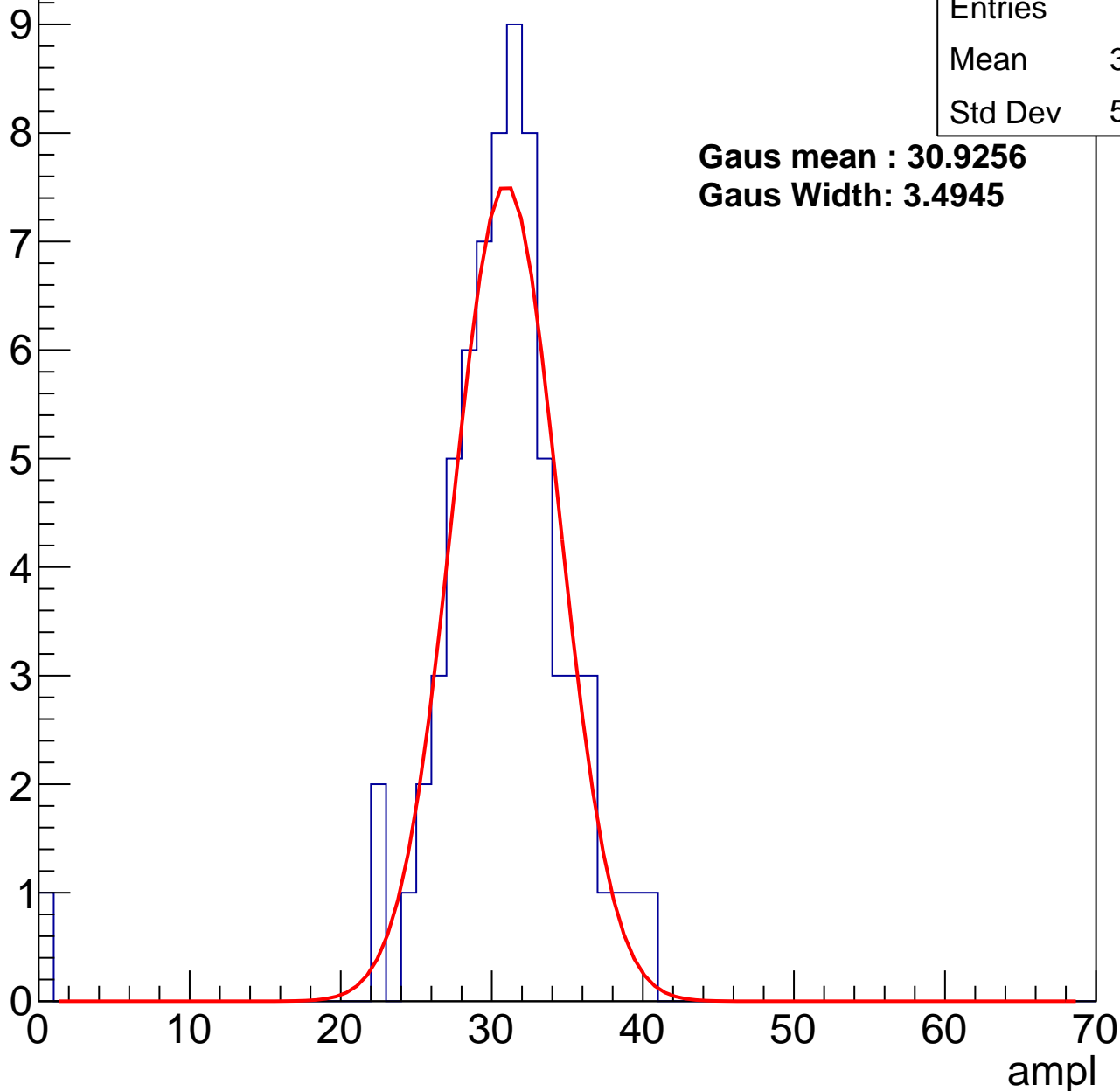
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.16
Std Dev	5.137

Gaus mean : 30.9256

Gaus Width: 3.4945



B1L101S, U21-ch30, adc1

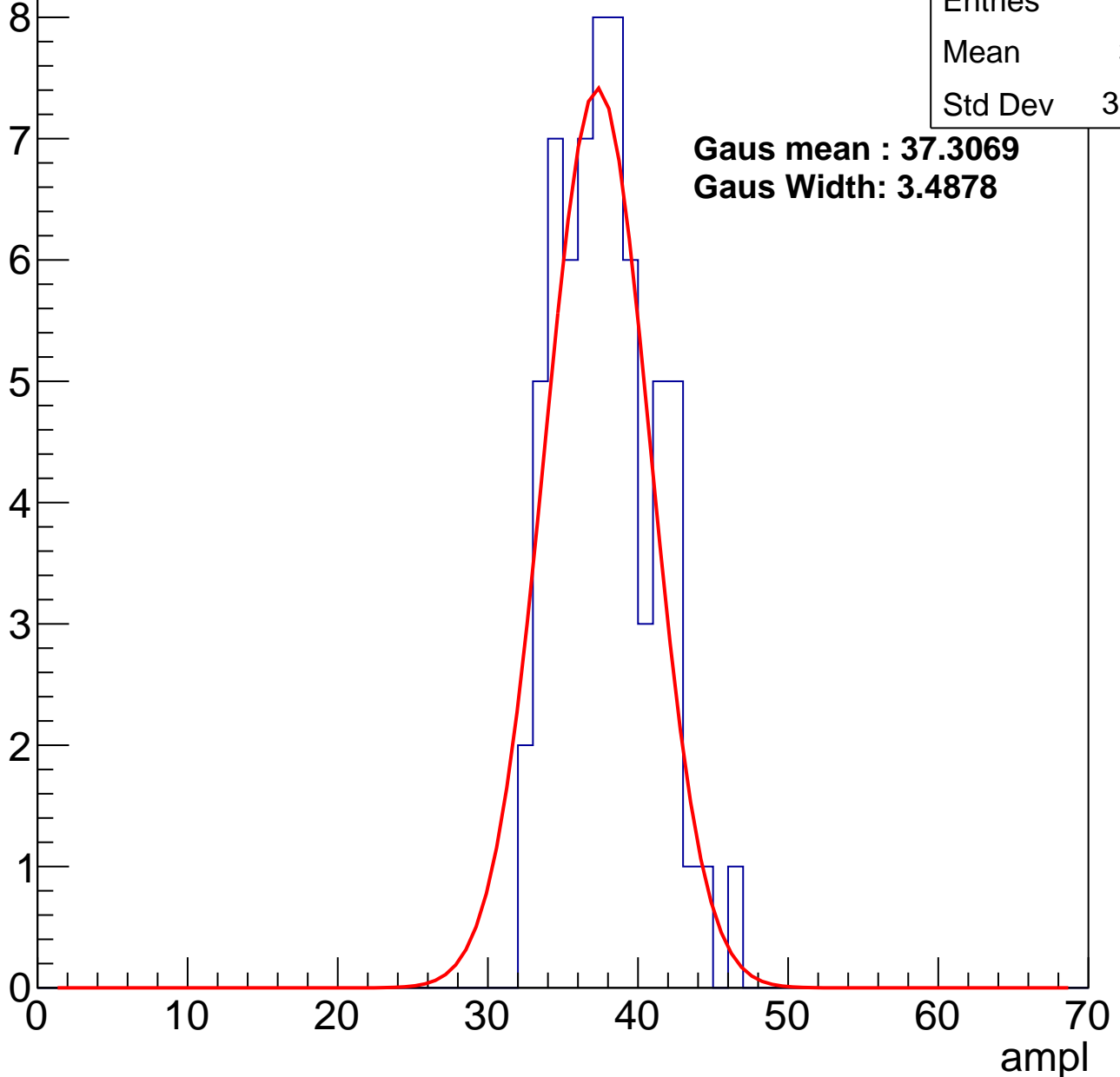
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.4
Std Dev	3.147

Gaus mean : 37.3069

Gaus Width: 3.4878



B1L101S, U21-ch30, adc2

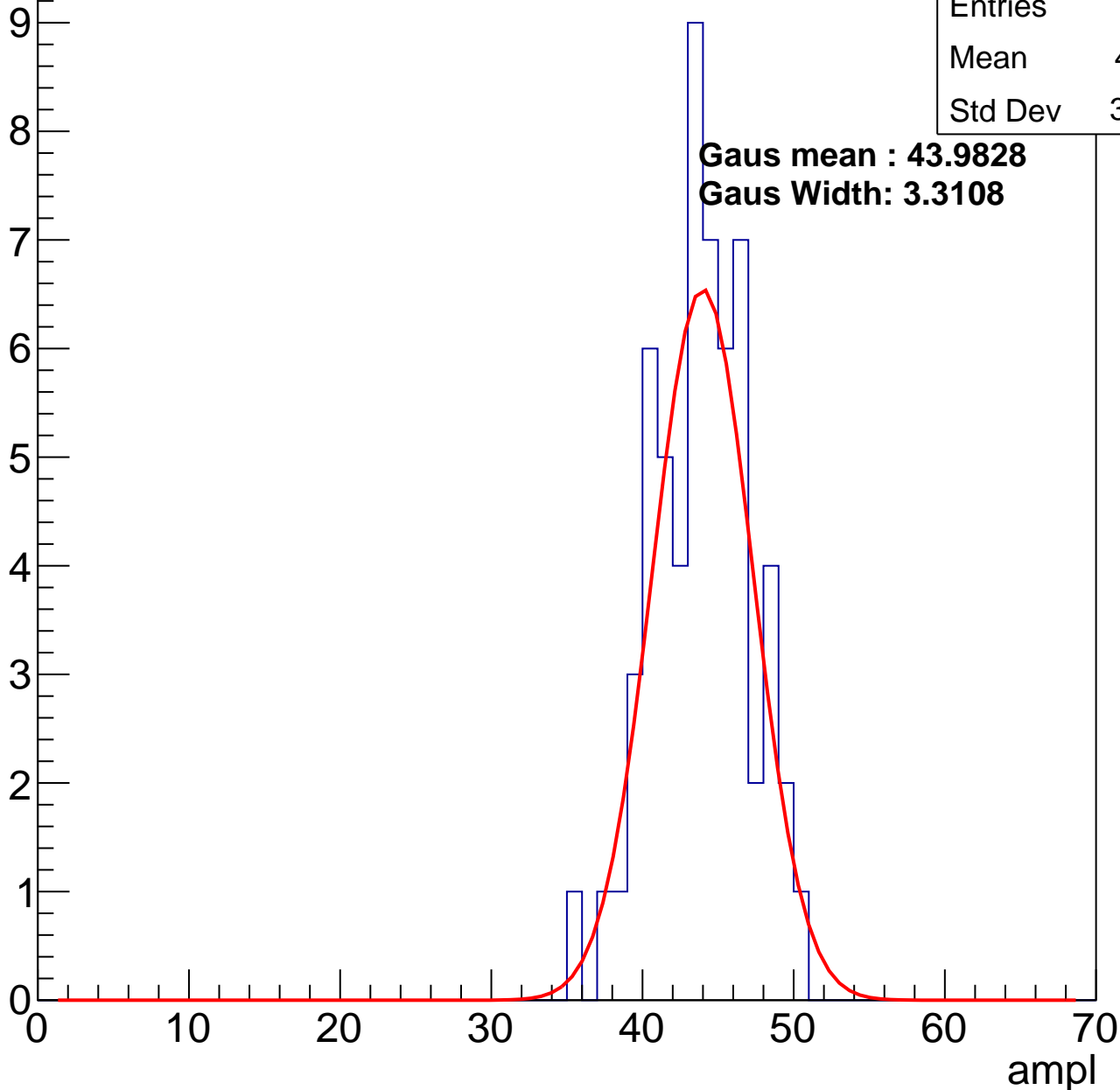
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.41
Std Dev	3.158

Gaus mean : 43.9828

Gaus Width: 3.3108

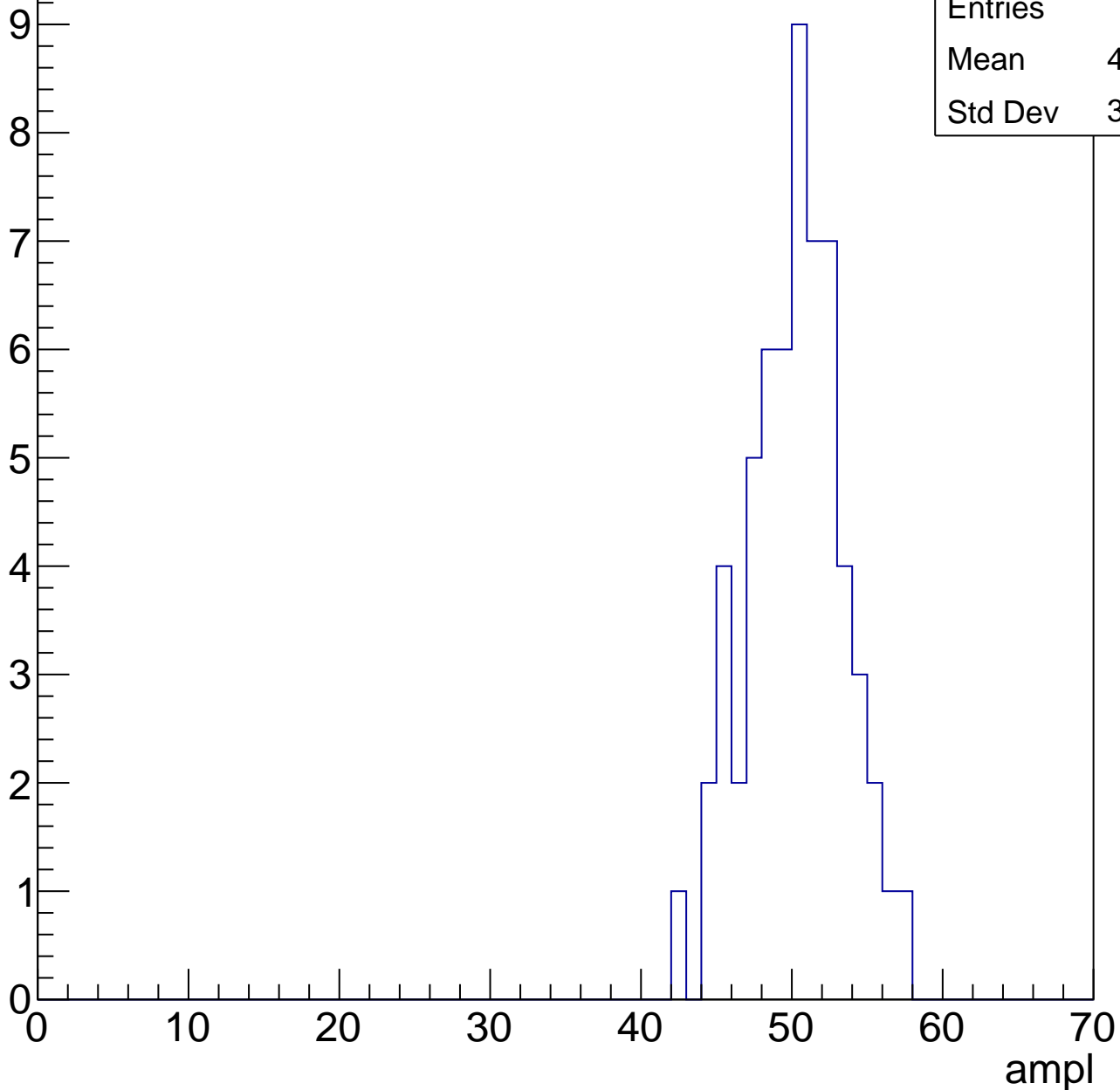


B1L101S, U21-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.78
Std Dev	3.147

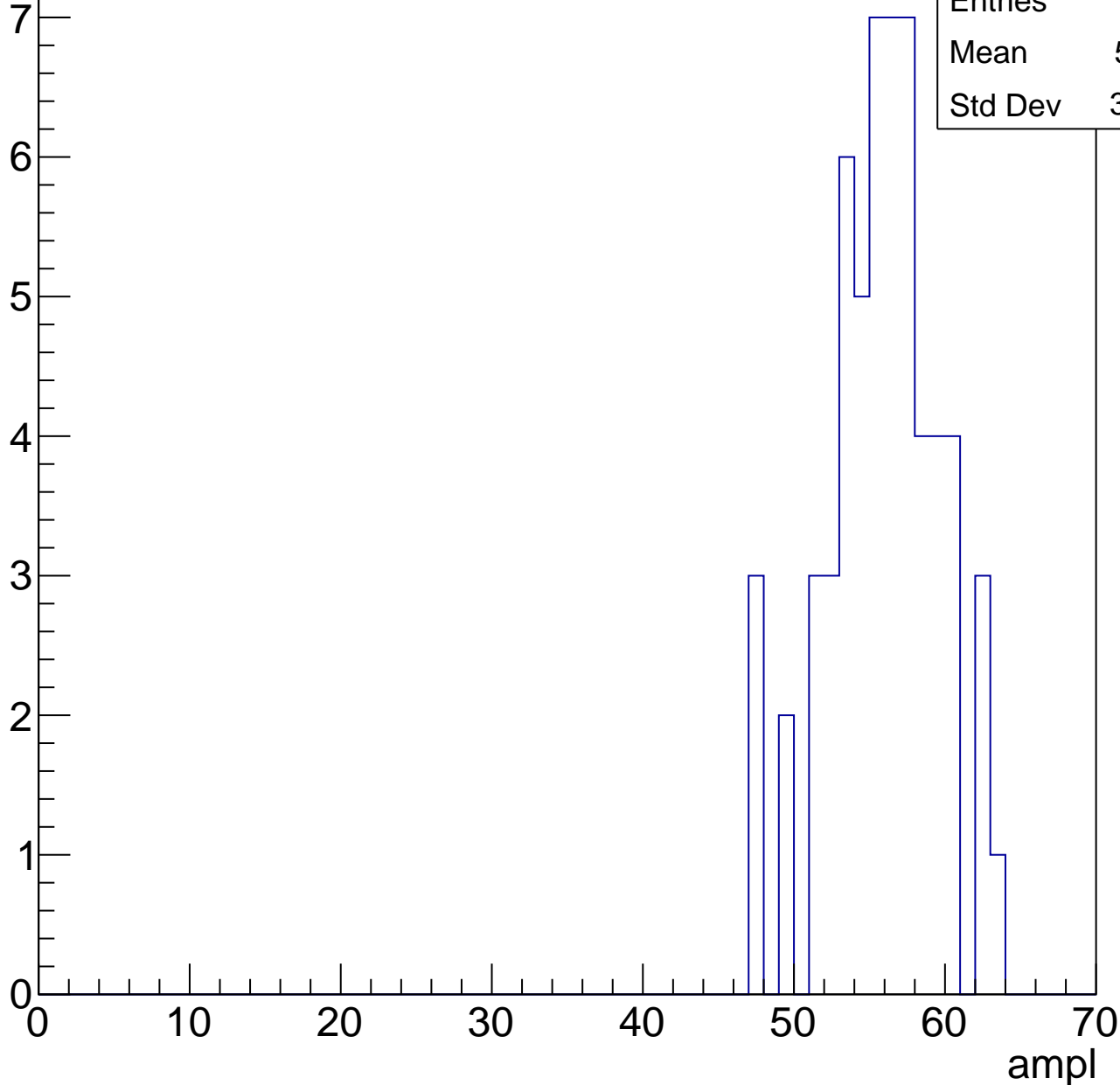


B1L101S, U21-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.41
Std Dev	3.692

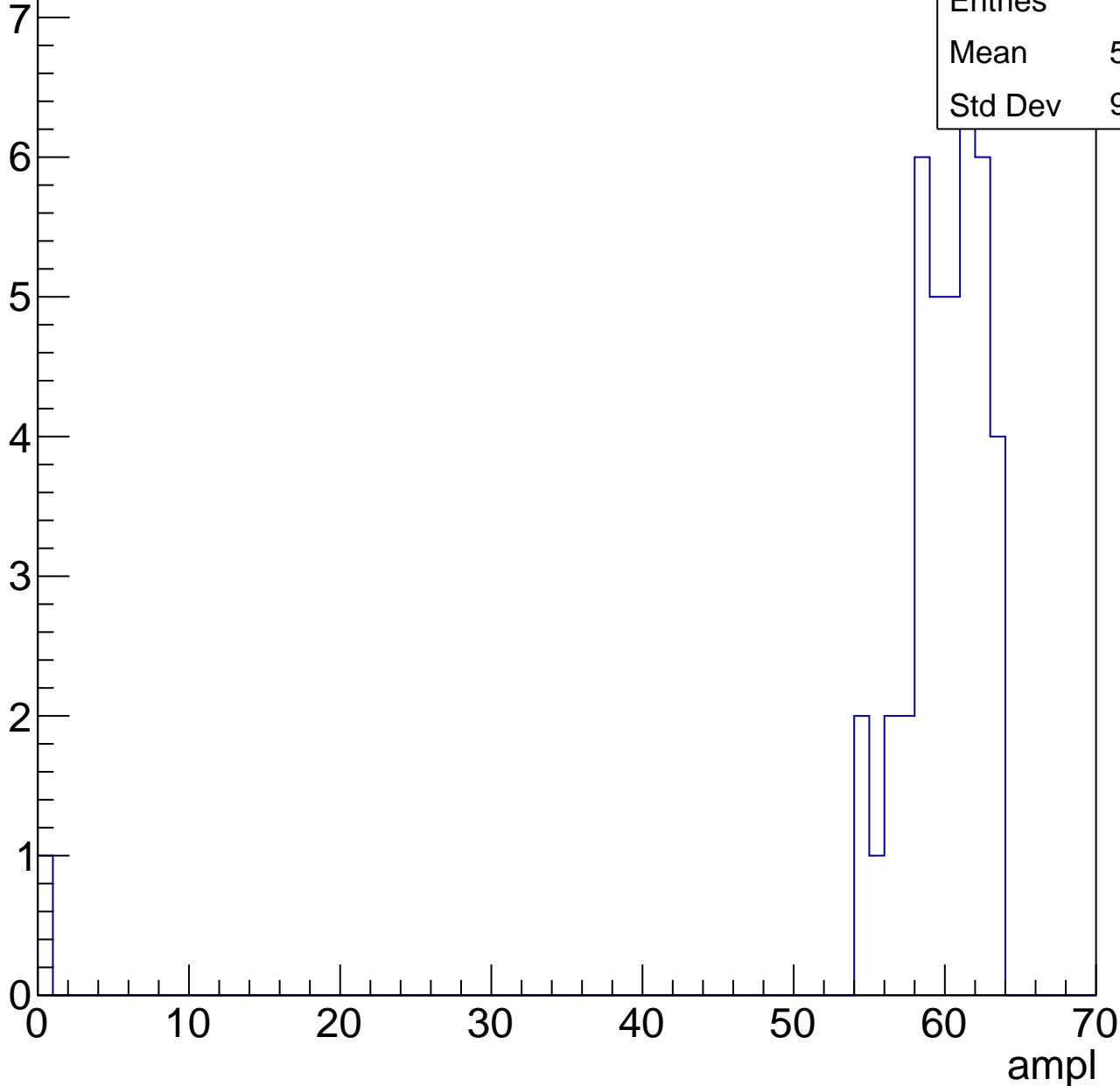


B1L101S, U21-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.12
Std Dev	9.498

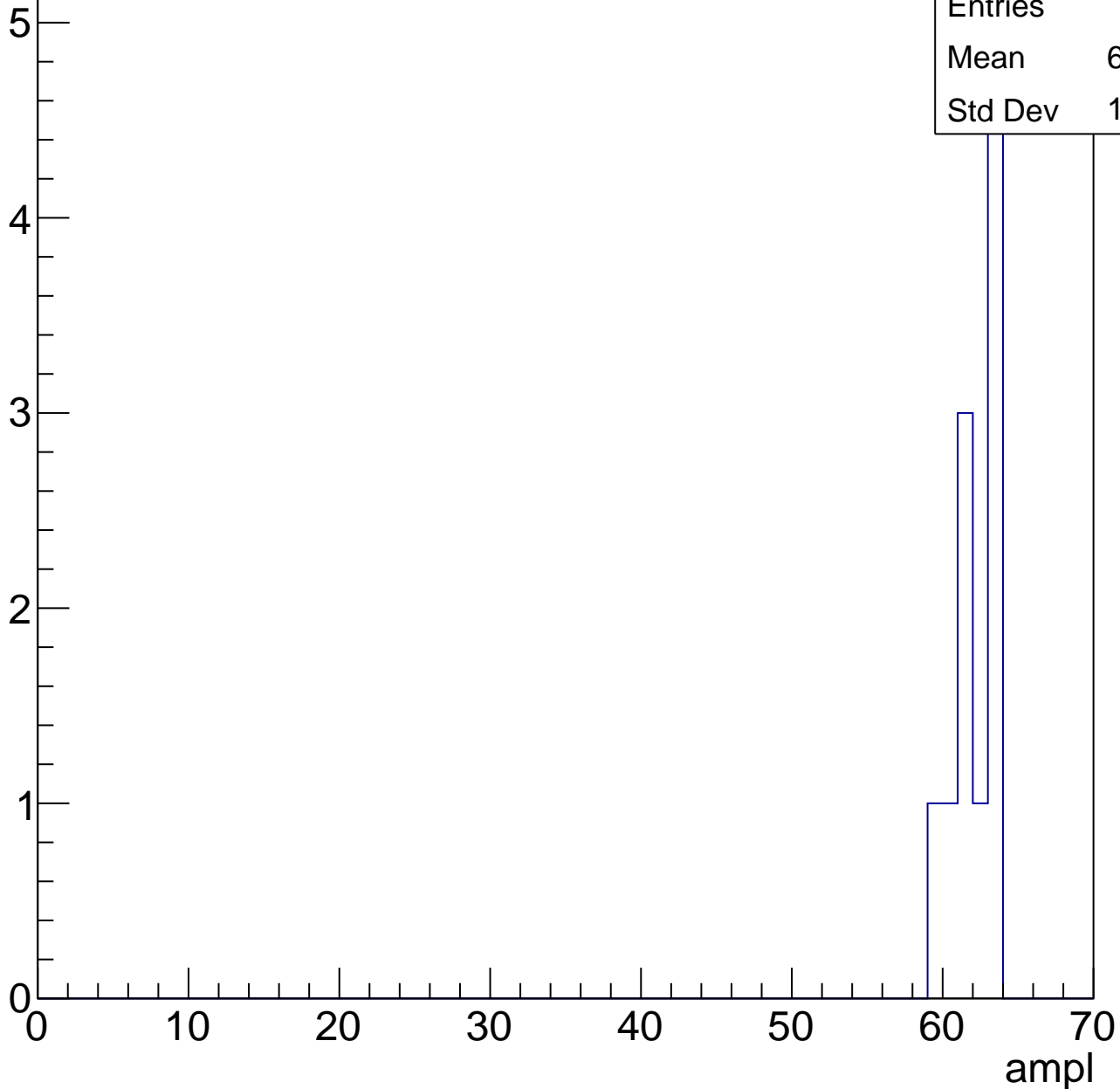


B1L101S, U21-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	61.73
Std Dev	1.355



B1L101S, U21-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch31, adc0

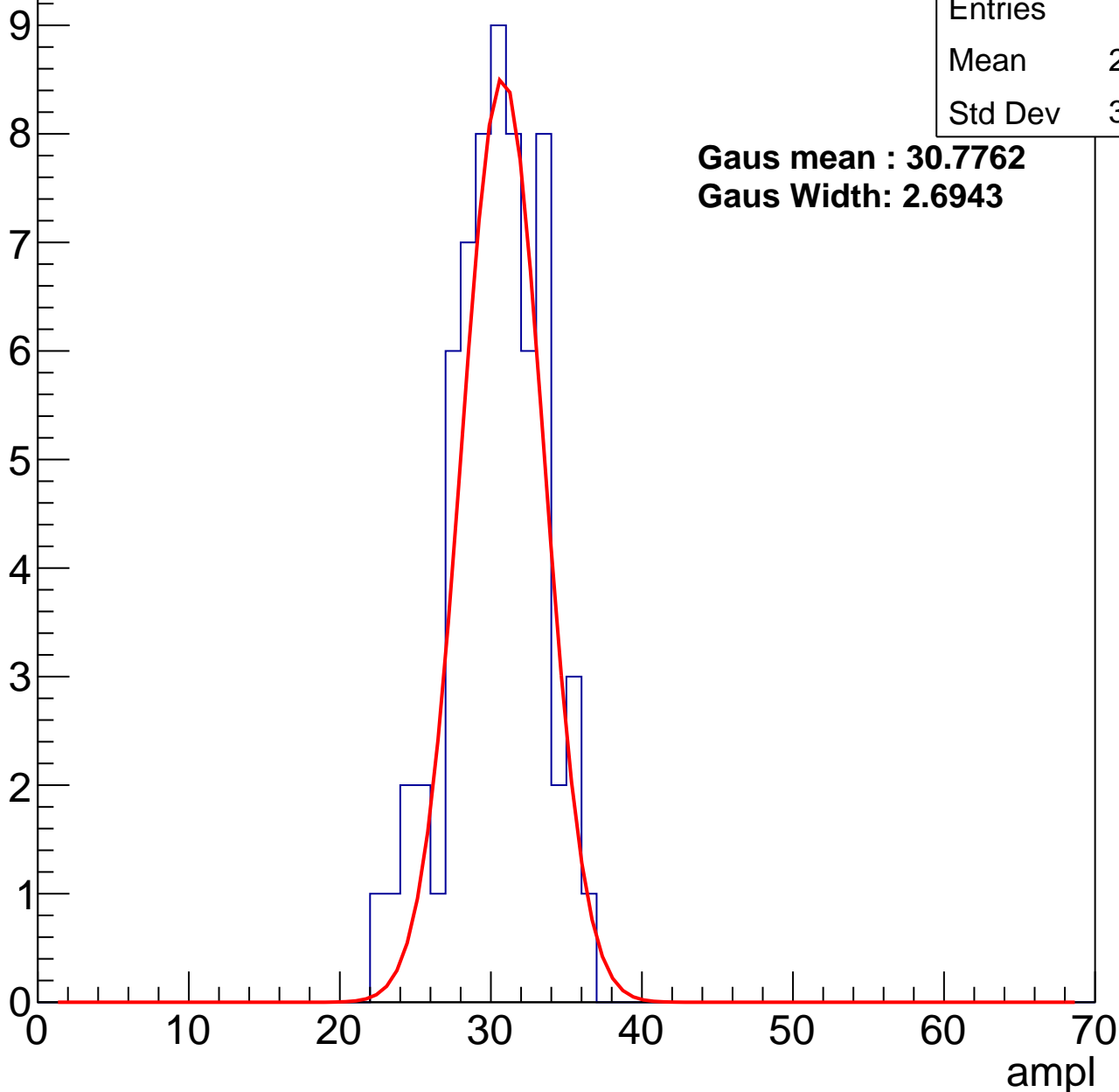
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	29.88
Std Dev	3.005

Gaus mean : 30.7762

Gaus Width: 2.6943



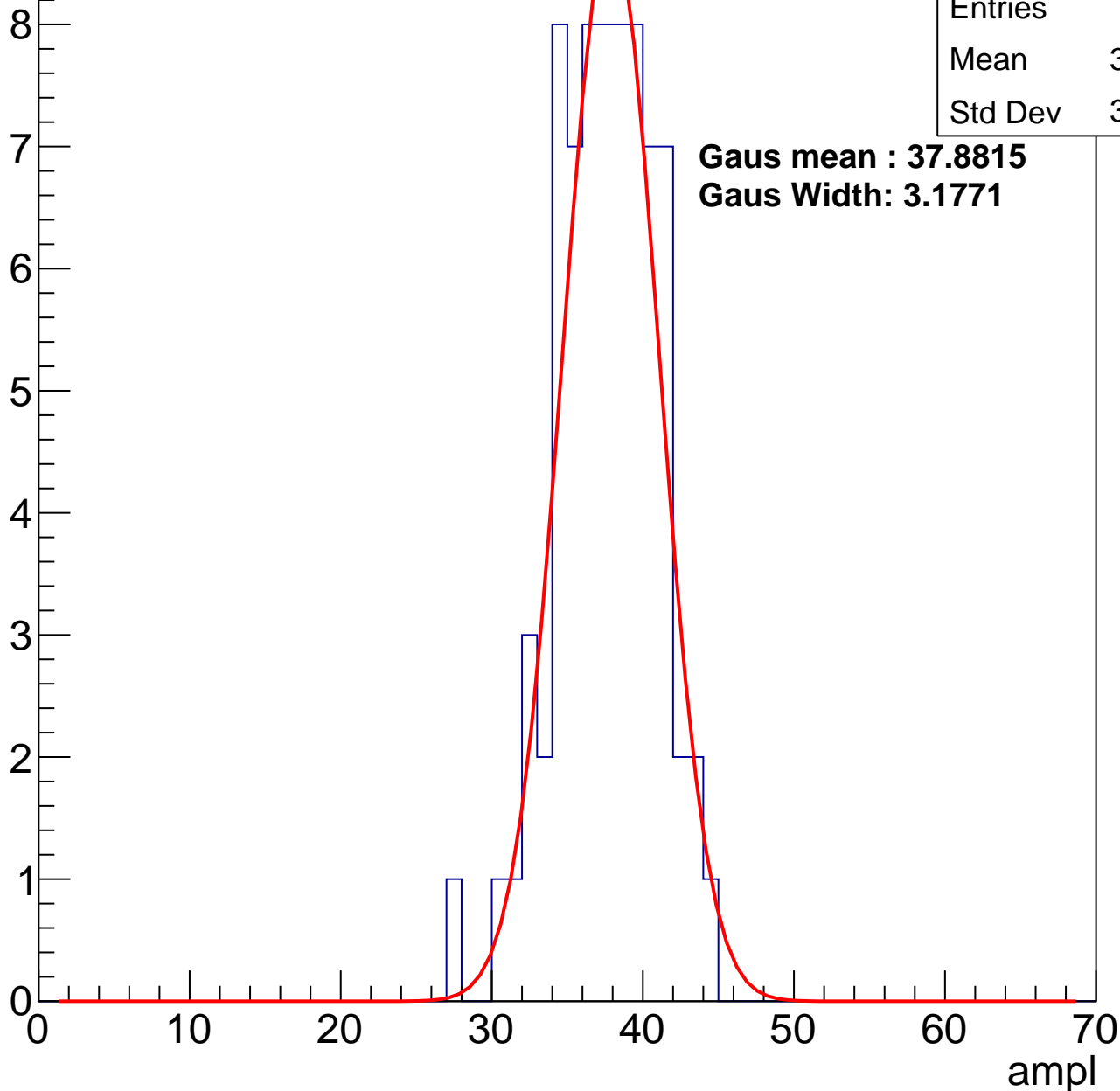
B1L101S, U21-ch31, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.14
Std Dev	3.256

Gaus mean : 37.8815
Gaus Width: 3.1771



B1L101S, U21-ch31, adc2

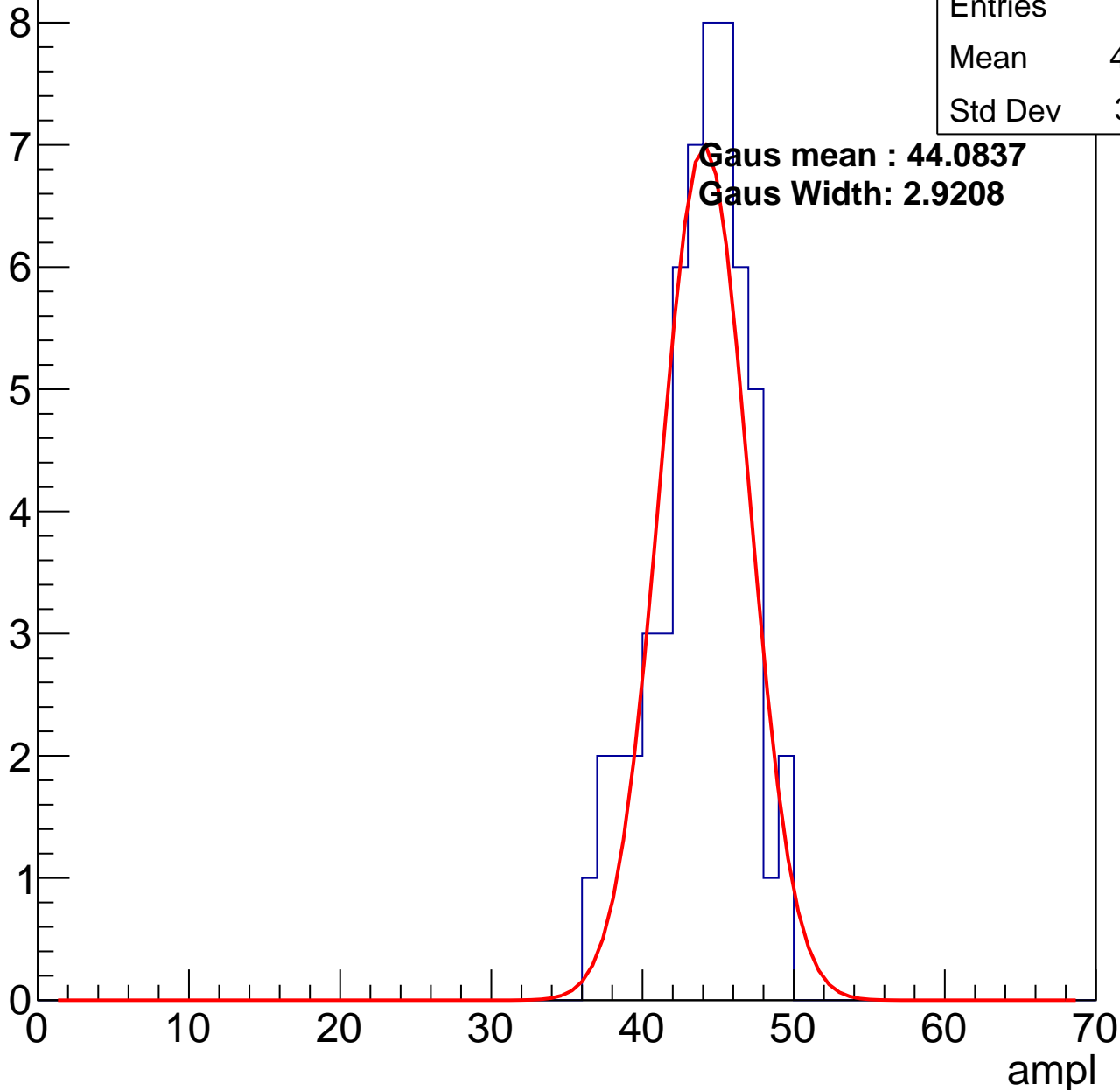
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.38
Std Dev	3.021

Gaus mean : 44.0837

Gaus Width: 2.9208

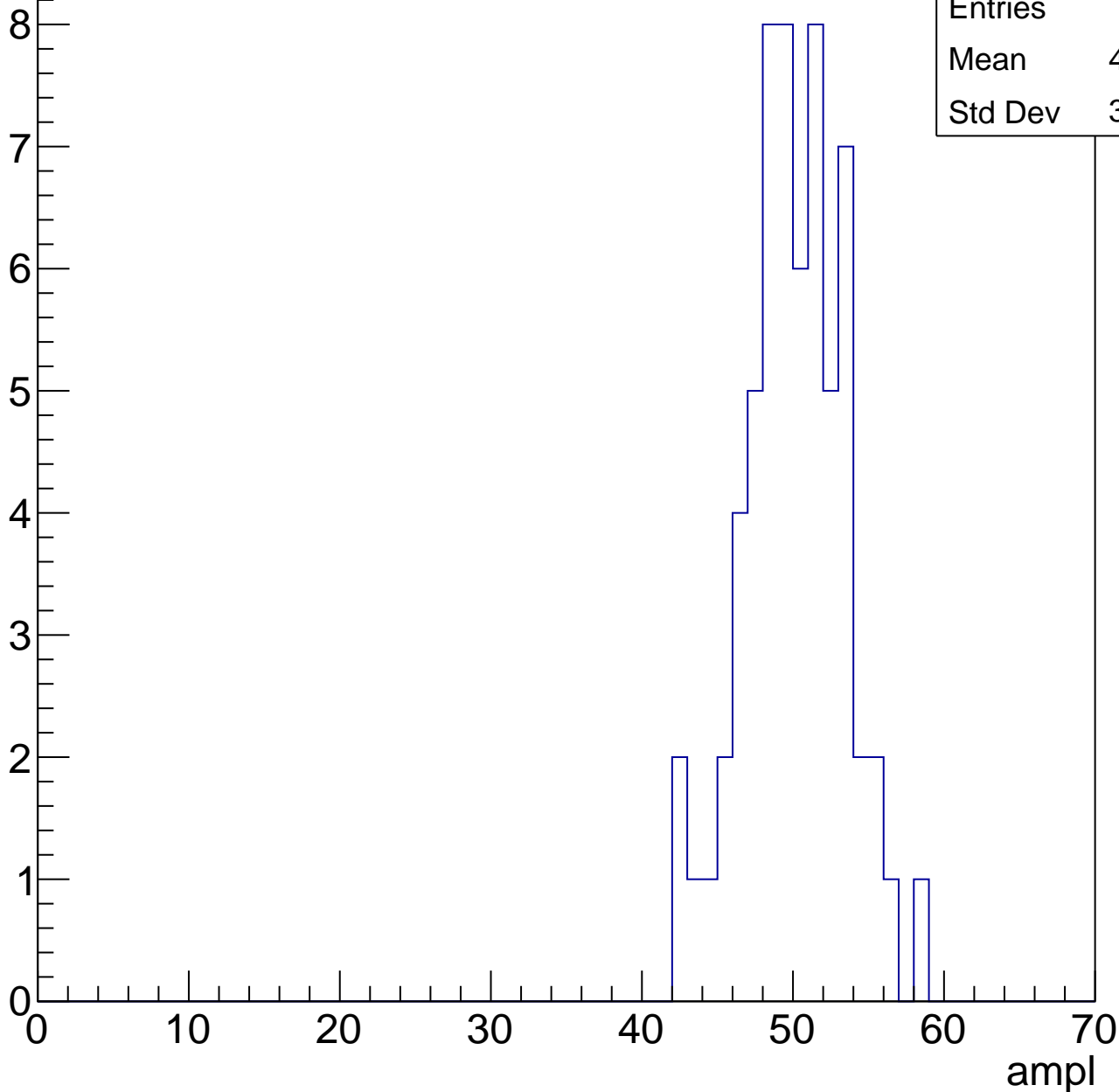


B1L101S, U21-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	49.63
Std Dev	3.277

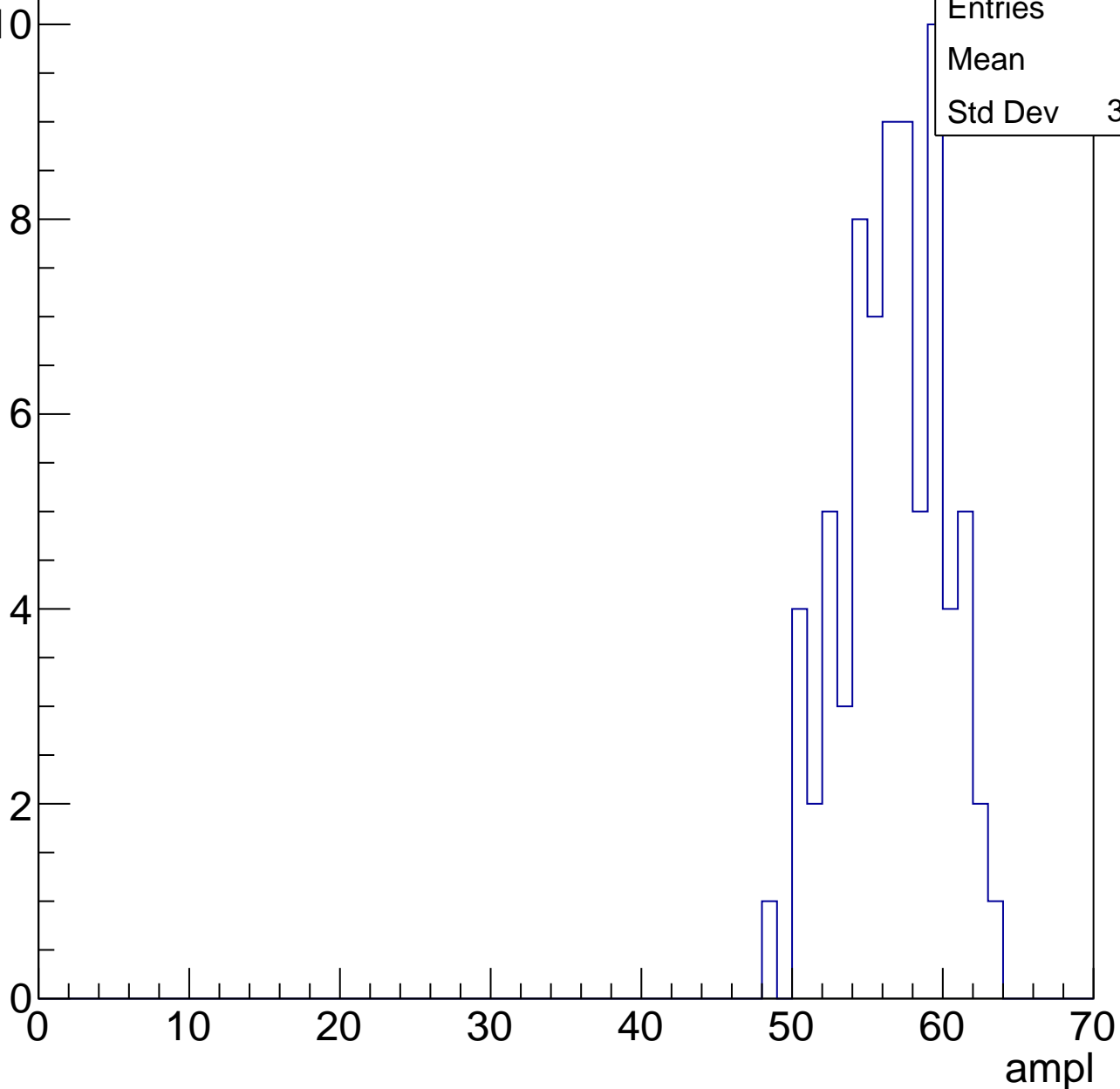


B1L101S, U21-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

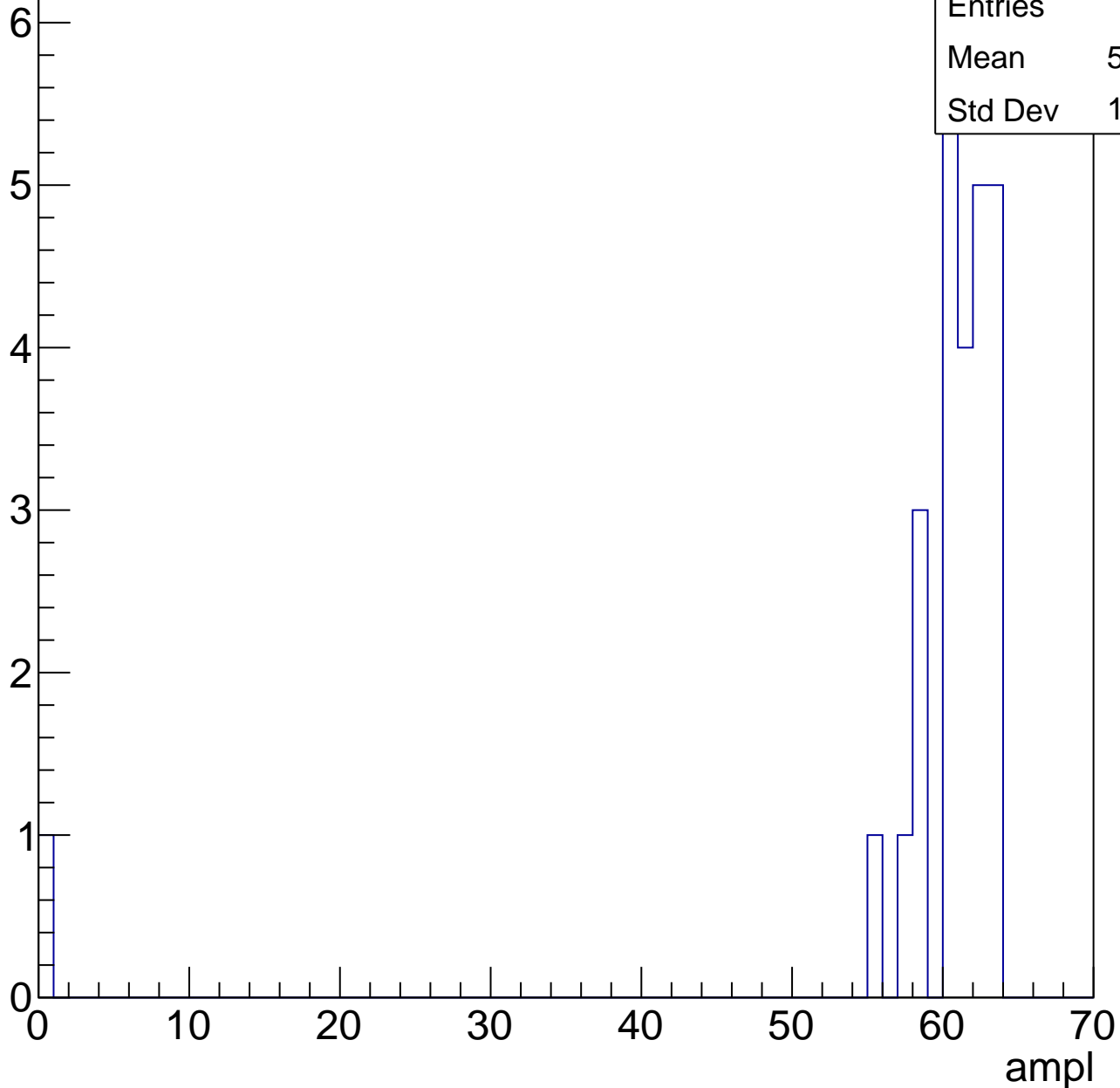
Entries	75
Mean	56.2
Std Dev	3.327



B1L101S, U21-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

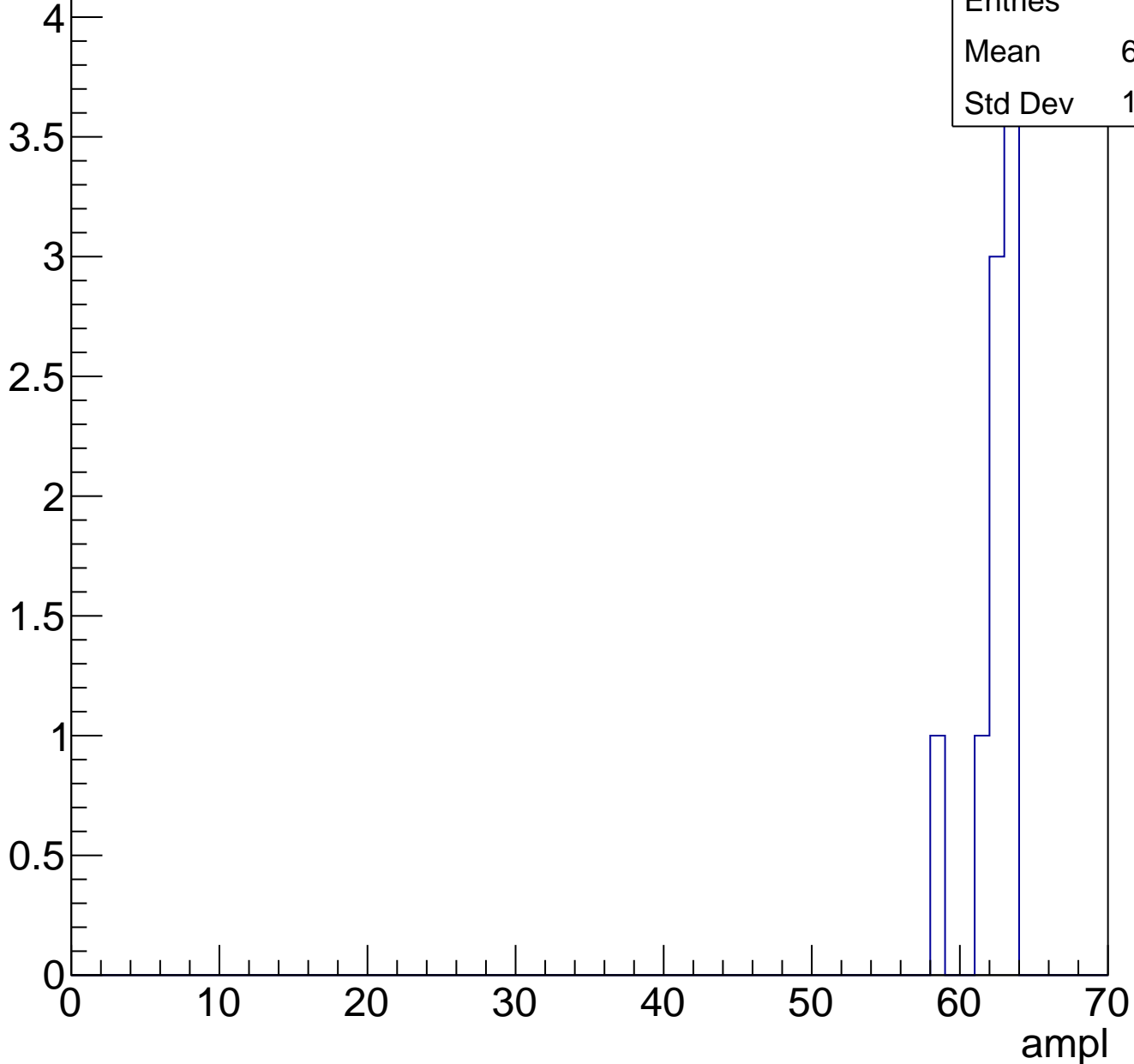
Entry



B1L101S, U21-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch32, adc0

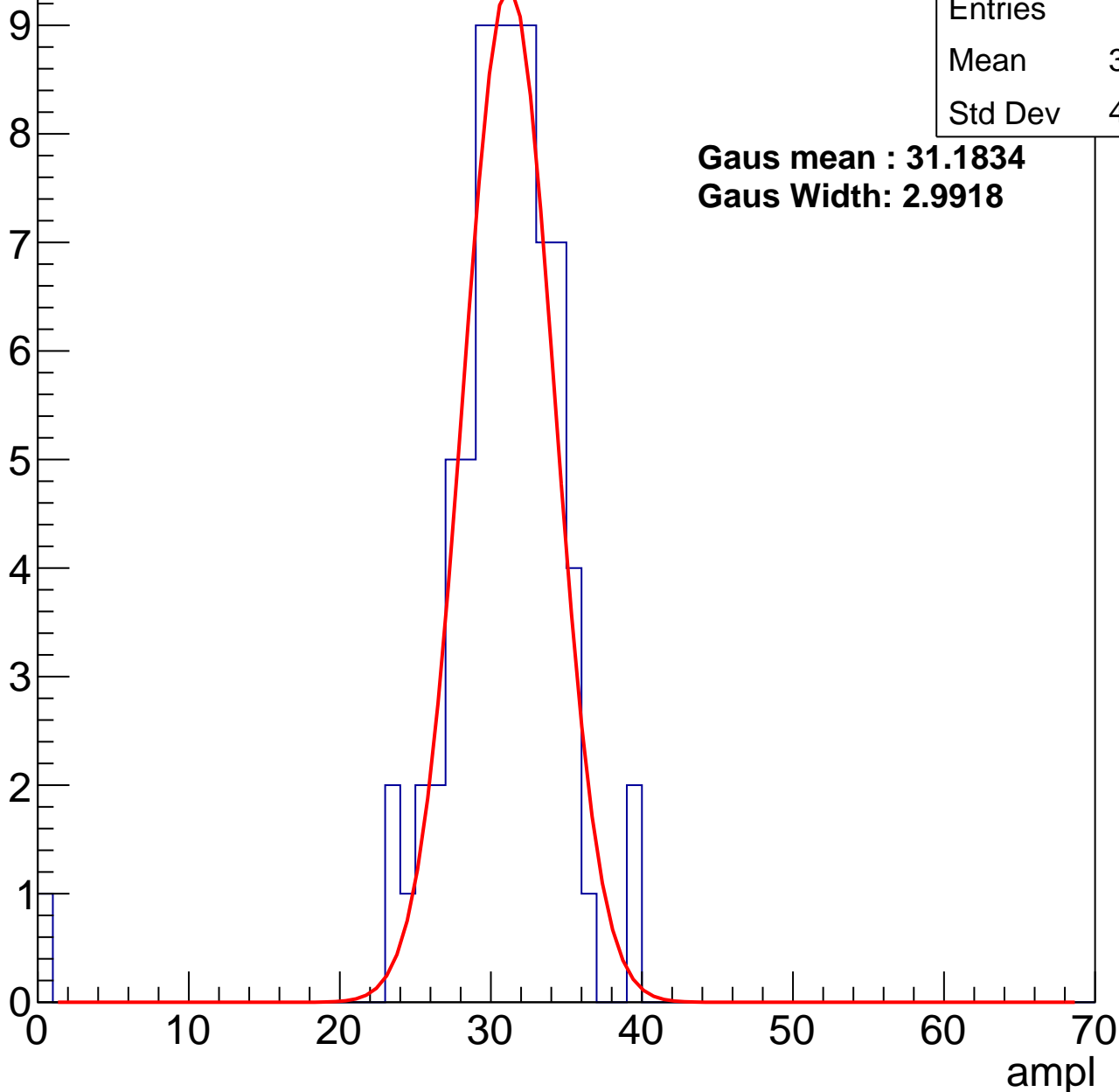
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.24
Std Dev	4.766

Gaus mean : 31.1834

Gaus Width: 2.9918



B1L101S, U21-ch32, adc1

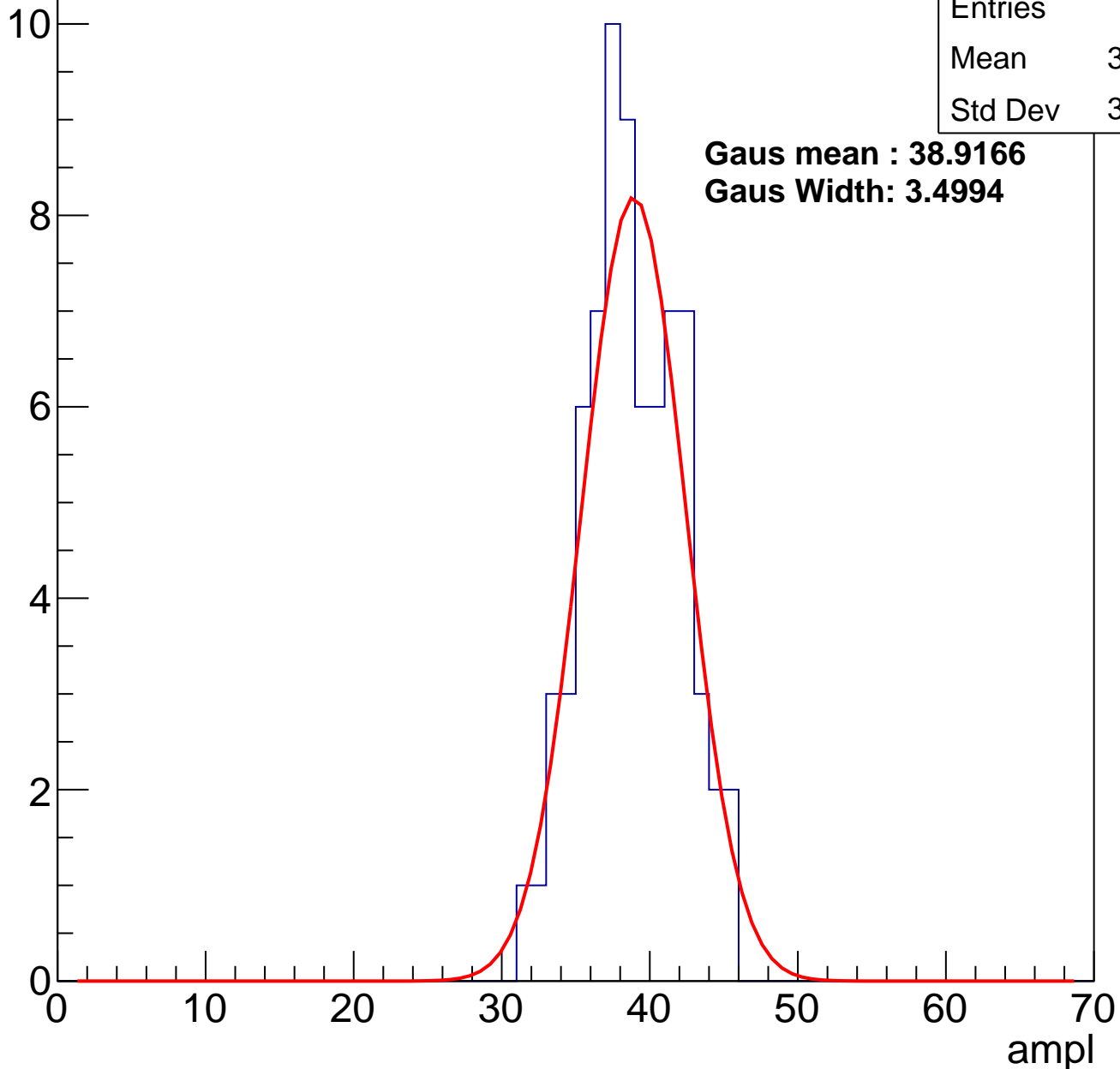
calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	38.36
Std Dev	3.185

Gaus mean : 38.9166

Gaus Width: 3.4994

Entry



B1L101S, U21-ch32, adc2

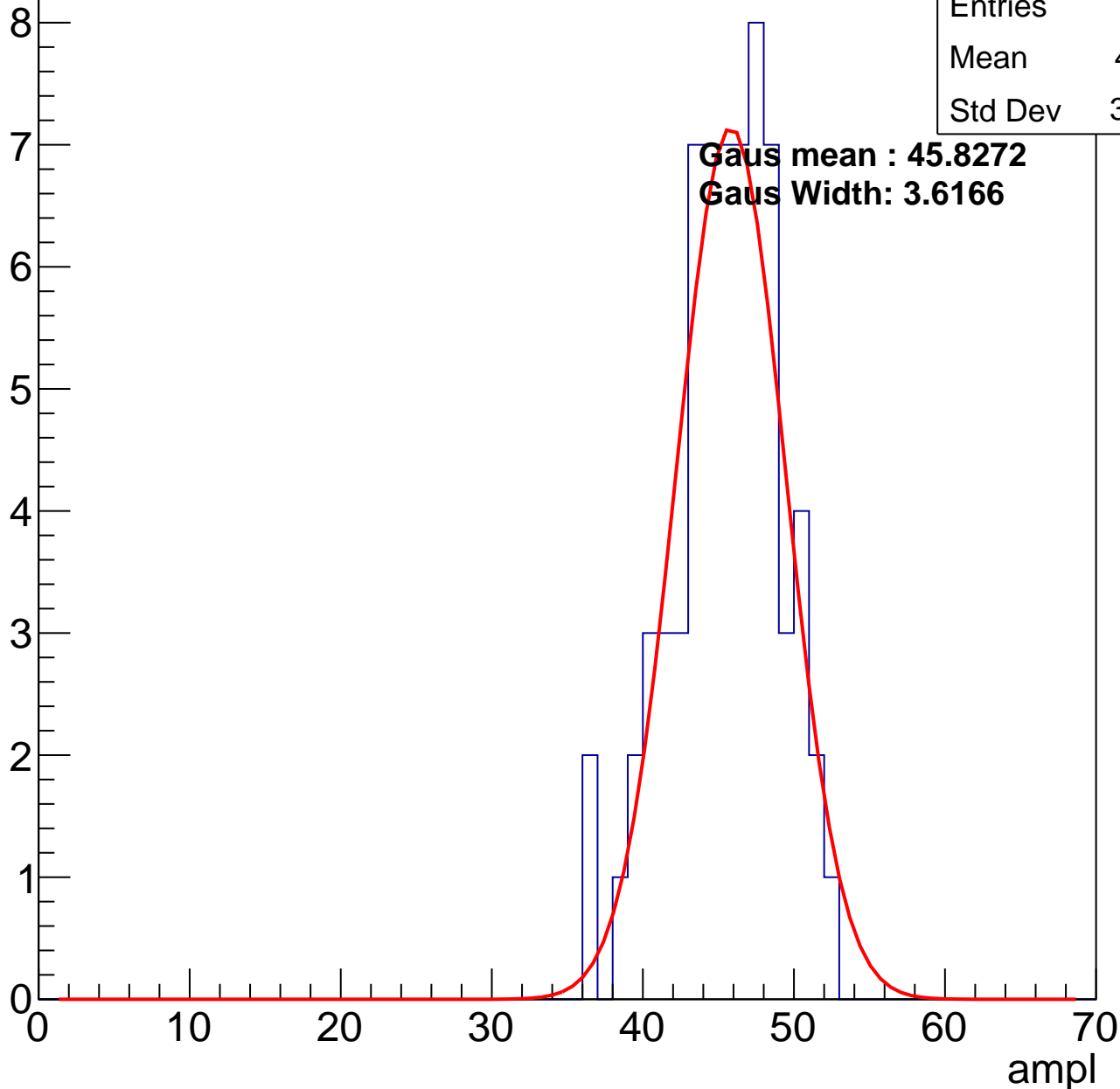
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	45.01
Std Dev	3.539

Gaus mean : 45.8272

Gaus Width: 3.6166

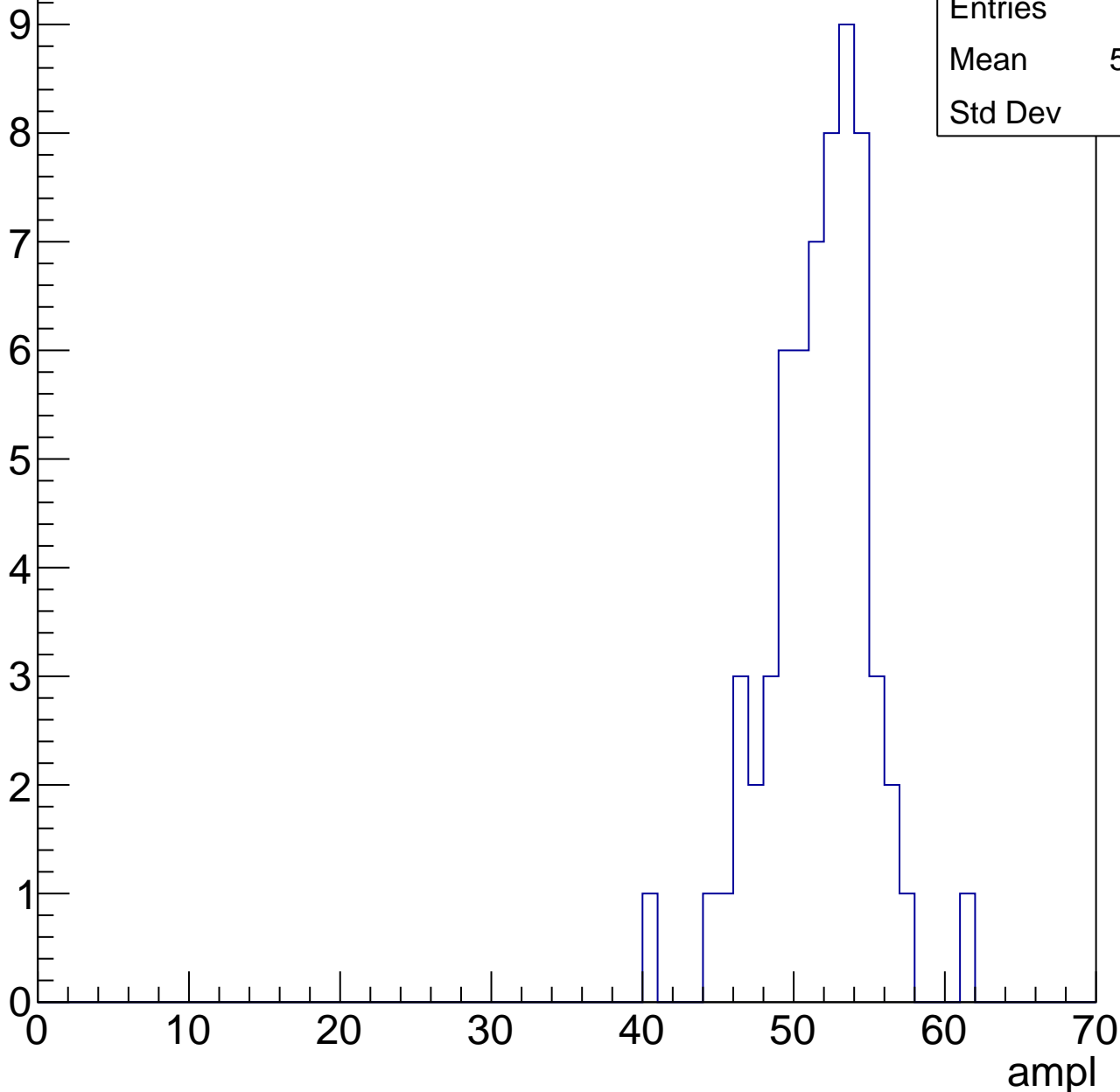


B1L101S, U21-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	51.23
Std Dev	3.4

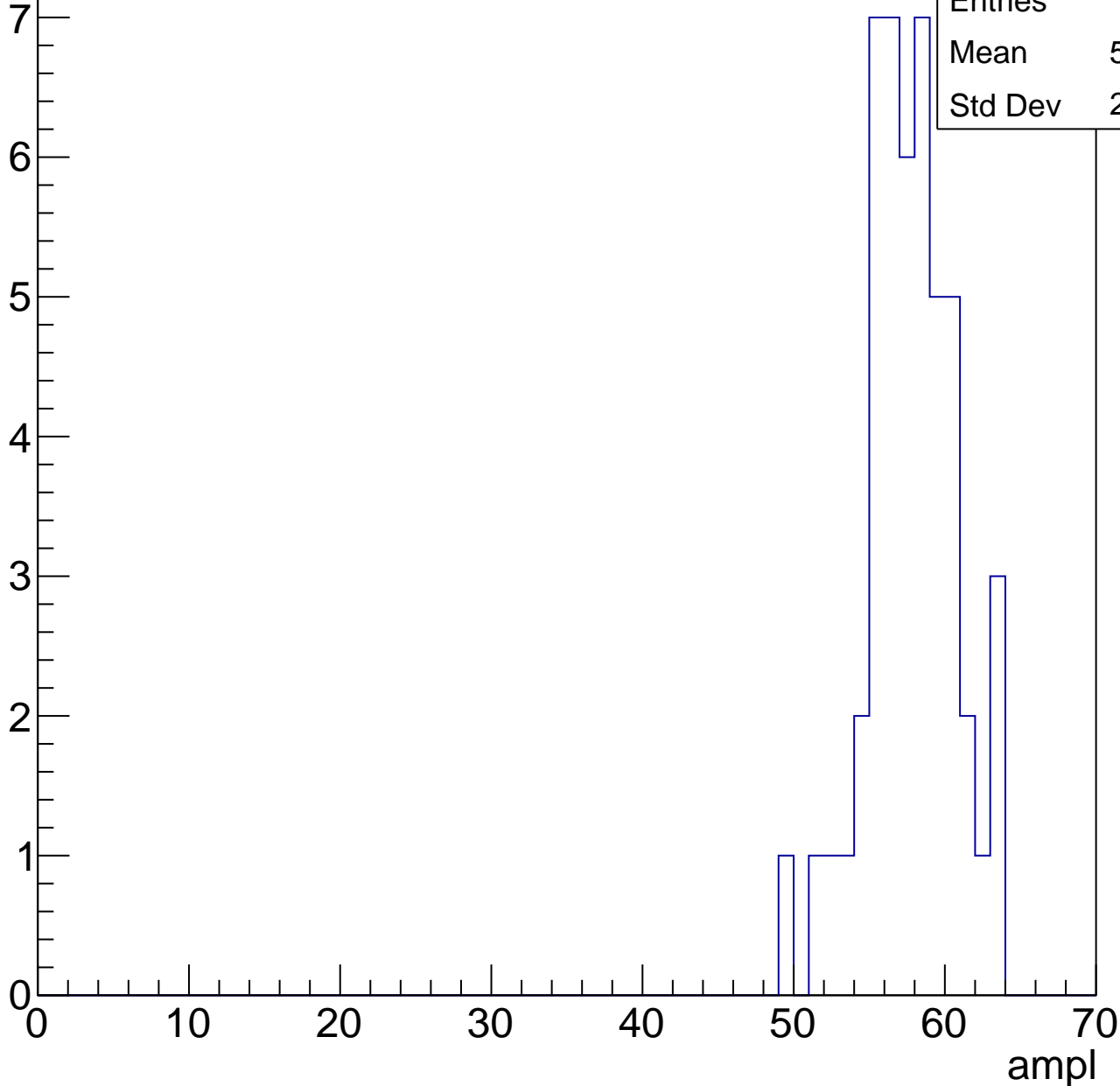


B1L101S, U21-ch32, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.27
Std Dev	2.954

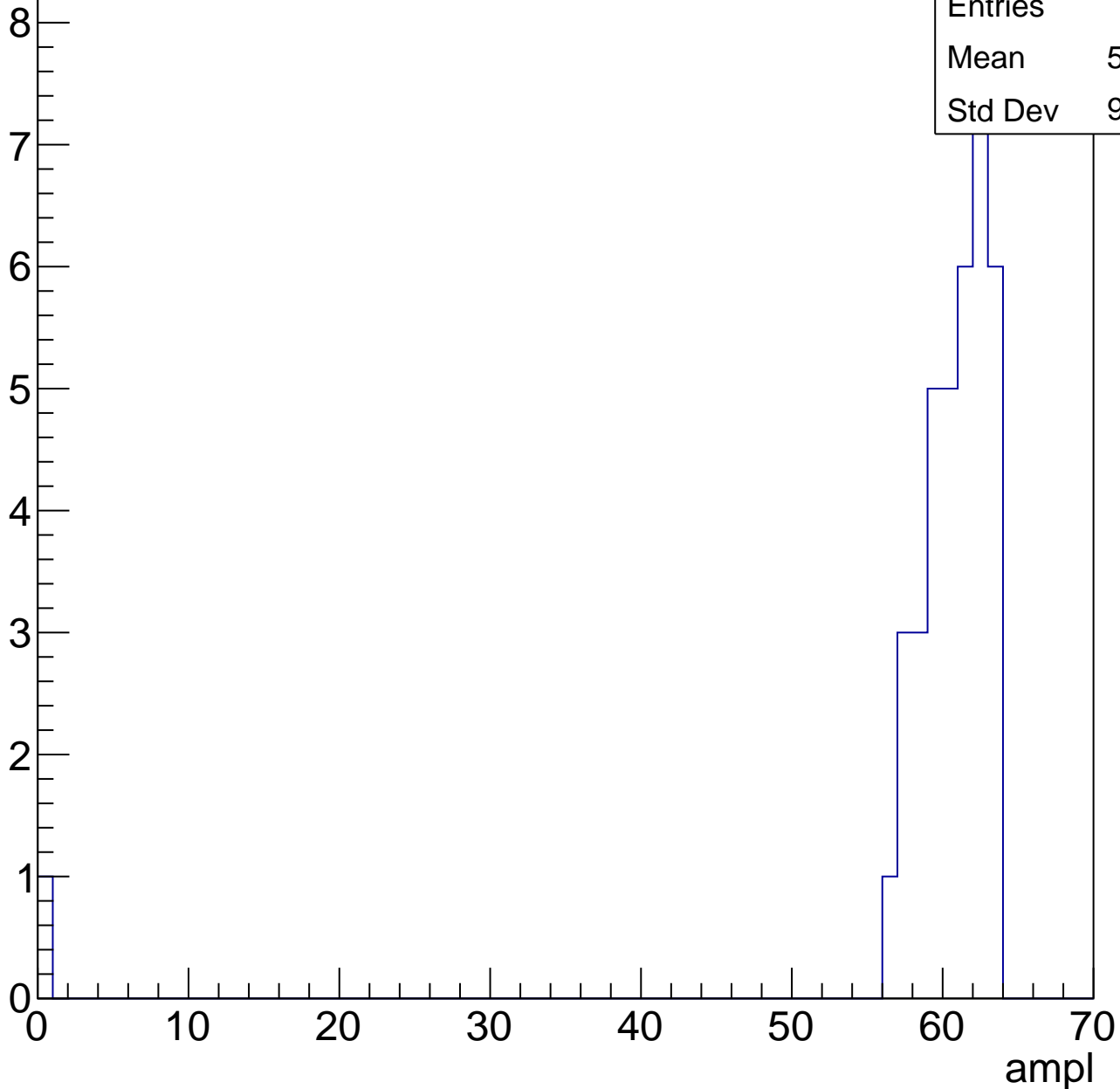


B1L101S, U21-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	38
Mean	58.84
Std Dev	9.869



B1L101S, U21-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

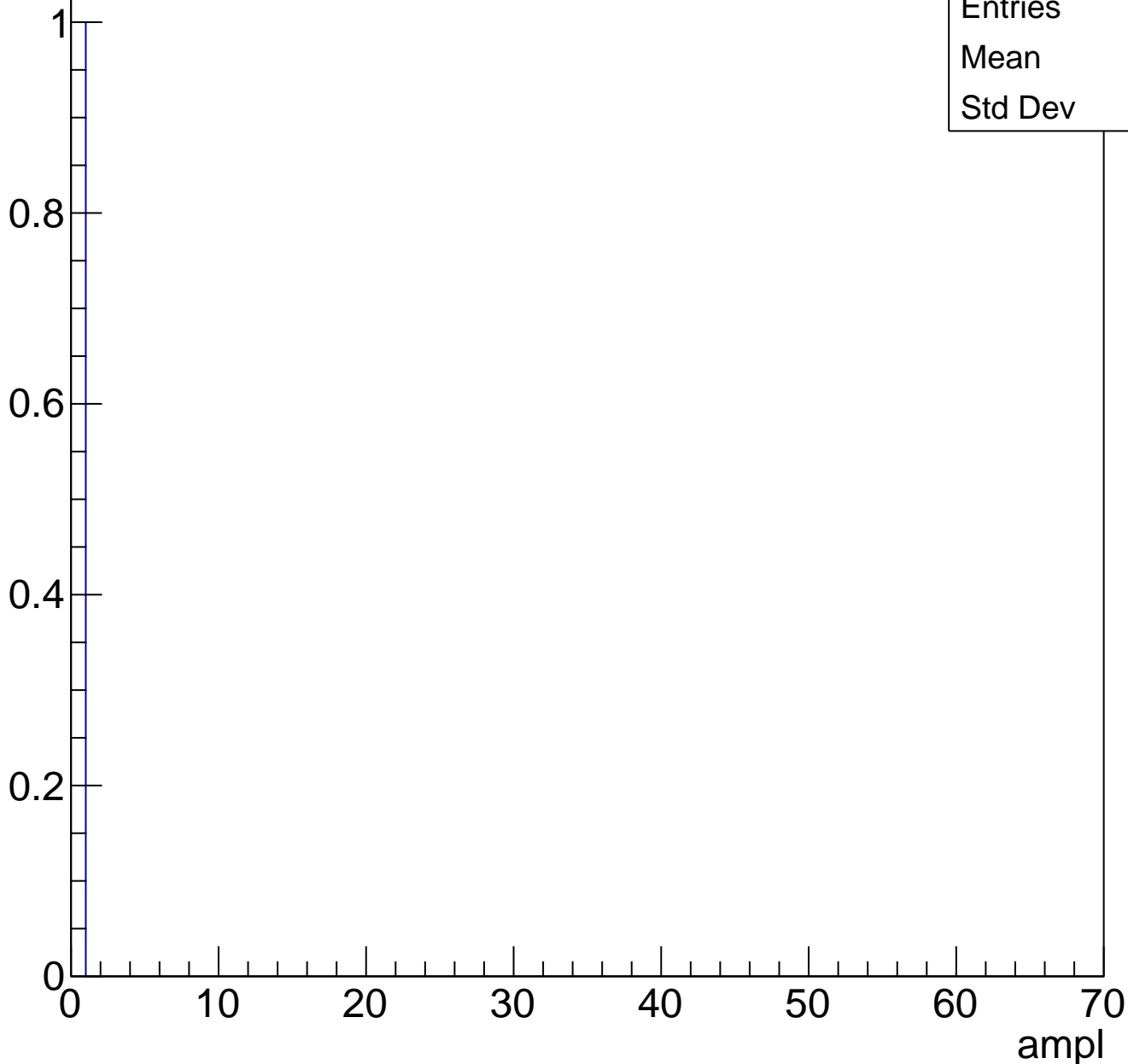
Entry



B1L101S, U21-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch33, adc0

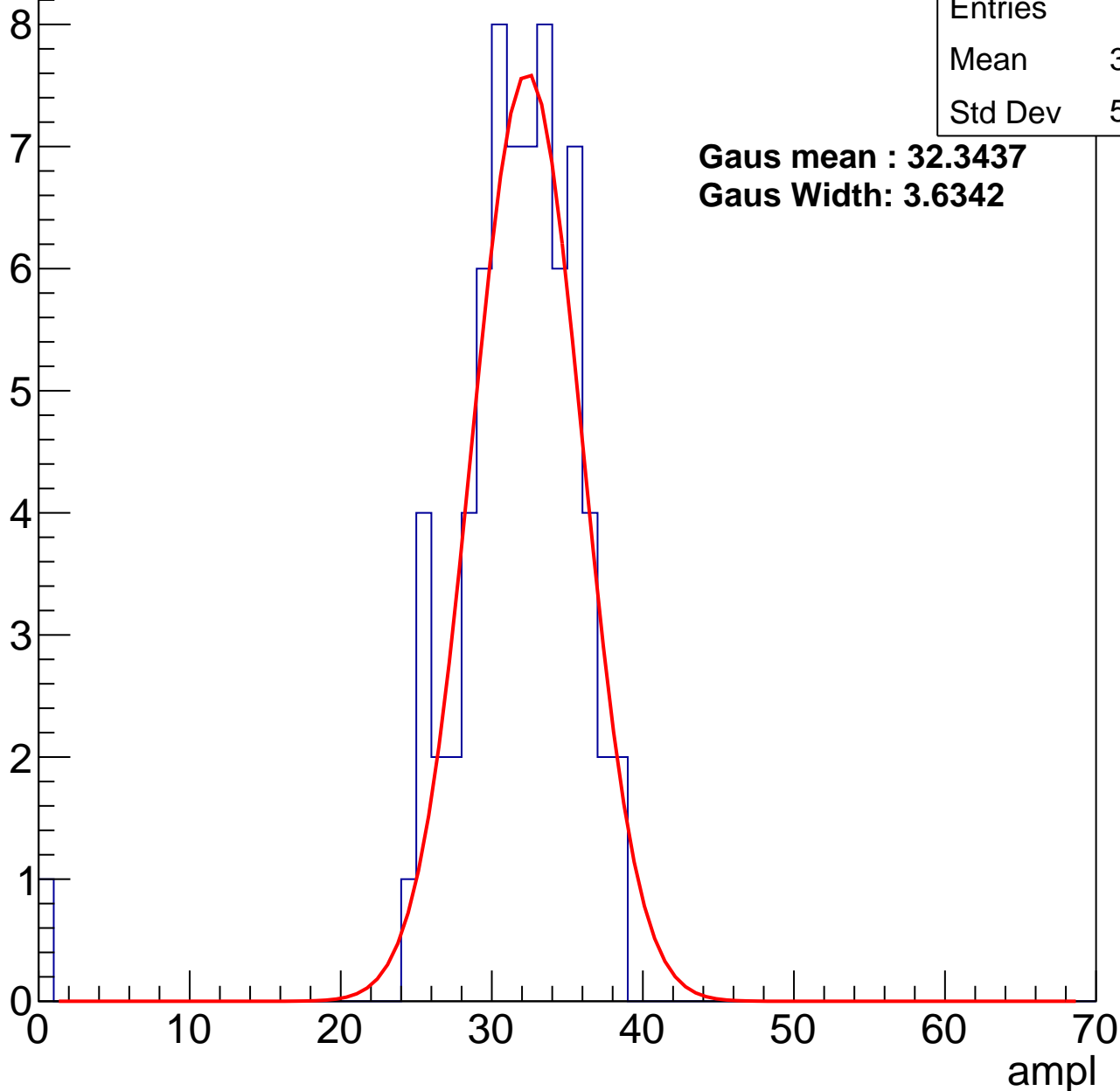
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	31.04
Std Dev	5.005

Gaus mean : 32.3437

Gaus Width: 3.6342



B1L101S, U21-ch33, adc1

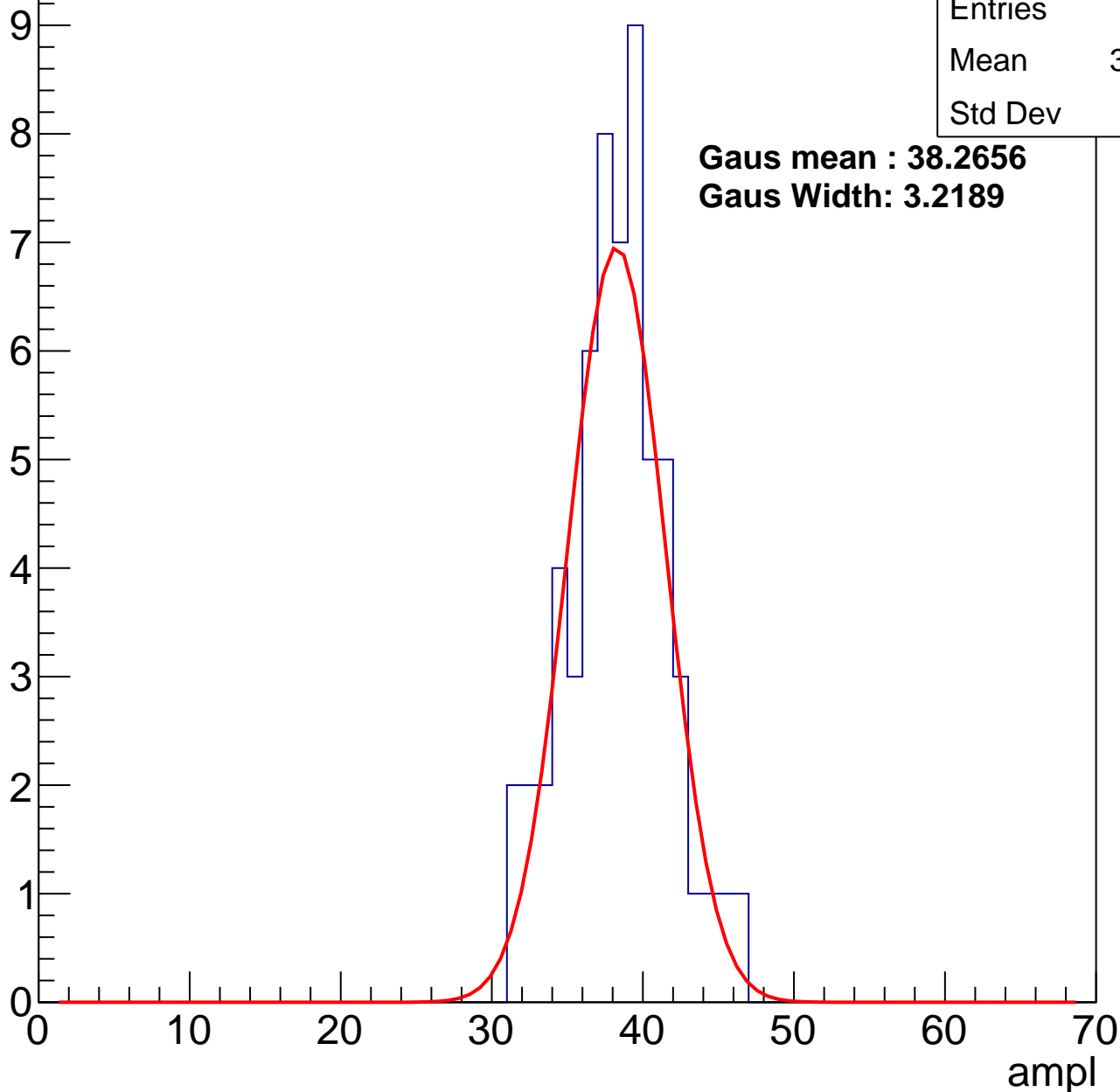
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.85
Std Dev	3.26

Gaus mean : 38.2656

Gaus Width: 3.2189



B1L101S, U21-ch33, adc2

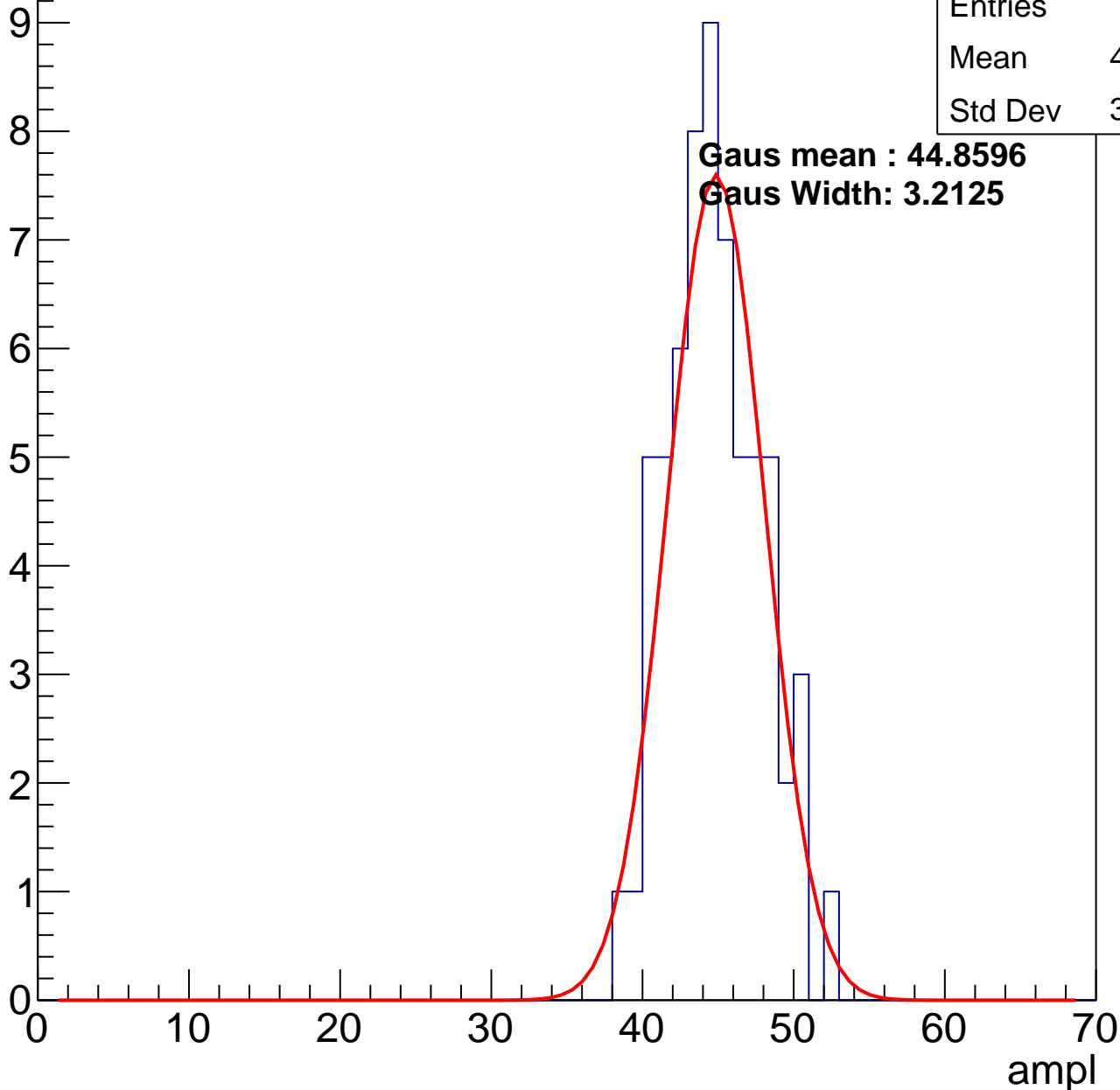
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.35
Std Dev	3.045

Gaus mean : 44.8596

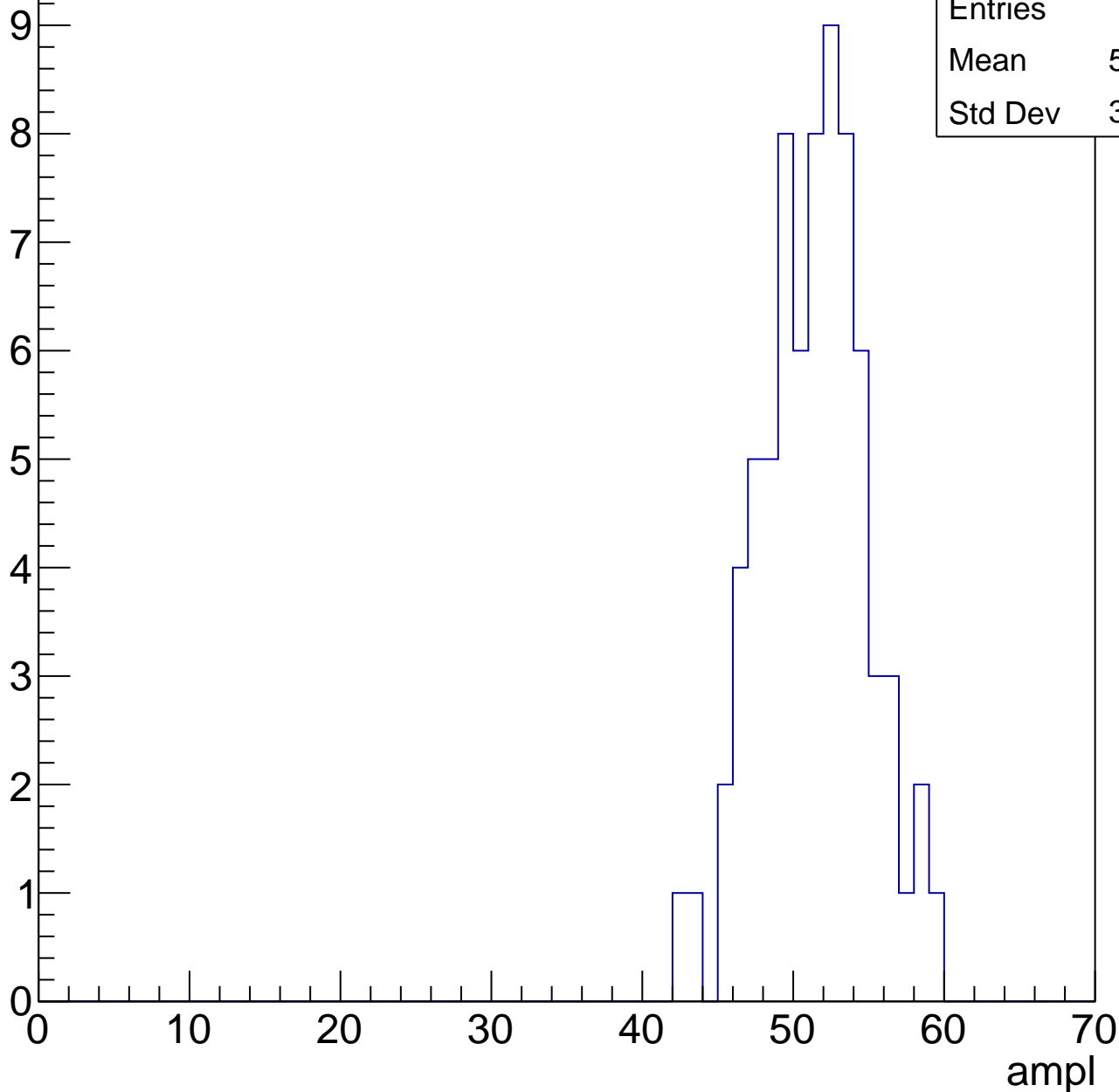
Gaus Width: 3.2125



B1L101S, U21-ch33, adc3

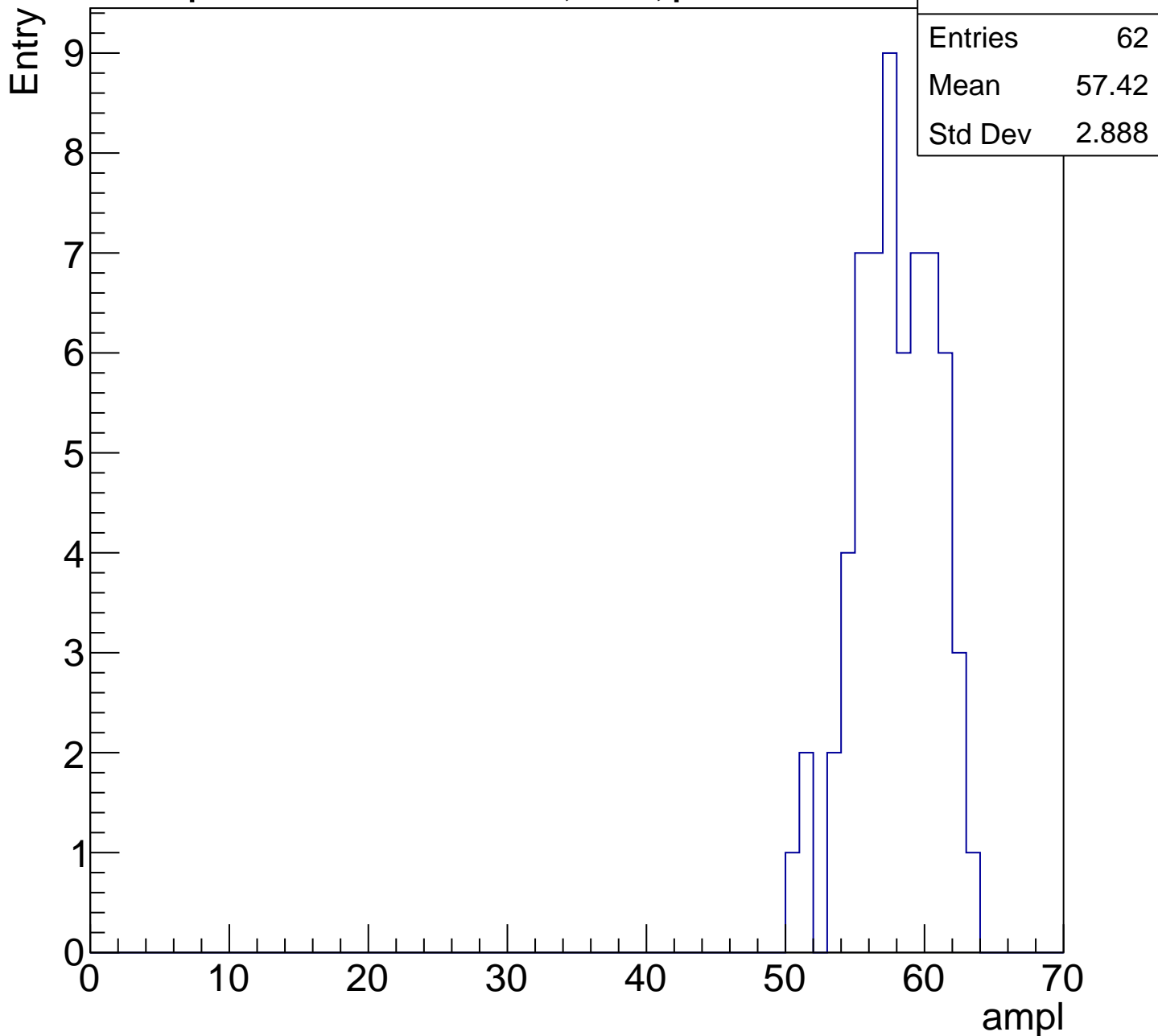
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch33, adc4

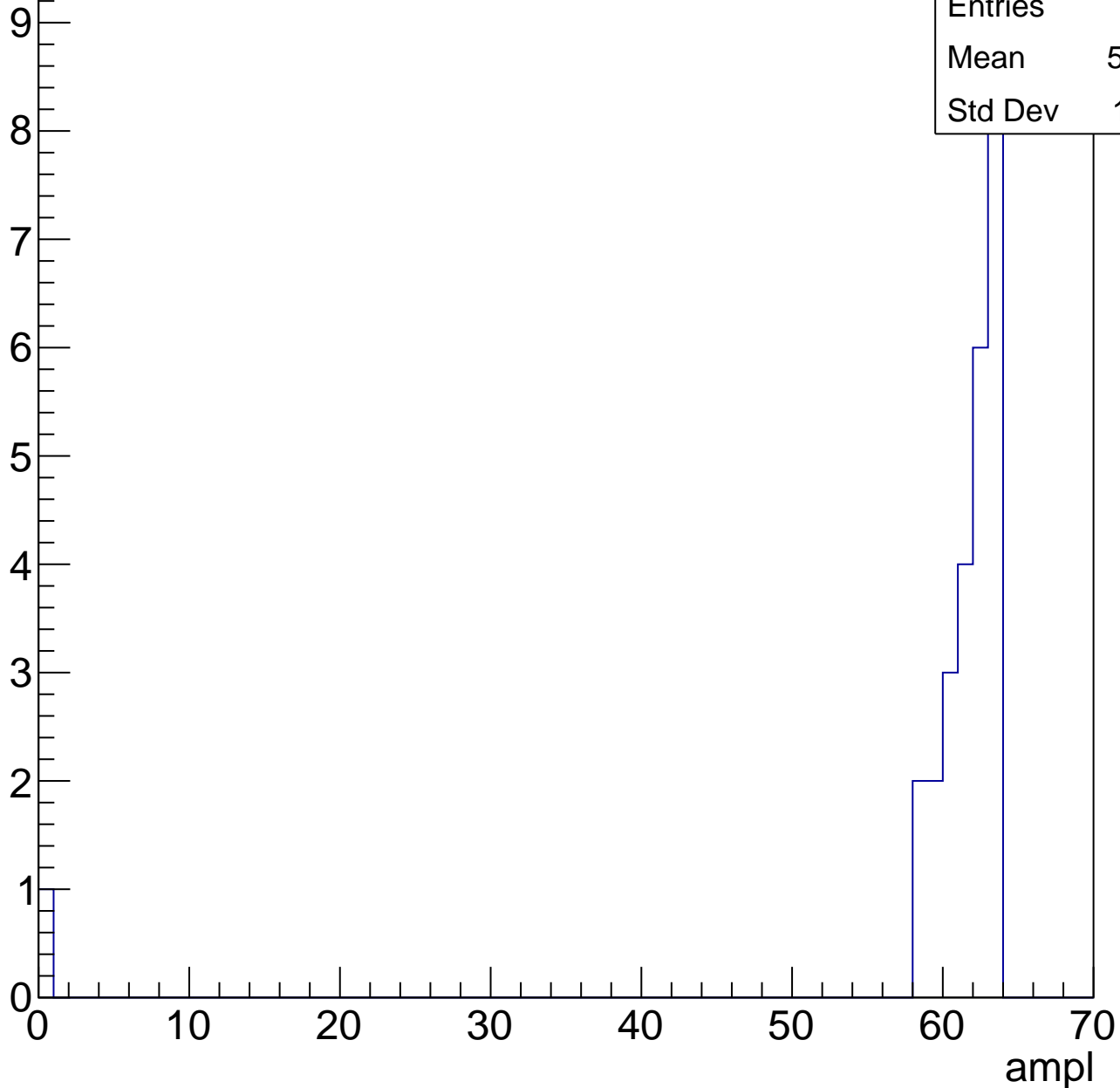
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

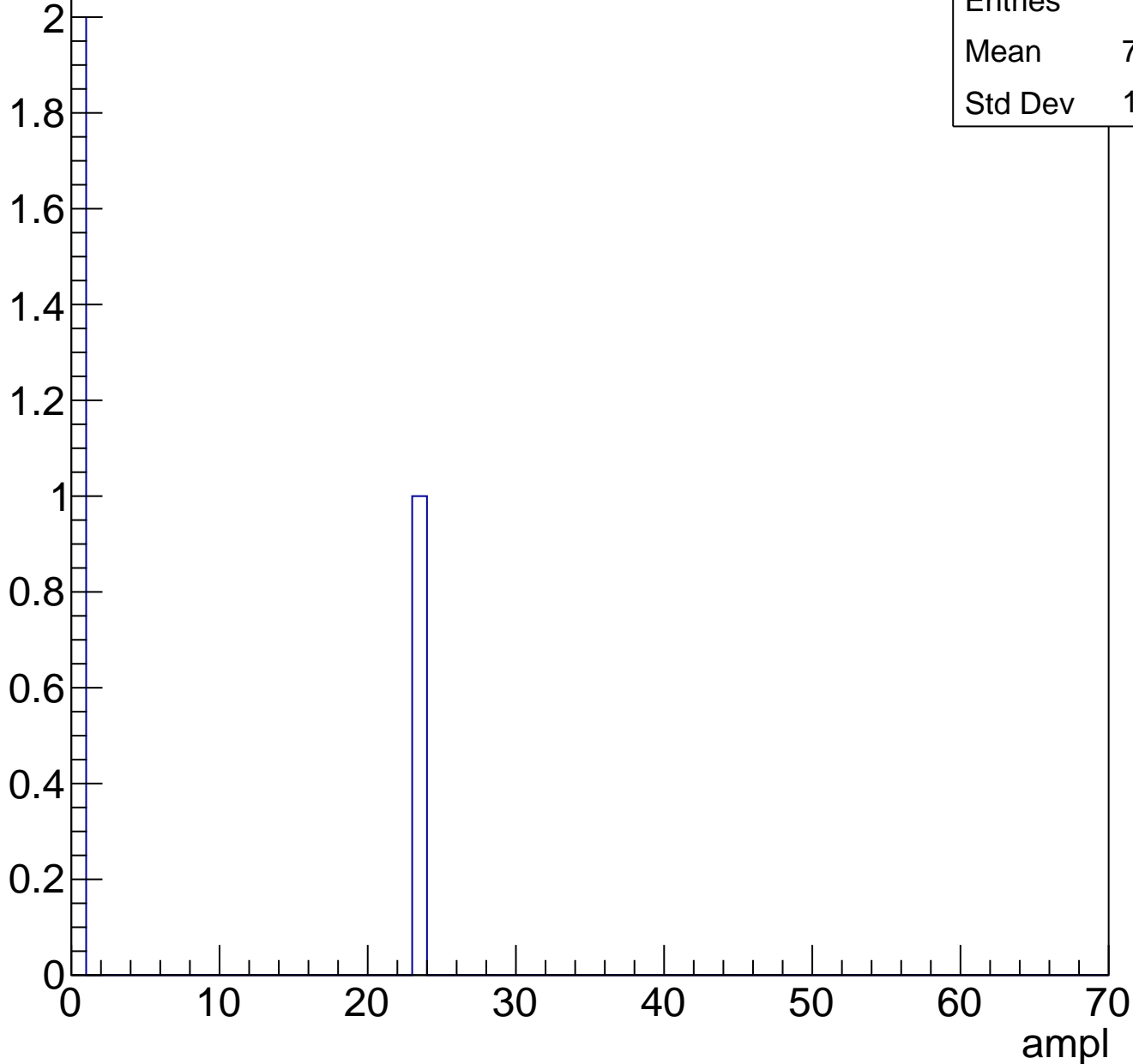


Entries	1
Mean	62
Std Dev	0

B1L101S, U21-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch34, adc0

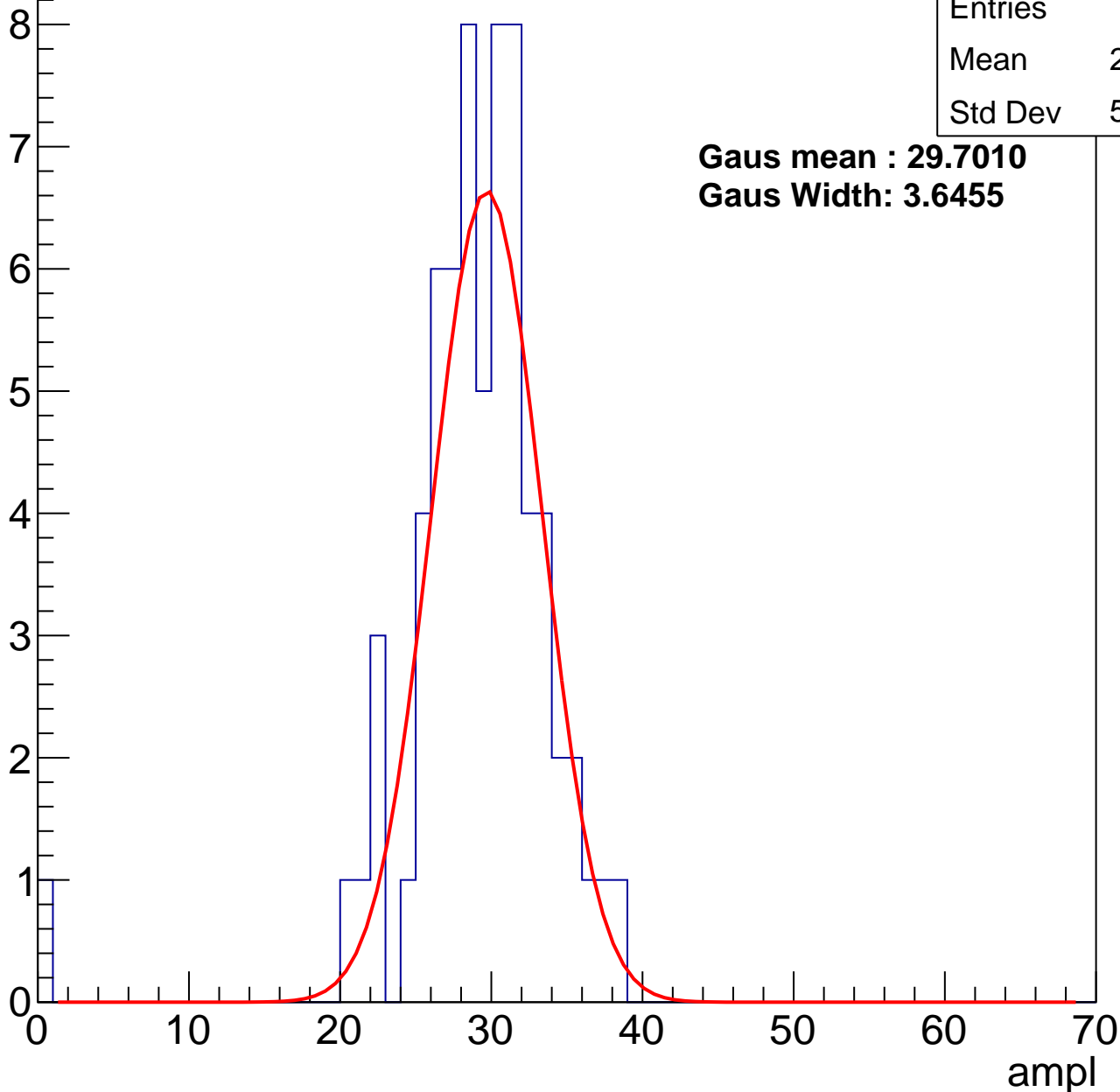
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.58
Std Dev	5.114

Gaus mean : 29.7010

Gaus Width: 3.6455



B1L101S, U21-ch34, adc1

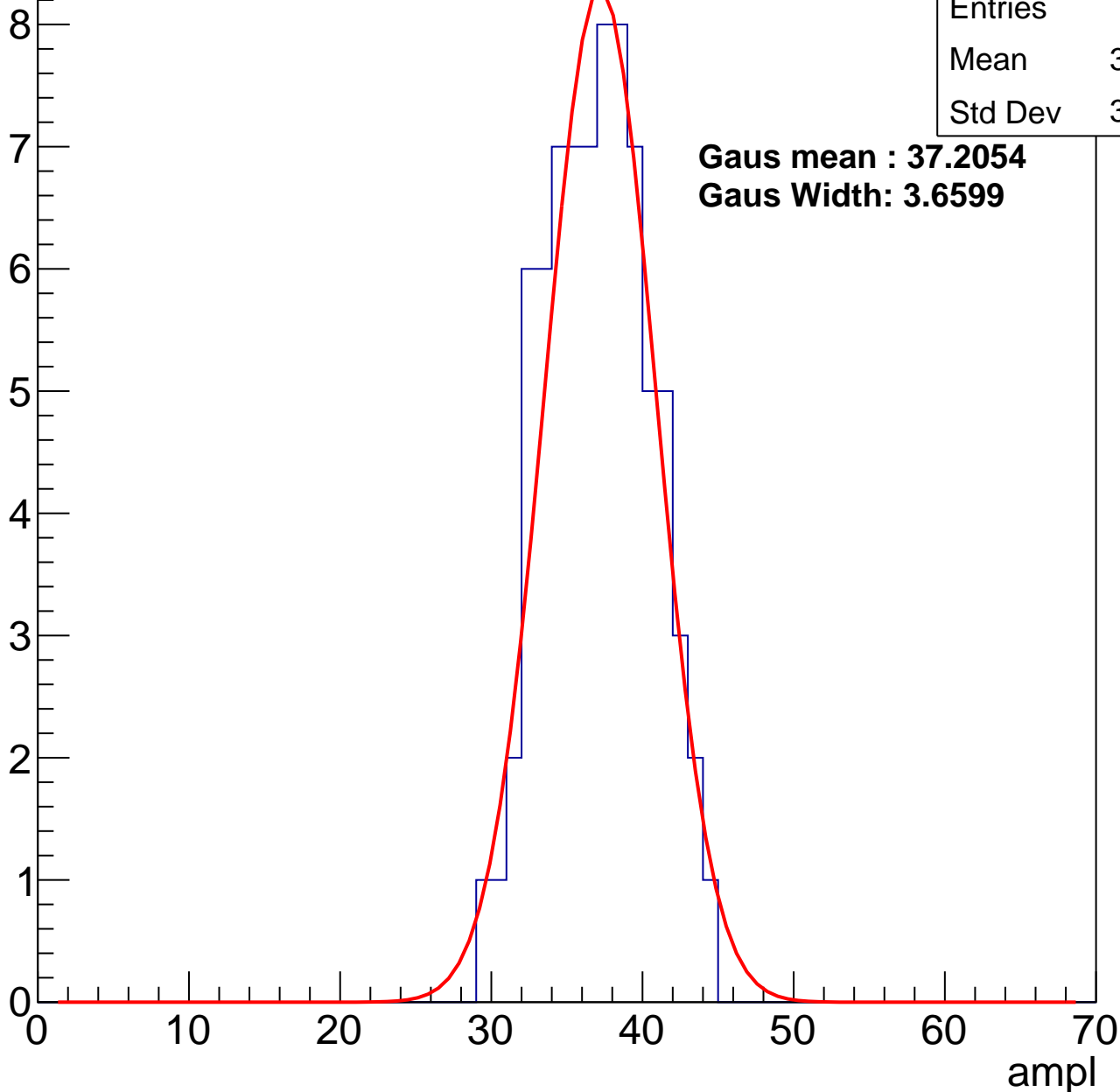
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.58
Std Dev	3.388

Gaus mean : 37.2054

Gaus Width: 3.6599



B1L101S, U21-ch34, adc2

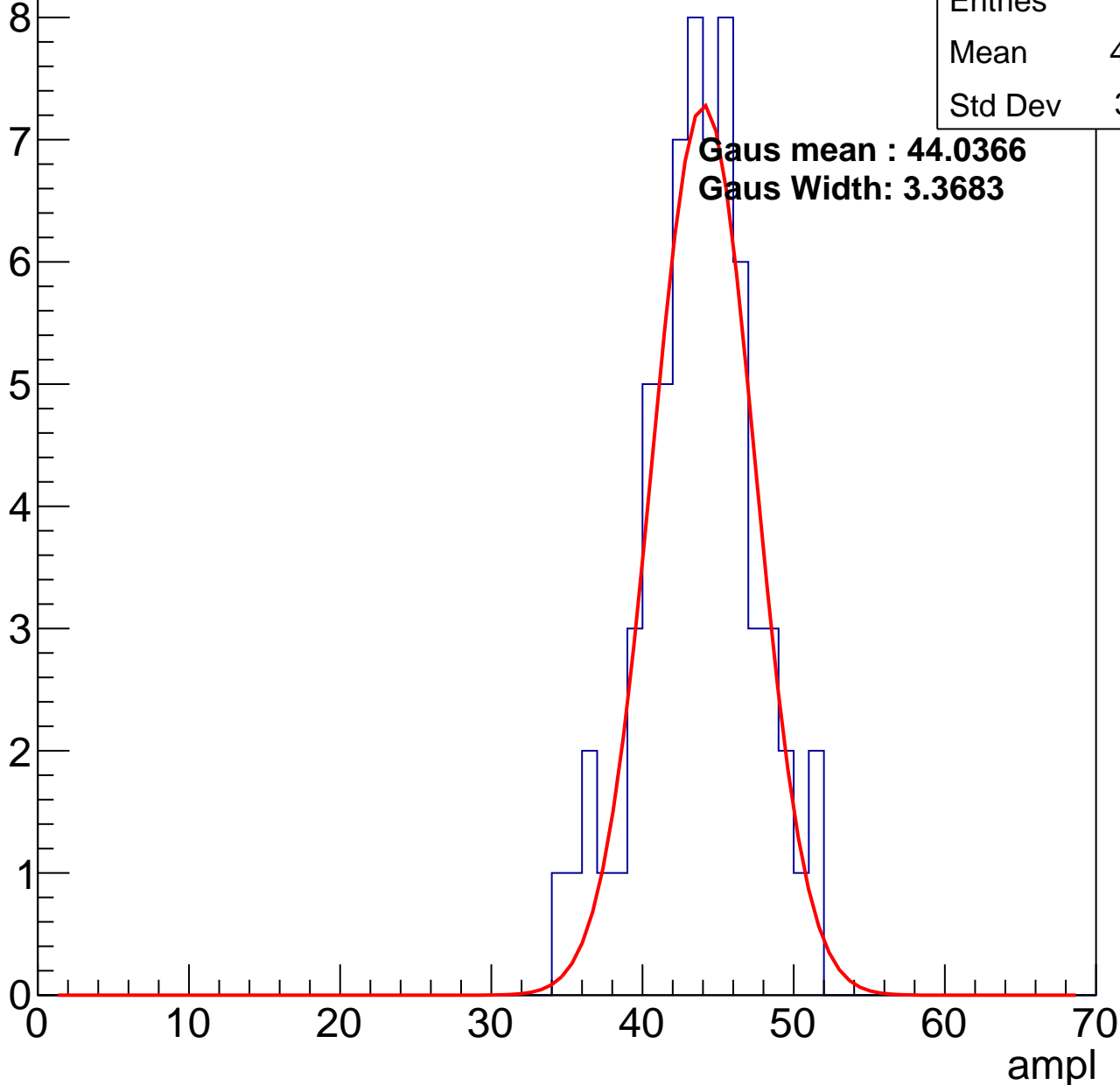
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.26
Std Dev	3.661

Gaus mean : 44.0366

Gaus Width: 3.3683

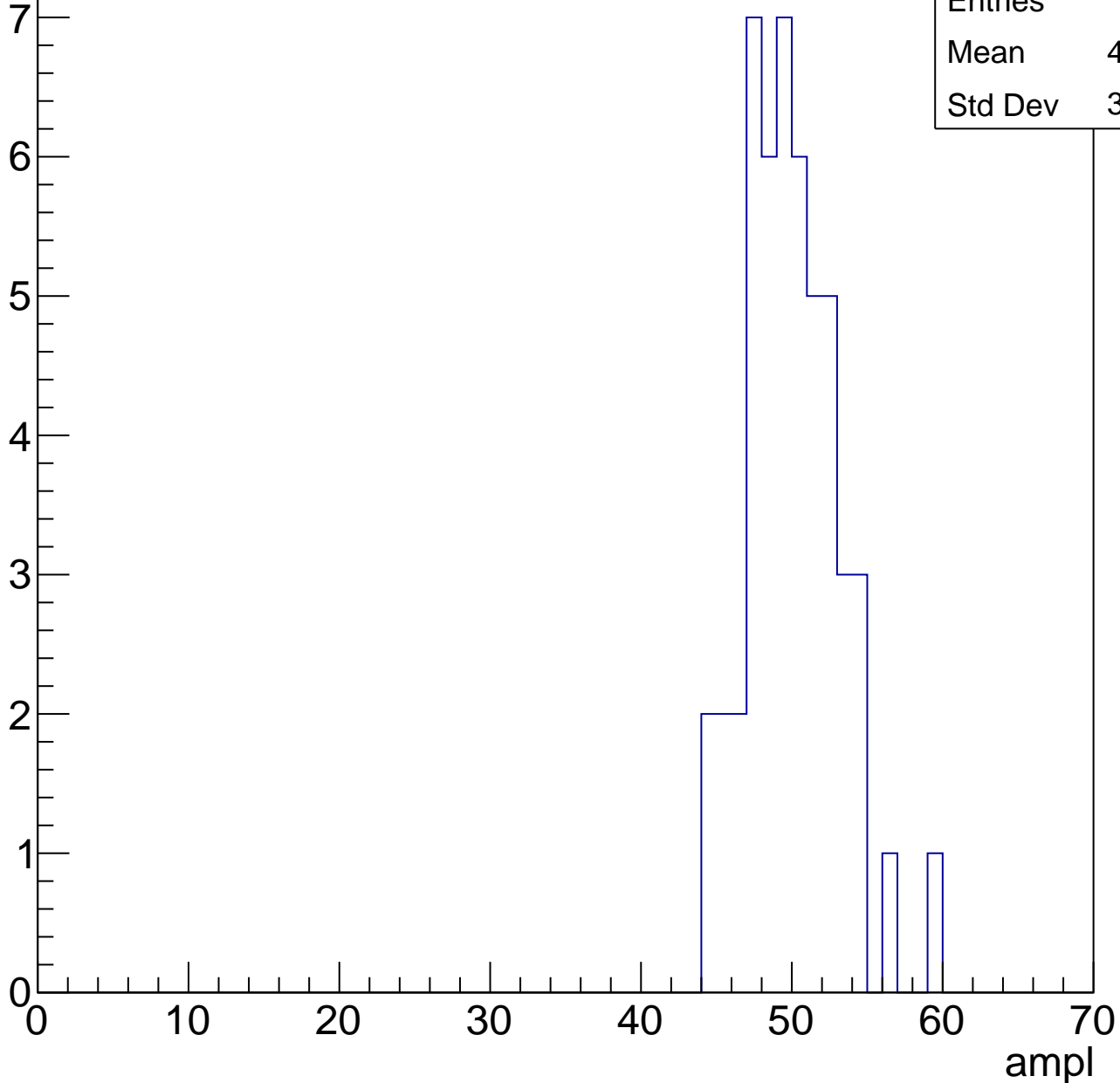


B1L101S, U21-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	49.62
Std Dev	3.026



B1L101S, U21-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	55.63
Std Dev	3.866

Entry

10

8

6

4

2

0

0

10

20

30

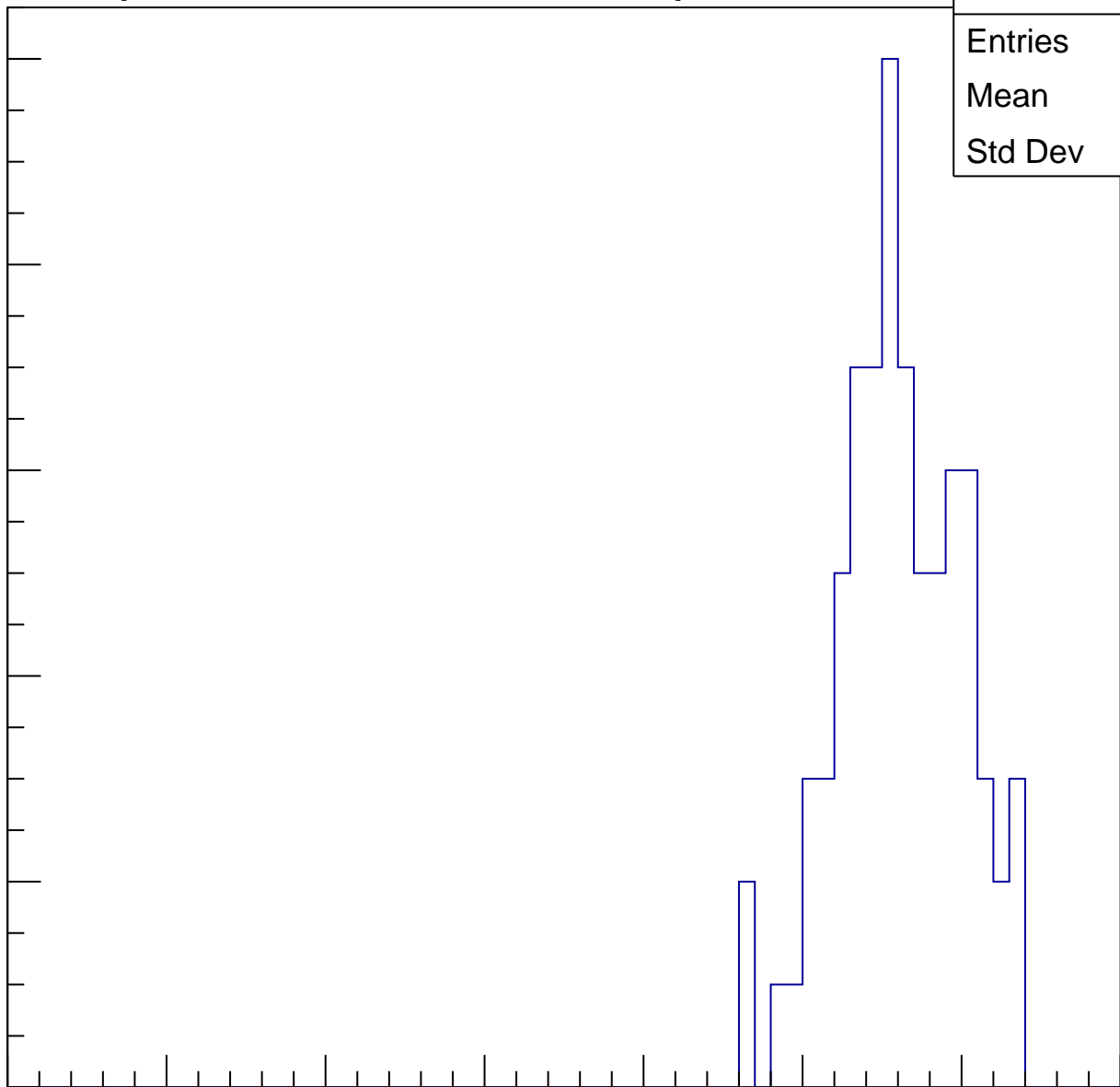
40

50

60

70

ampl

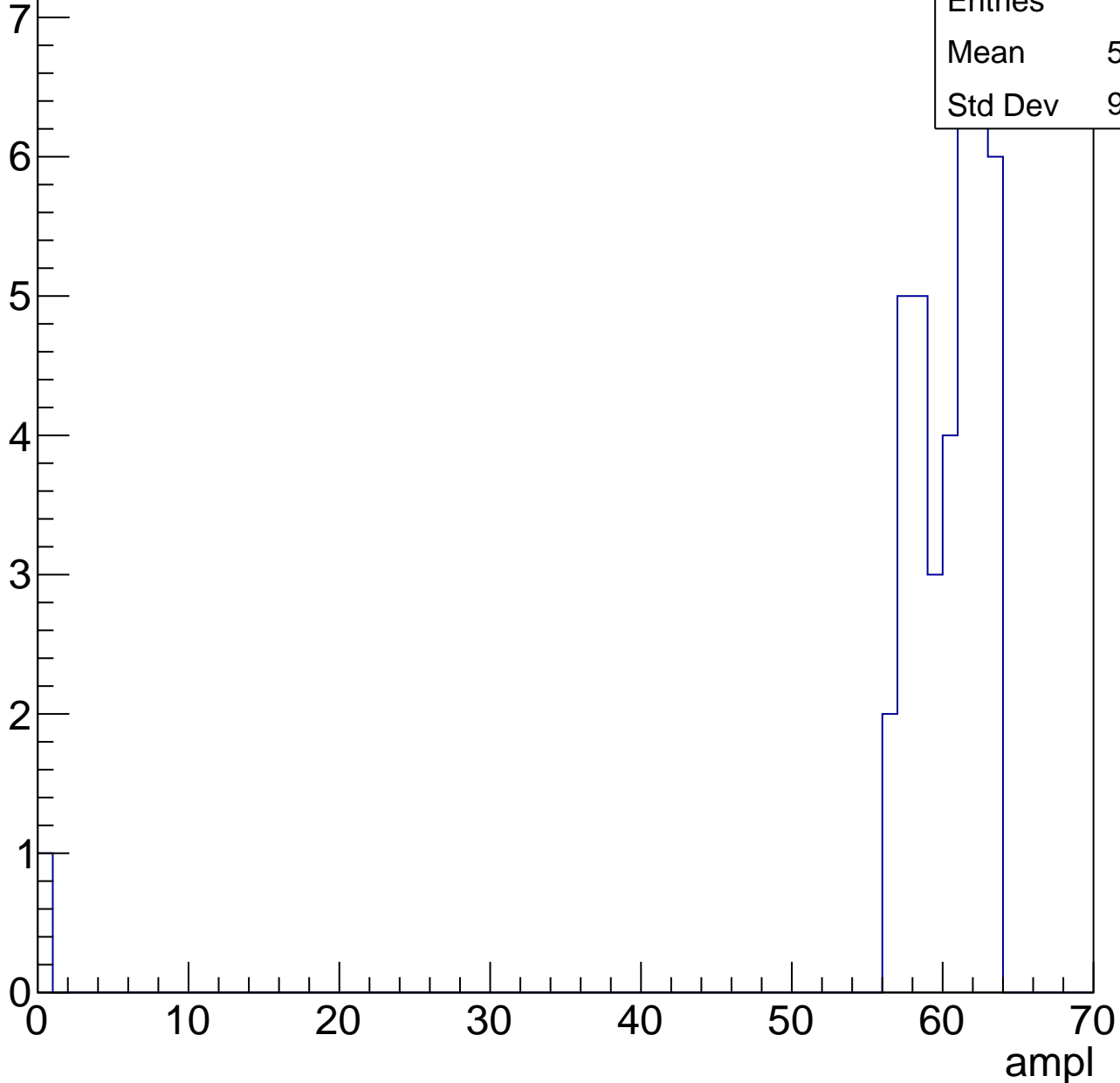


B1L101S, U21-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	58.58
Std Dev	9.628



B1L101S, U21-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch35, adc0

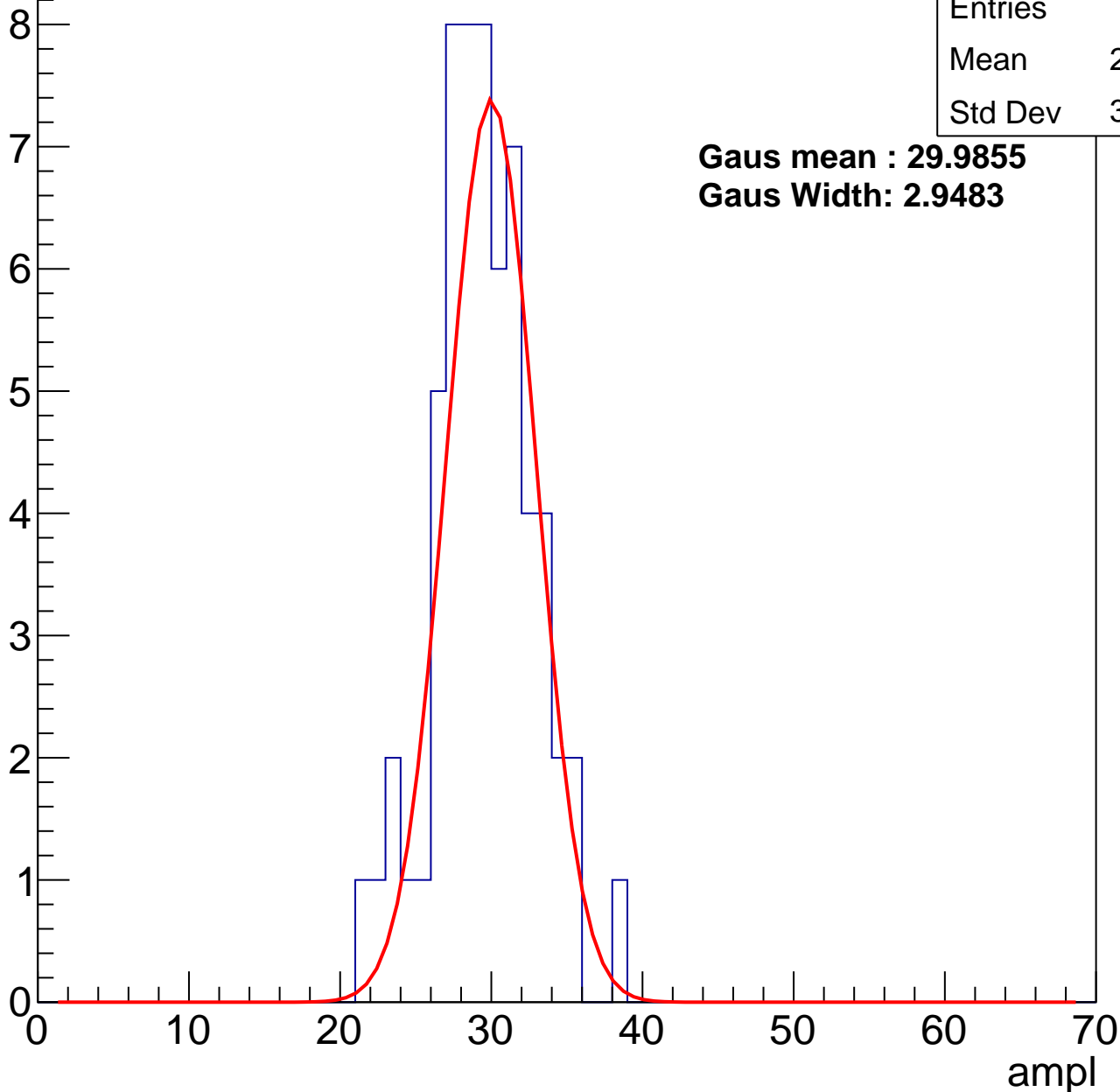
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	29.07
Std Dev	3.259

Gaus mean : 29.9855

Gaus Width: 2.9483



B1L101S, U21-ch35, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	35.96
Std Dev	3.564

Gaus mean : 36.2304

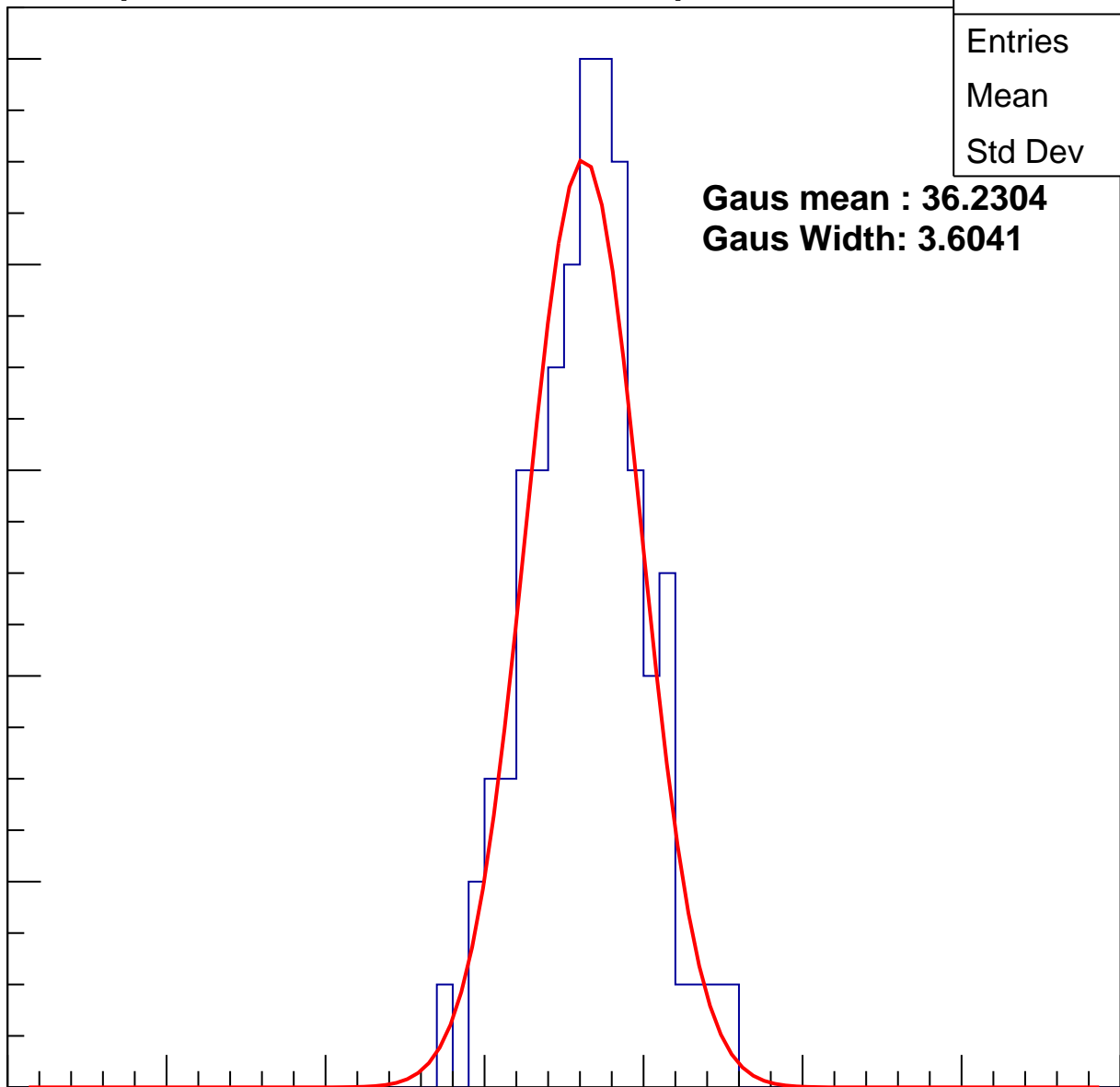
Gaus Width: 3.6041

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch35, adc2

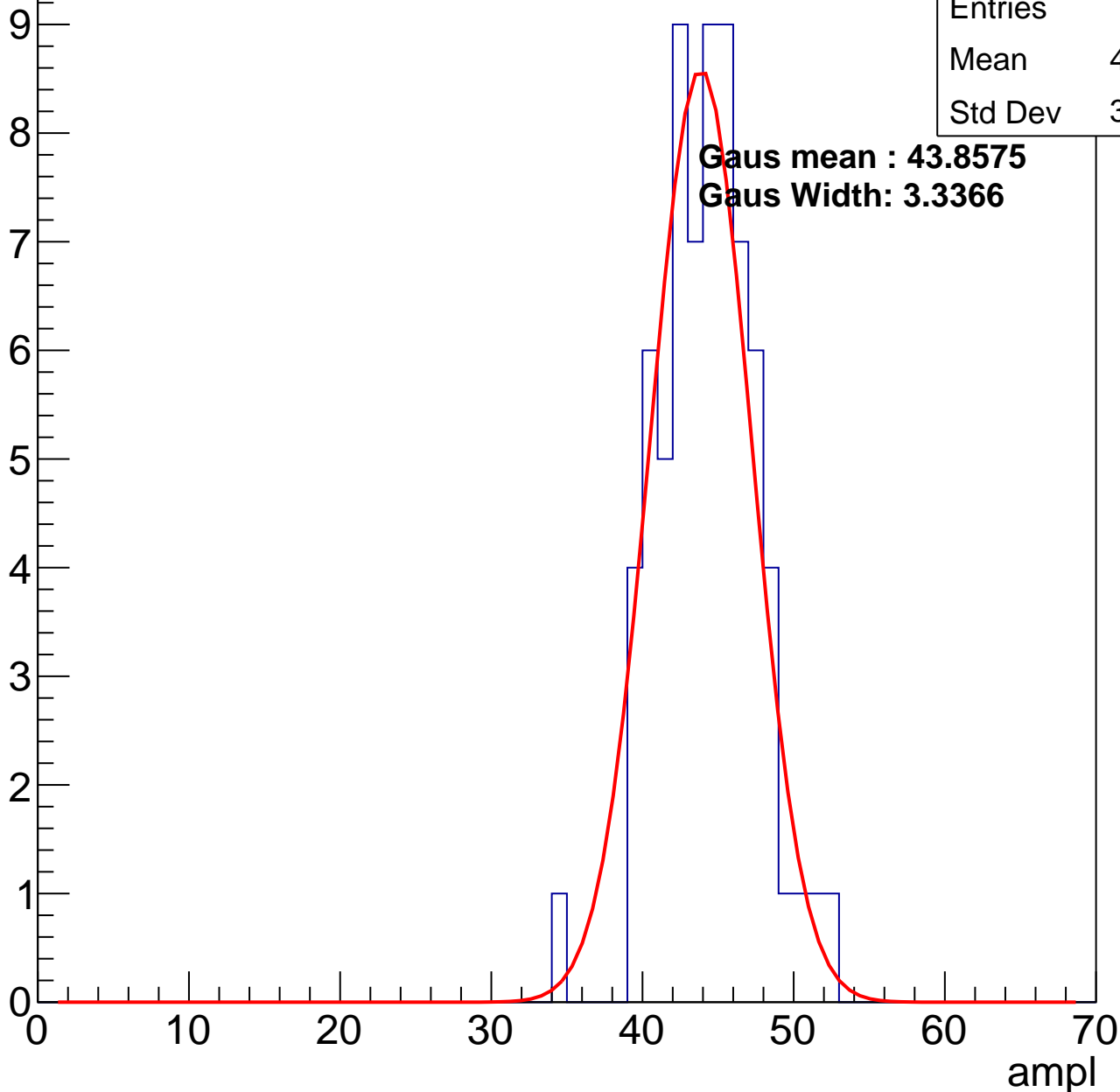
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.85
Std Dev	3.165

Gaus mean : 43.8575

Gaus Width: 3.3366

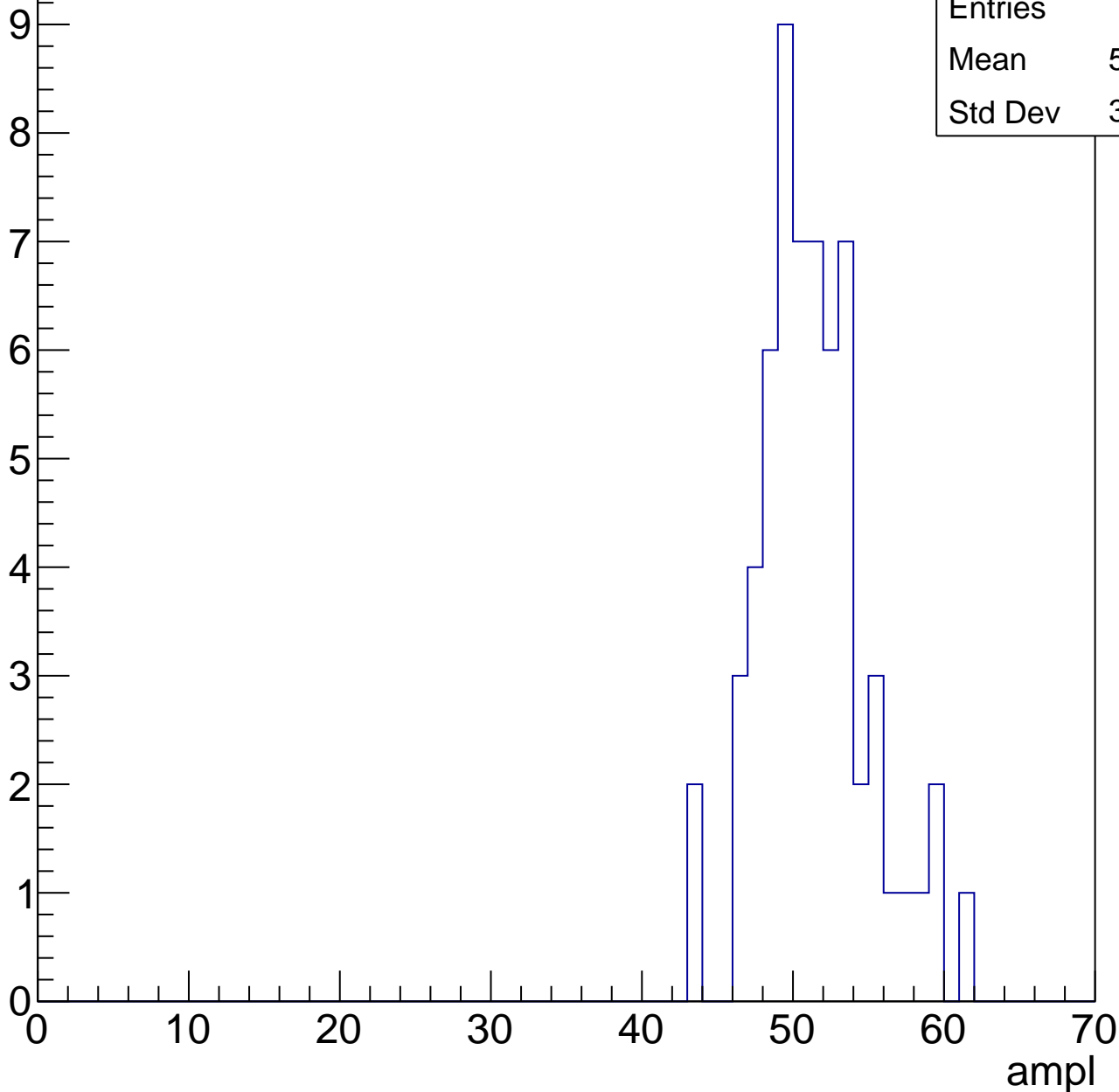


B1L101S, U21-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	50.87
Std Dev	3.594

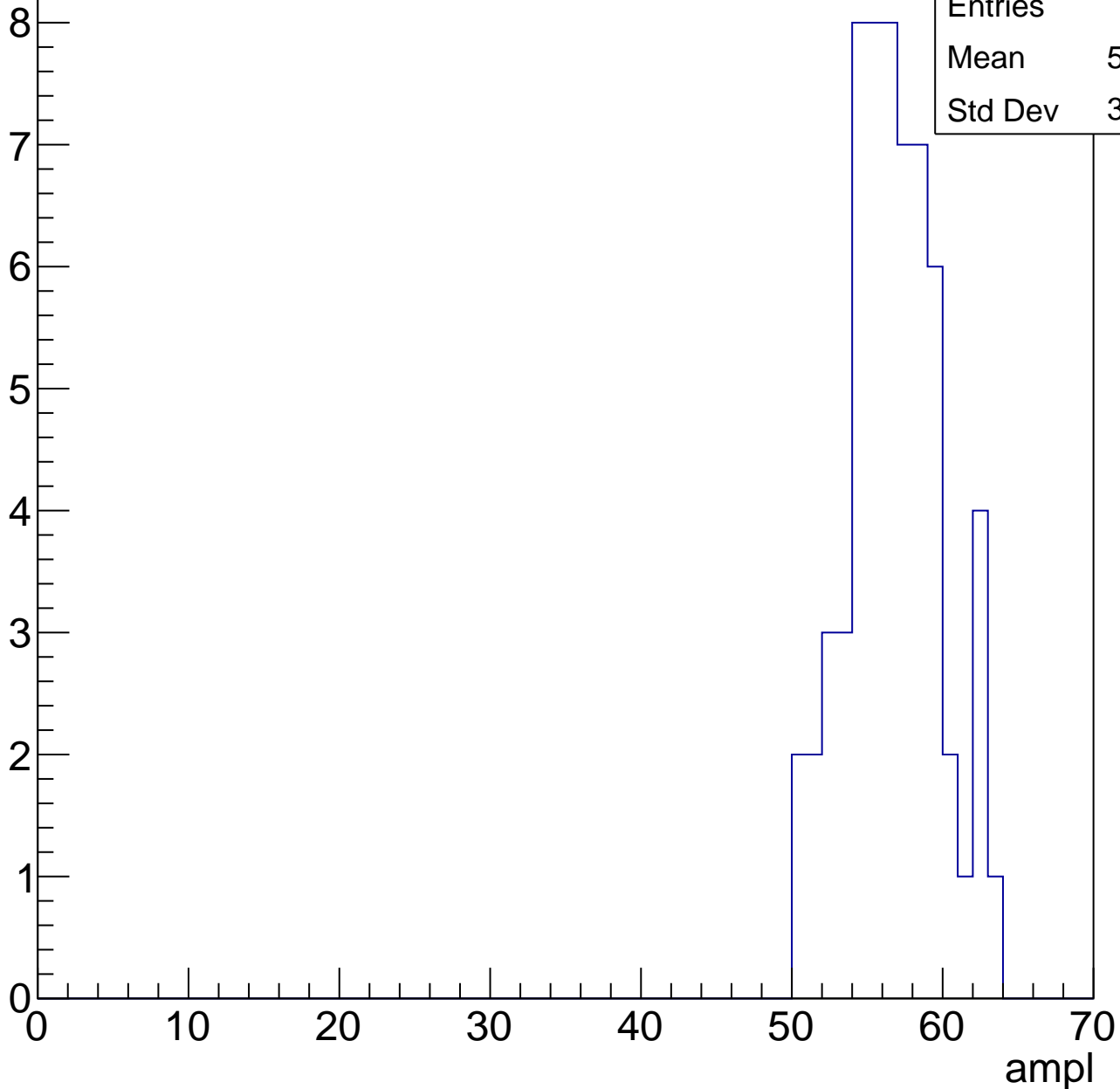


B1L101S, U21-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

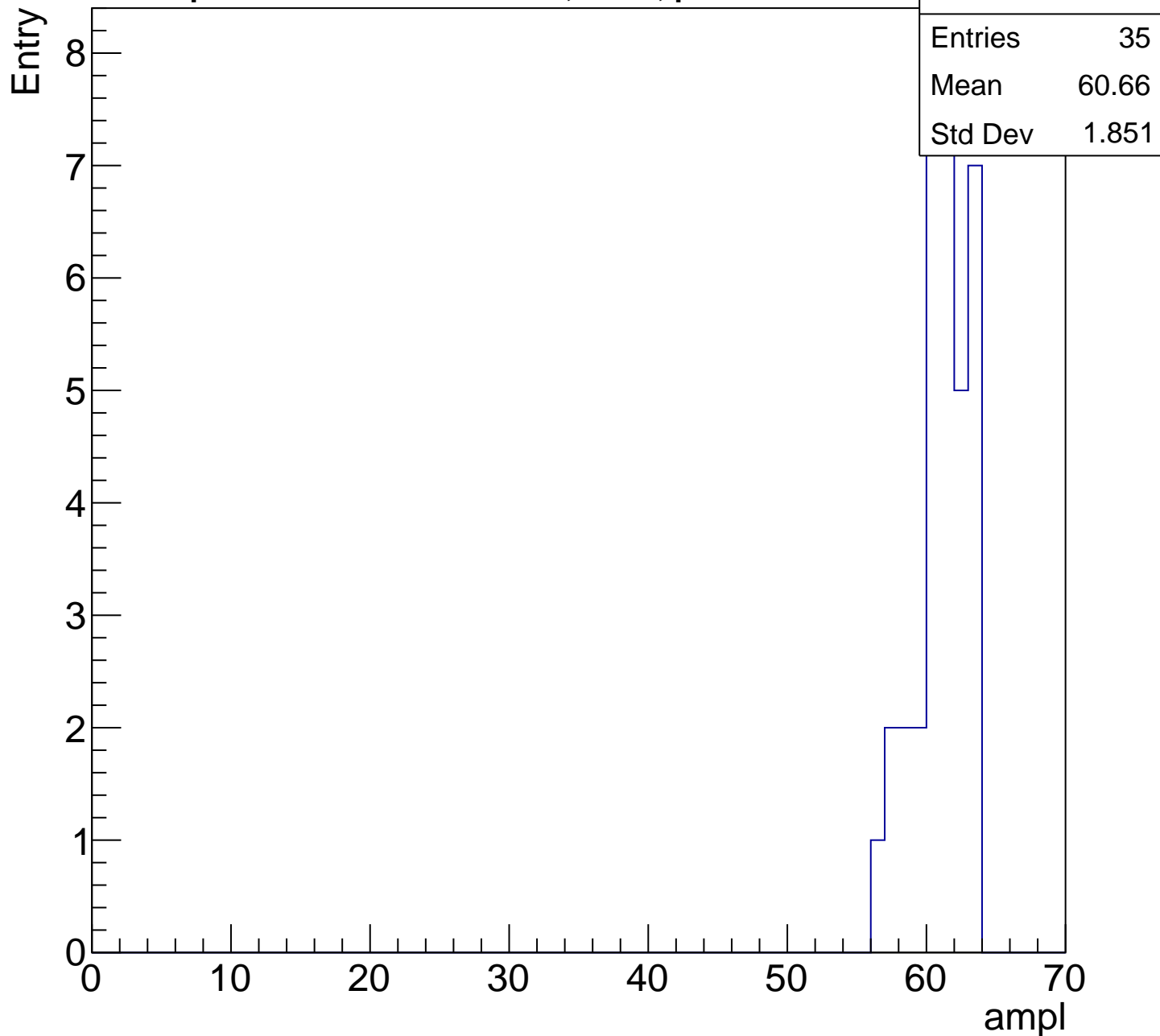
Entry

Entries	62
Mean	56.26
Std Dev	3.037



B1L101S, U21-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch36, adc0

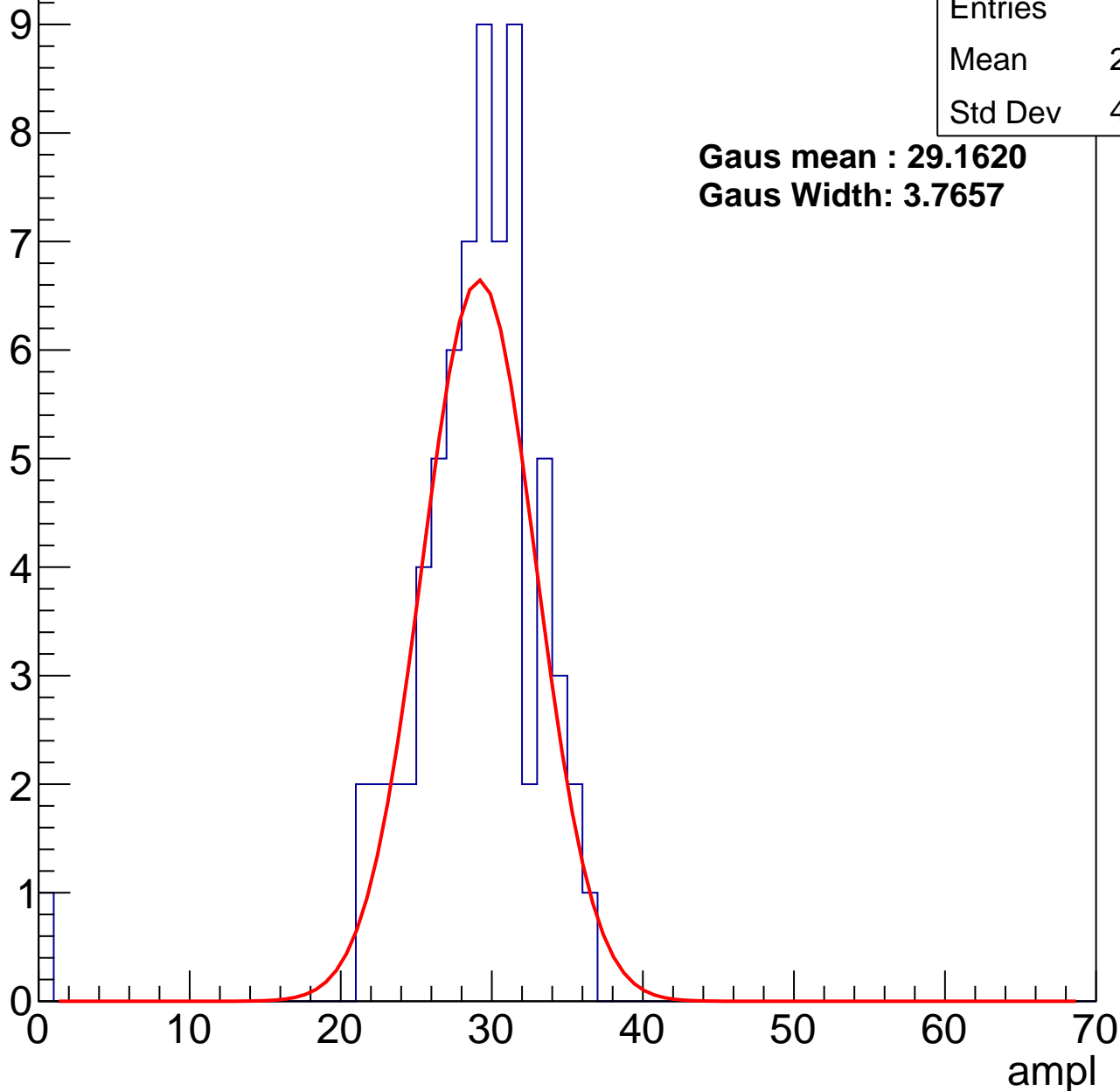
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.33
Std Dev	4.868

Gaus mean : 29.1620

Gaus Width: 3.7657



B1L101S, U21-ch36, adc1

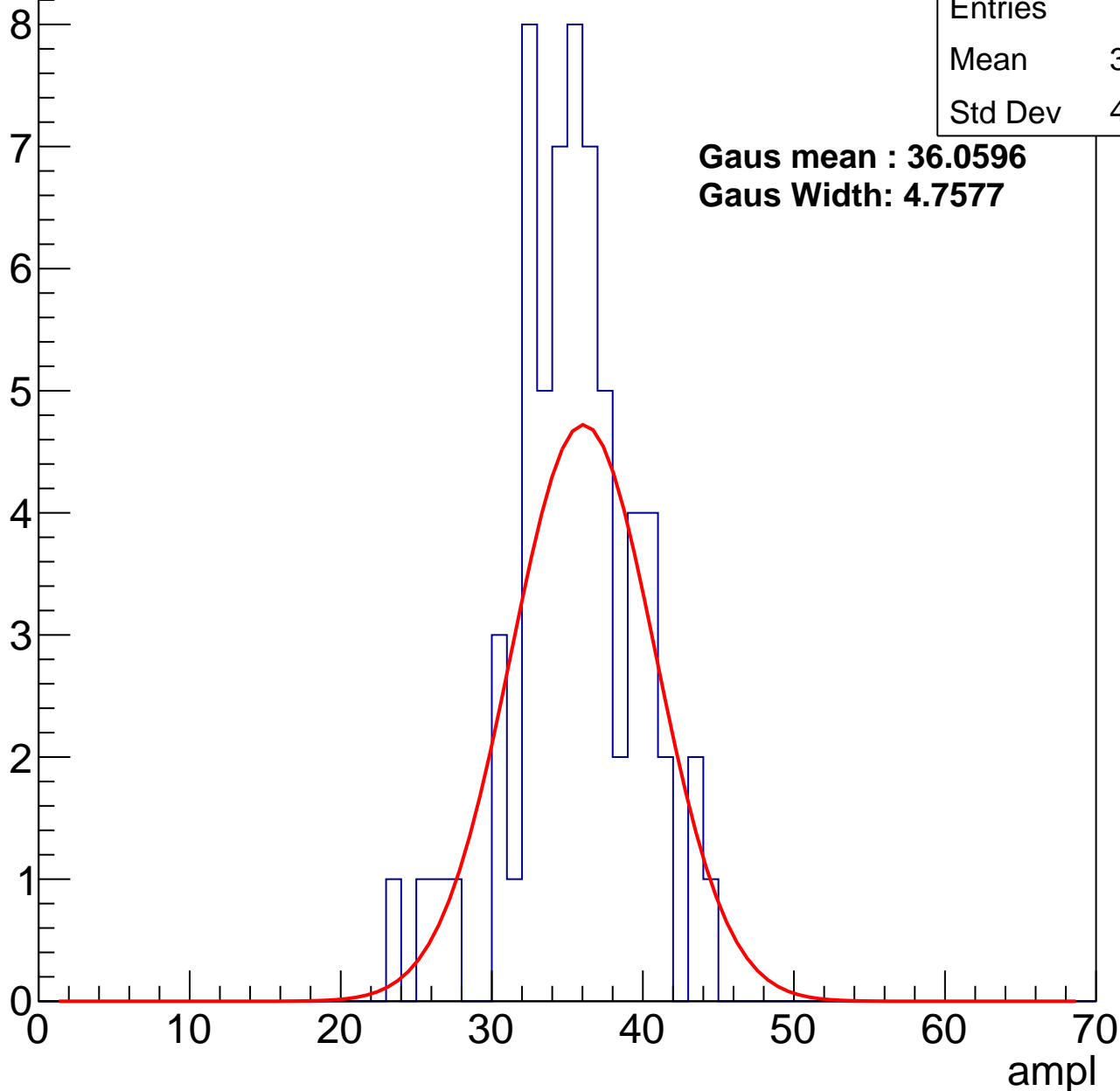
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	34.95
Std Dev	4.119

Gaus mean : 36.0596

Gaus Width: 4.7577



B1L101S, U21-ch36, adc2

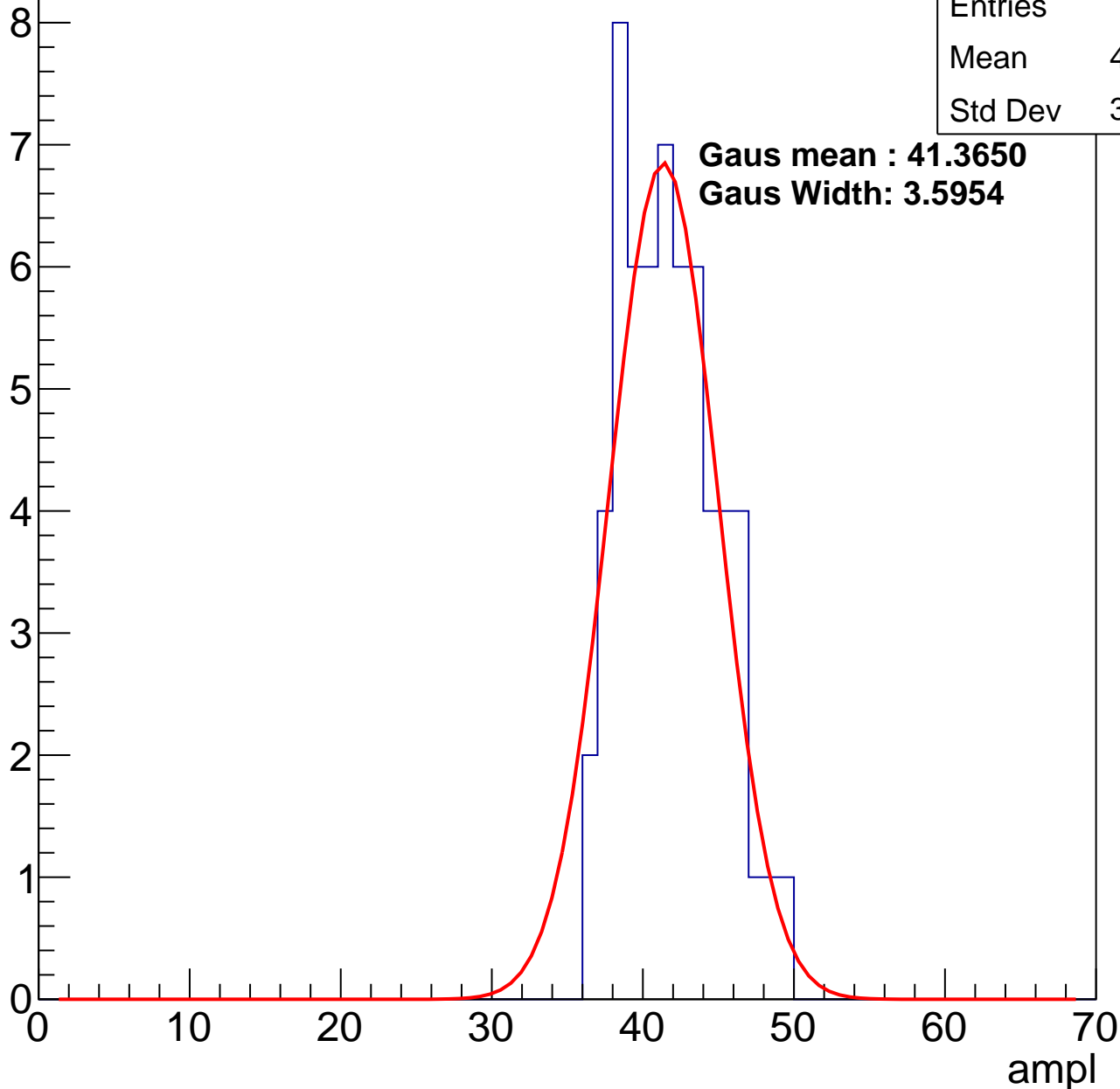
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.32
Std Dev	3.133

Gaus mean : 41.3650

Gaus Width: 3.5954

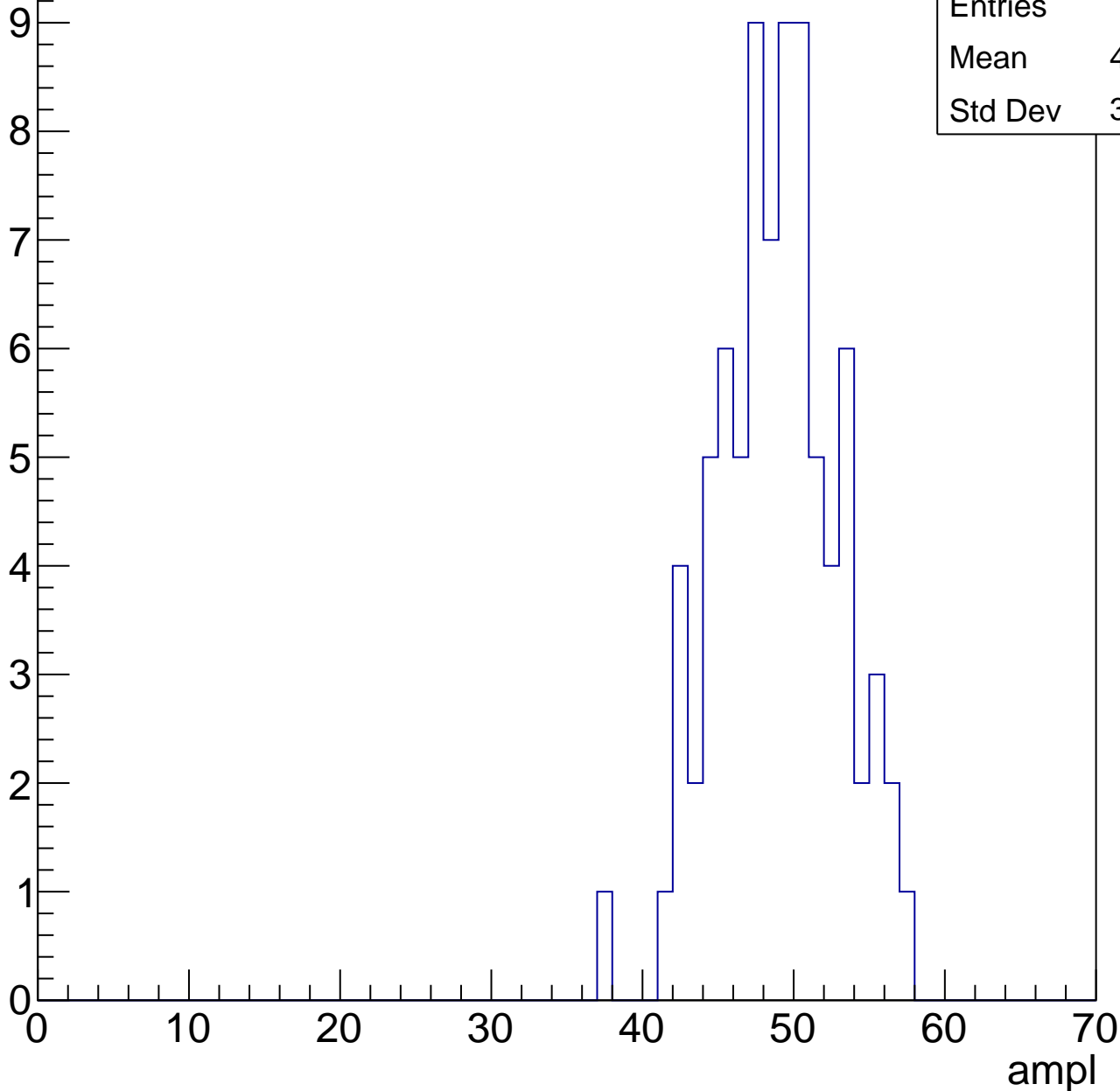


B1L101S, U21-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	48.46
Std Dev	3.916

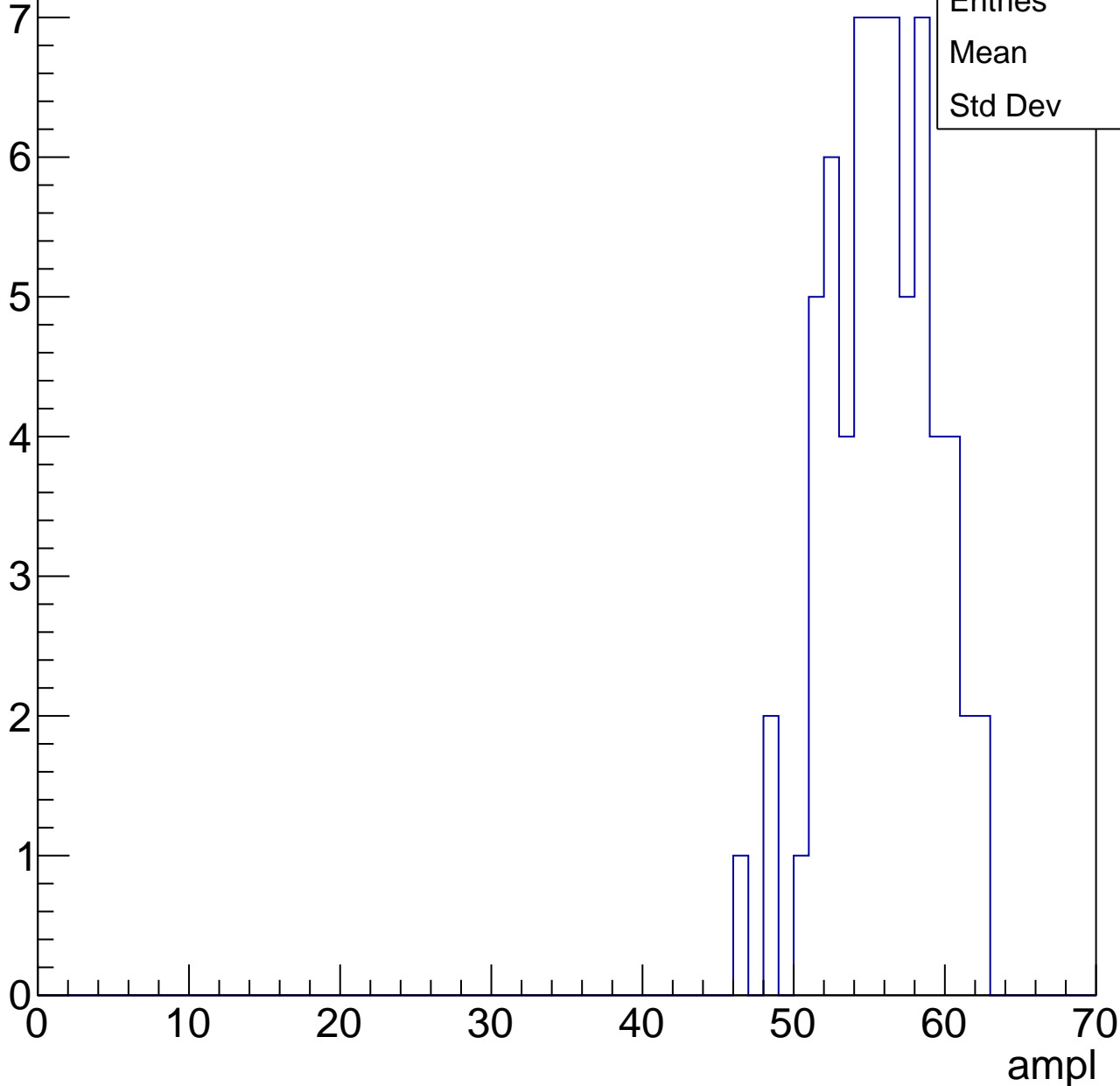


B1L101S, U21-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	55.3
Std Dev	3.49

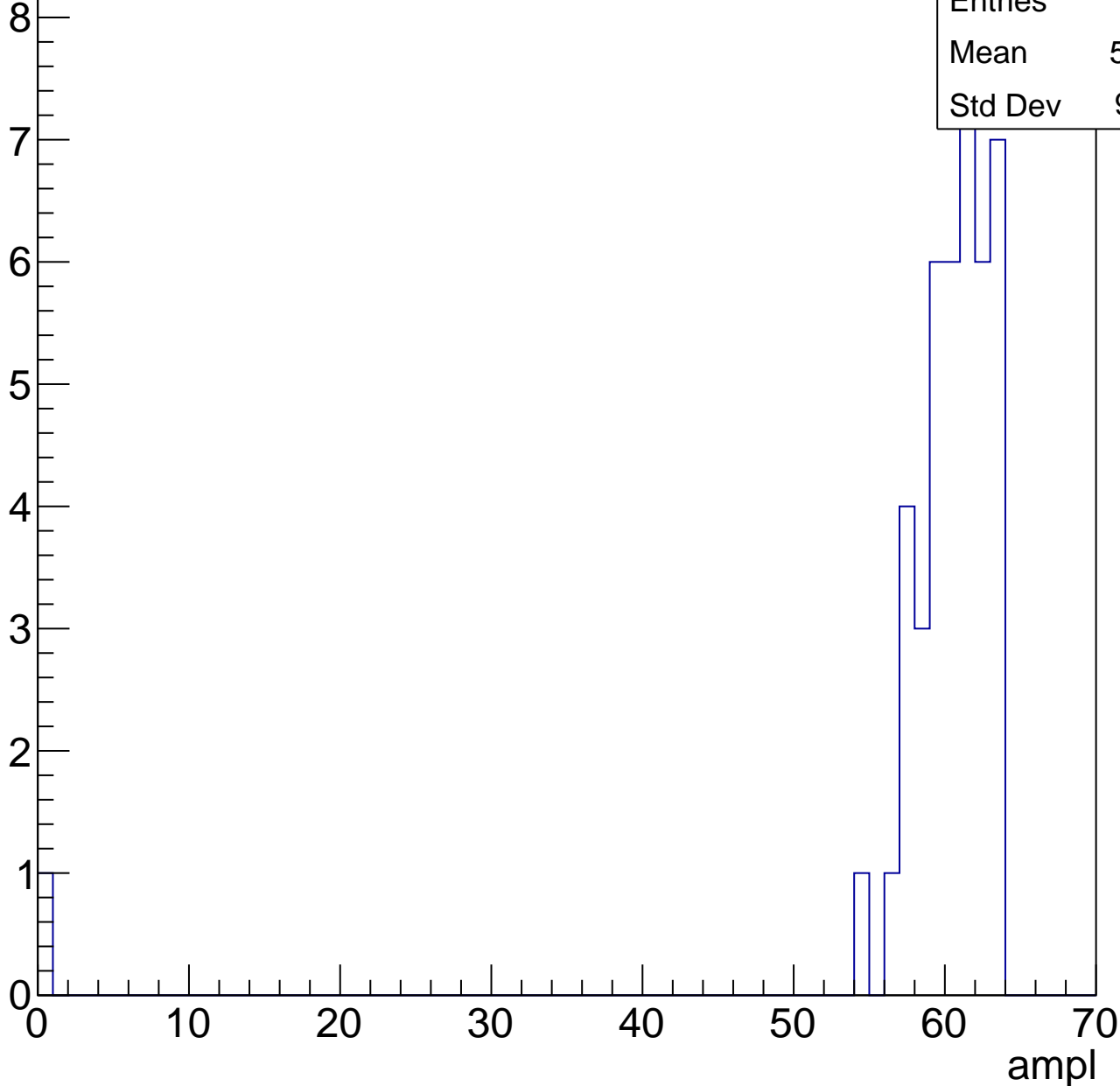


B1L101S, U21-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.77
Std Dev	9.321



B1L101S, U21-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch37, adc0

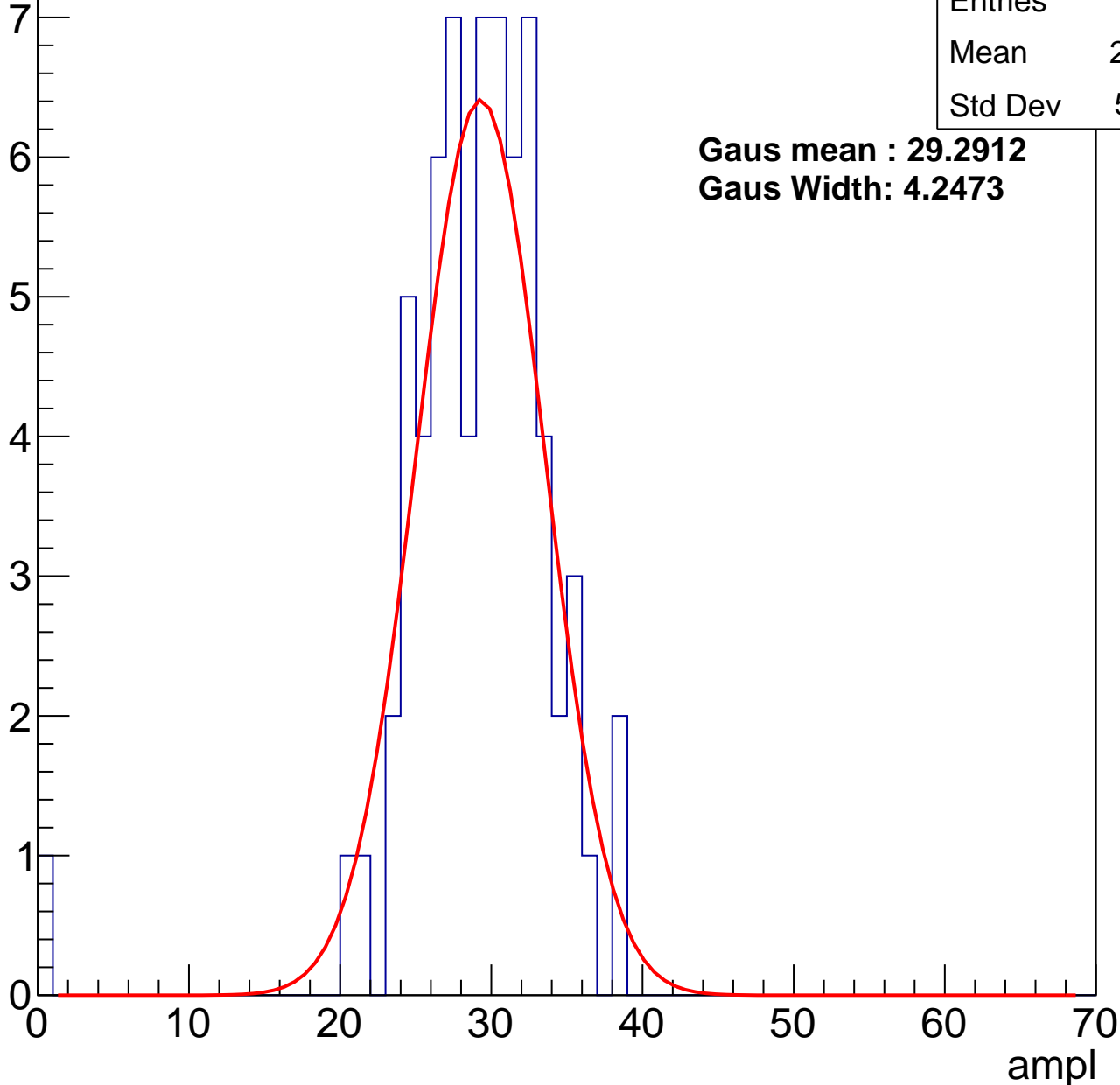
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.63
Std Dev	5.141

Gaus mean : 29.2912

Gaus Width: 4.2473



B1L101S, U21-ch37, adc1

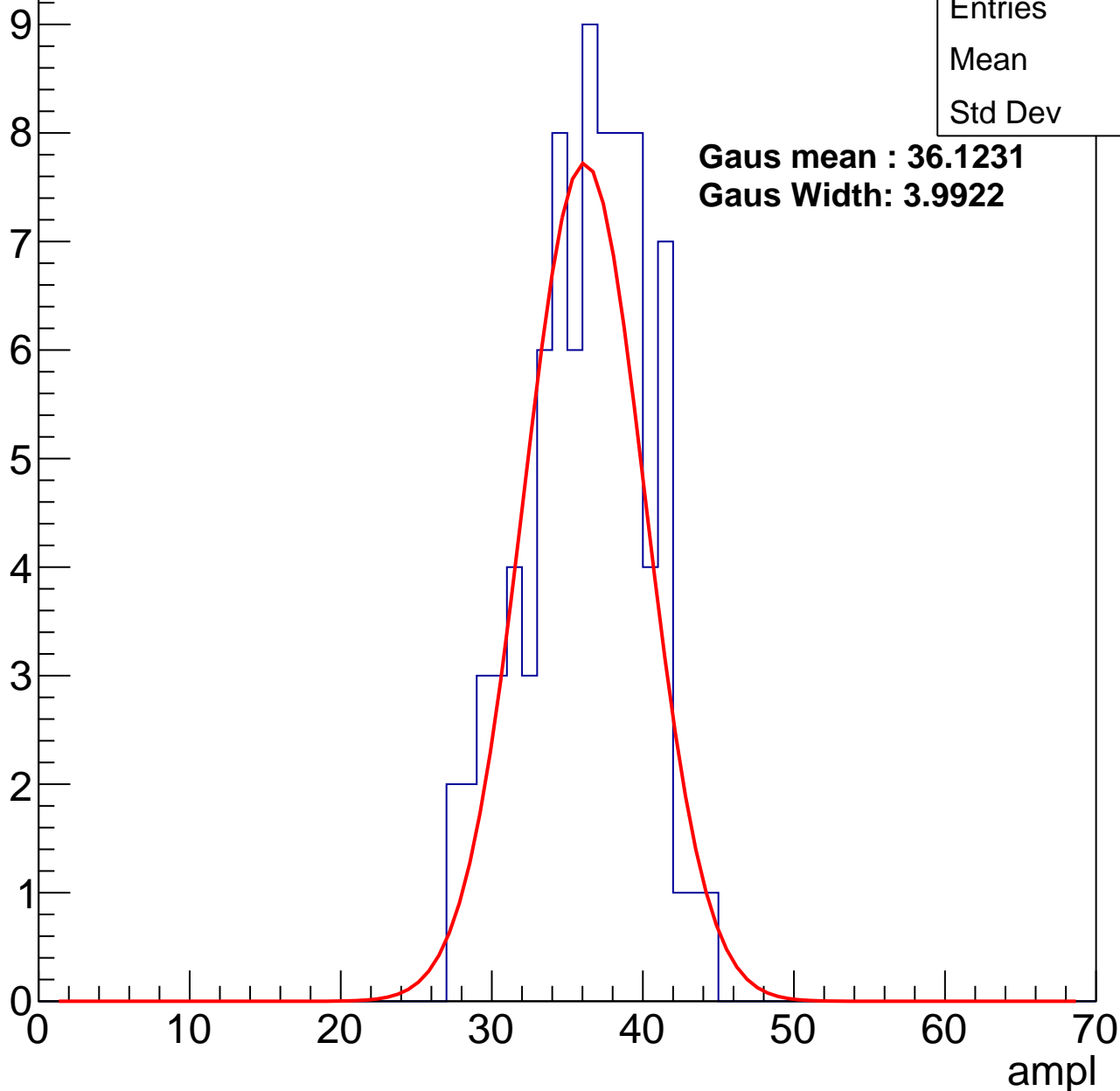
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	35.7
Std Dev	3.9

Gaus mean : 36.1231

Gaus Width: 3.9922



B1L101S, U21-ch37, adc2

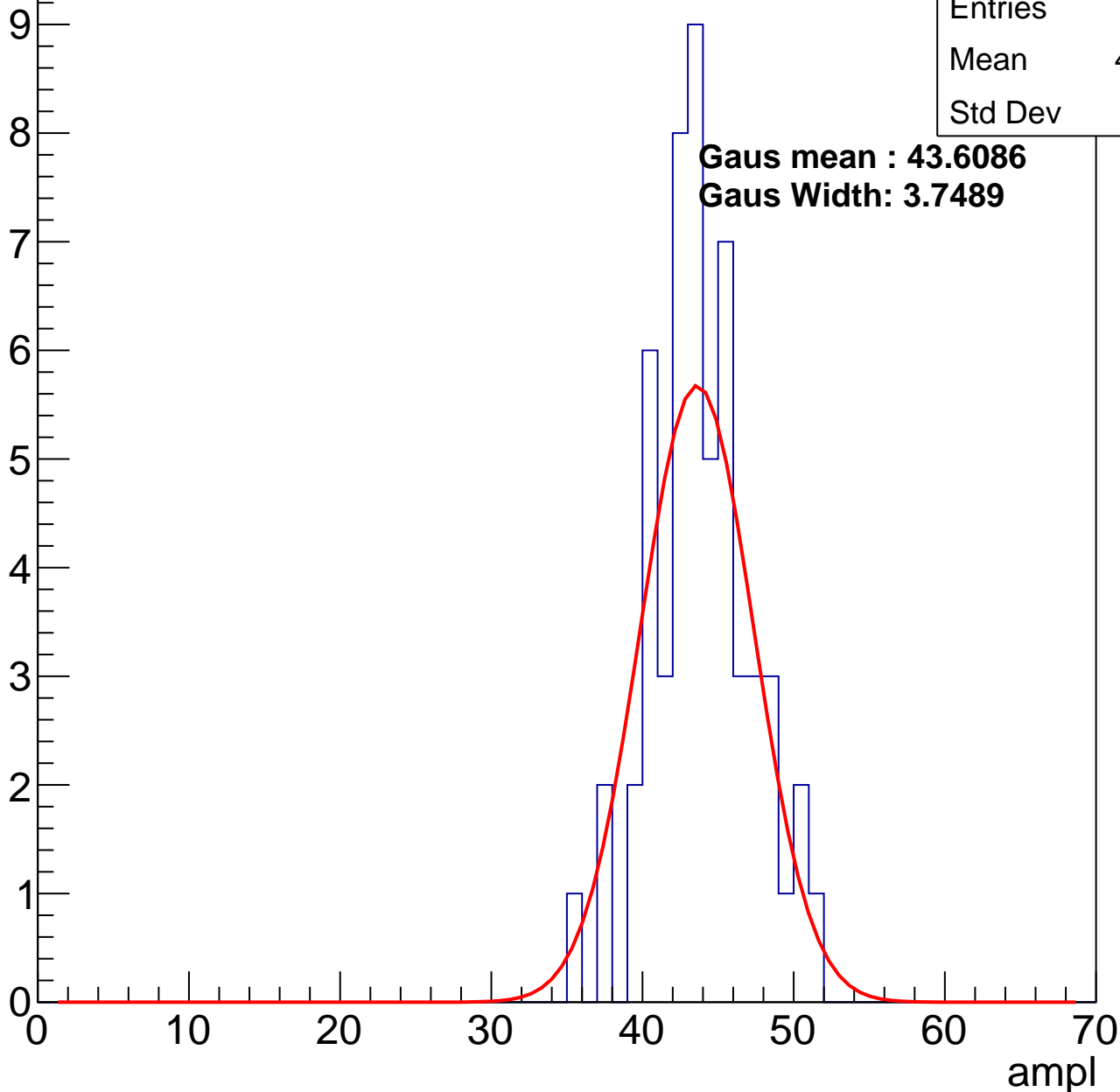
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.41
Std Dev	3.31

Gaus mean : 43.6086

Gaus Width: 3.7489

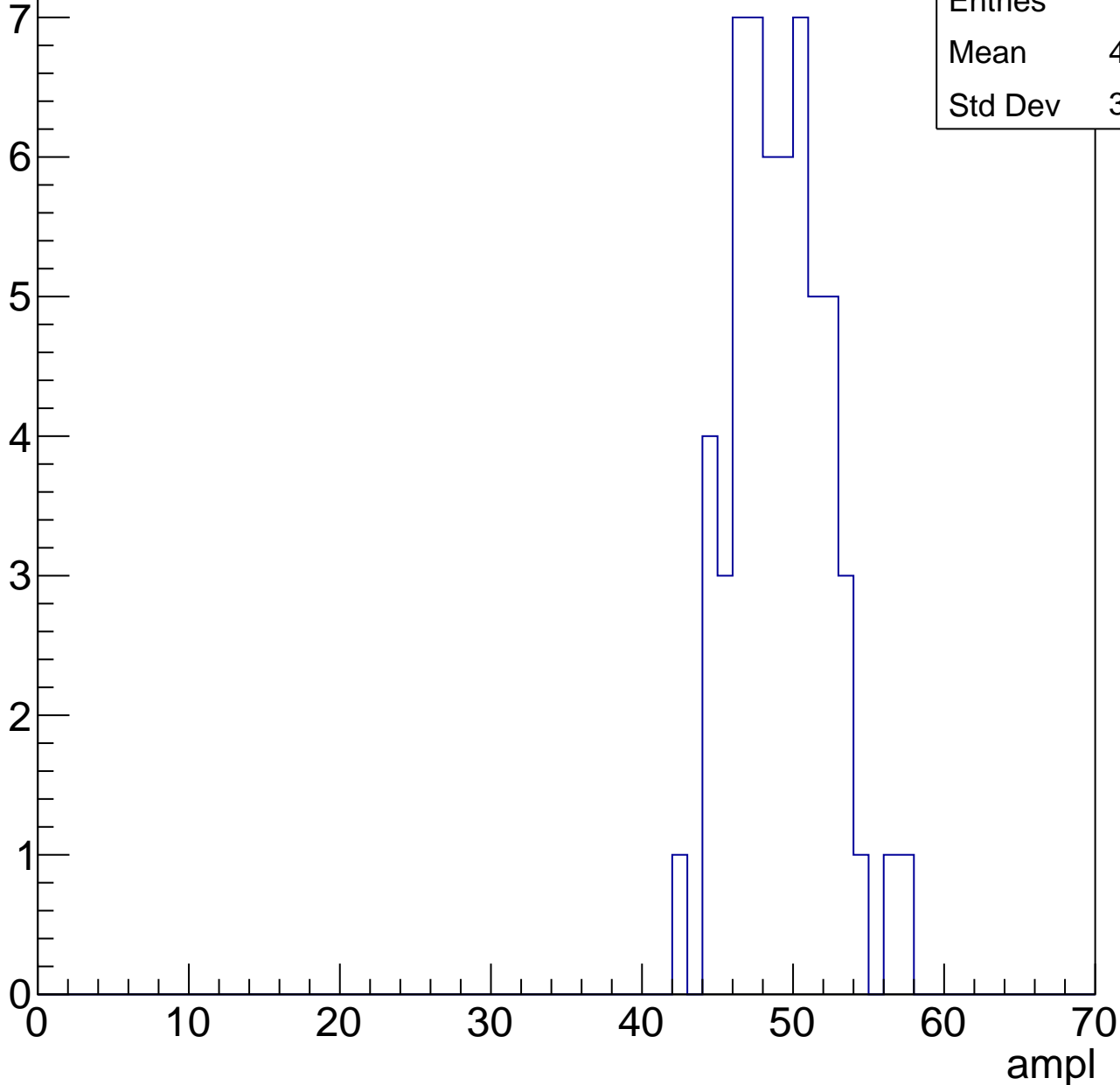


B1L101S, U21-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

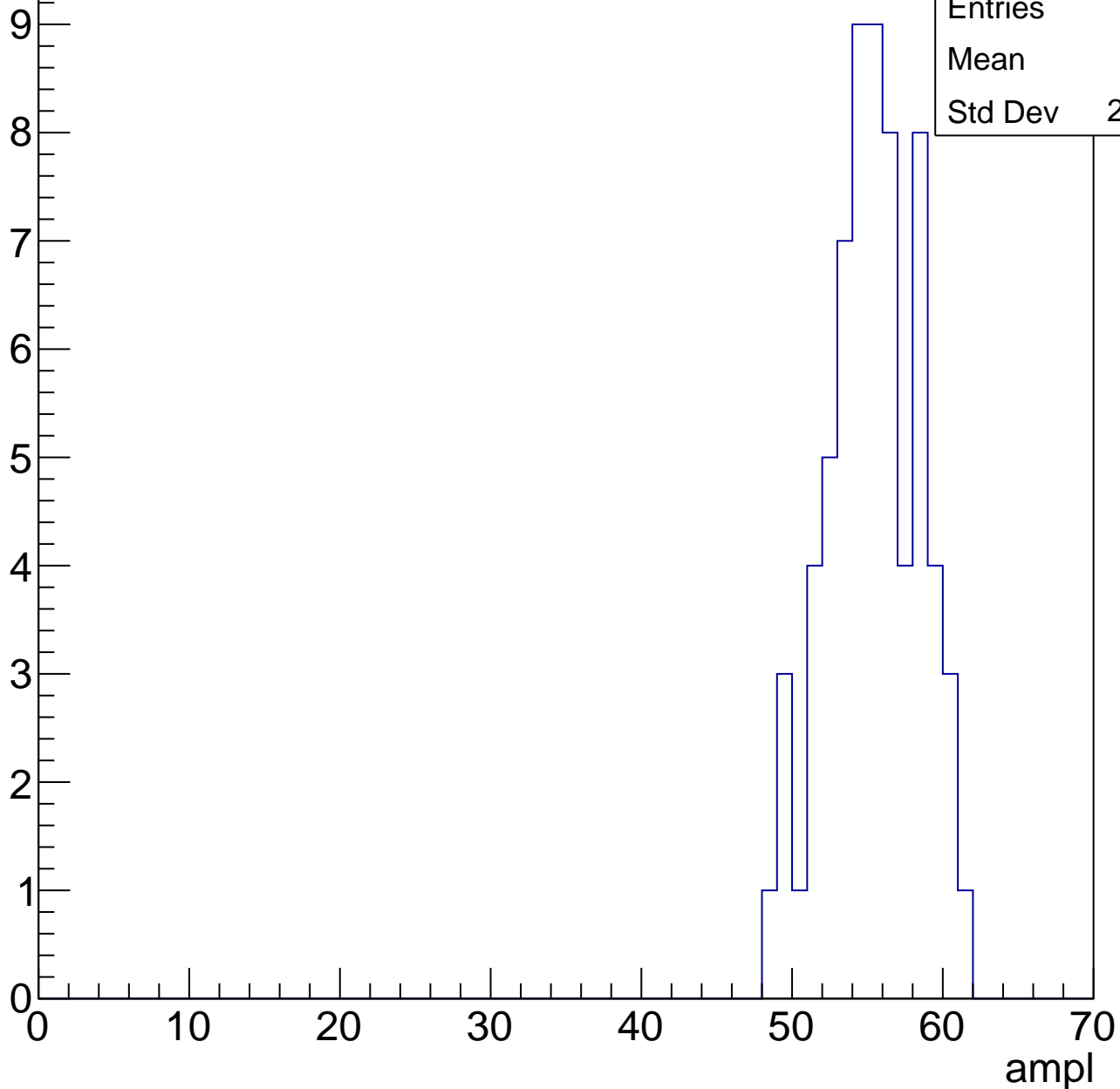
Entries	57
Mean	48.72
Std Dev	3.099



B1L101S, U21-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	47
Mean	59.02
Std Dev	8.95

0

2

4

6

8

10

B1L101S, U21-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch38, adc0

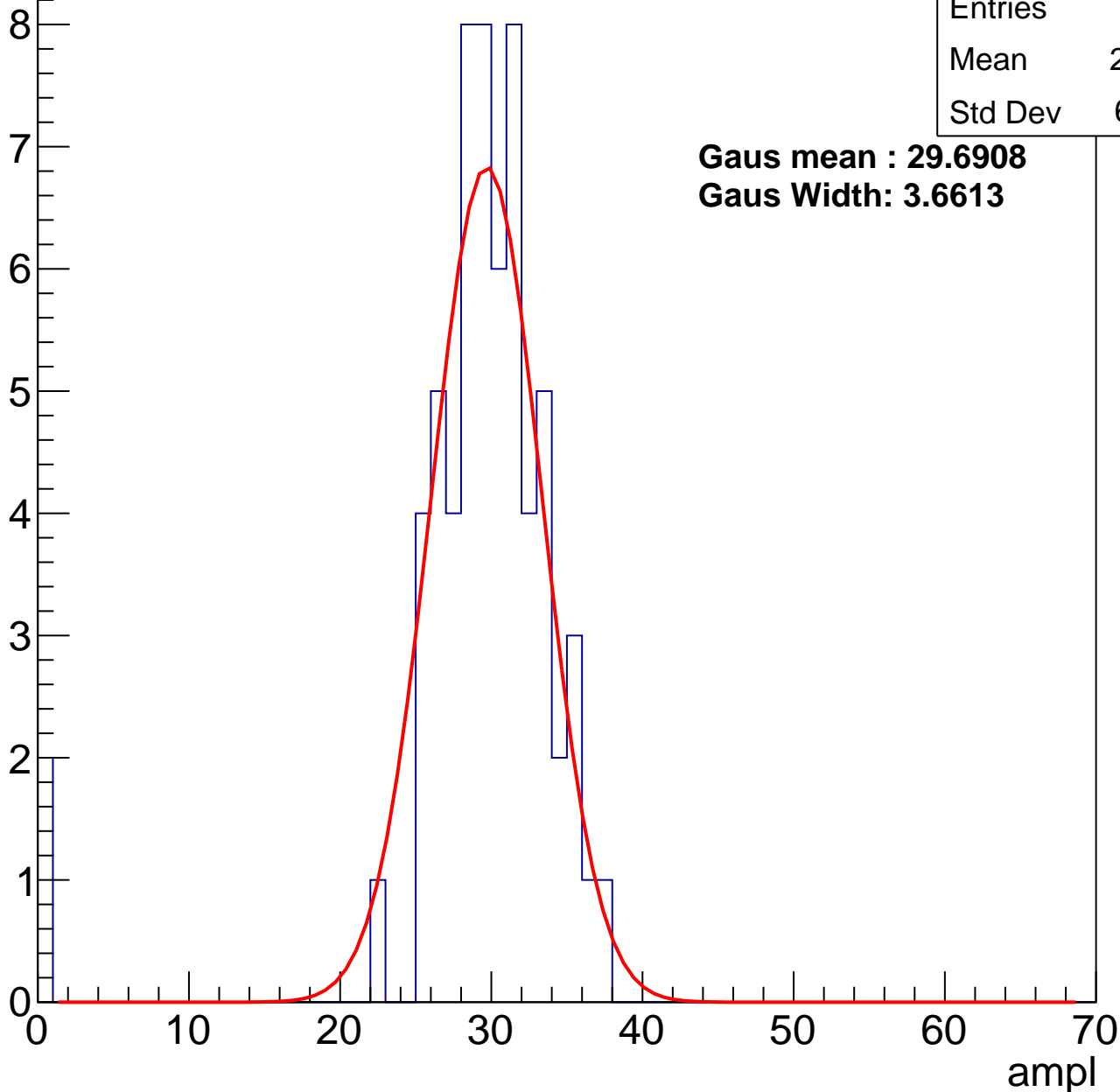
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.76
Std Dev	6.071

Gaus mean : 29.6908

Gaus Width: 3.6613



B1L101S, U21-ch38, adc1

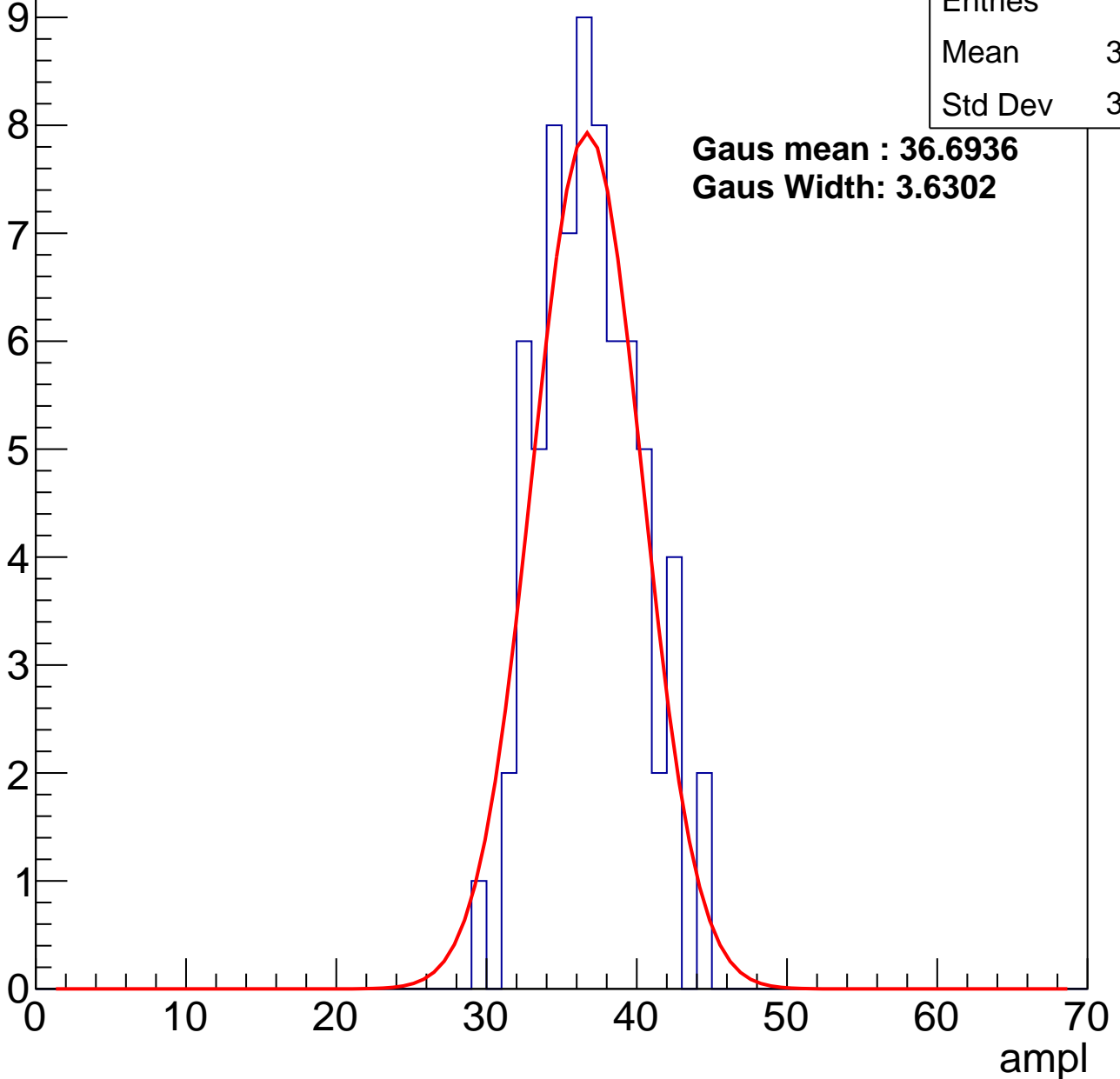
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.41
Std Dev	3.261

Gaus mean : 36.6936

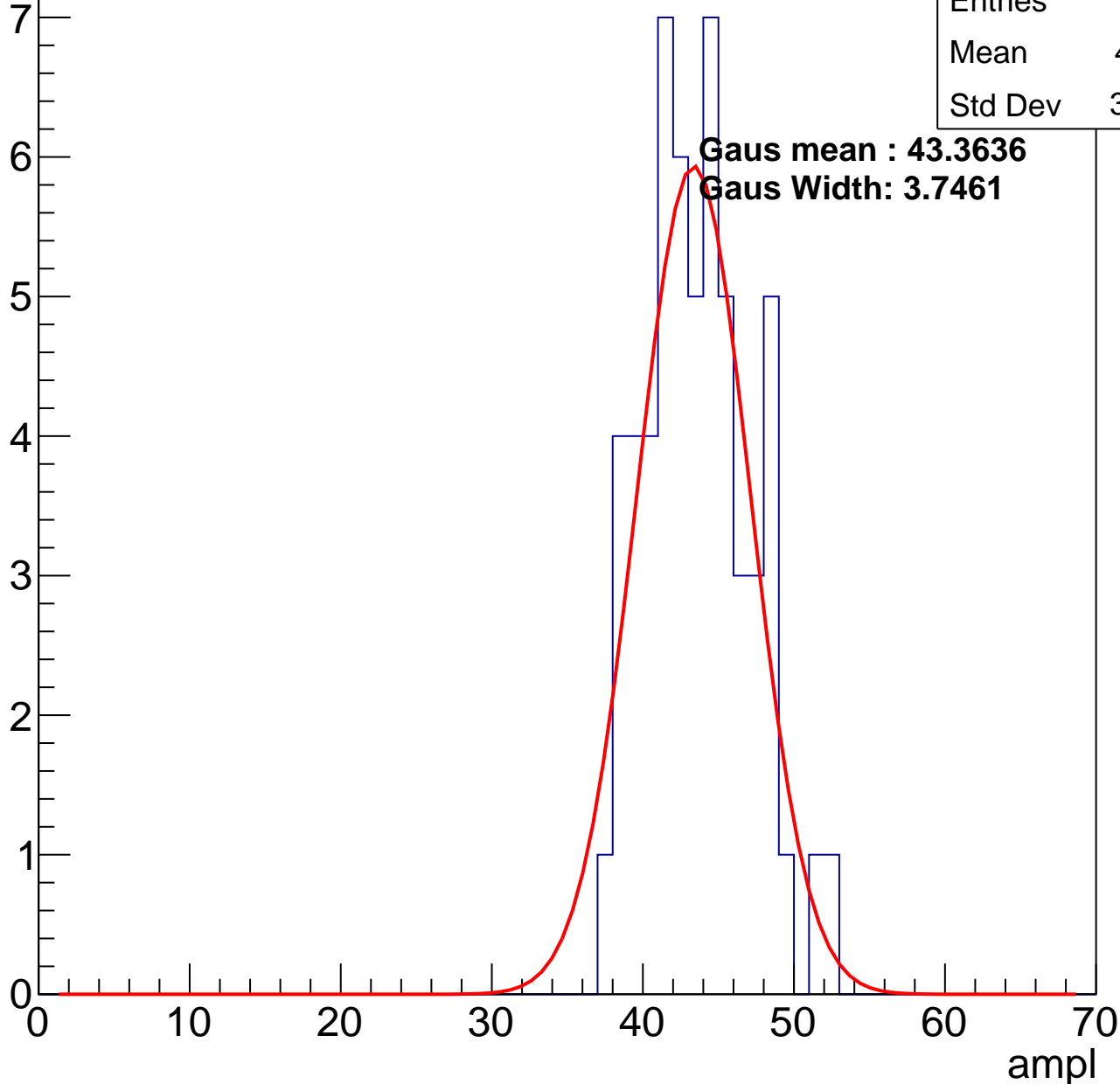
Gaus Width: 3.6302



B1L101S, U21-ch38, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

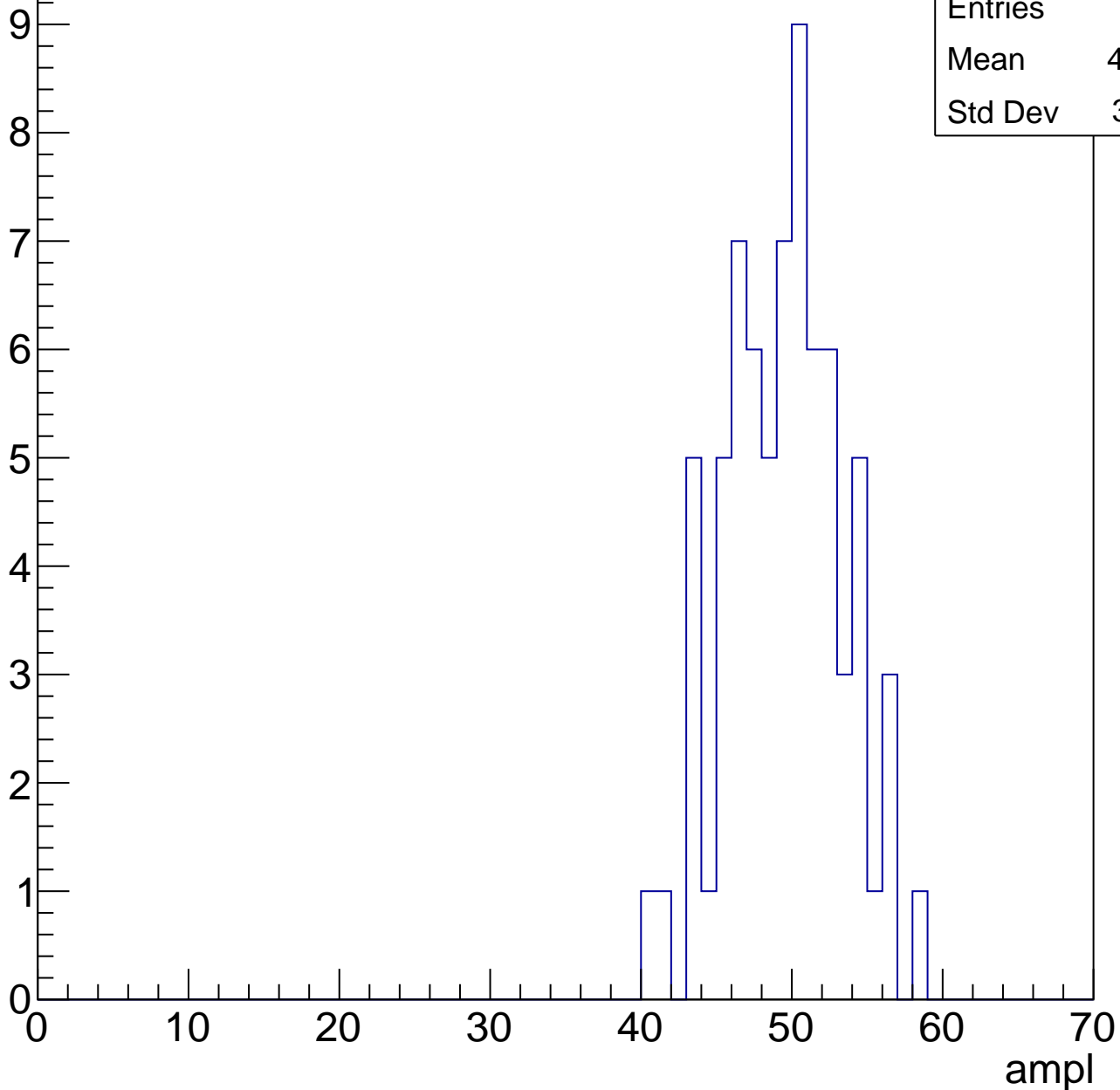


B1L101S, U21-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

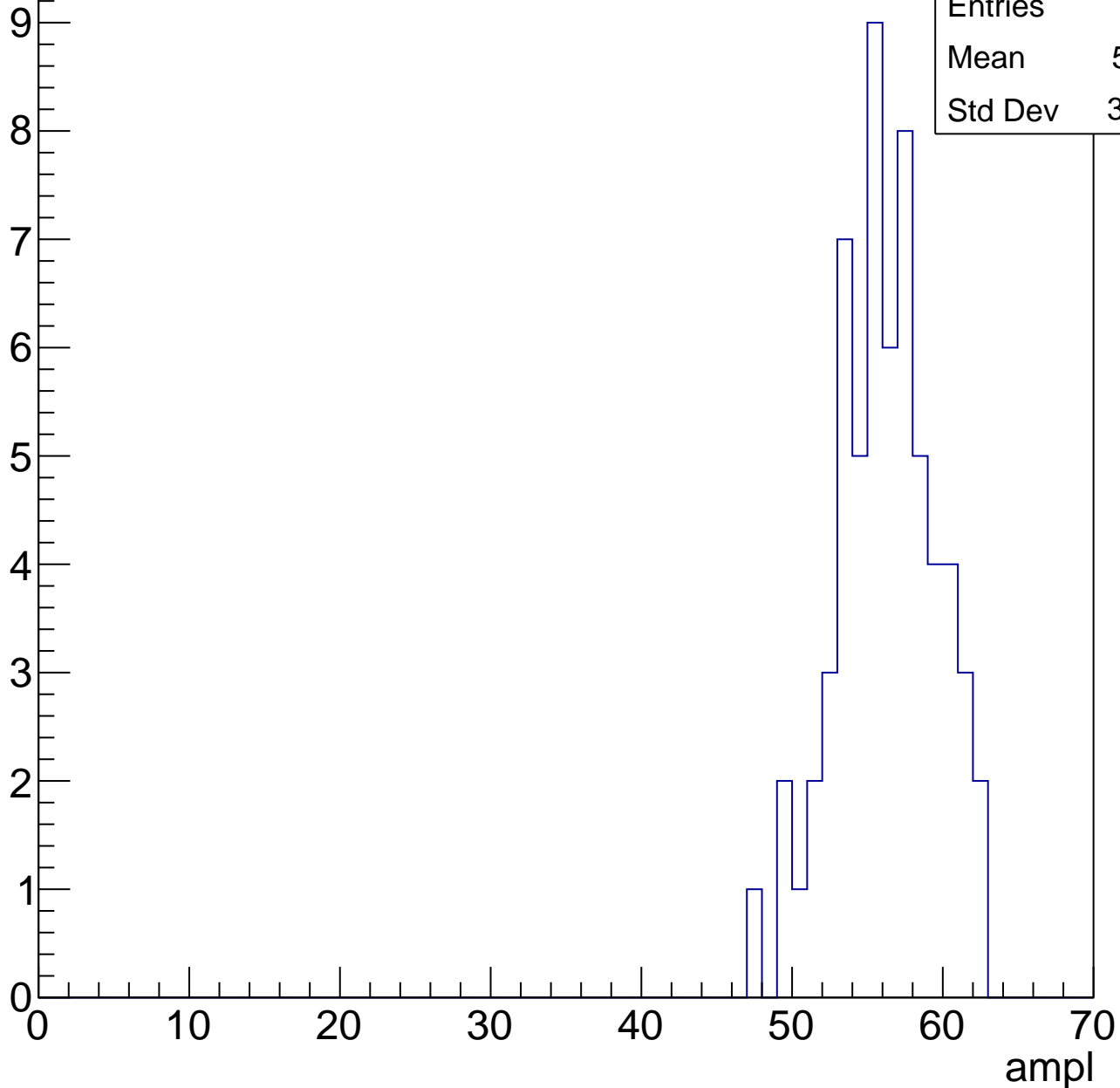
Entries	72
Mean	49.03
Std Dev	3.811



B1L101S, U21-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



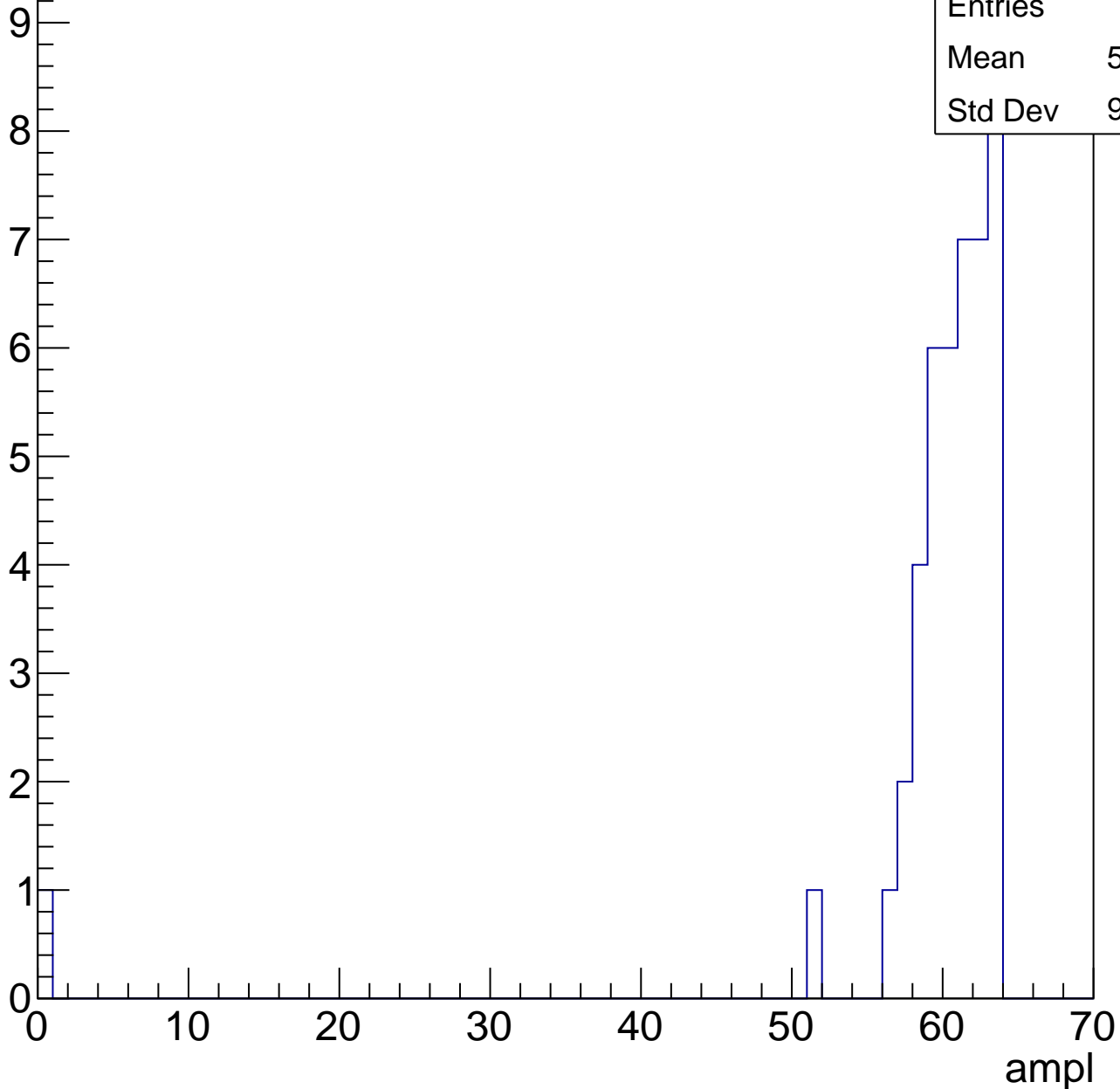
Entries	62
Mean	55.71
Std Dev	3.299

B1L101S, U21-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.98
Std Dev	9.302



B1L101S, U21-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

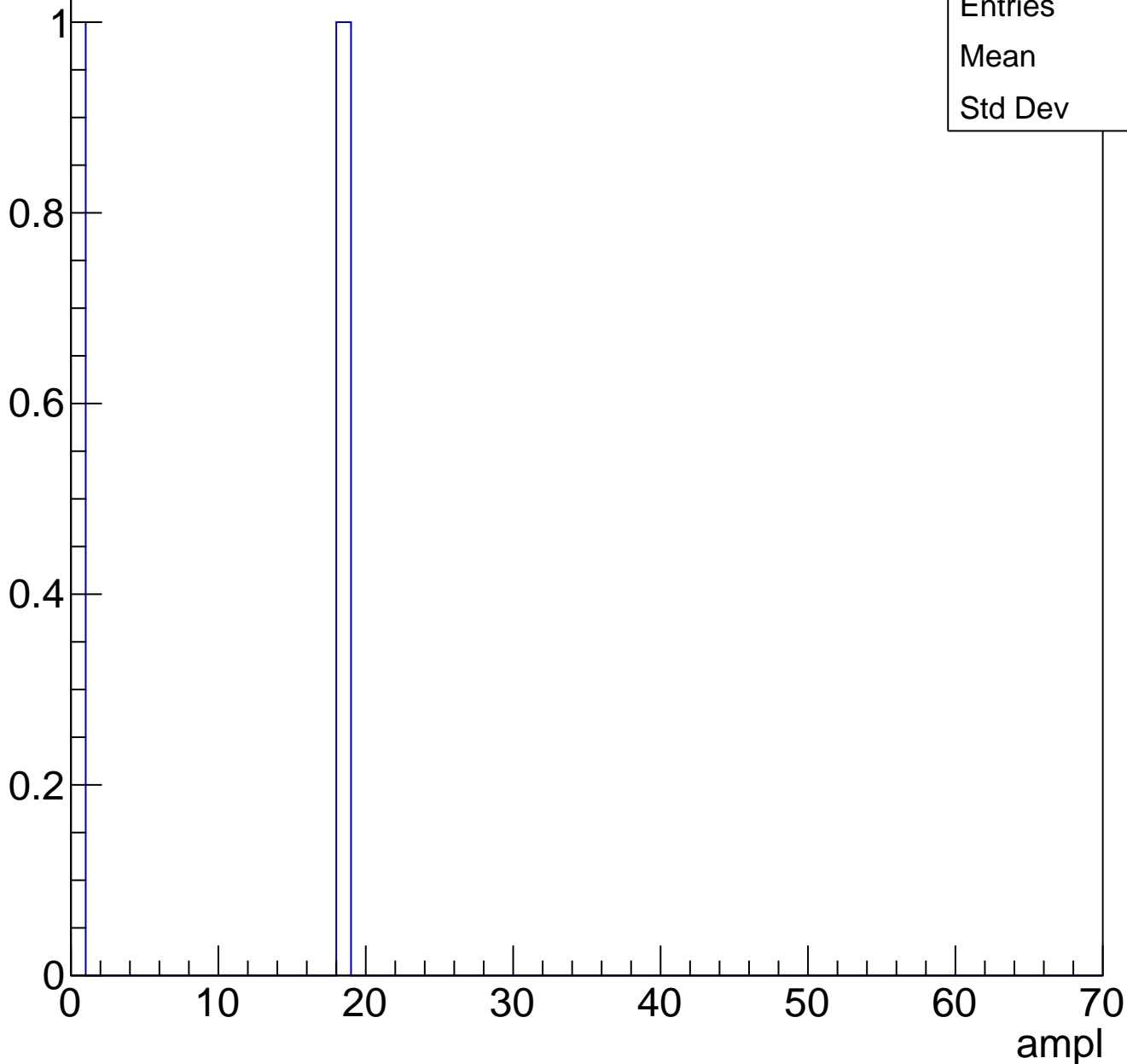
Entry



B1L101S, U21-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch39, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	32.42
Std Dev	2.935

Gaus mean : 32.7023

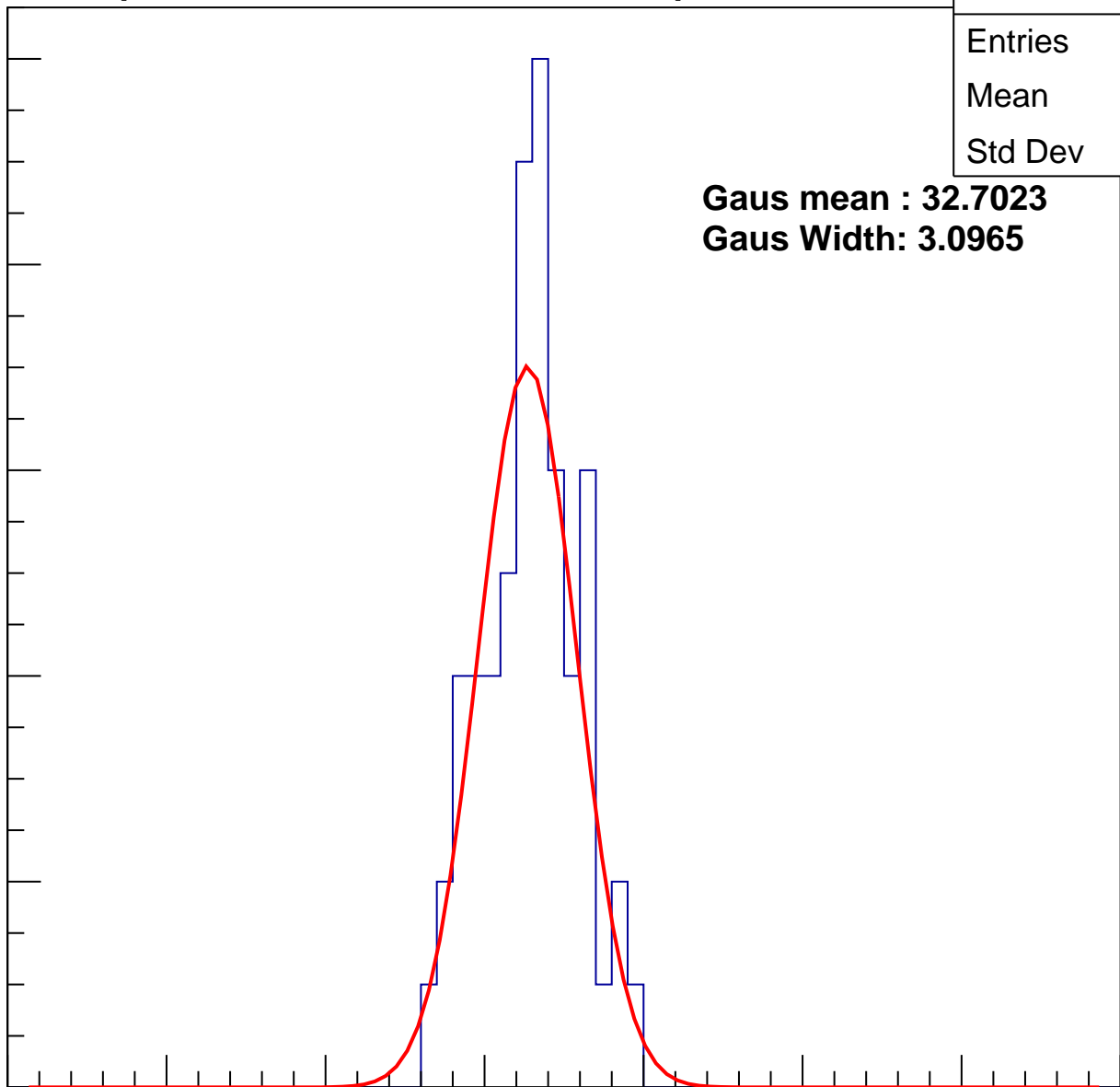
Gaus Width: 3.0965

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



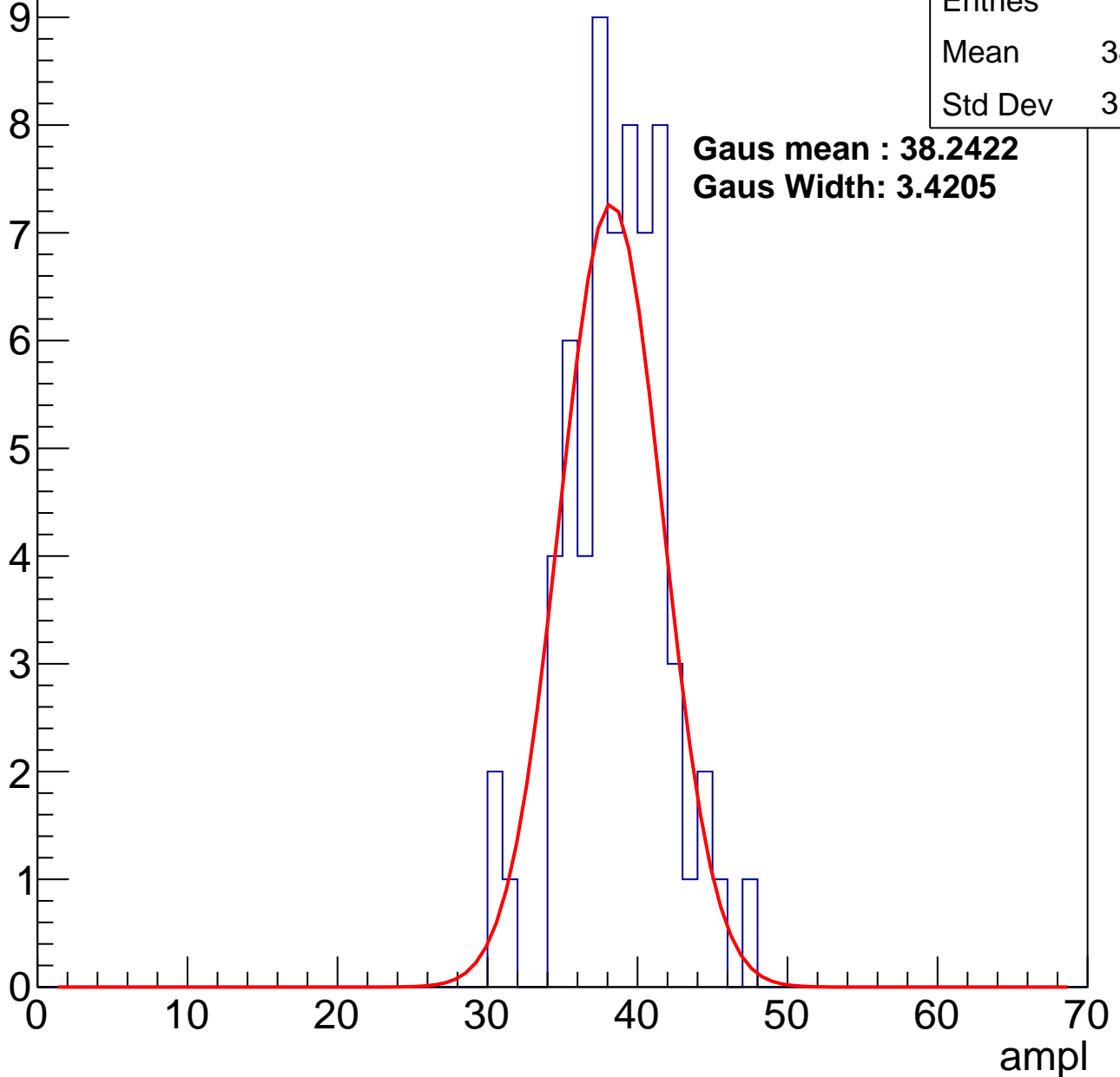
B1L101S, U21-ch39, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	38.27
Std Dev	3.304

Gaus mean : 38.2422
Gaus Width: 3.4205



B1L101S, U21-ch39, adc2

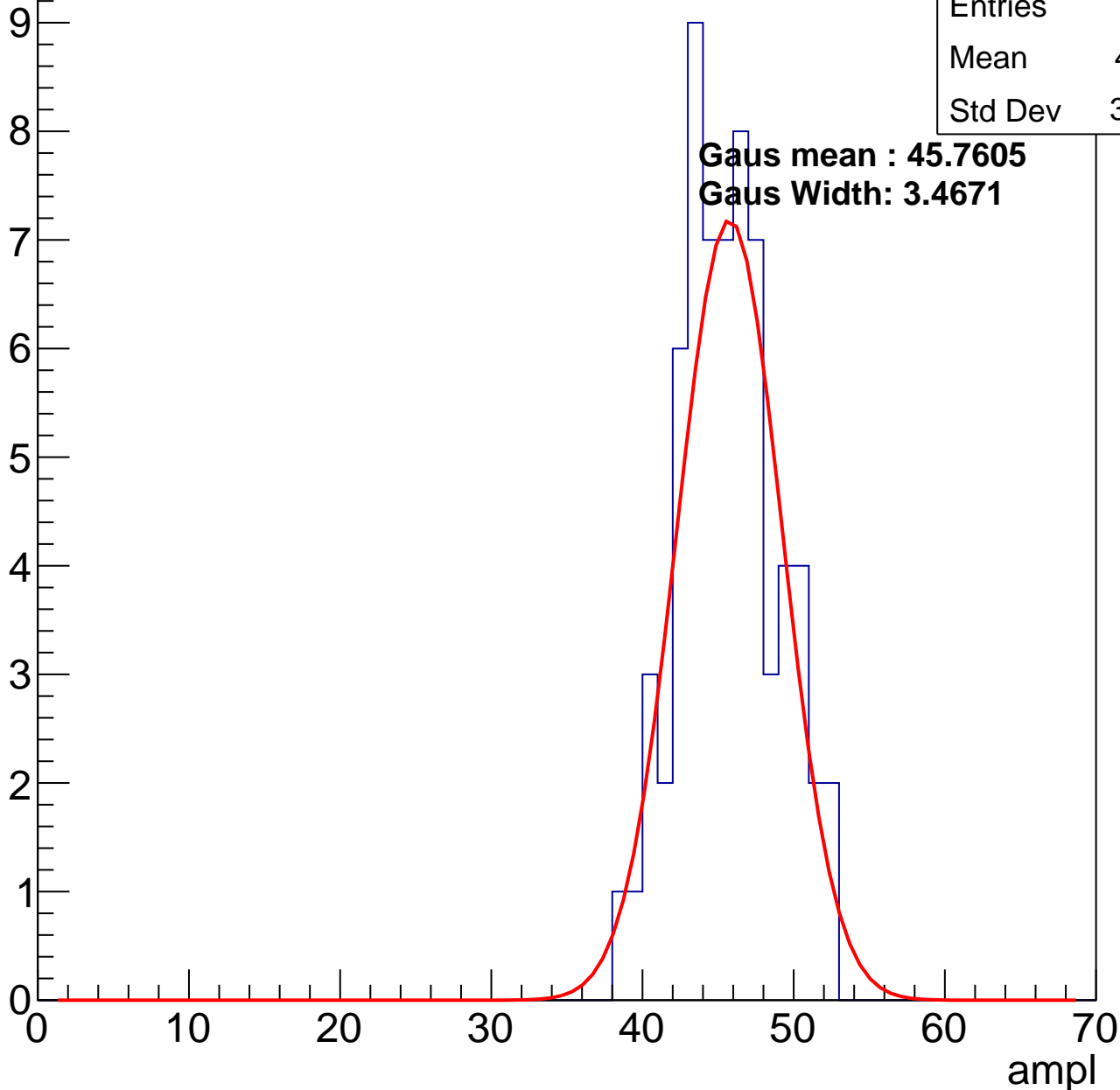
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	45.21
Std Dev	3.217

Gaus mean : 45.7605

Gaus Width: 3.4671

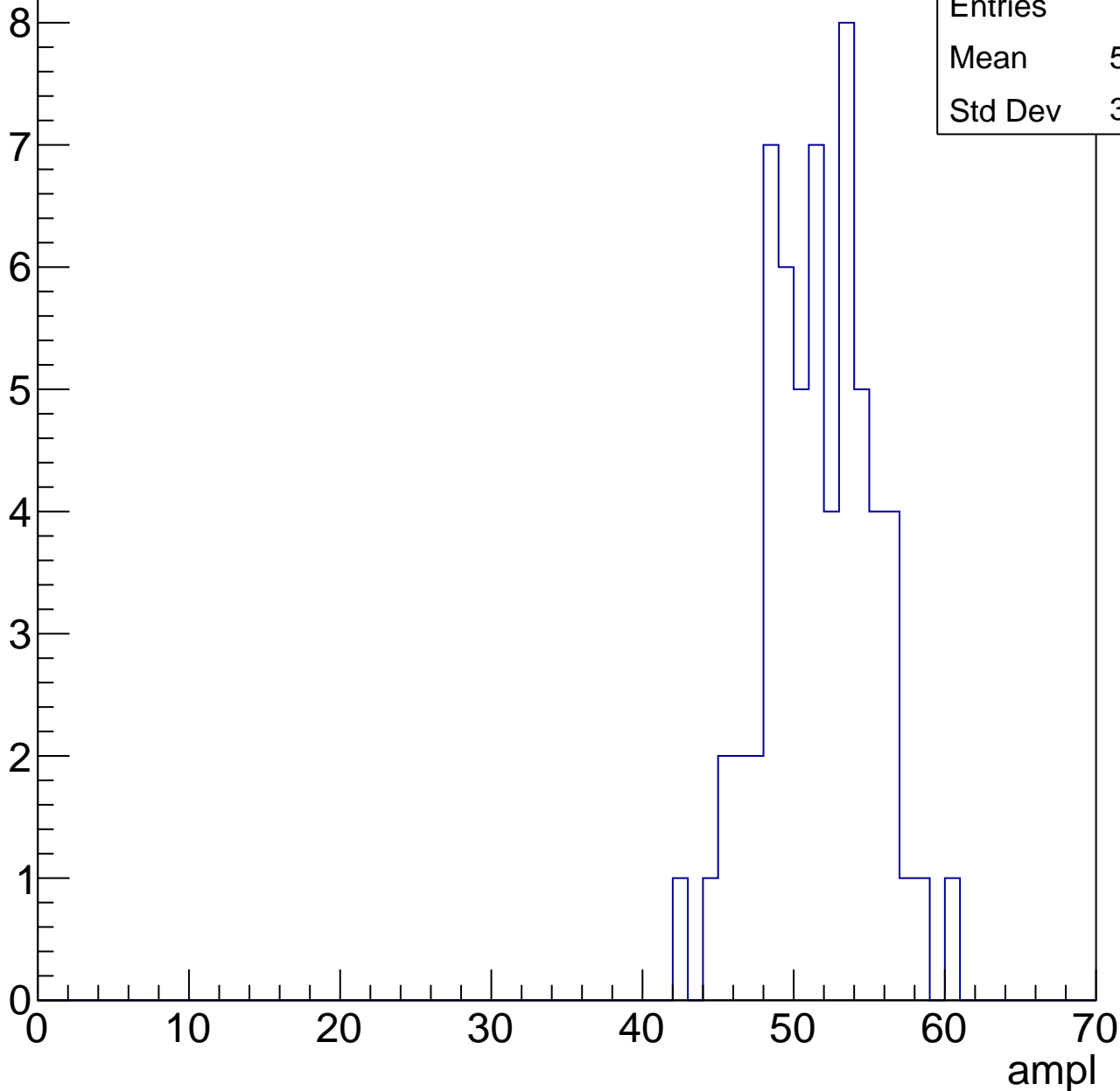


B1L101S, U21-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	51.15
Std Dev	3.589

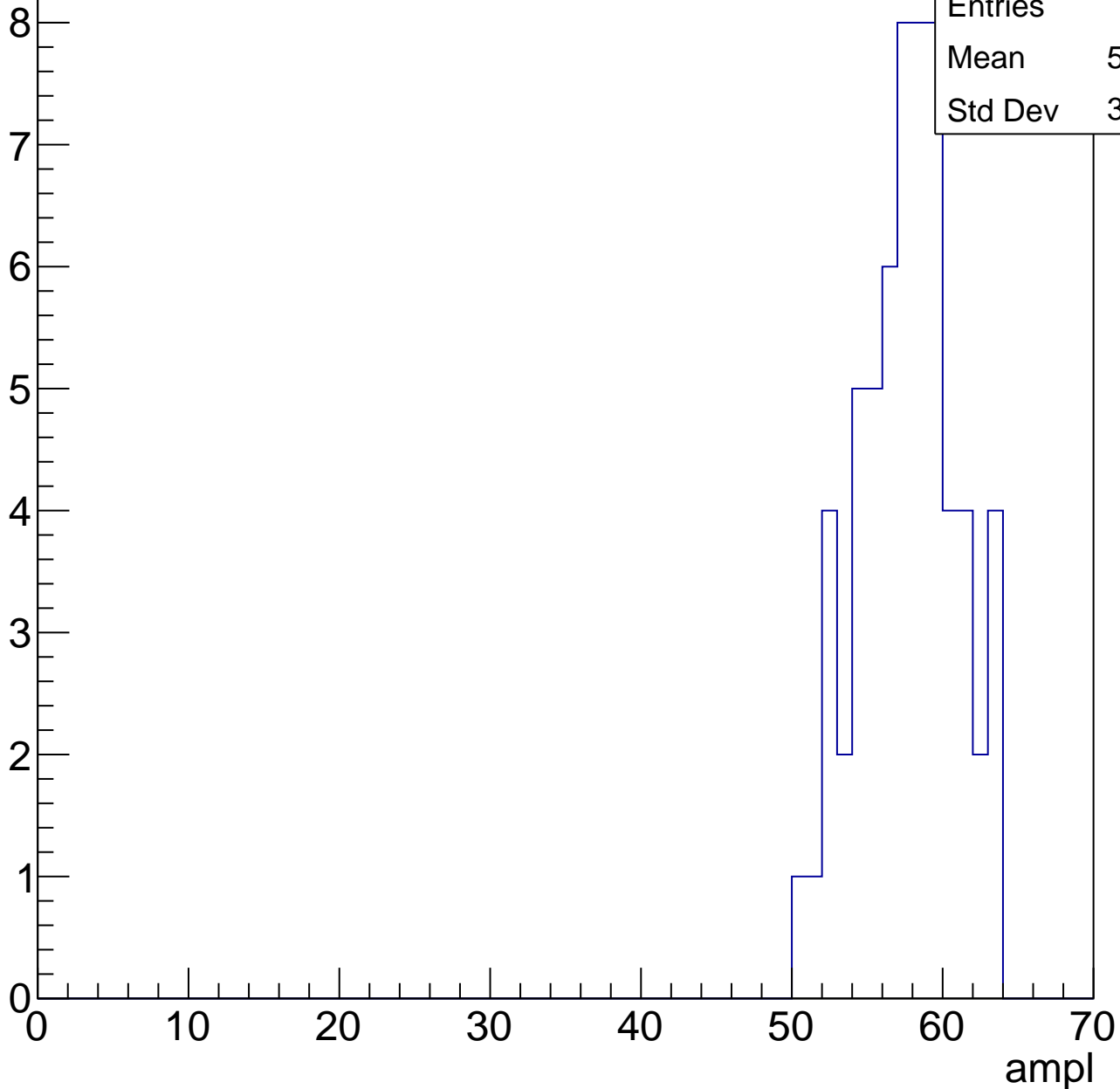


B1L101S, U21-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	57.23
Std Dev	3.159

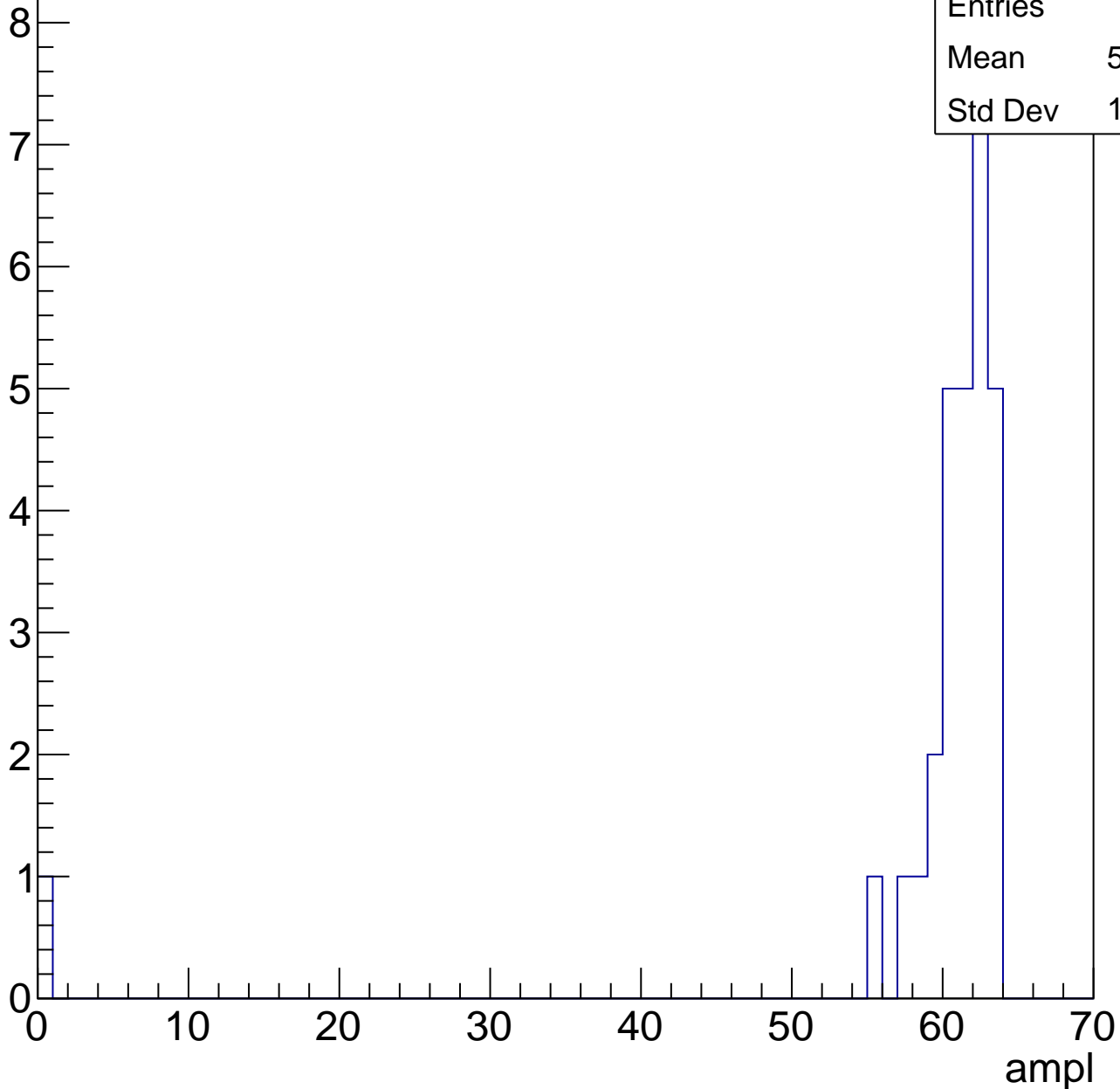


B1L101S, U21-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	29
Mean	58.76
Std Dev	11.26



B1L101S, U21-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	26
Std Dev	0

ampl

B1L101S, U21-ch40, adc0

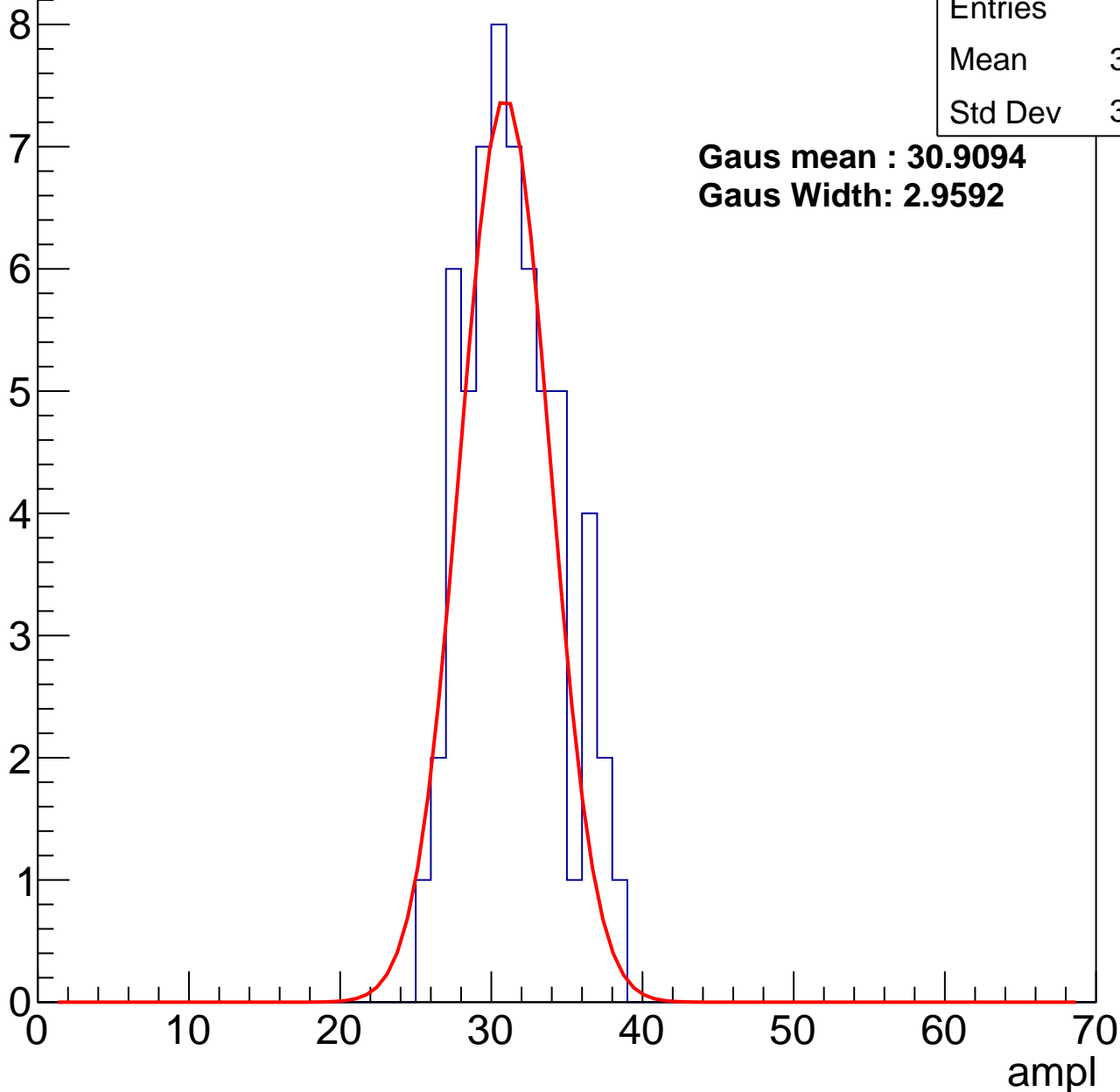
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.95
Std Dev	3.085

Gaus mean : 30.9094

Gaus Width: 2.9592



B1L101S, U21-ch40, adc1

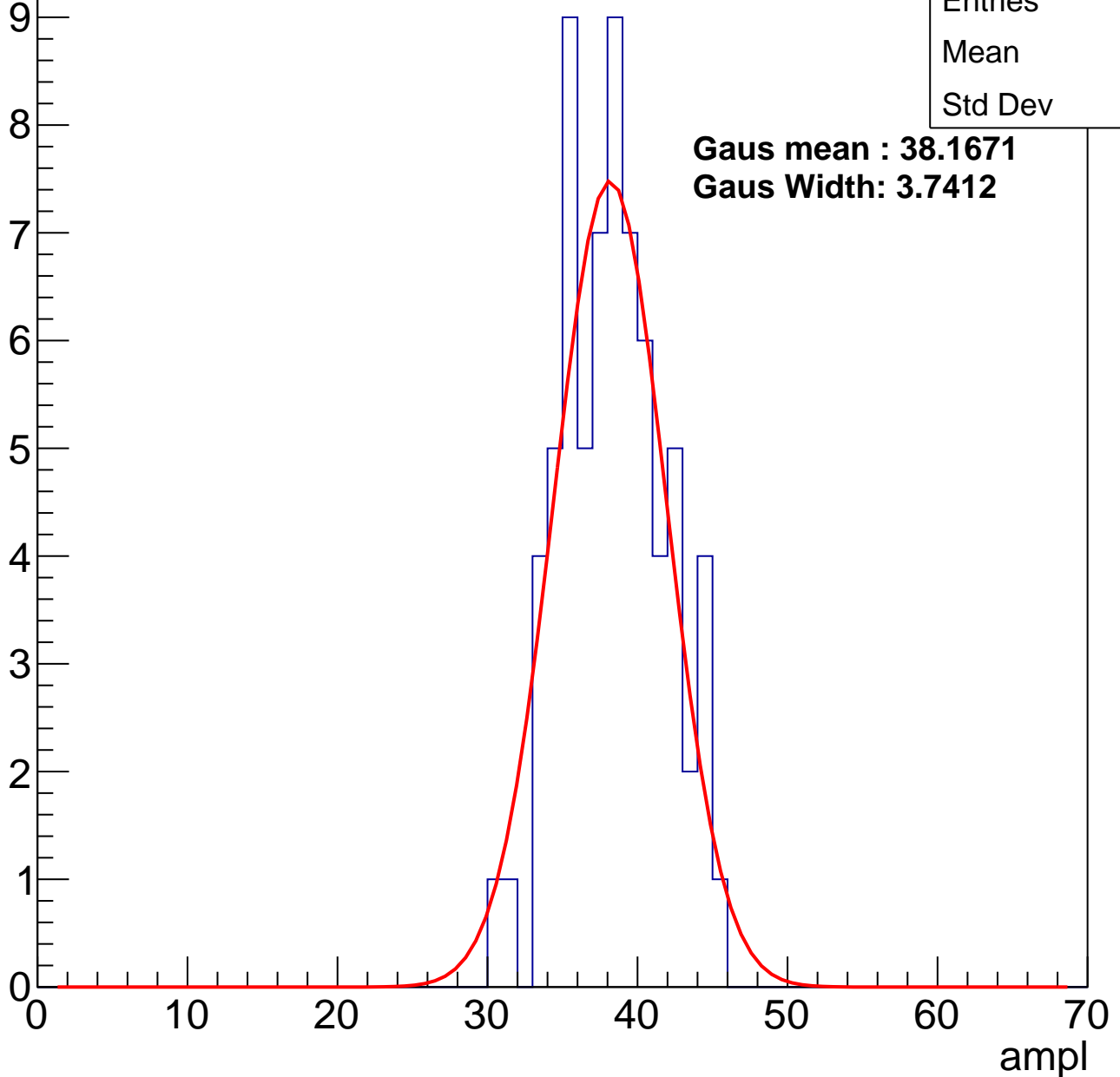
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.9
Std Dev	3.36

Gaus mean : 38.1671

Gaus Width: 3.7412



B1L101S, U21-ch40, adc2

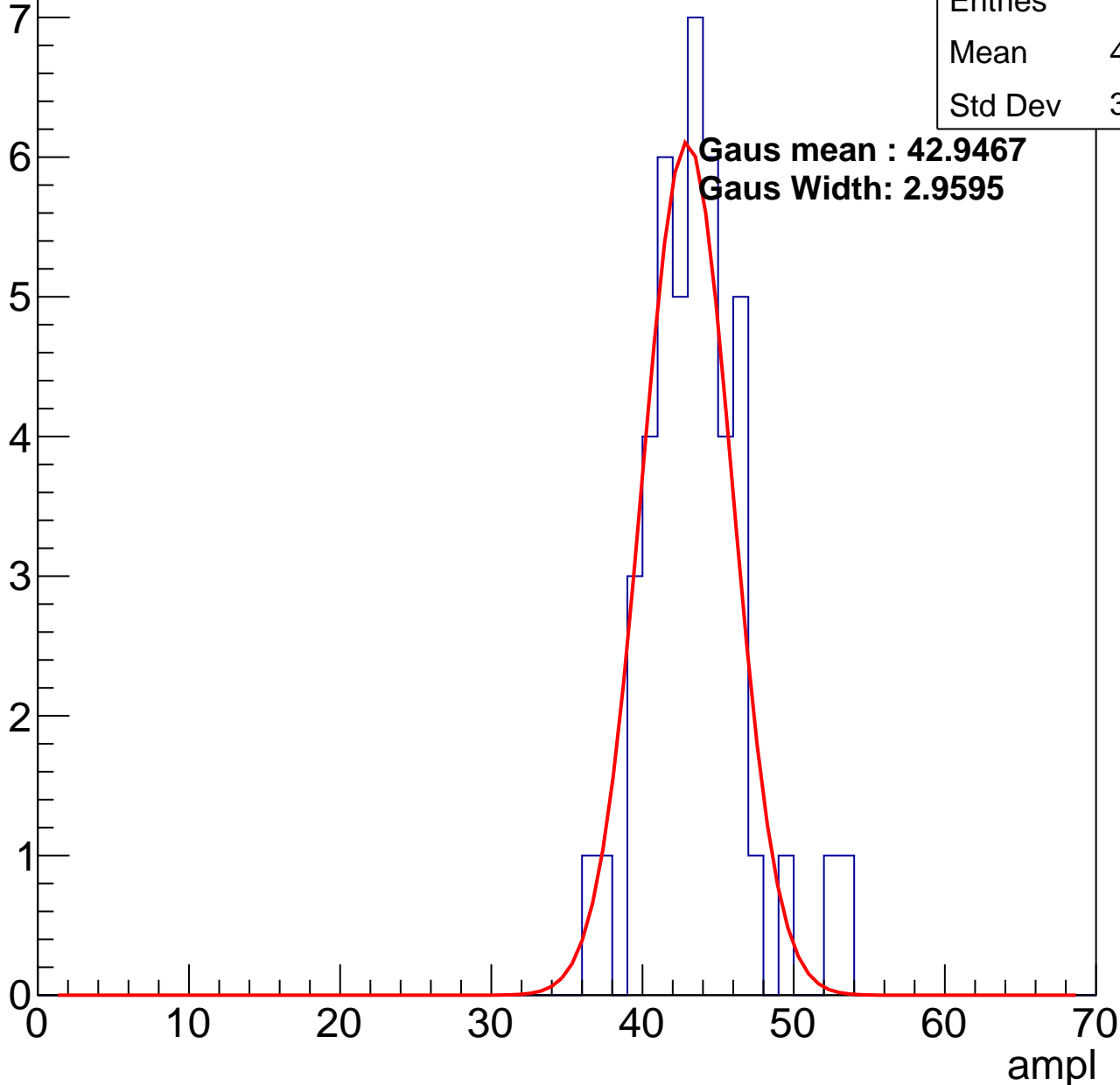
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	43.09
Std Dev	3.289

Gaus mean : 42.9467

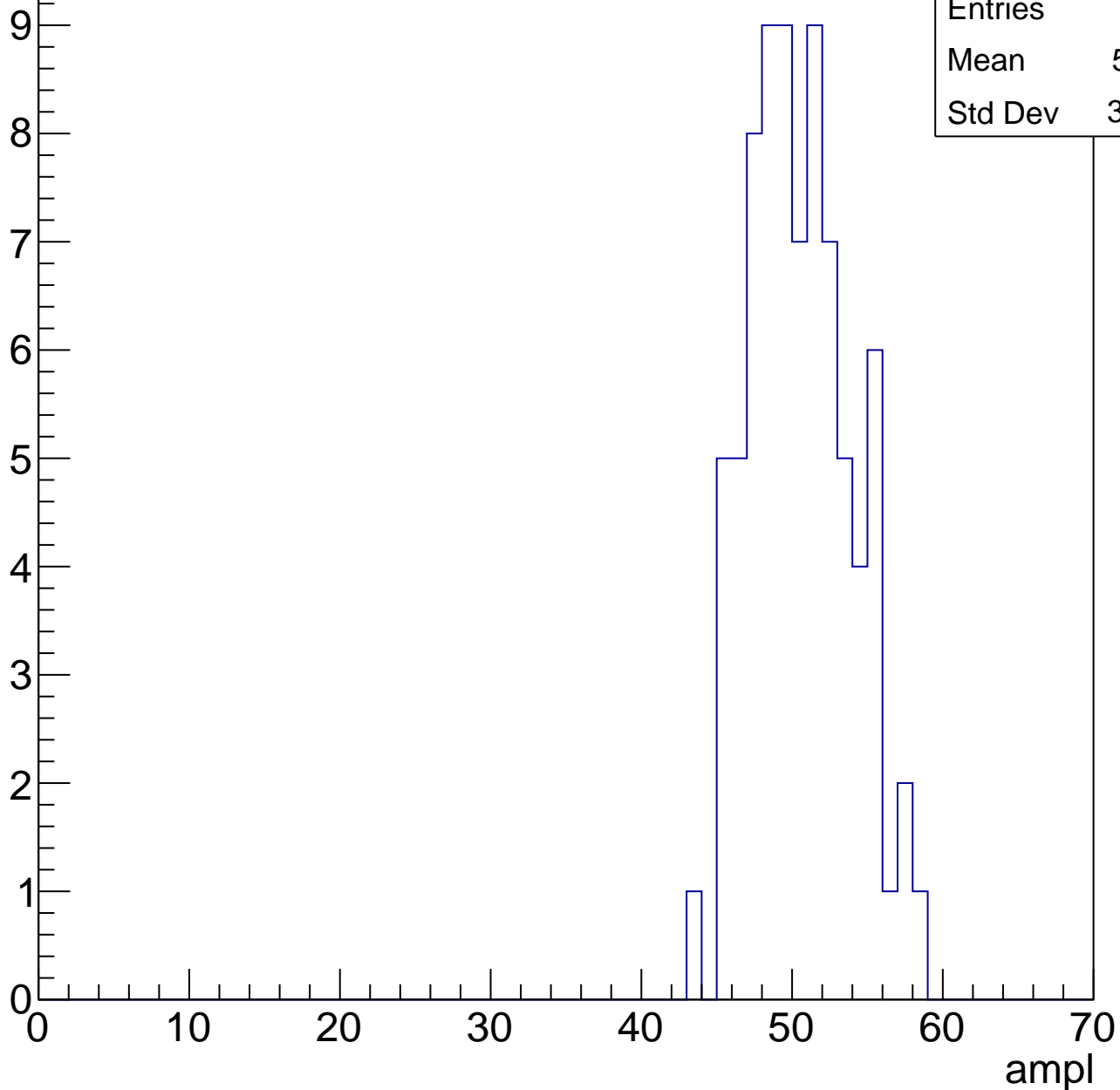
Gaus Width: 2.9595



B1L101S, U21-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

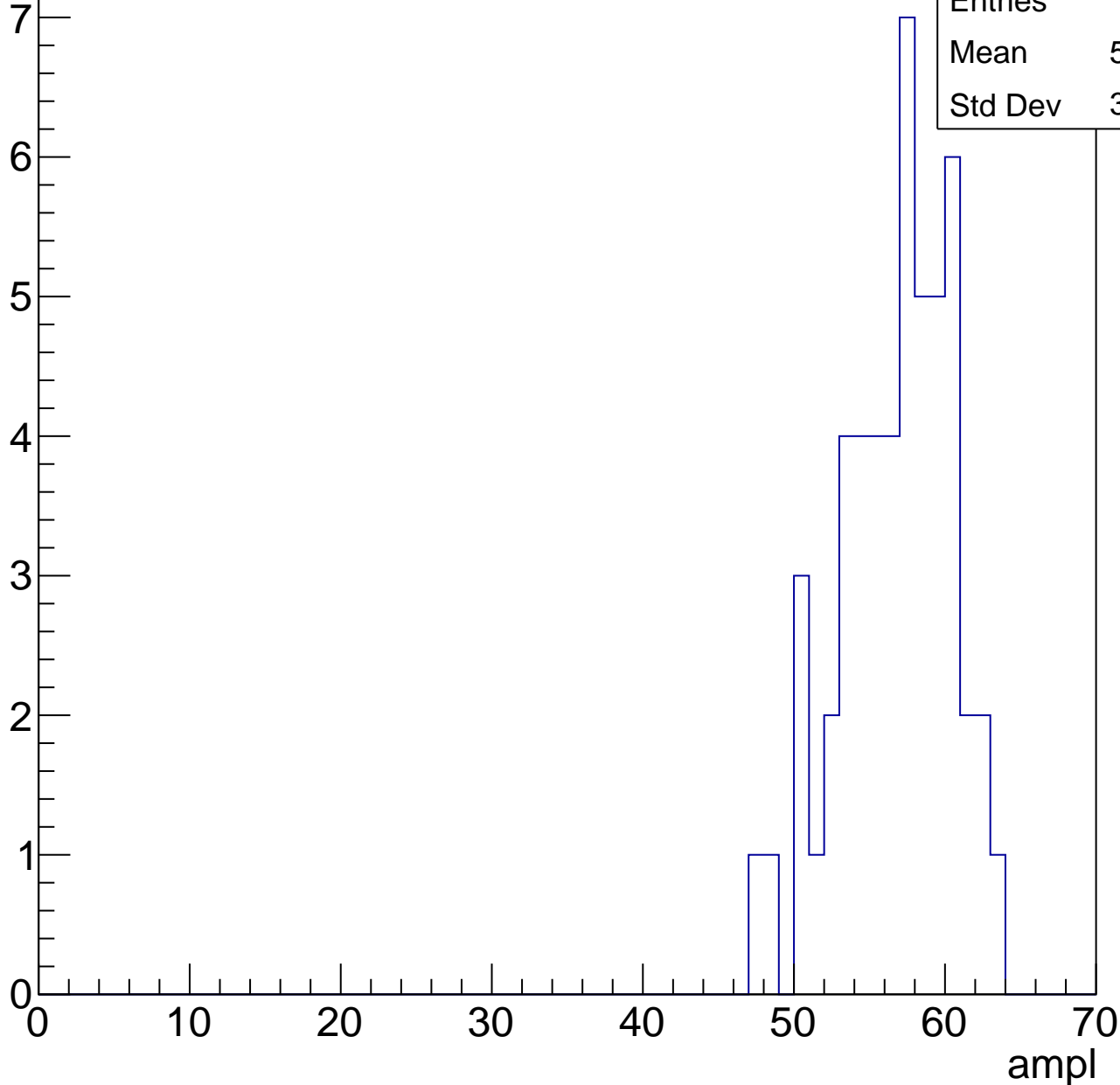
Entry



B1L101S, U21-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

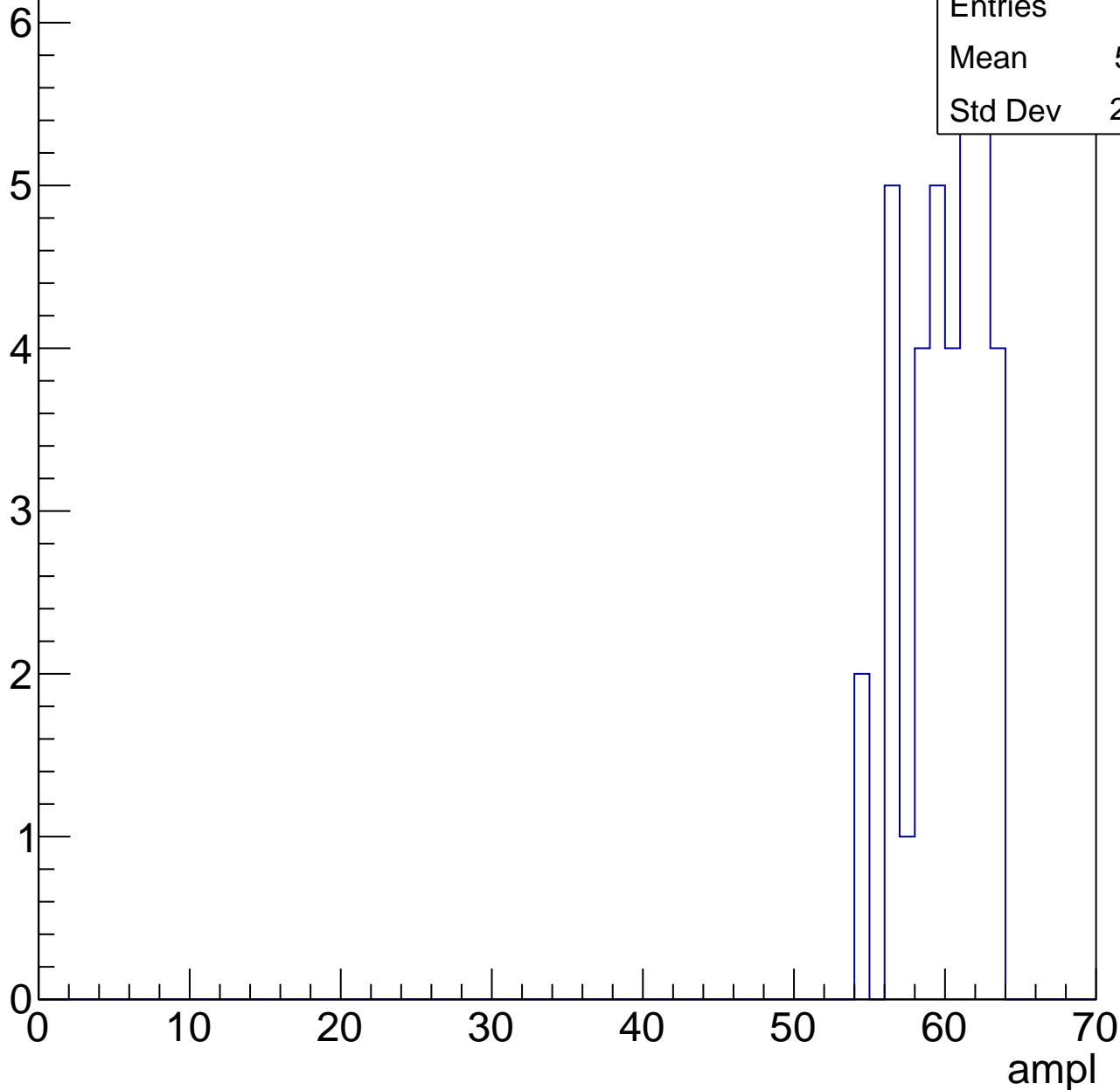


B1L101S, U21-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

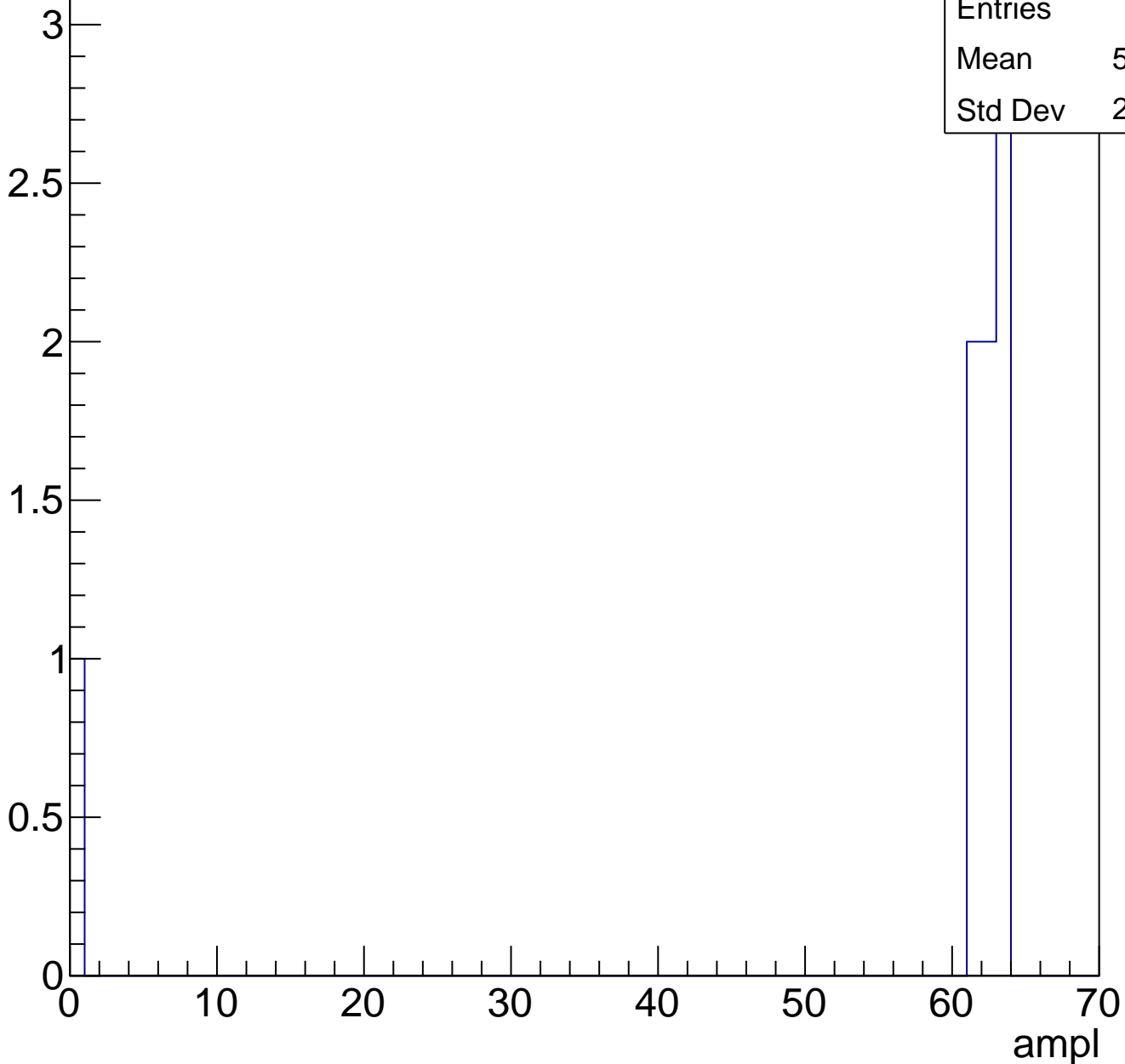
Entries	37
Mean	59.51
Std Dev	2.543



B1L101S, U21-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch41, adc0

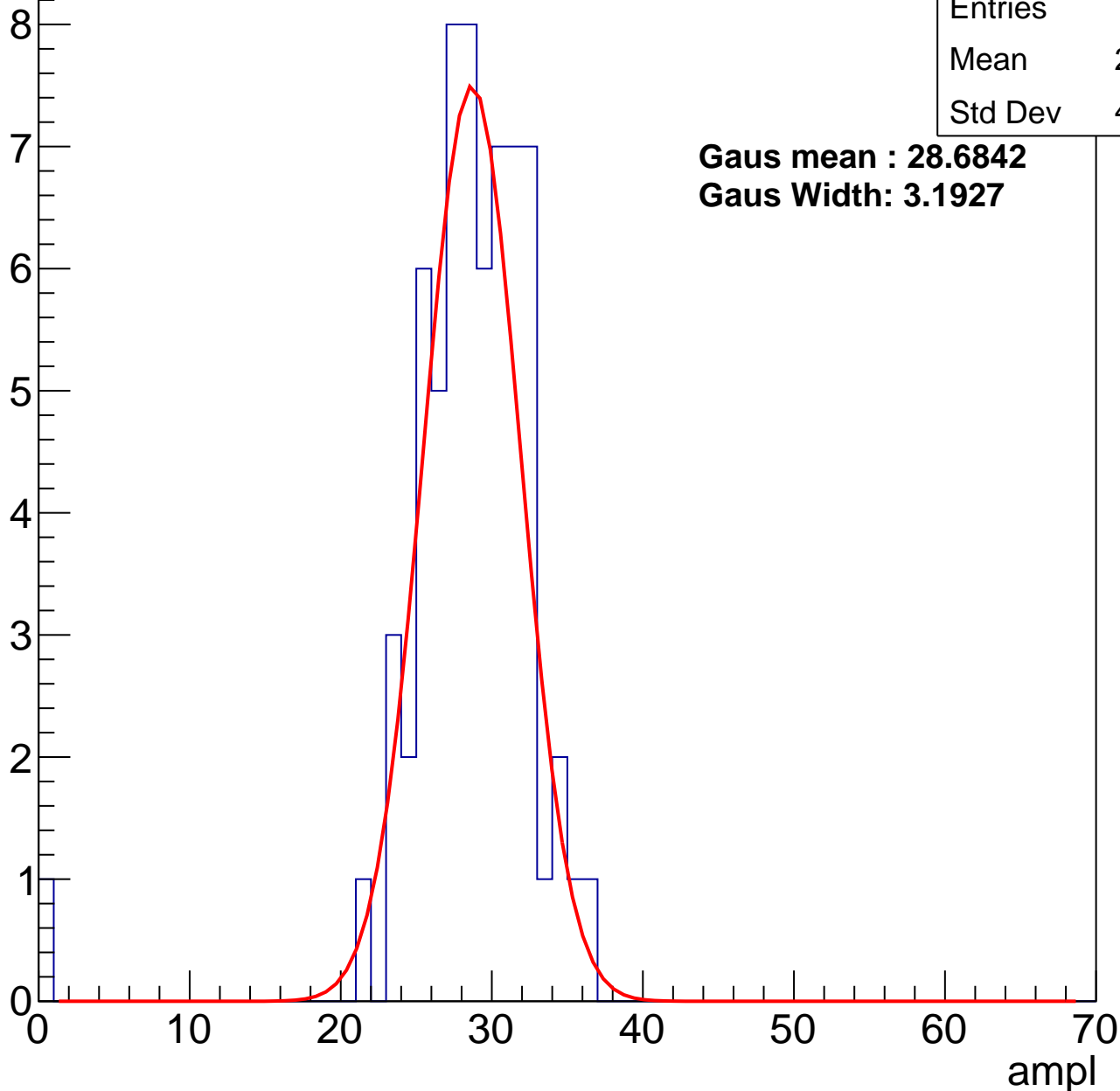
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	28.11
Std Dev	4.671

Gaus mean : 28.6842

Gaus Width: 3.1927



B1L101S, U21-ch41, adc1

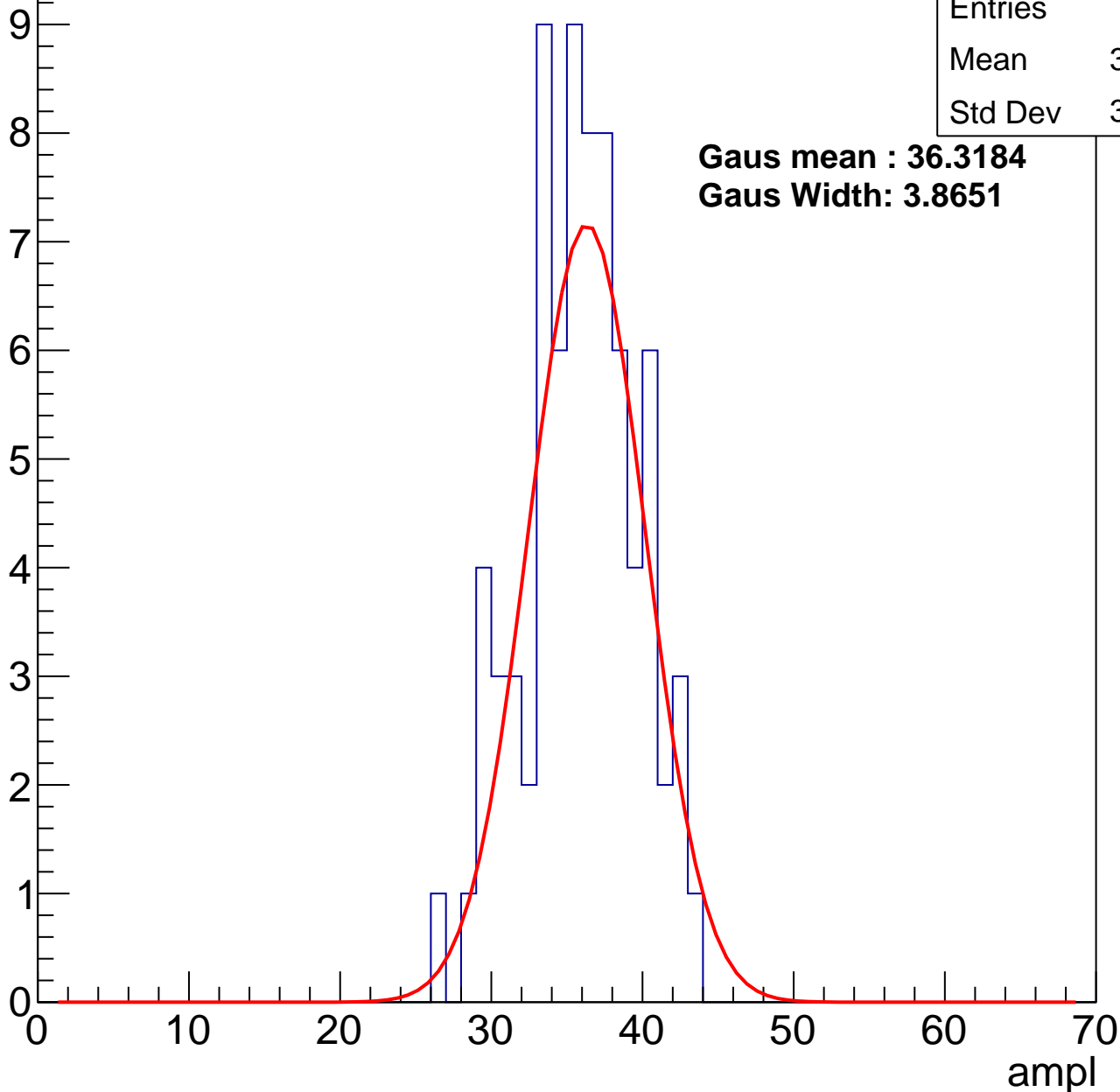
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.42
Std Dev	3.689

Gaus mean : 36.3184

Gaus Width: 3.8651



B1L101S, U21-ch41, adc2

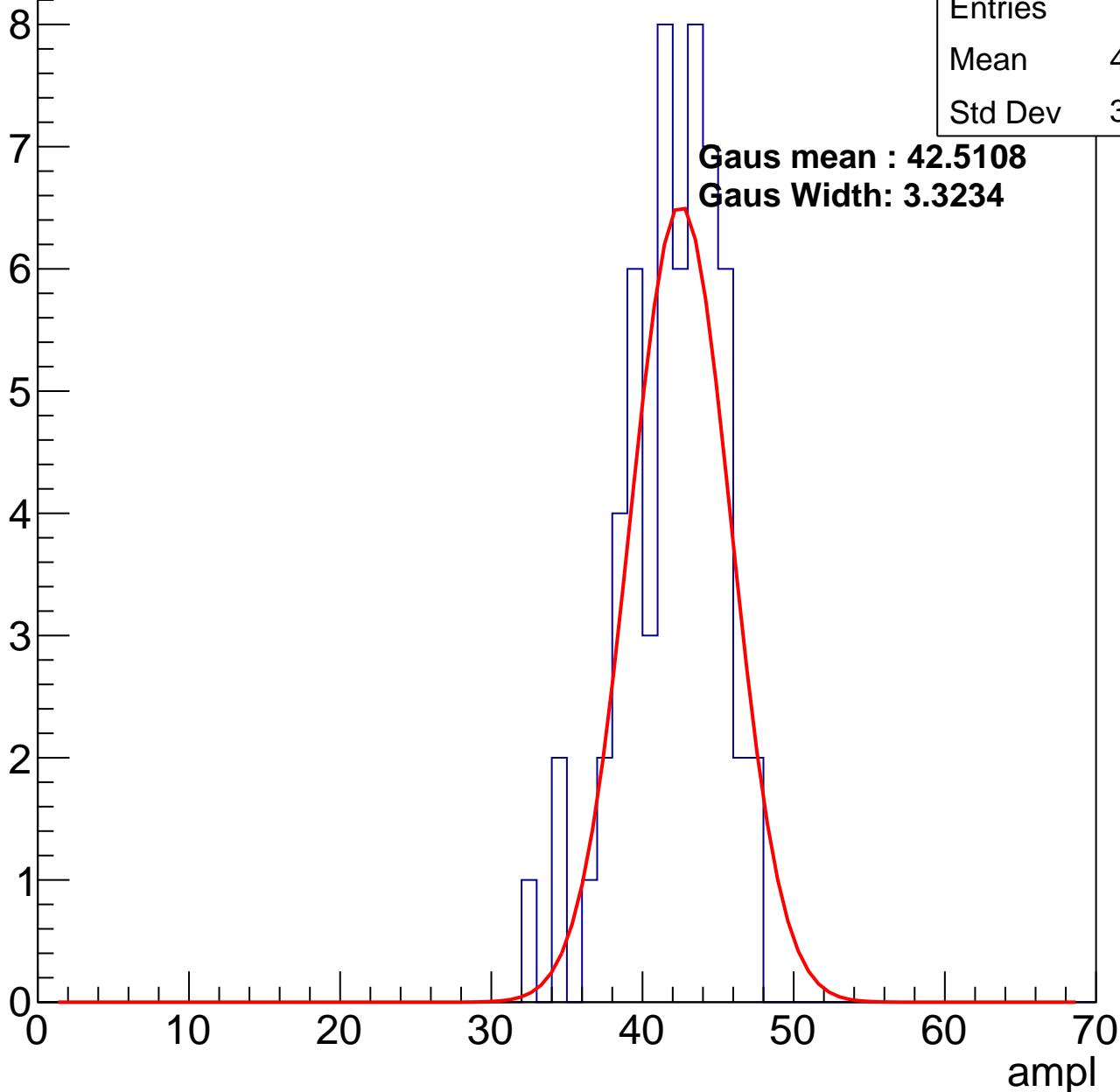
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	41.45
Std Dev	3.233

Gaus mean : 42.5108

Gaus Width: 3.3234

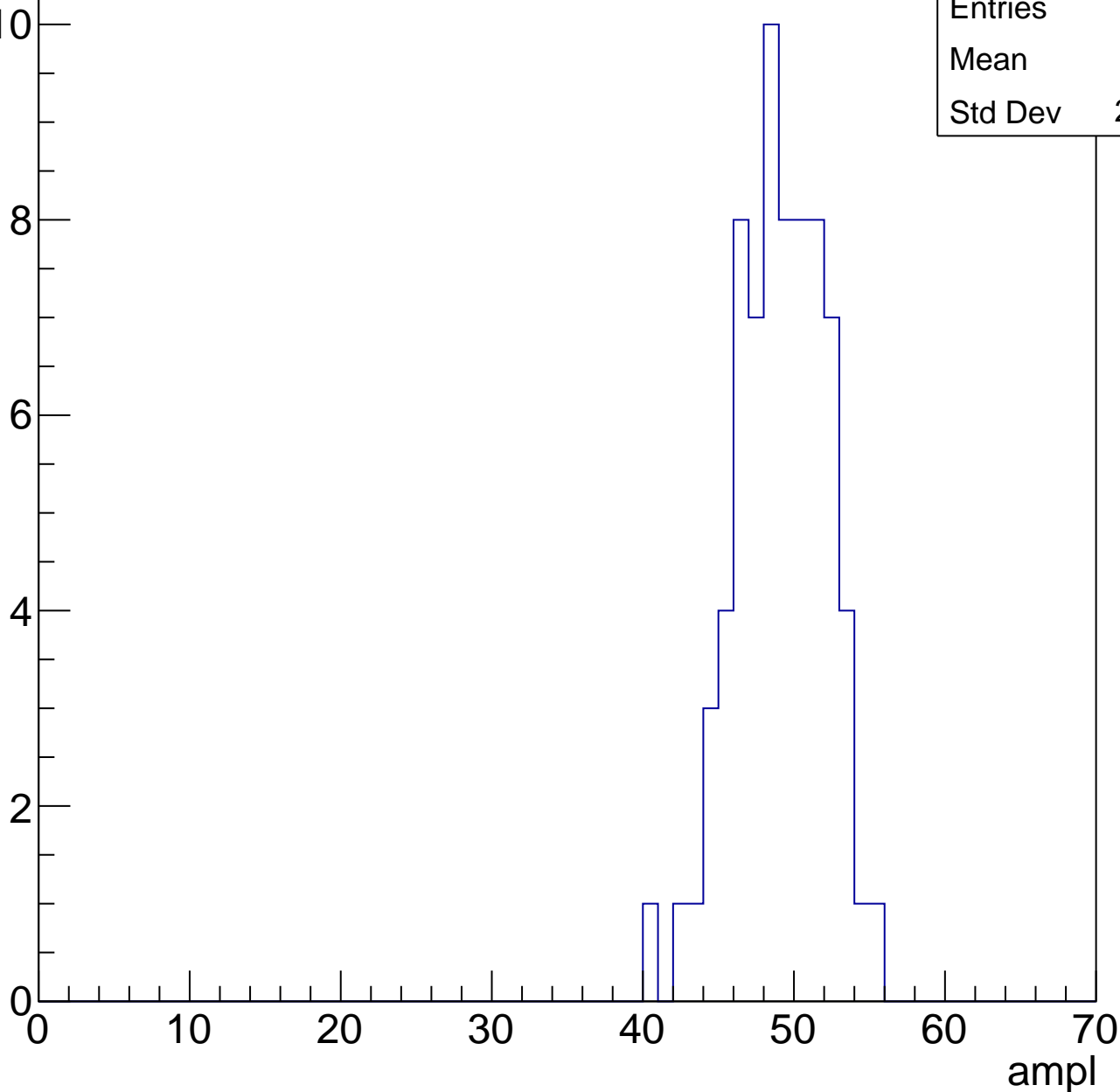


B1L101S, U21-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	48.6
Std Dev	2.961

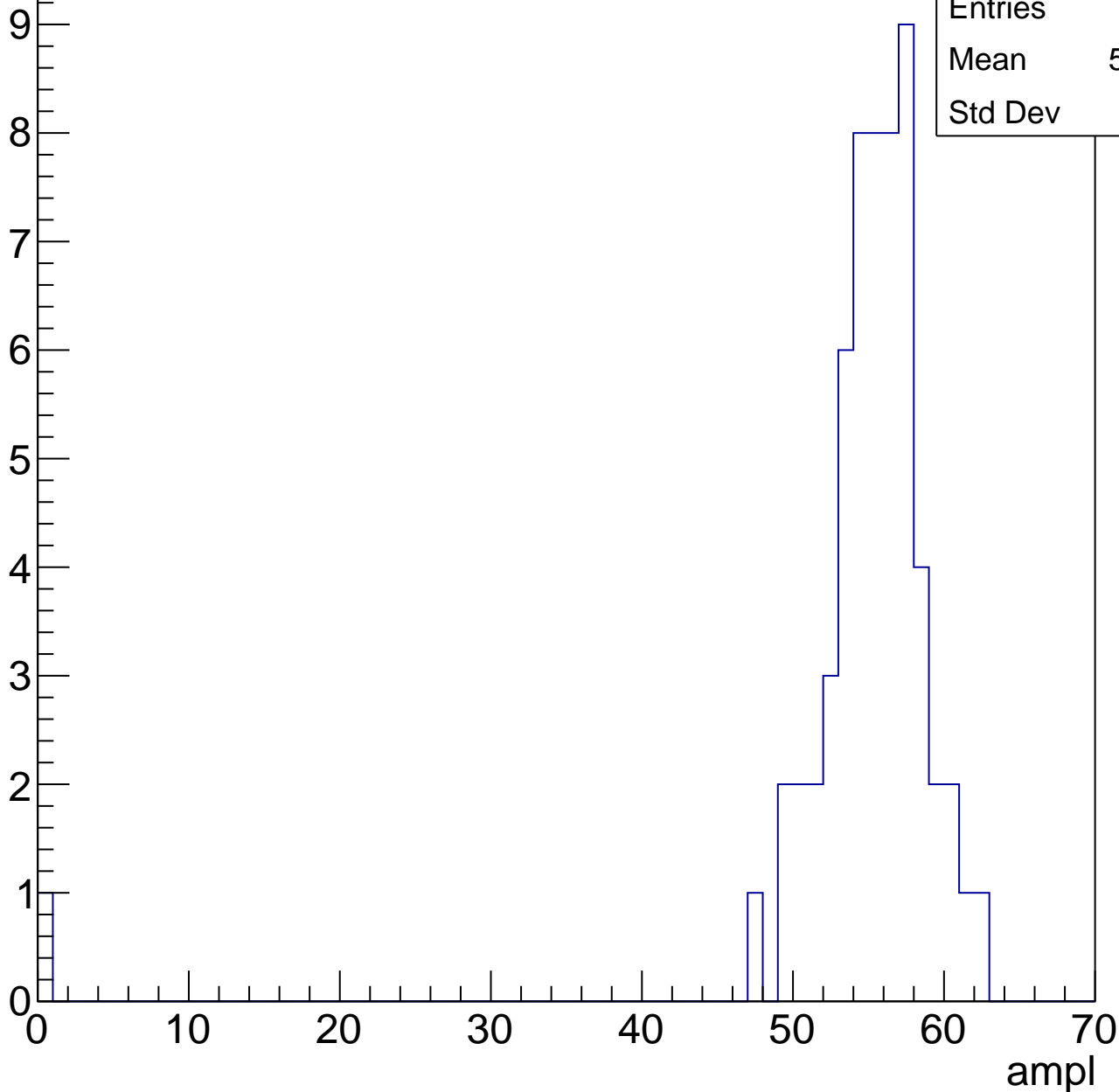


B1L101S, U21-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.12
Std Dev	7.64



B1L101S, U21-ch41, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	59.96
Std Dev	2.201

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

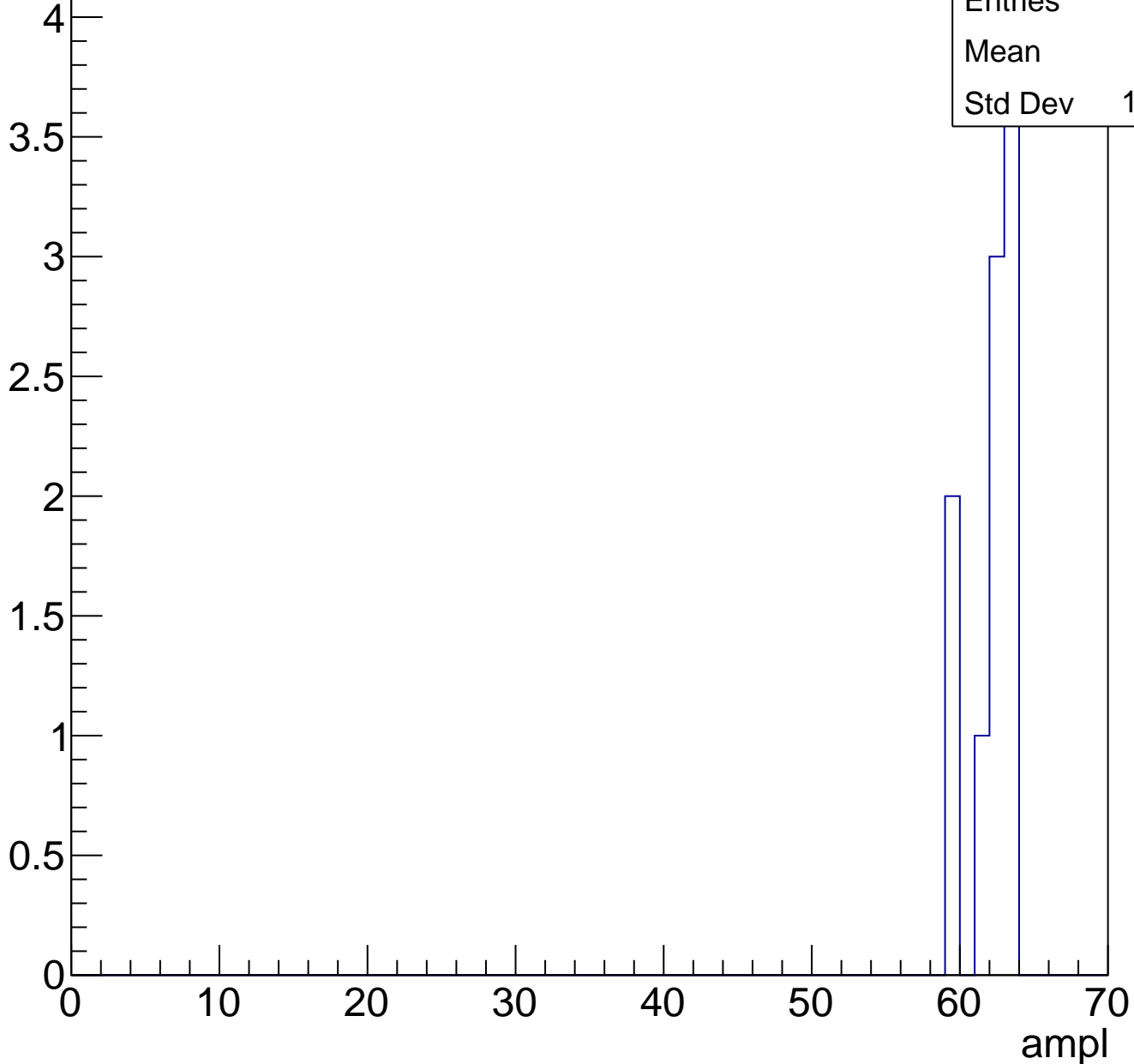
7

8

B1L101S, U21-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	10
Mean	61.7
Std Dev	1.487

B1L101S, U21-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch42, adc0

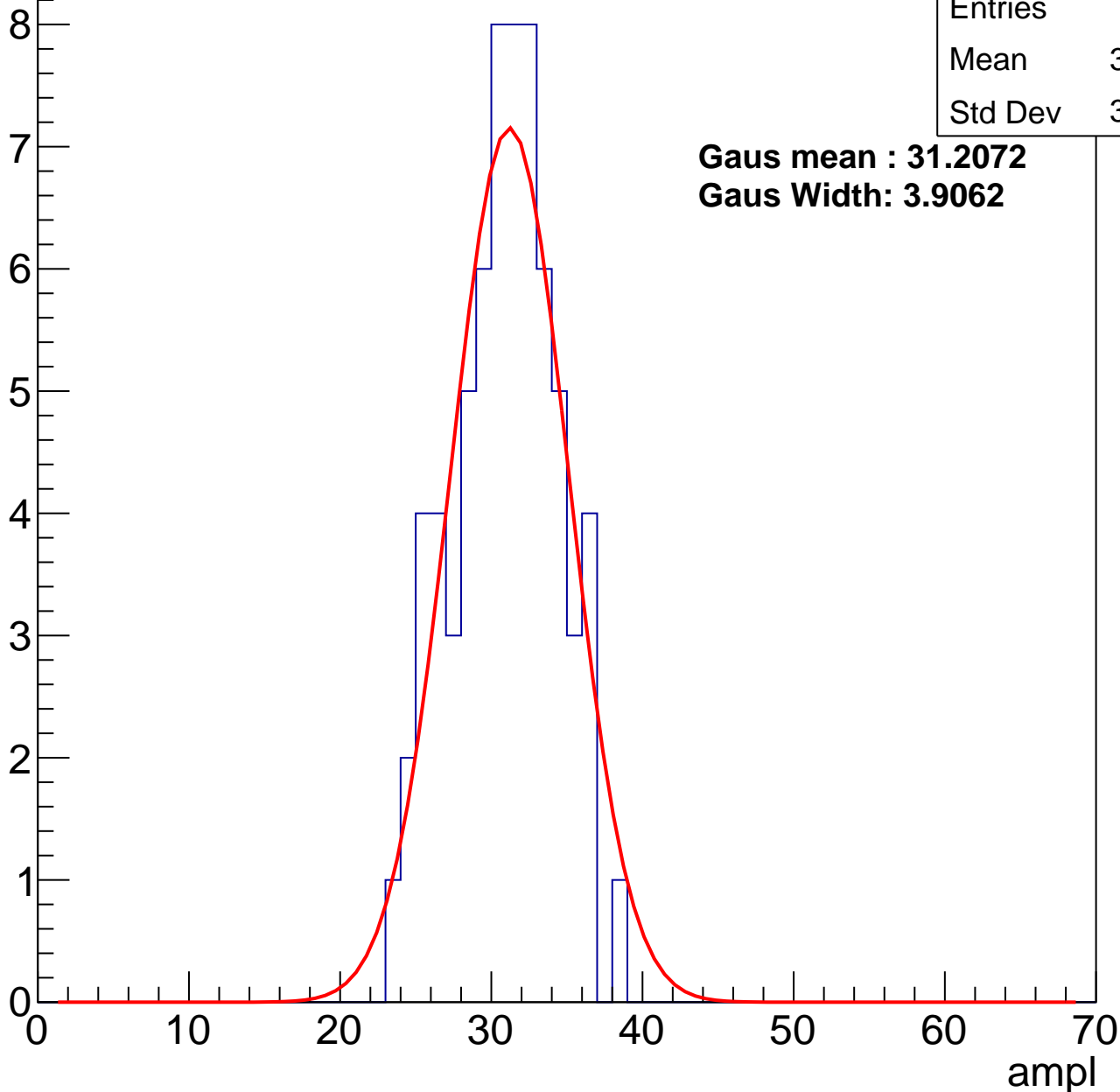
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.43
Std Dev	3.393

Gaus mean : 31.2072

Gaus Width: 3.9062



B1L101S, U21-ch42, adc1

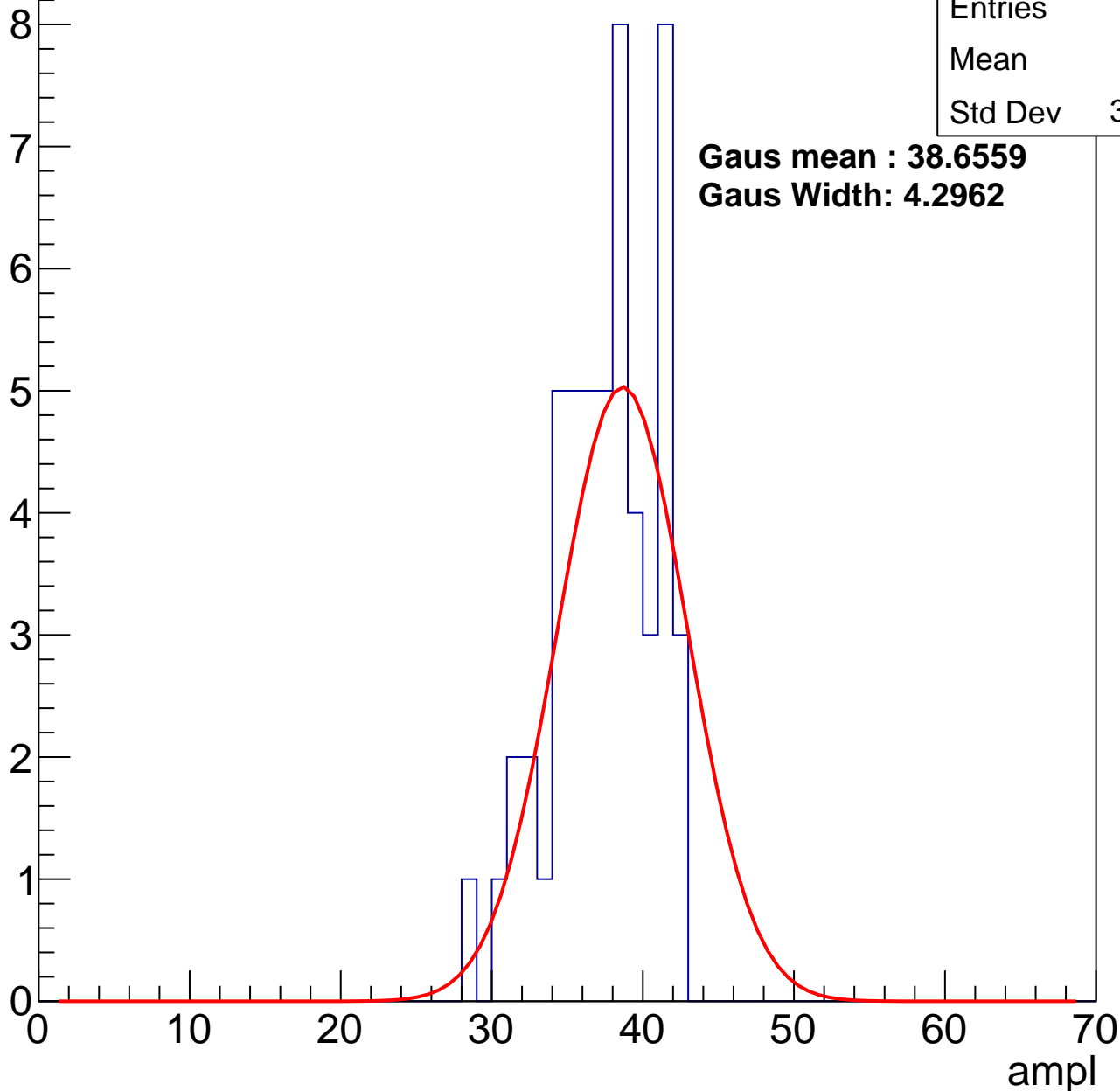
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	37
Std Dev	3.342

Gaus mean : 38.6559

Gaus Width: 4.2962



B1L101S, U21-ch42, adc2

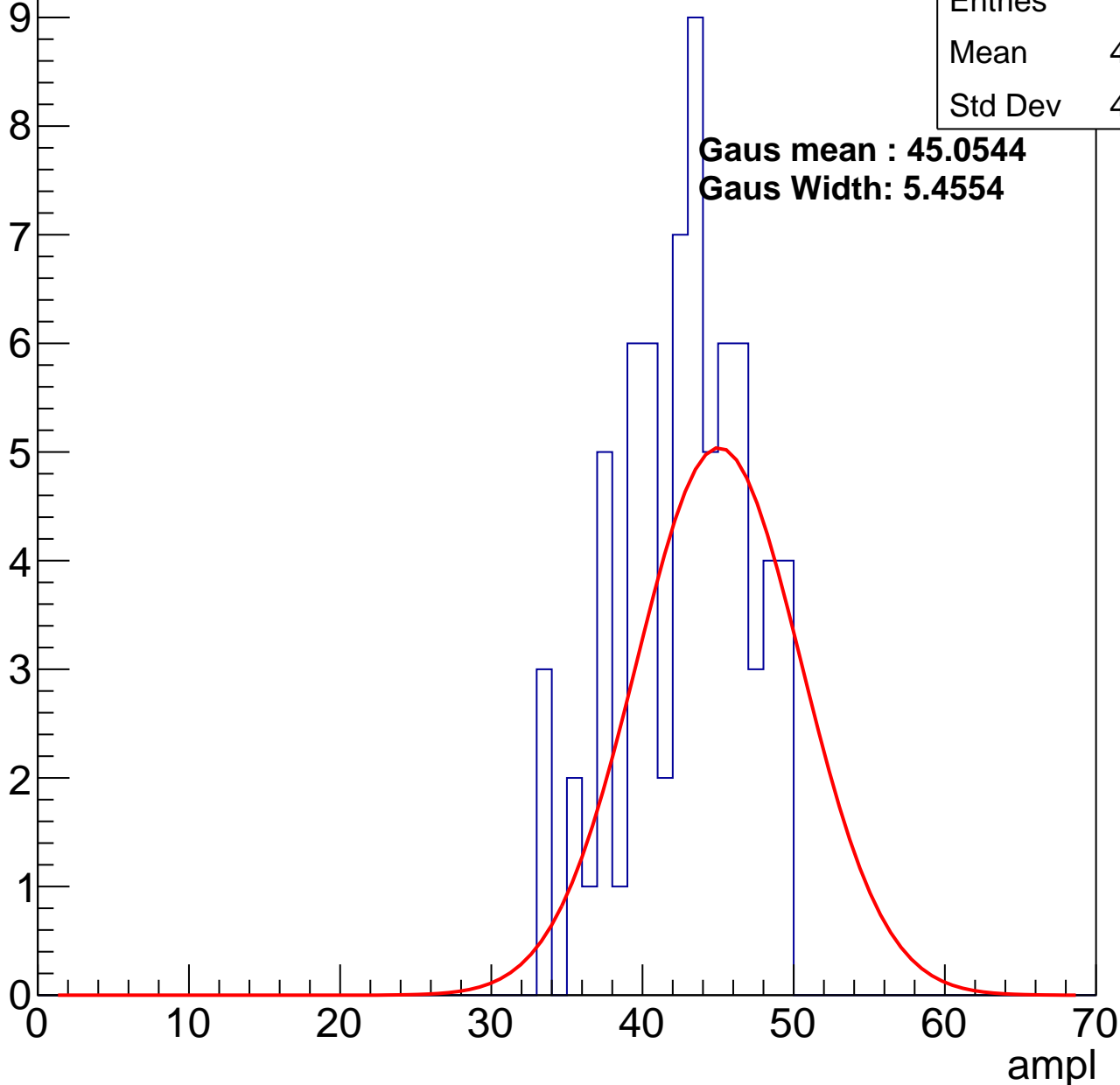
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.29
Std Dev	4.117

Gaus mean : 45.0544

Gaus Width: 5.4554

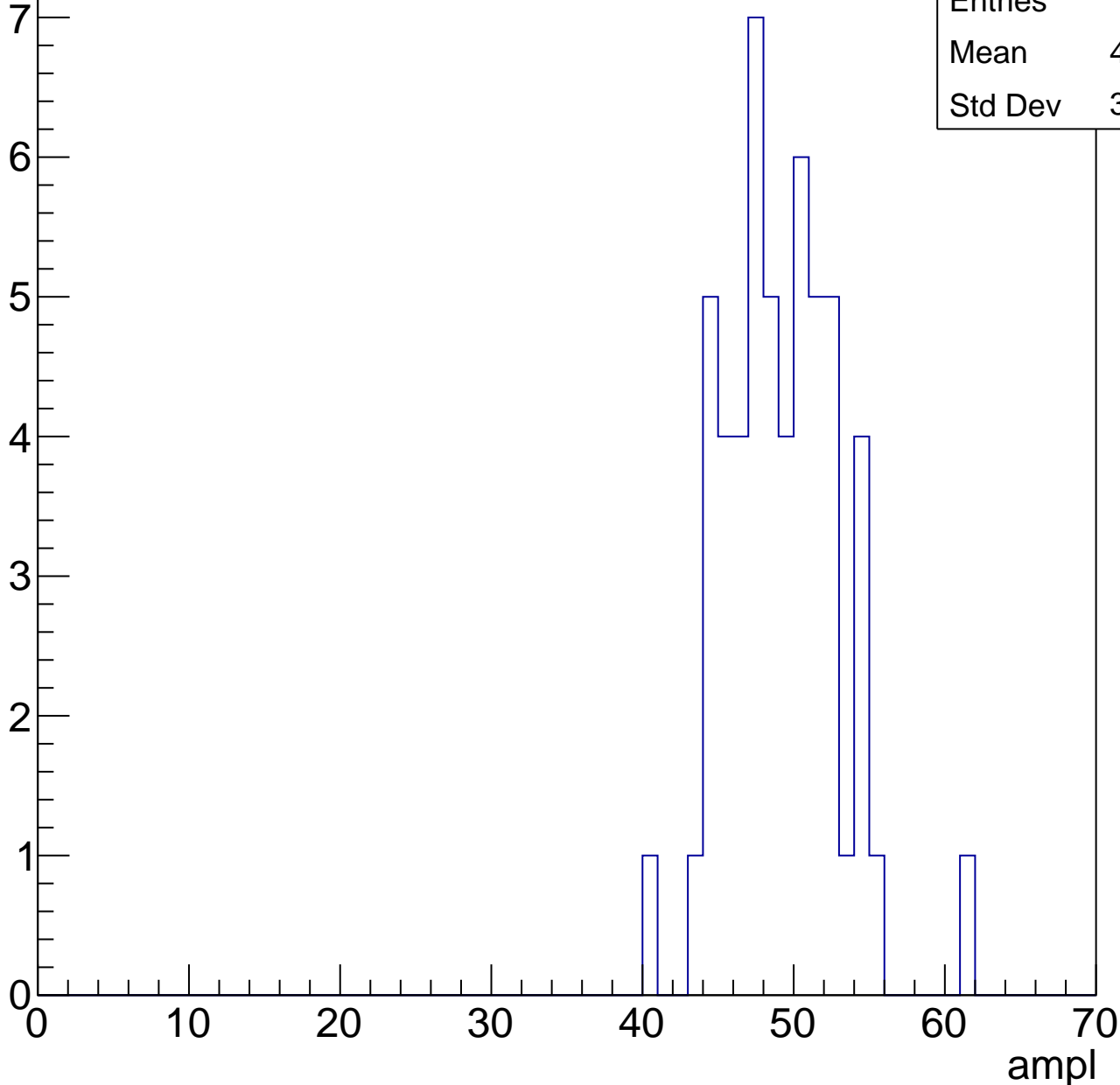


B1L101S, U21-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	48.74
Std Dev	3.698

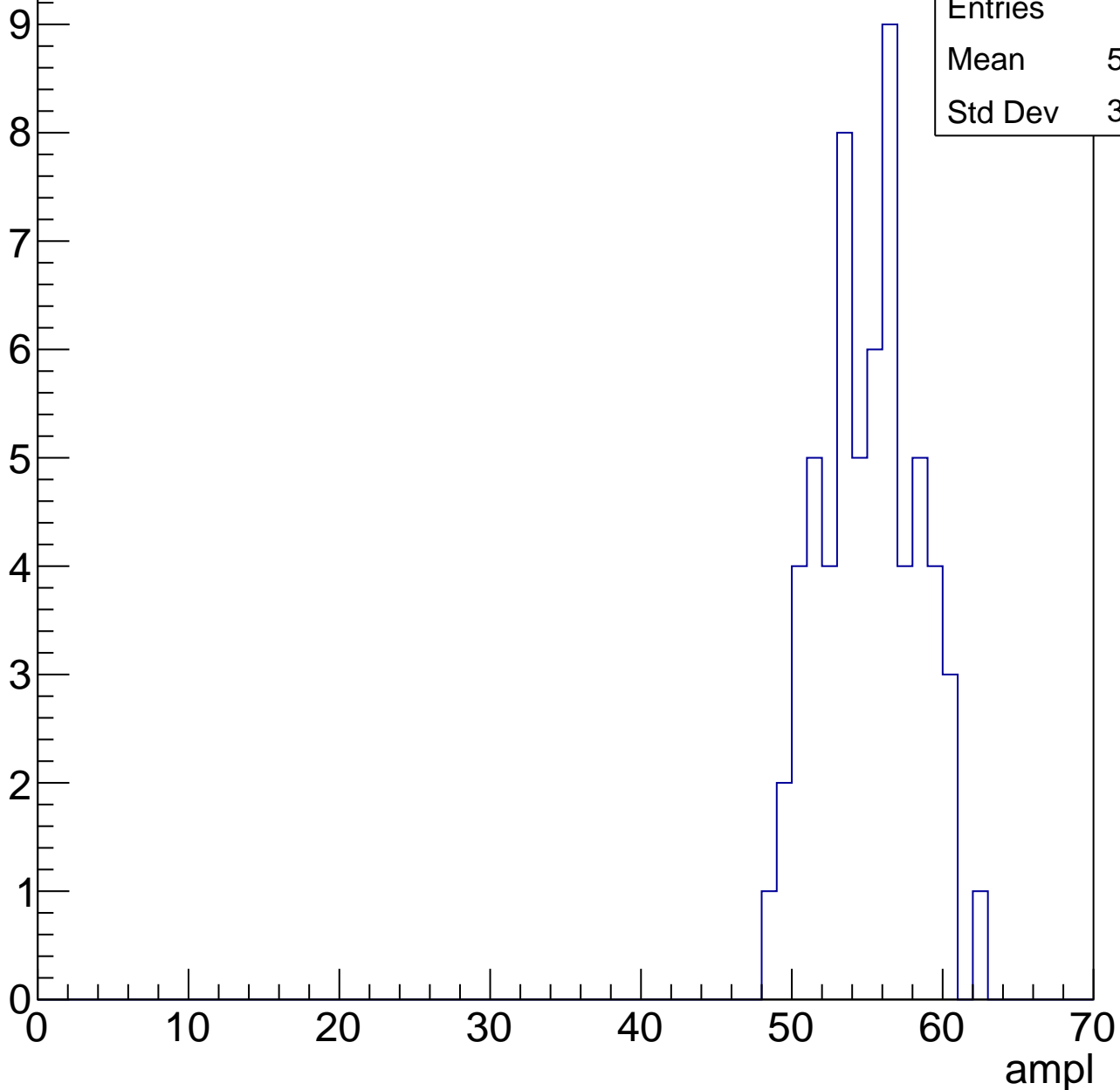


B1L101S, U21-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	54.64
Std Dev	3.199

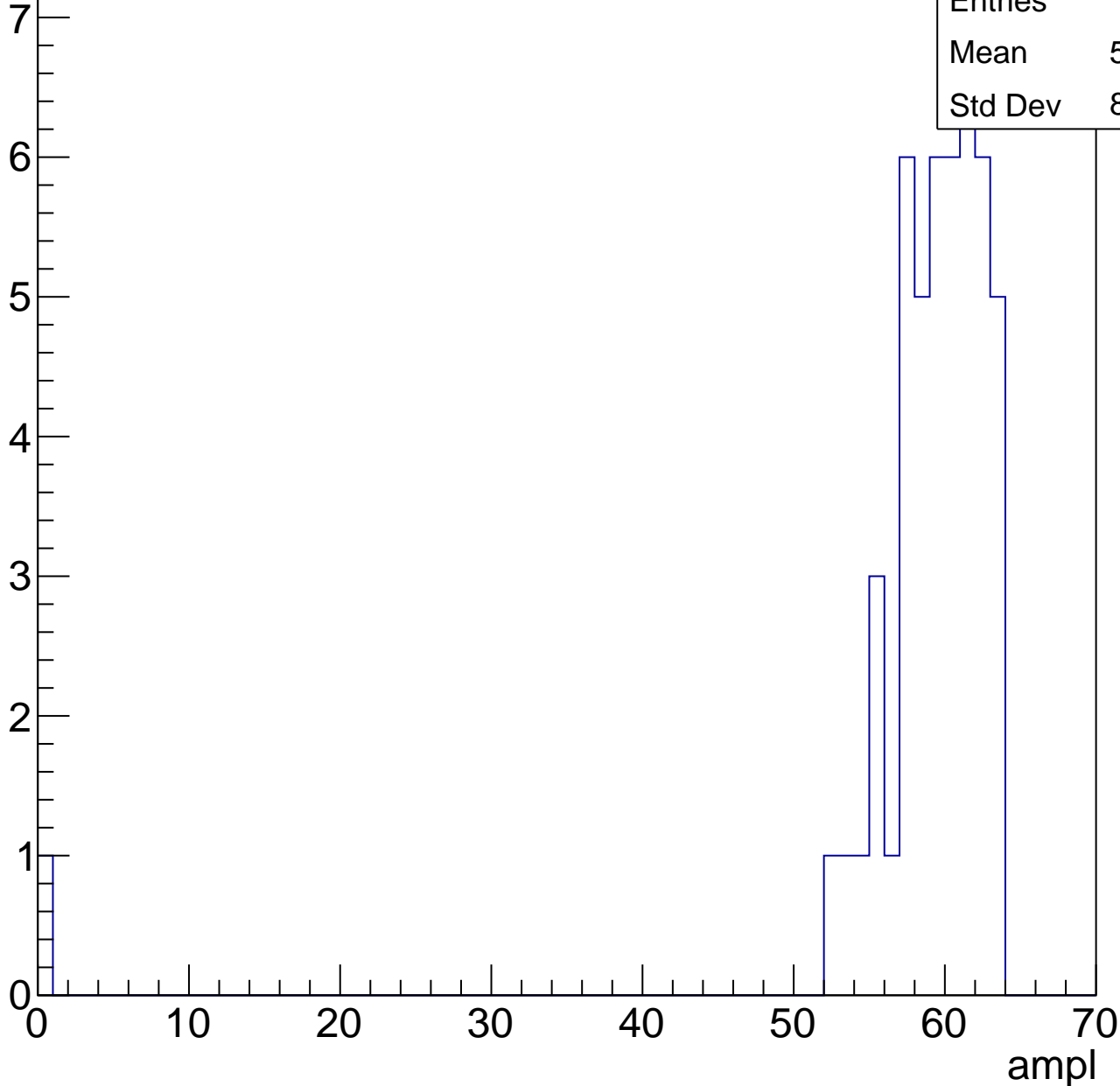


B1L101S, U21-ch42, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	57.96
Std Dev	8.797

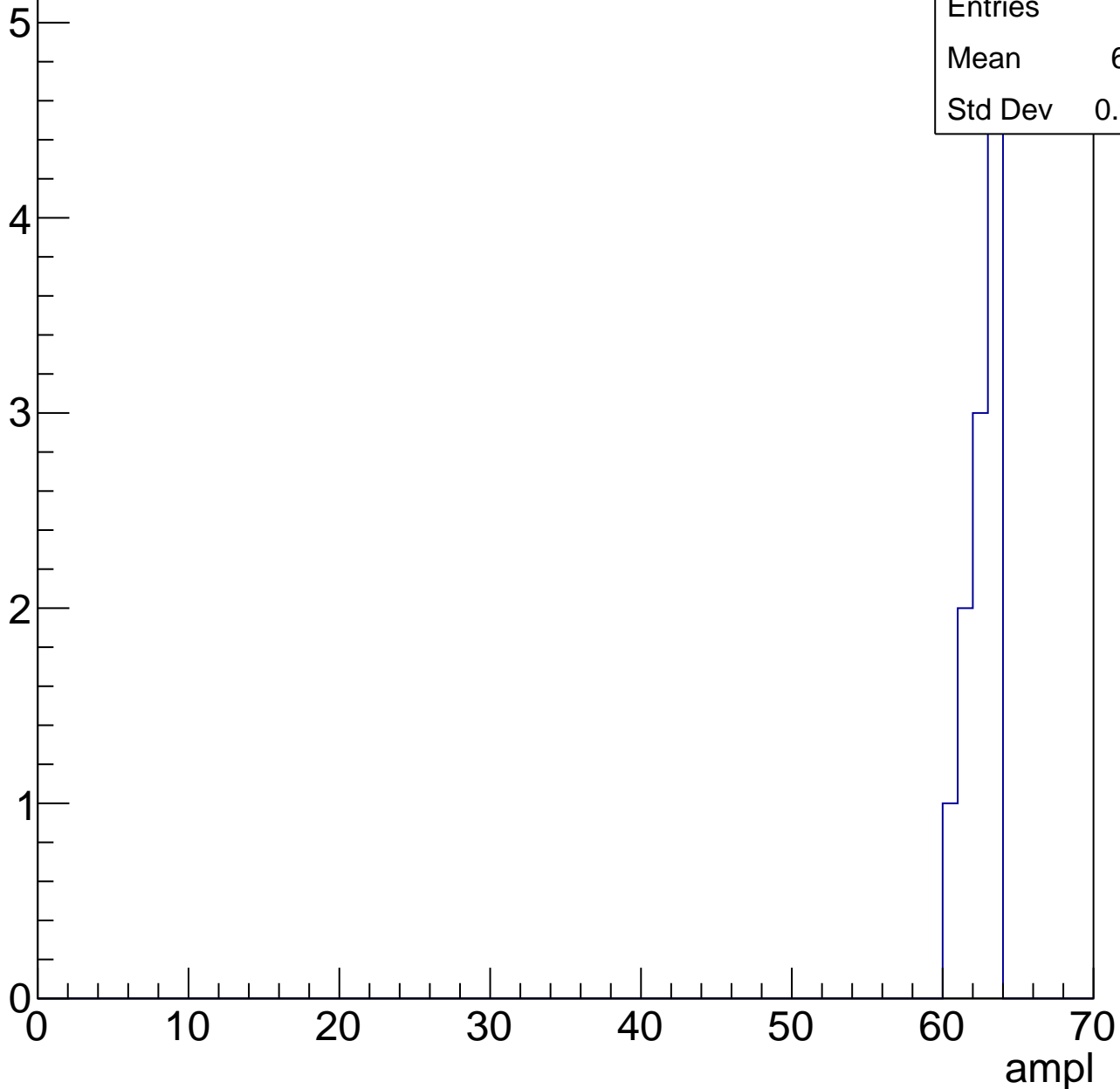


B1L101S, U21-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	62.09
Std Dev	0.9959



B1L101S, U21-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch43, adc0

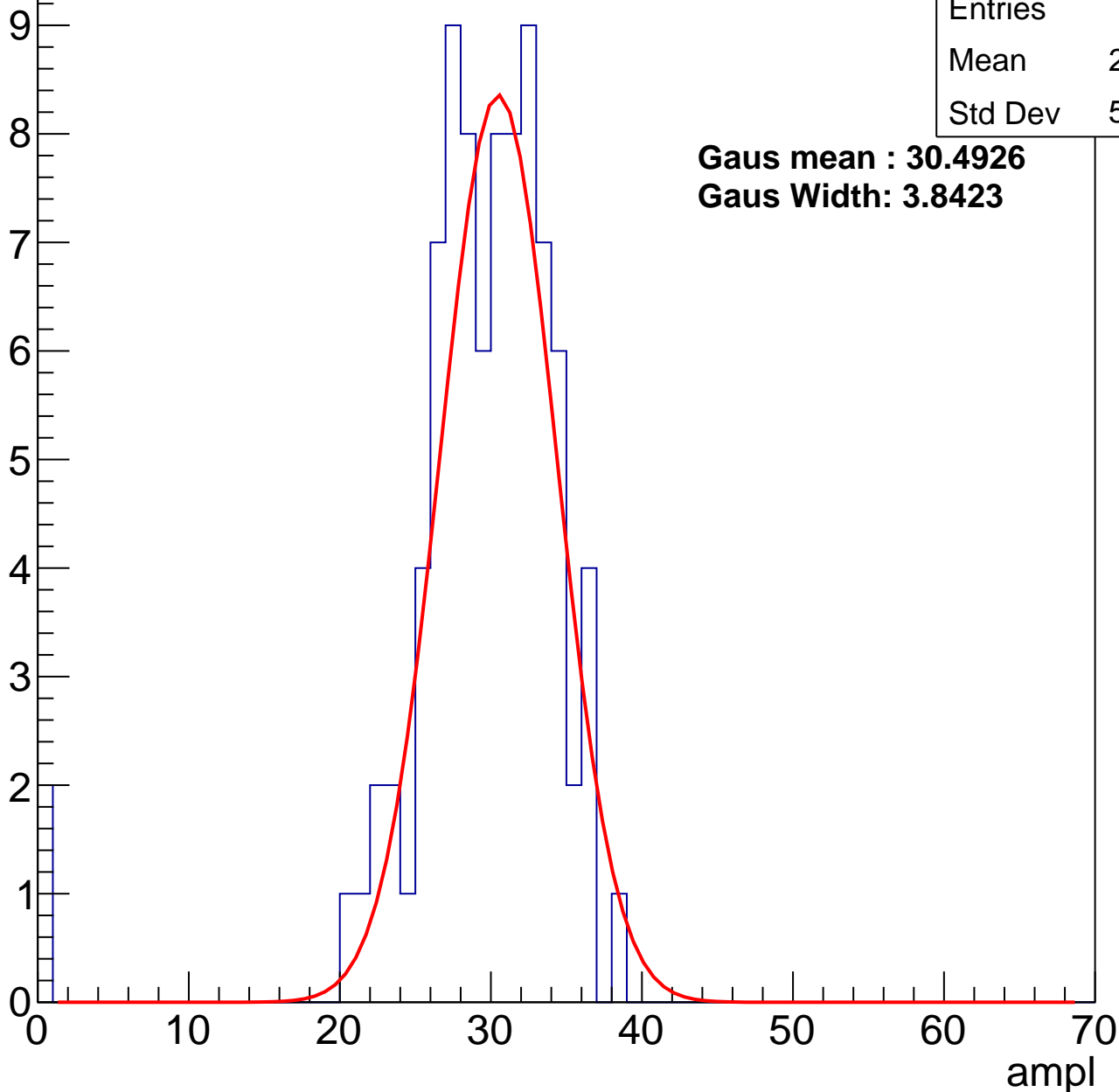
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	28.88
Std Dev	5.764

Gaus mean : 30.4926

Gaus Width: 3.8423



B1L101S, U21-ch43, adc1

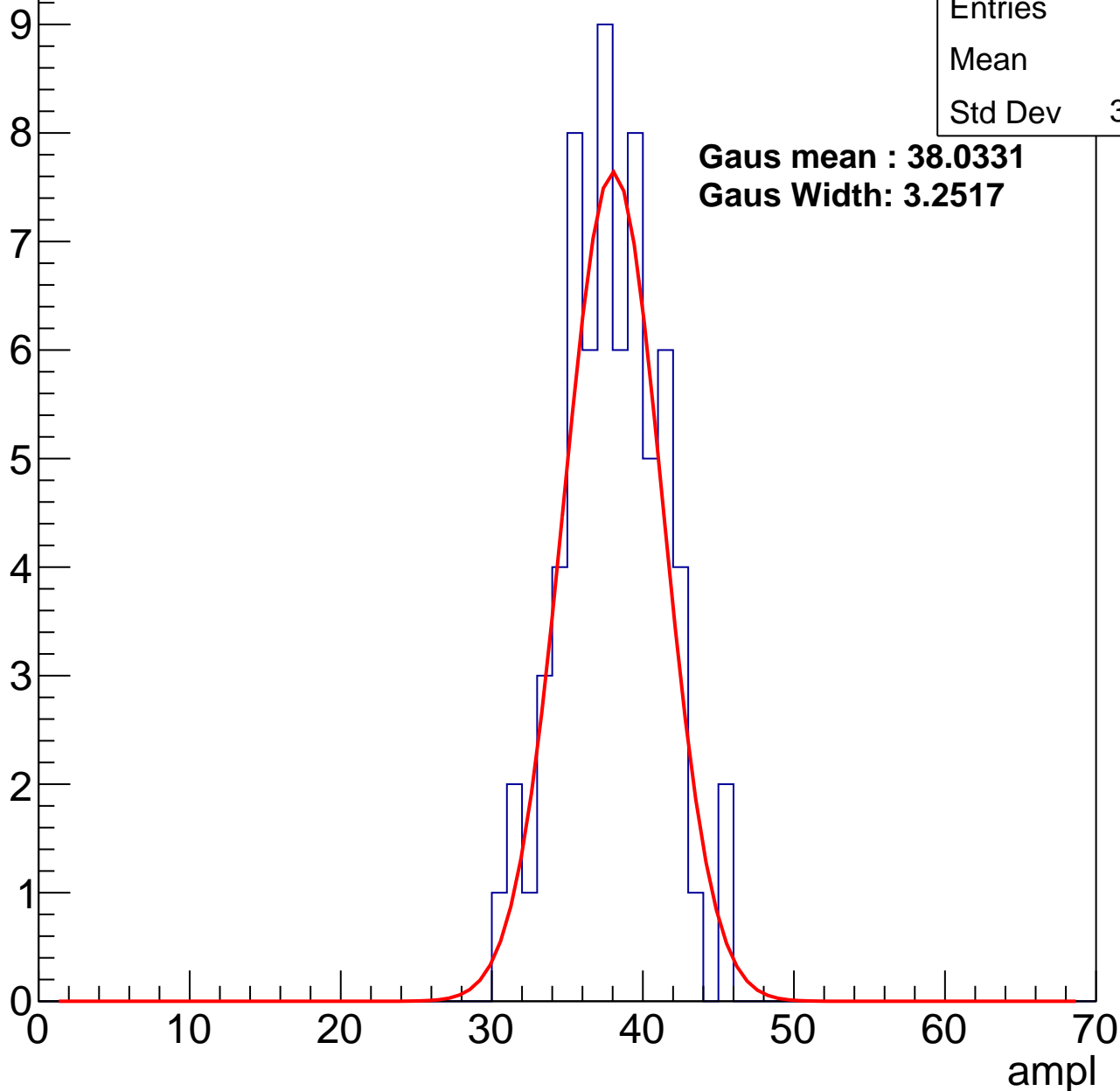
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.5
Std Dev	3.244

Gaus mean : 38.0331

Gaus Width: 3.2517



B1L101S, U21-ch43, adc2

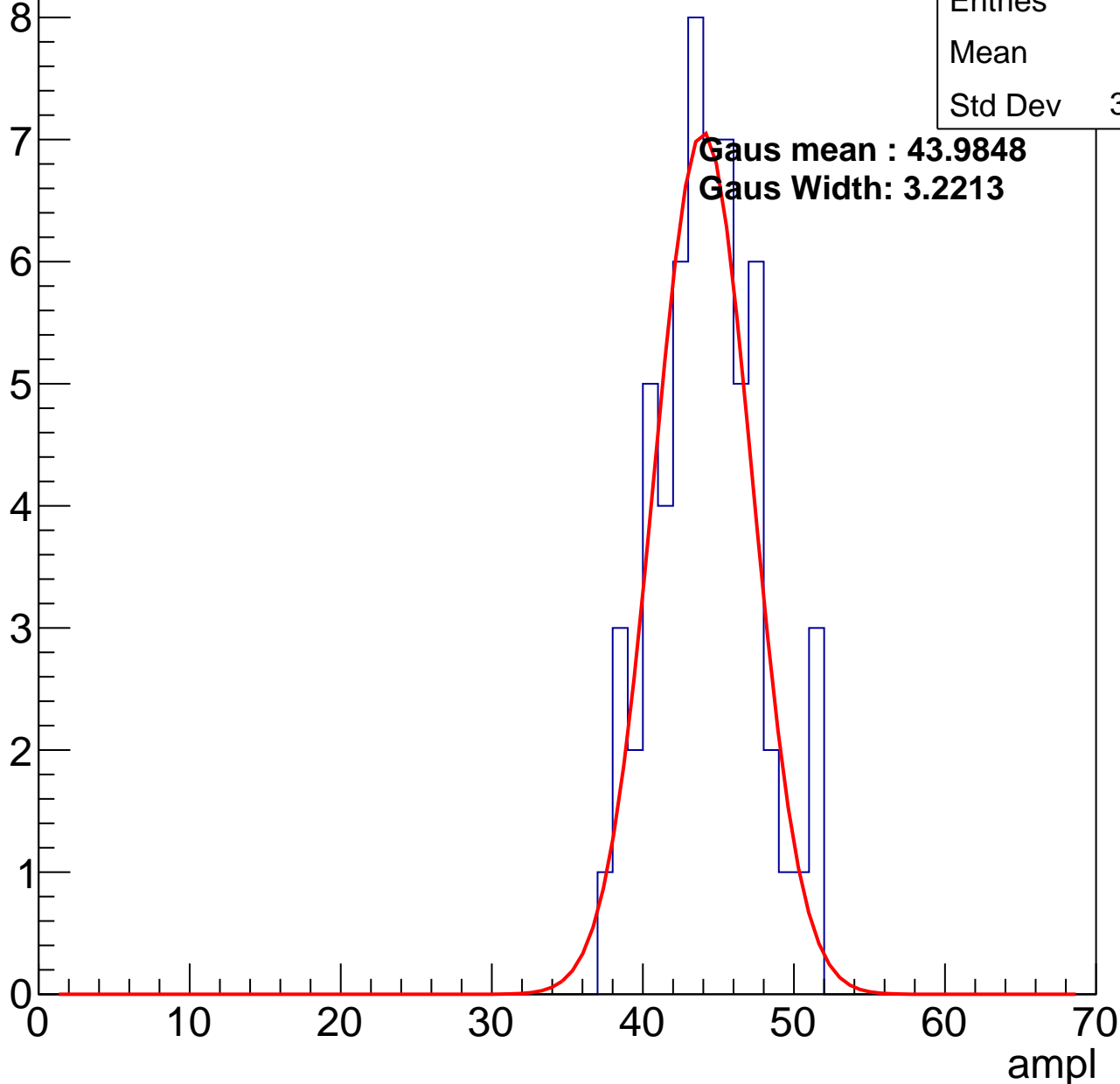
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.8
Std Dev	3.323

Gaus mean : 43.9848

Gaus Width: 3.2213

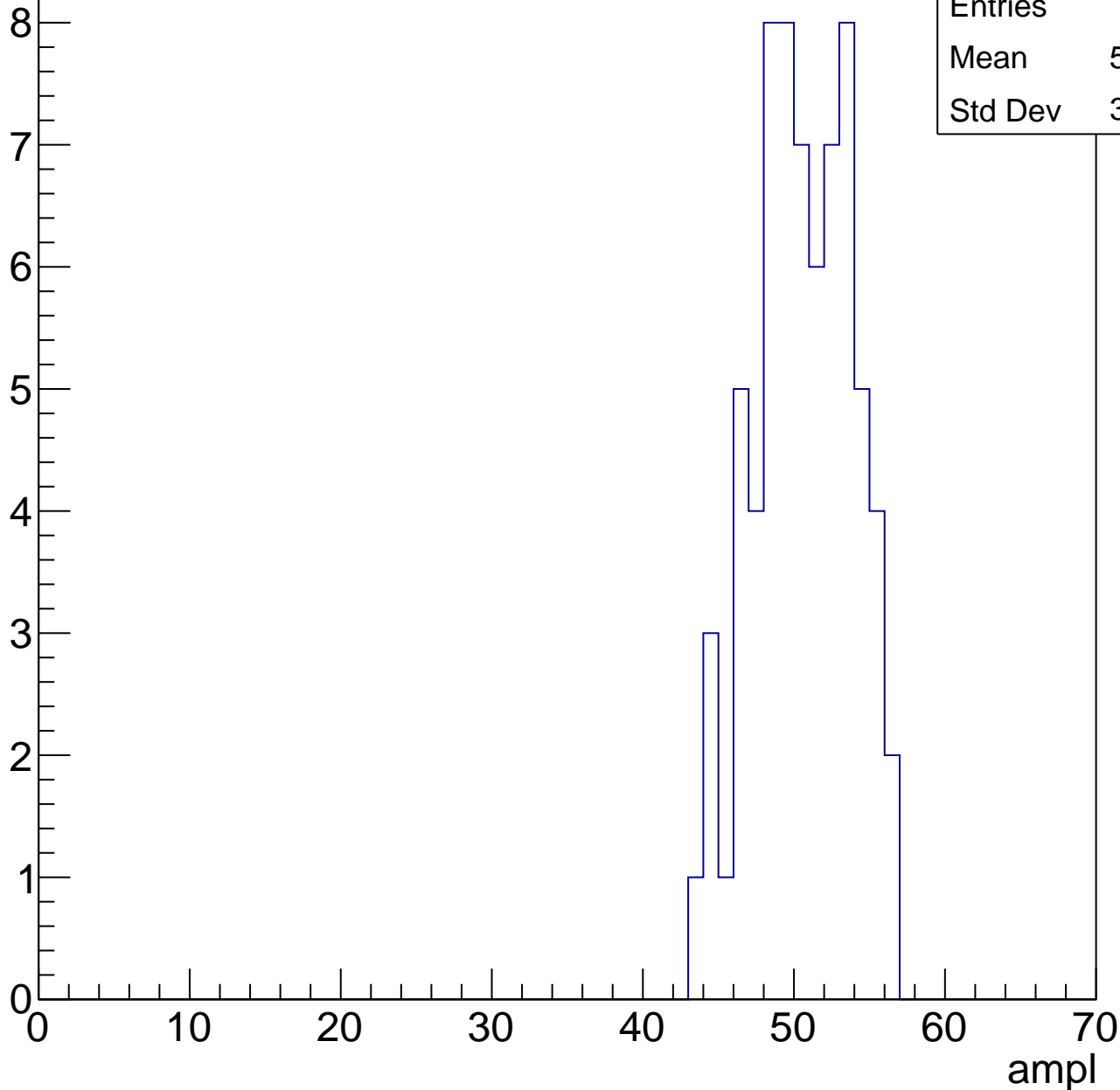


B1L101S, U21-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

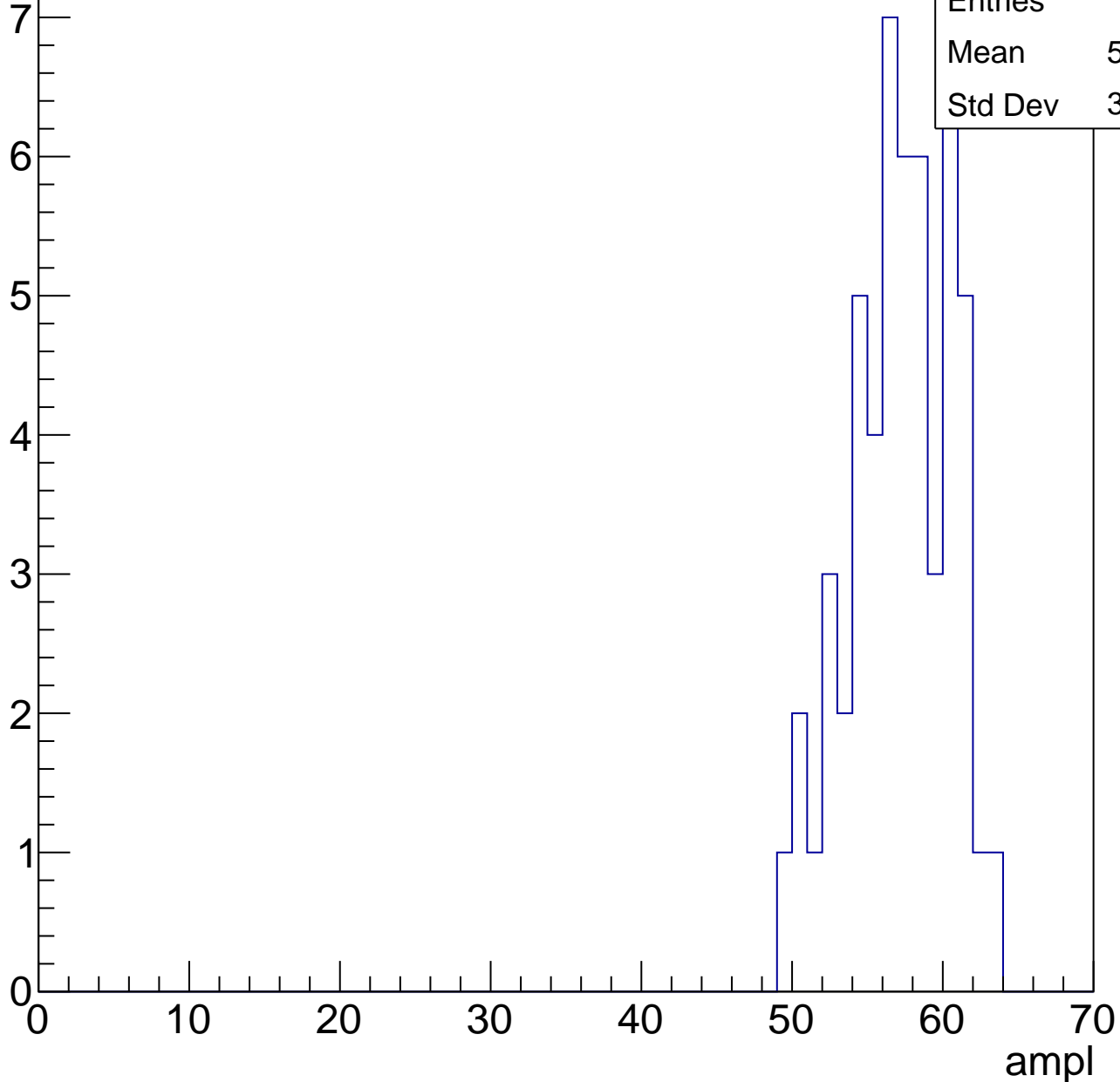
Entries	69
Mean	50.14
Std Dev	3.173



B1L101S, U21-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

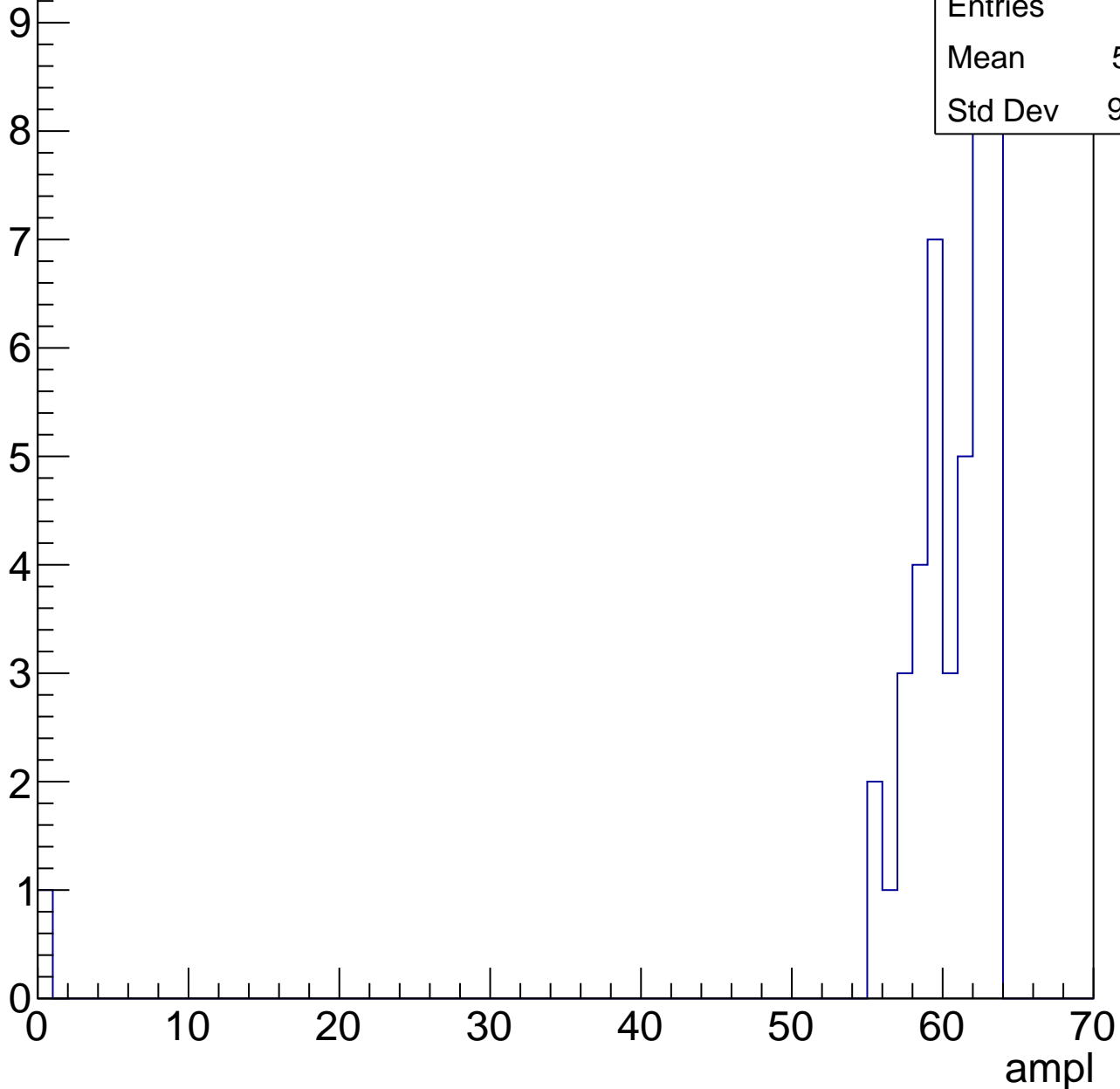


B1L101S, U21-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

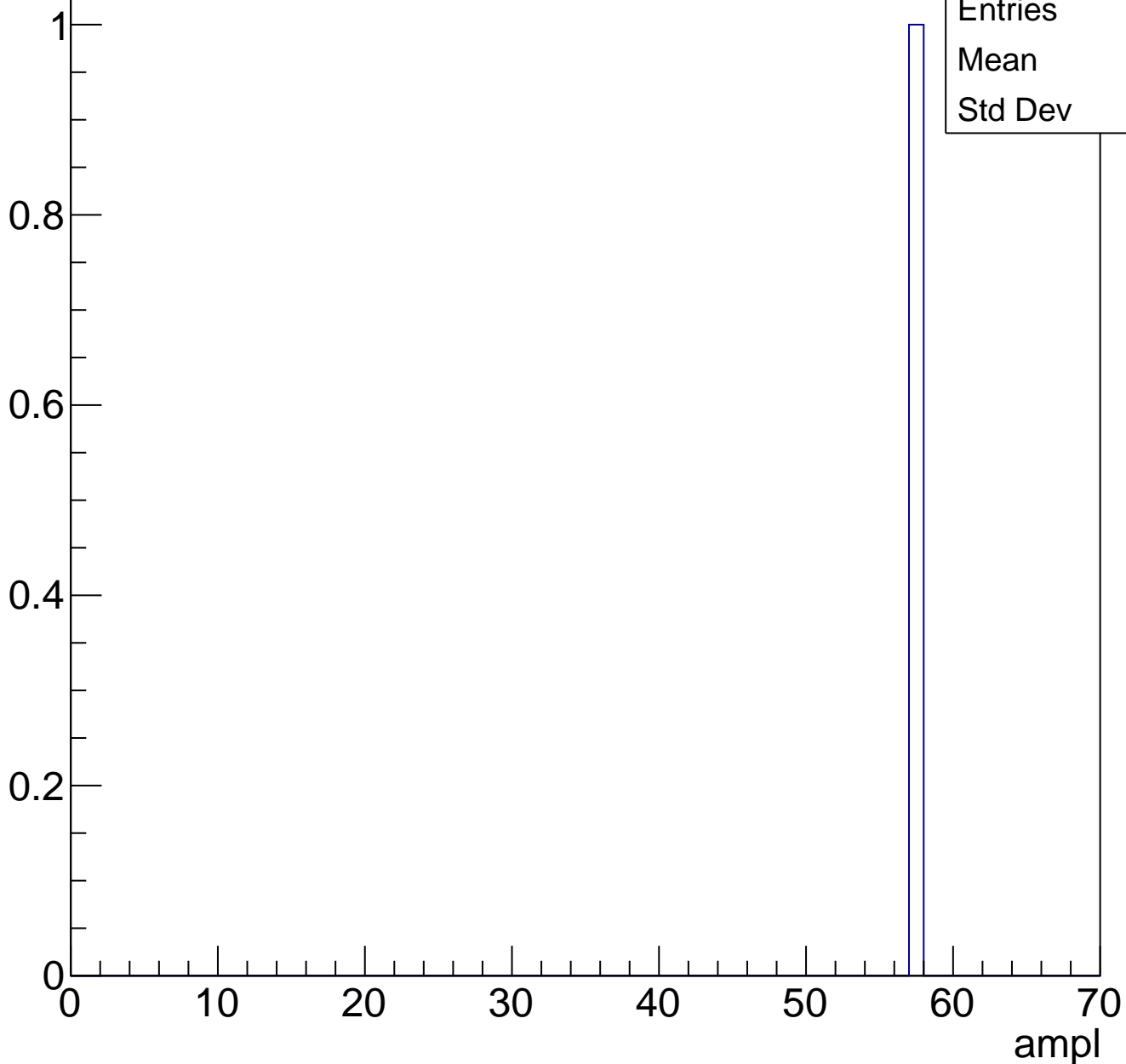
Entries	44
Mean	58.91
Std Dev	9.276



B1L101S, U21-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch44, adc0

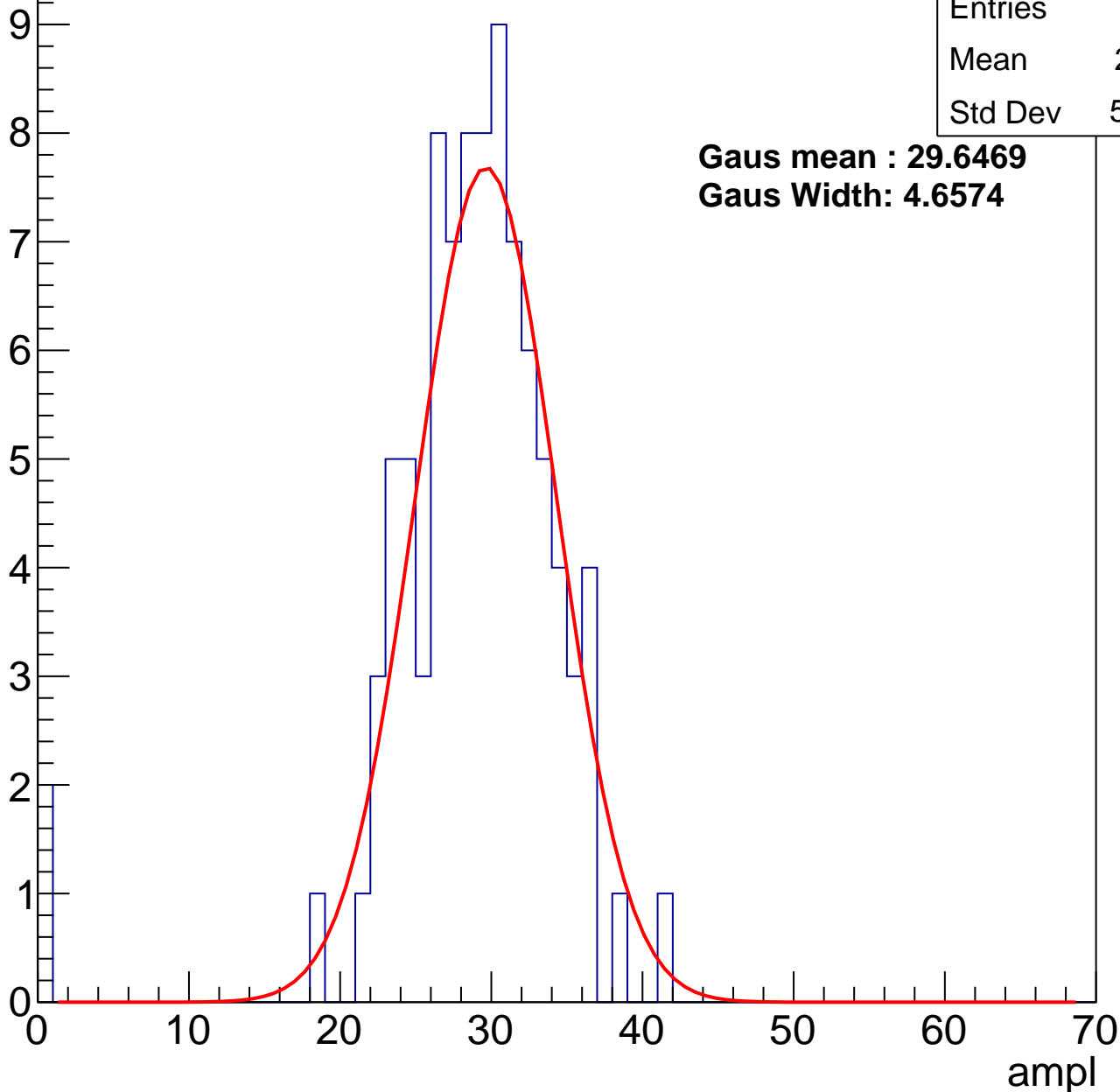
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	91
Mean	28.31
Std Dev	5.959

Gaus mean : 29.6469

Gaus Width: 4.6574



B1L101S, U21-ch44, adc1

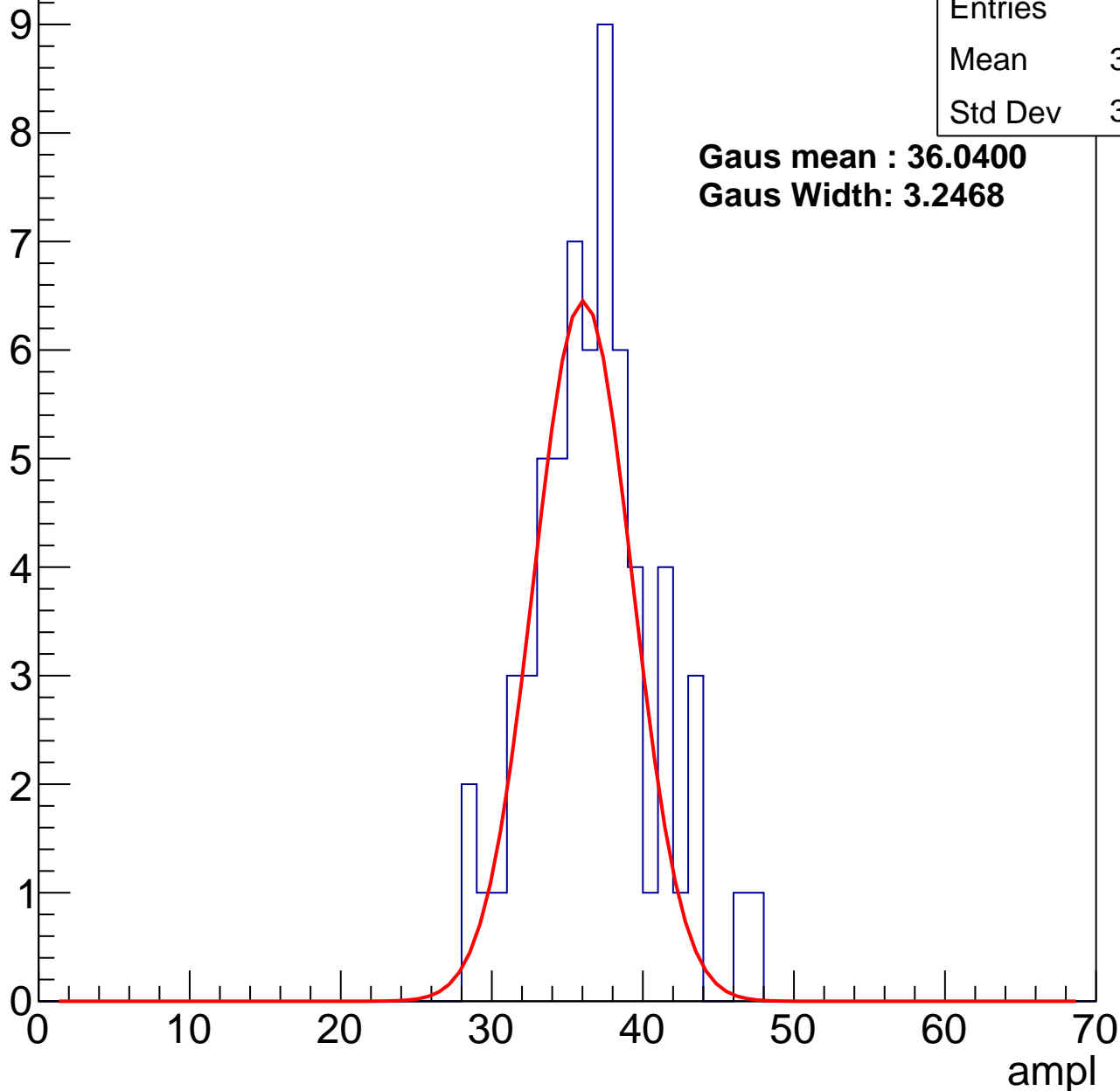
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.27
Std Dev	3.969

Gaus mean : 36.0400

Gaus Width: 3.2468



B1L101S, U21-ch44, adc2

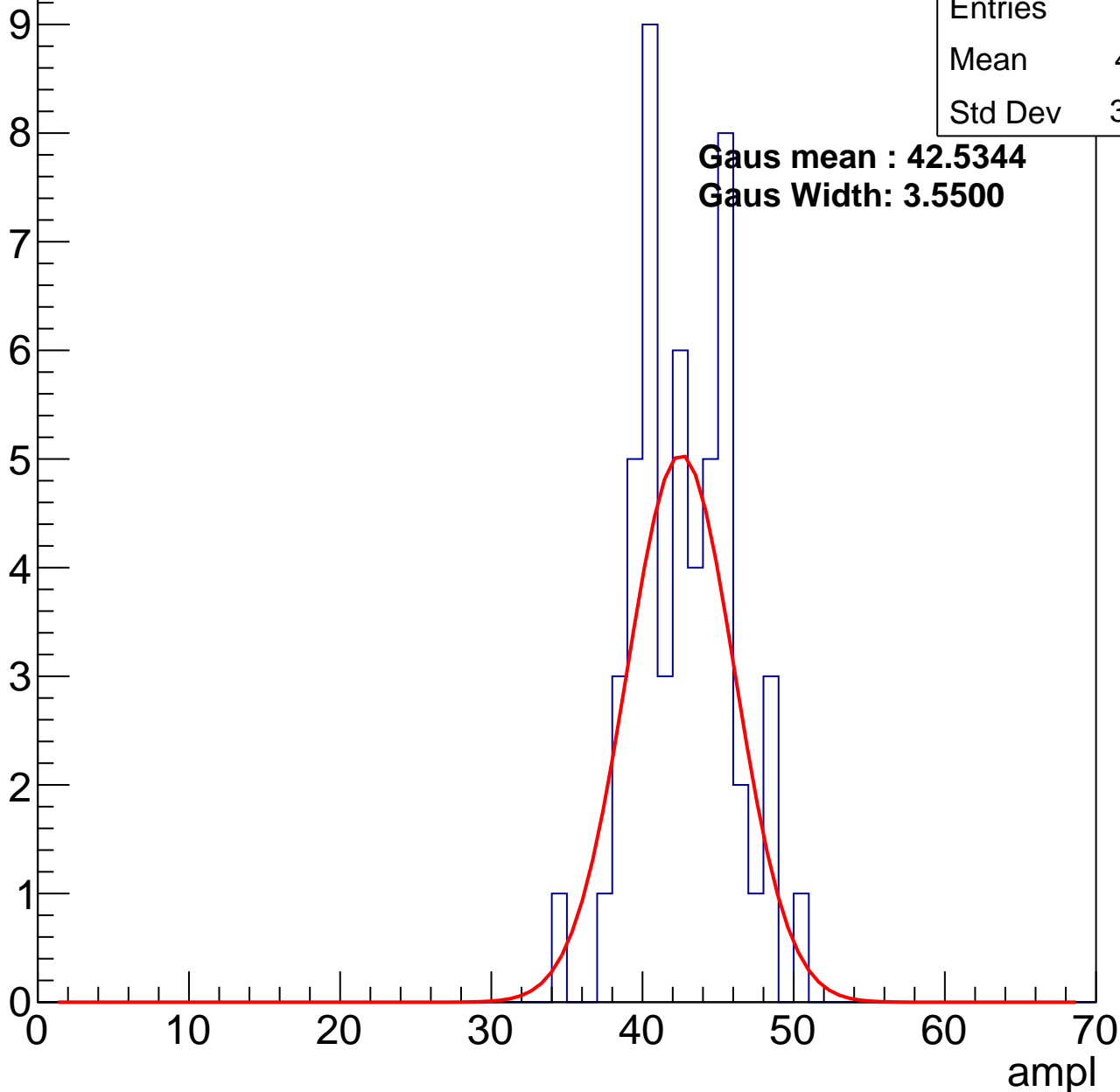
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	42.31
Std Dev	3.226

Gaus mean : 42.5344

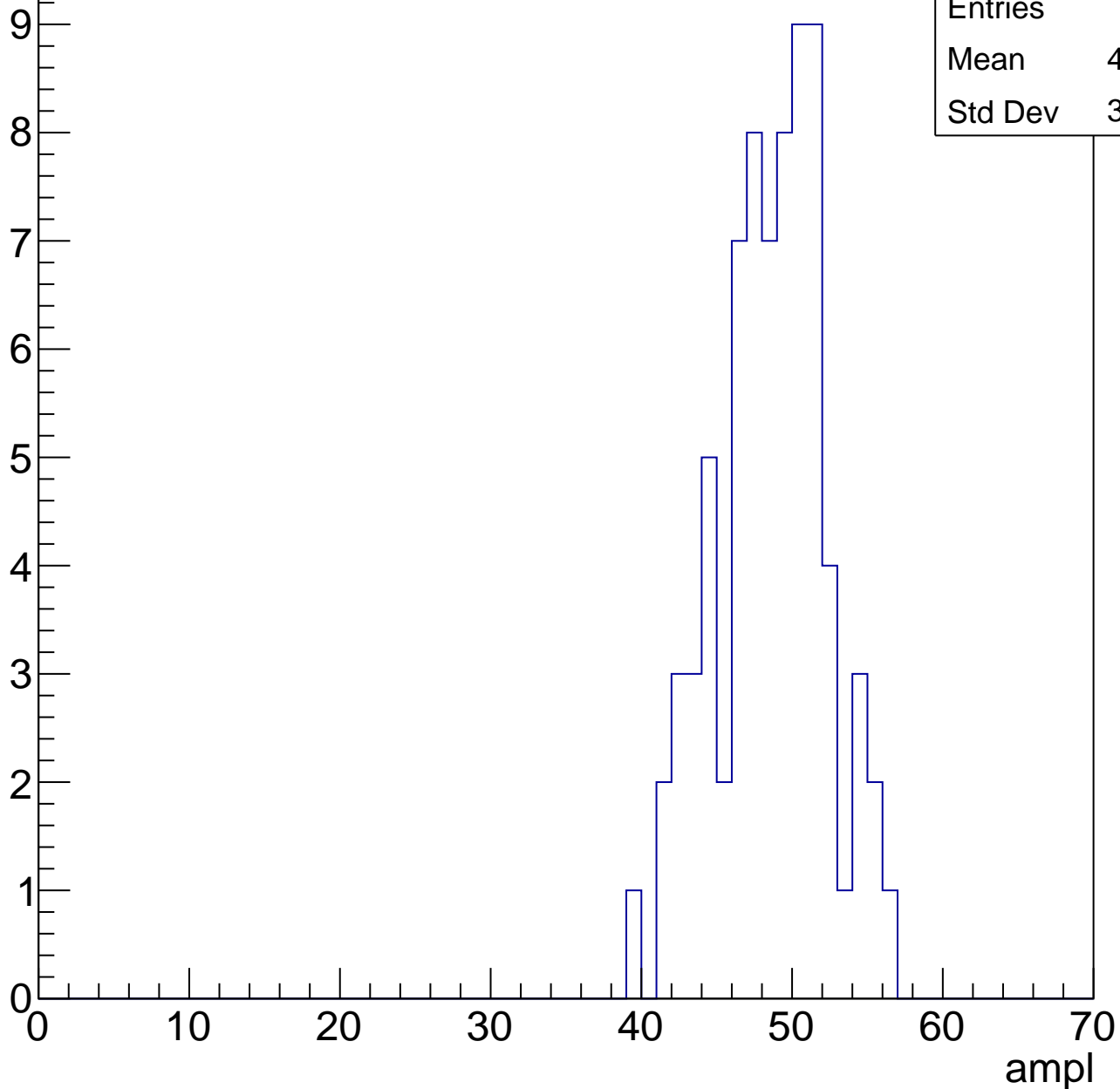
Gaus Width: 3.5500



B1L101S, U21-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

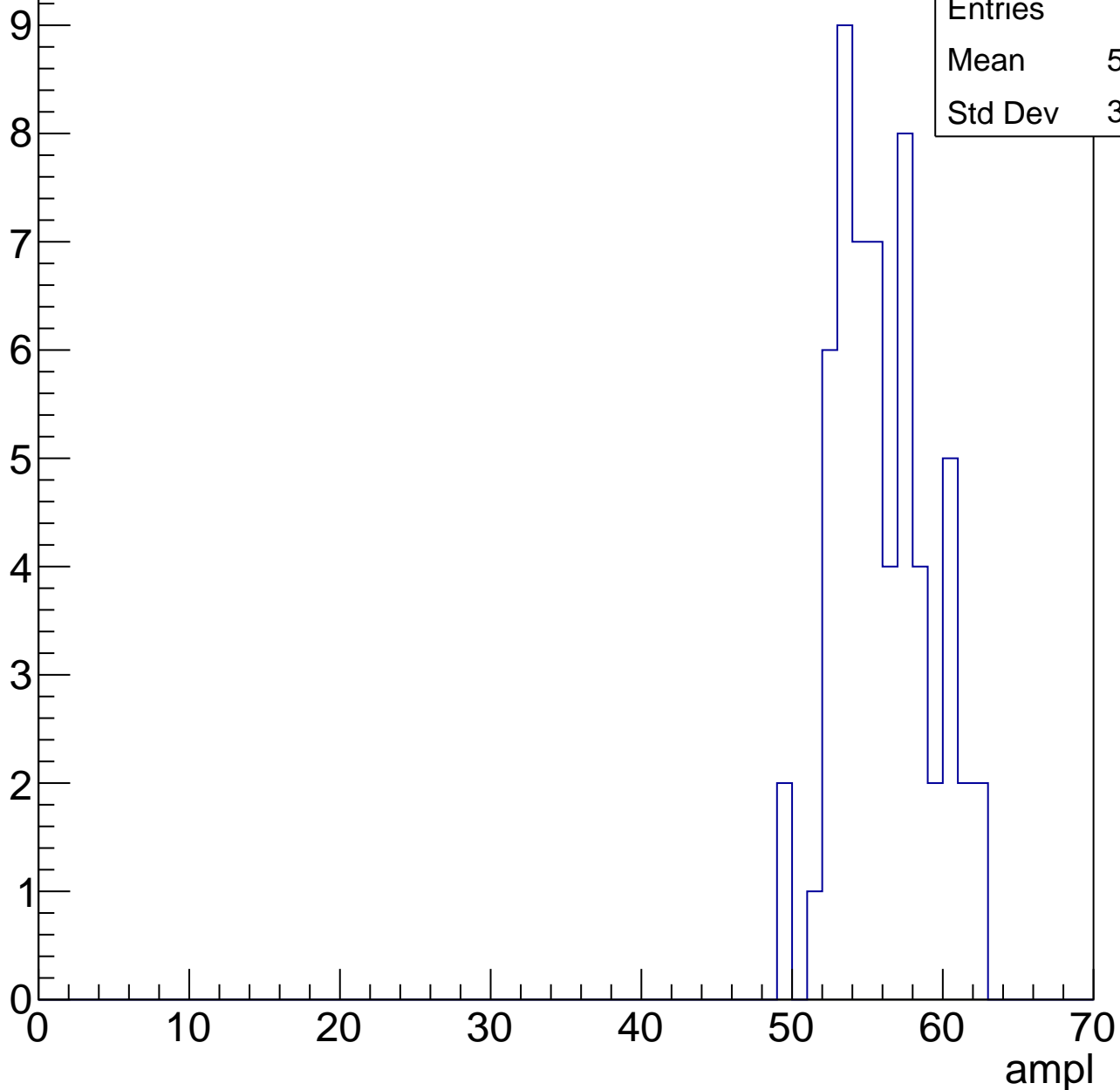
Entry



B1L101S, U21-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	59
Mean	55.54
Std Dev	3.099

B1L101S, U21-ch44, adc5

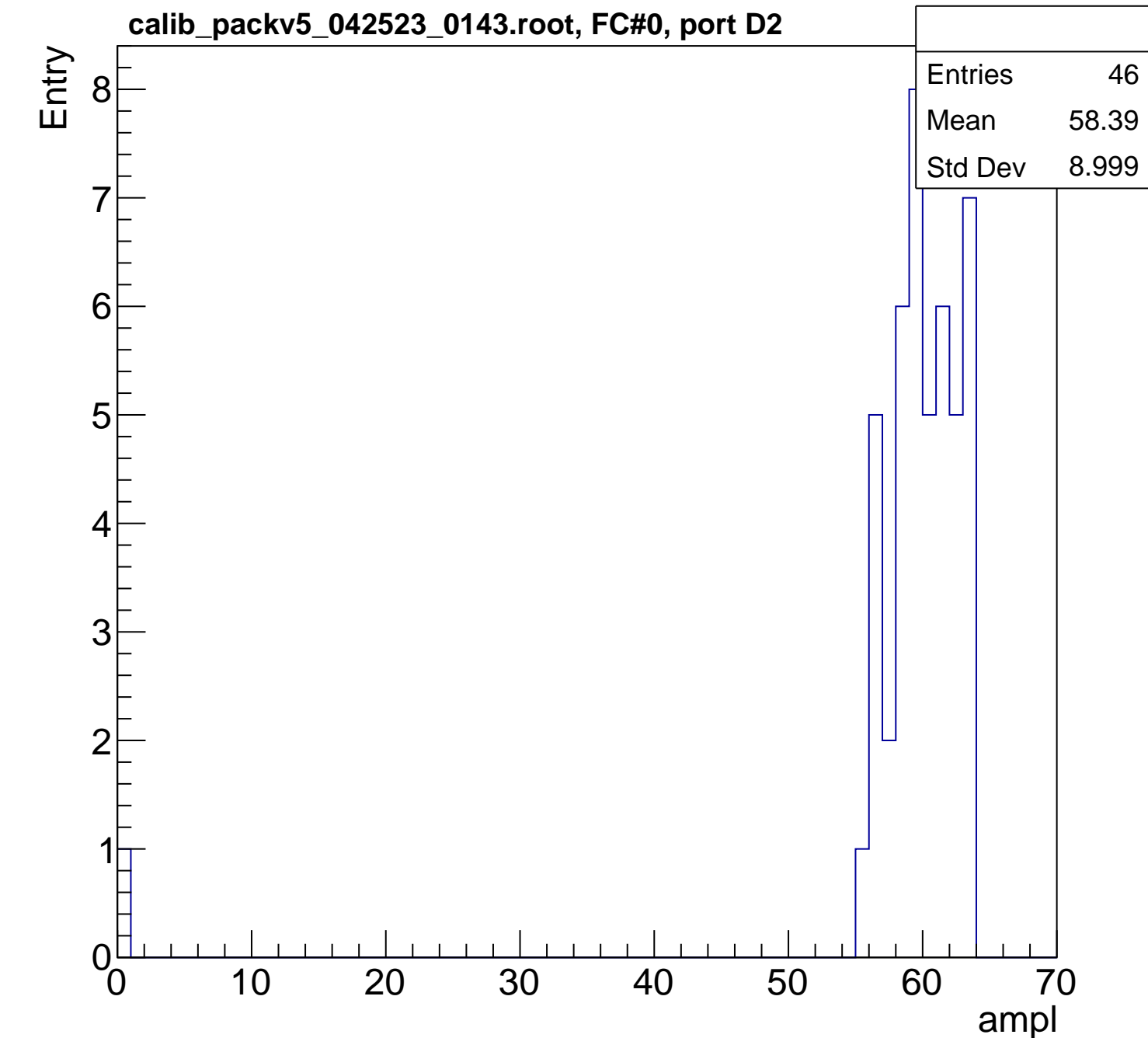
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.39
Std Dev	8.999

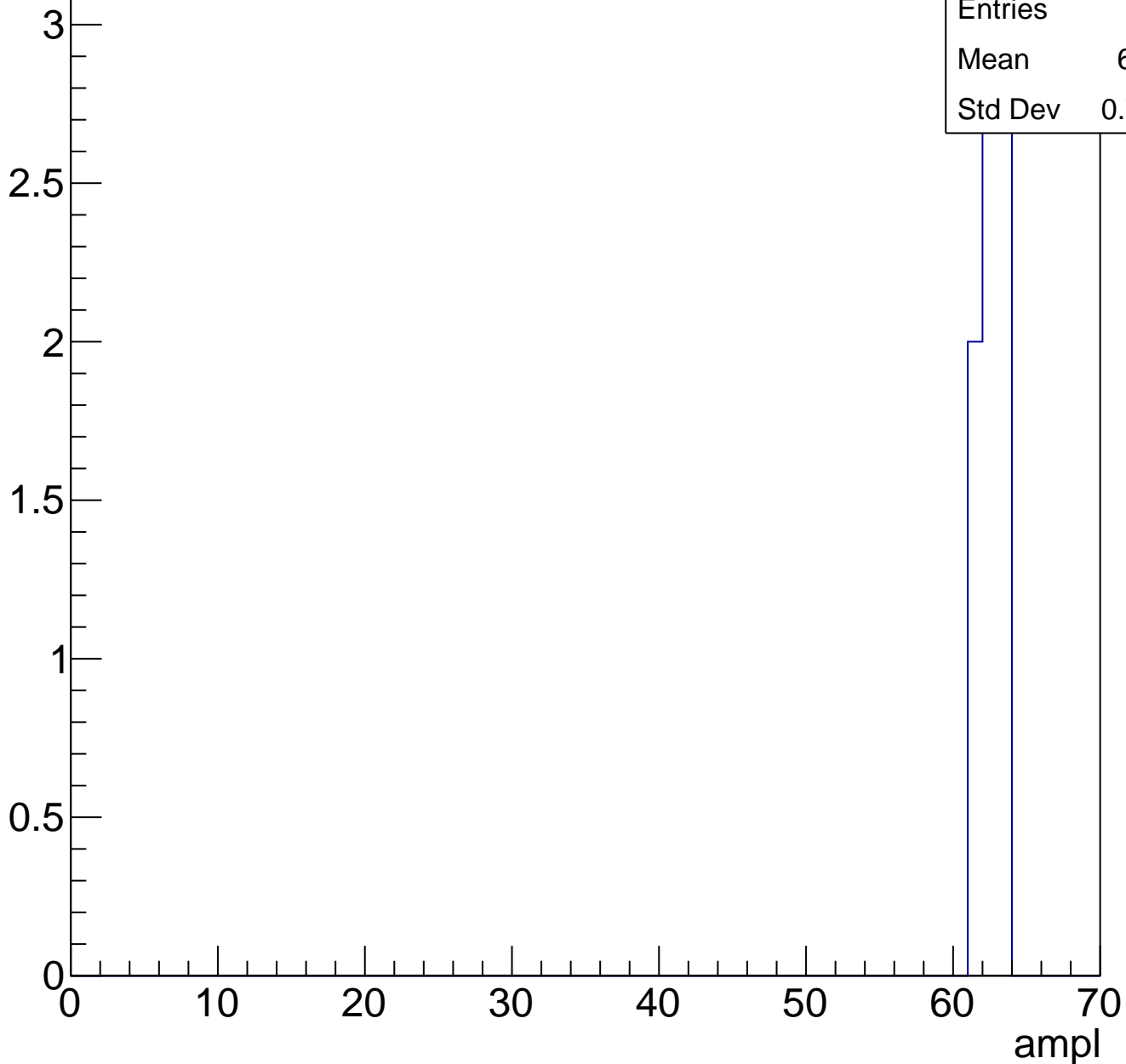
ampl



B1L101S, U21-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch45, adc0

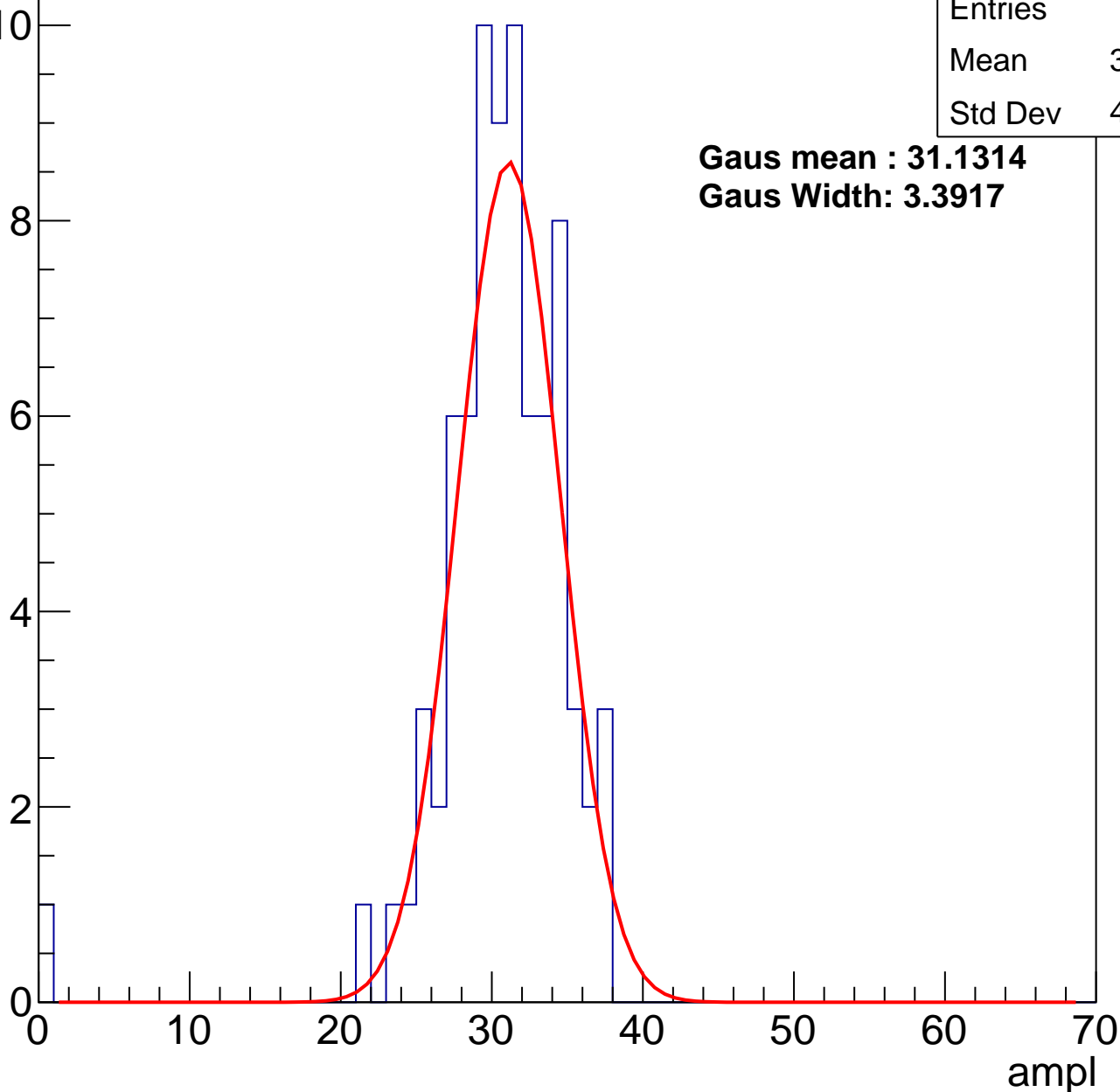
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	30.06
Std Dev	4.765

Gaus mean : 31.1314

Gaus Width: 3.3917



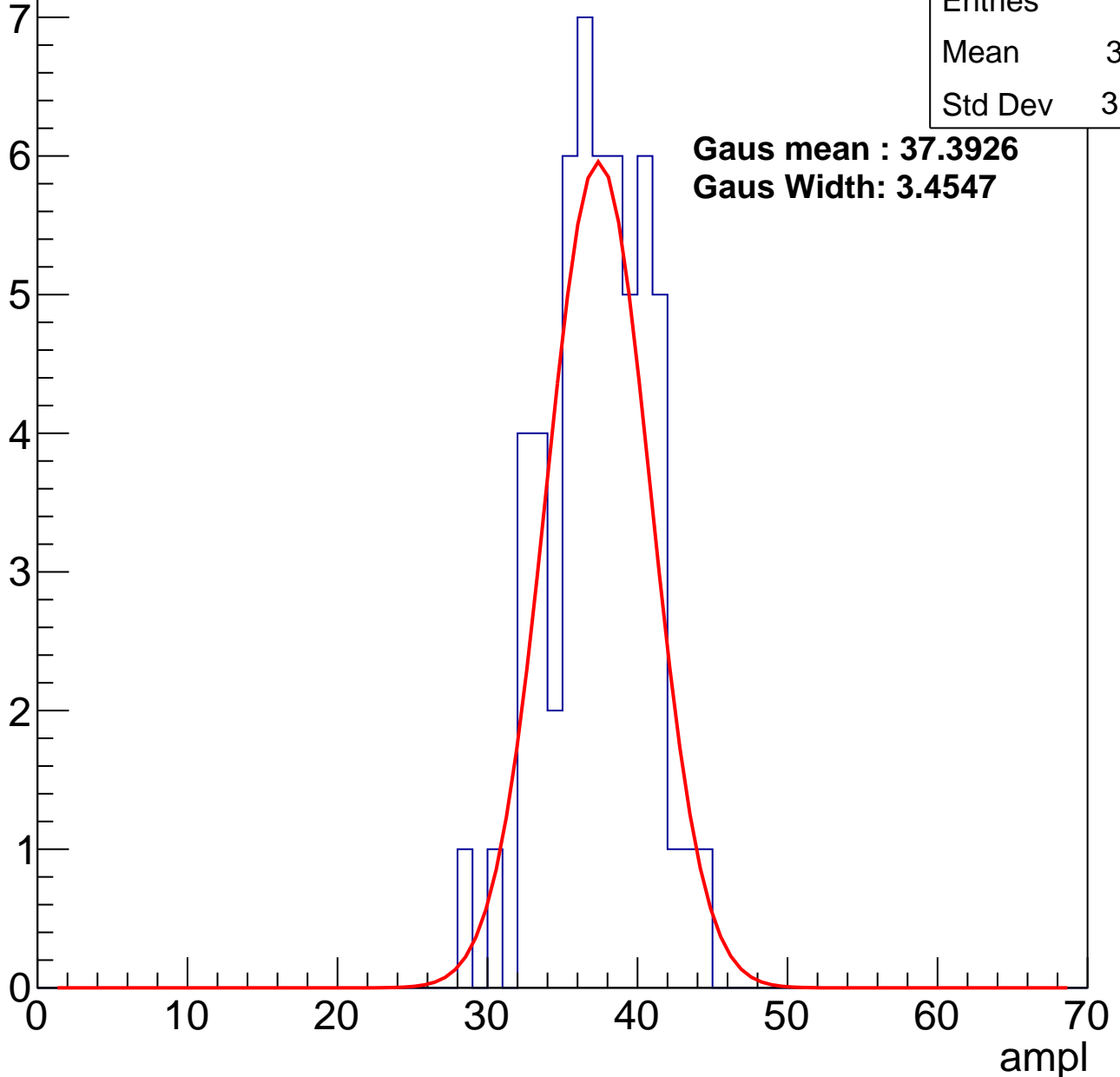
B1L101S, U21-ch45, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	36.91
Std Dev	3.307

Gaus mean : 37.3926
Gaus Width: 3.4547



B1L101S, U21-ch45, adc2

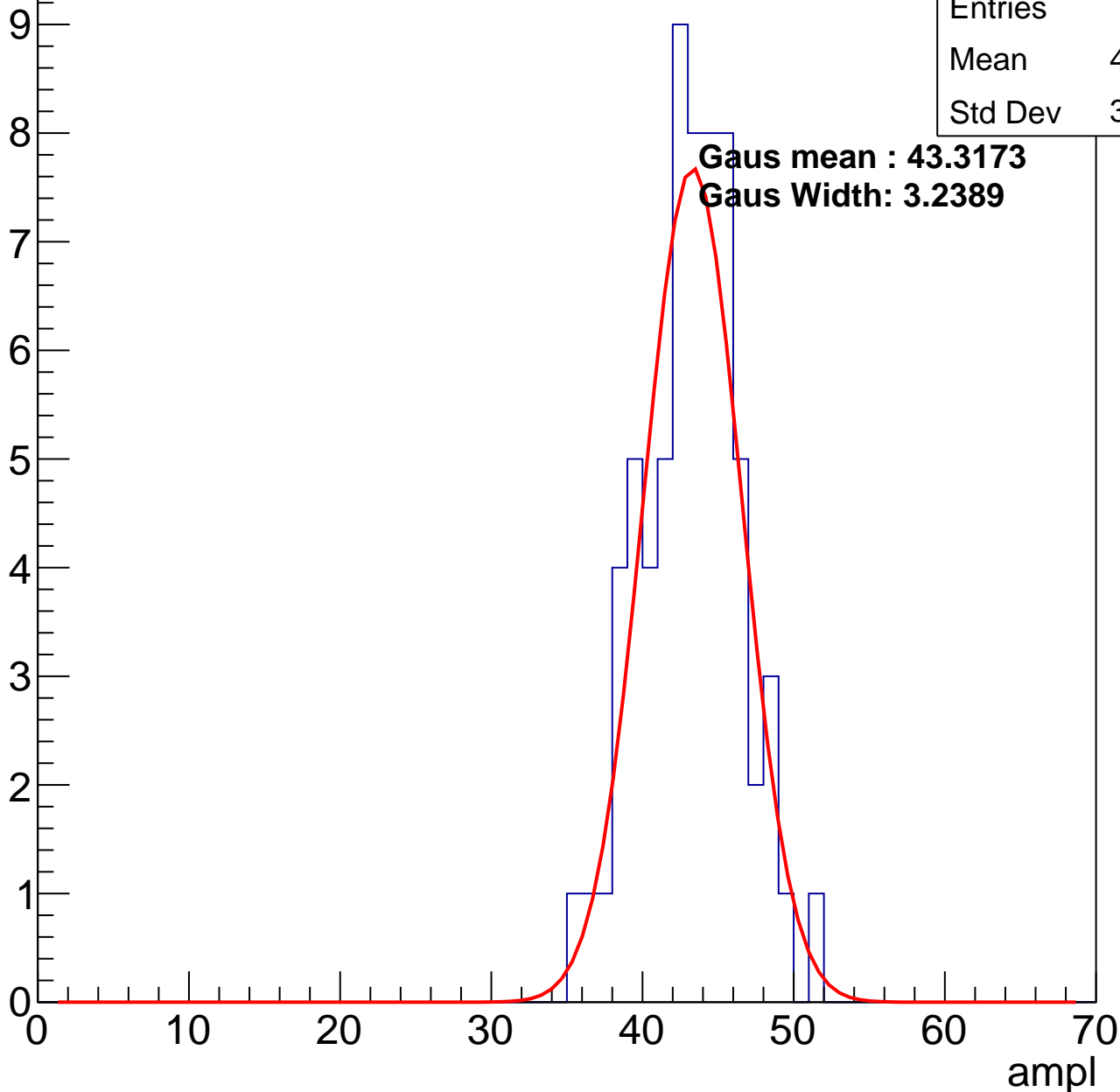
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.76
Std Dev	3.215

Gaus mean : 43.3173

Gaus Width: 3.2389

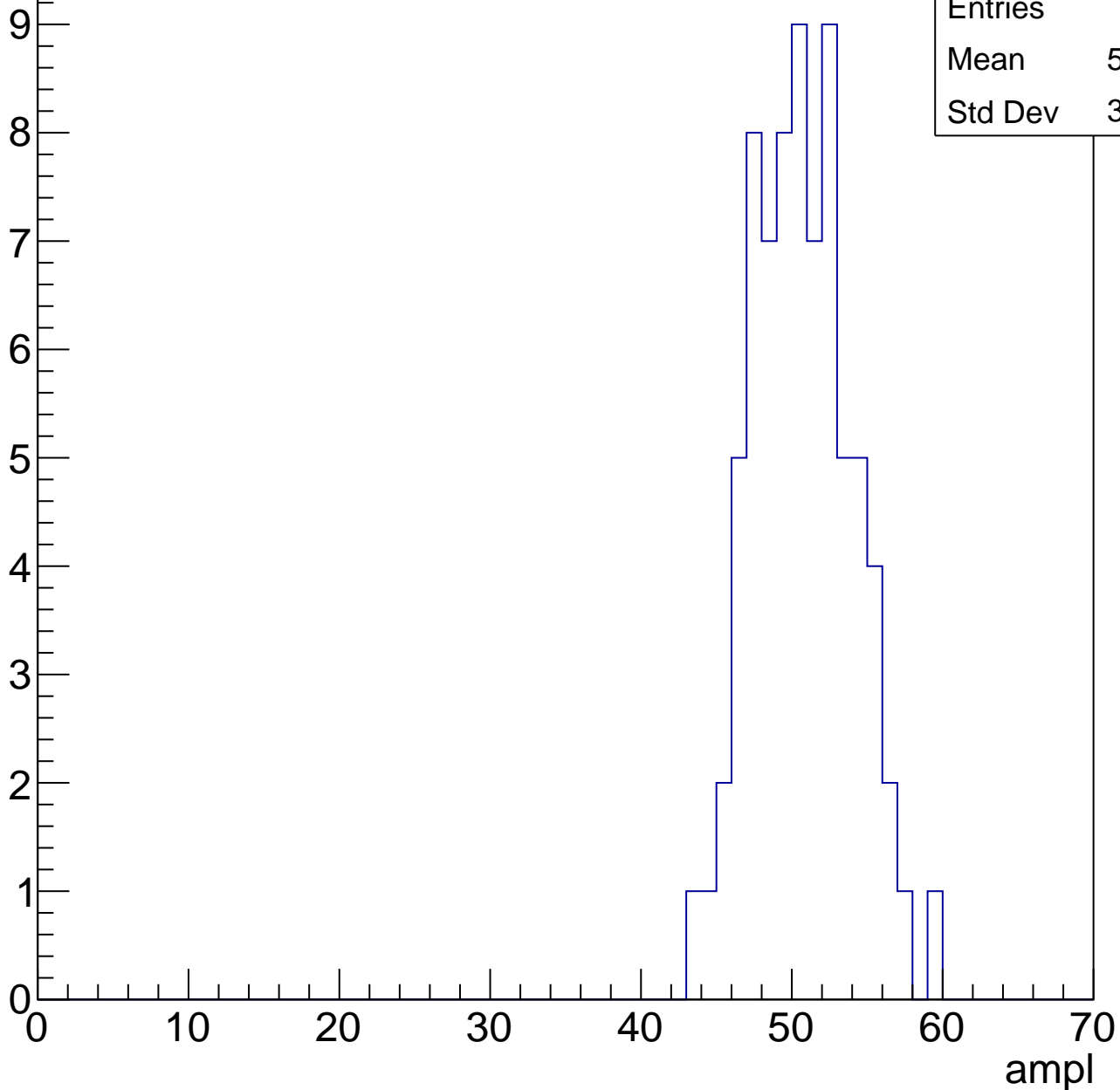


B1L101S, U21-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	50.25
Std Dev	3.242

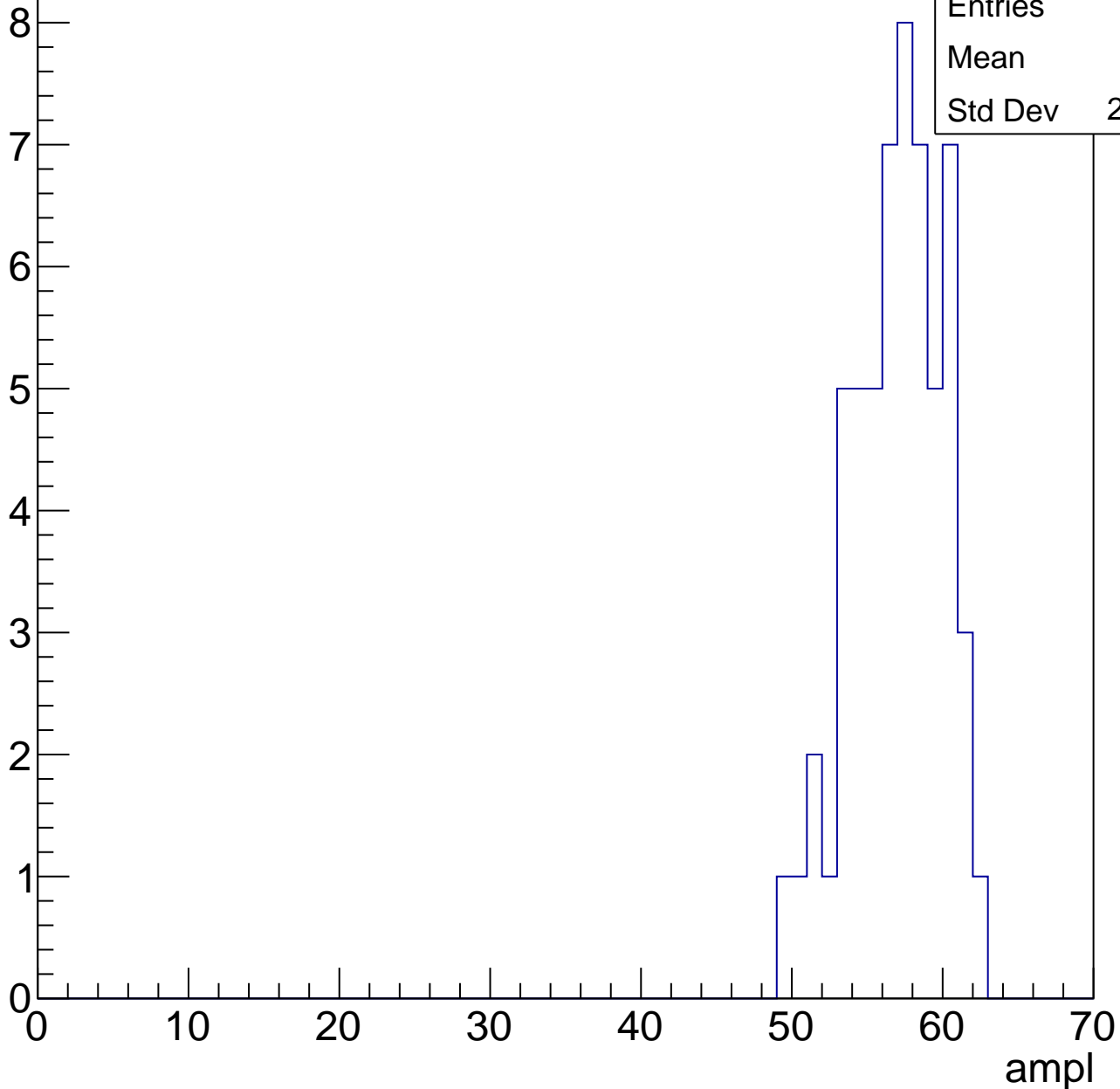


B1L101S, U21-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.5
Std Dev	2.967

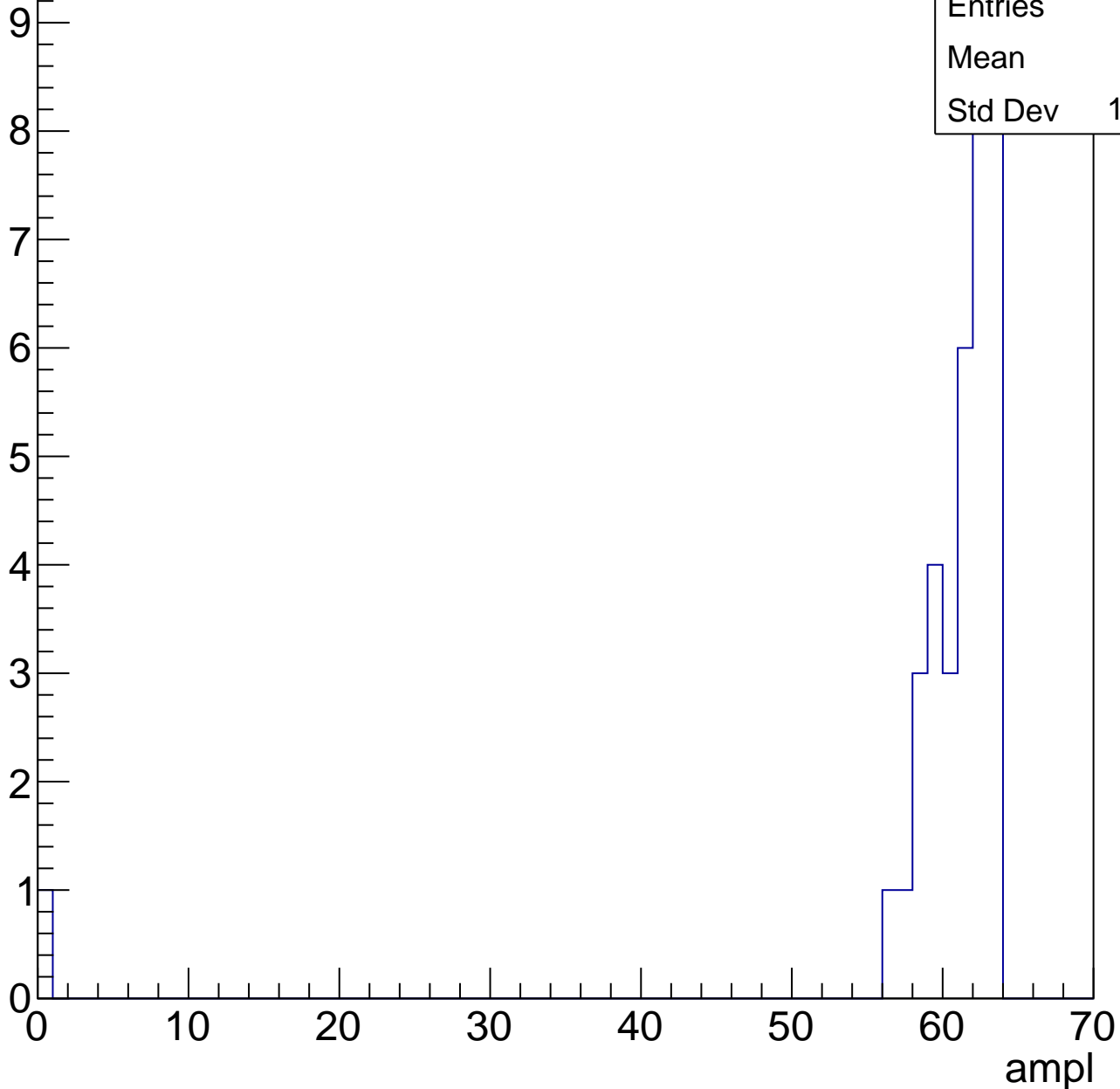


B1L101S, U21-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

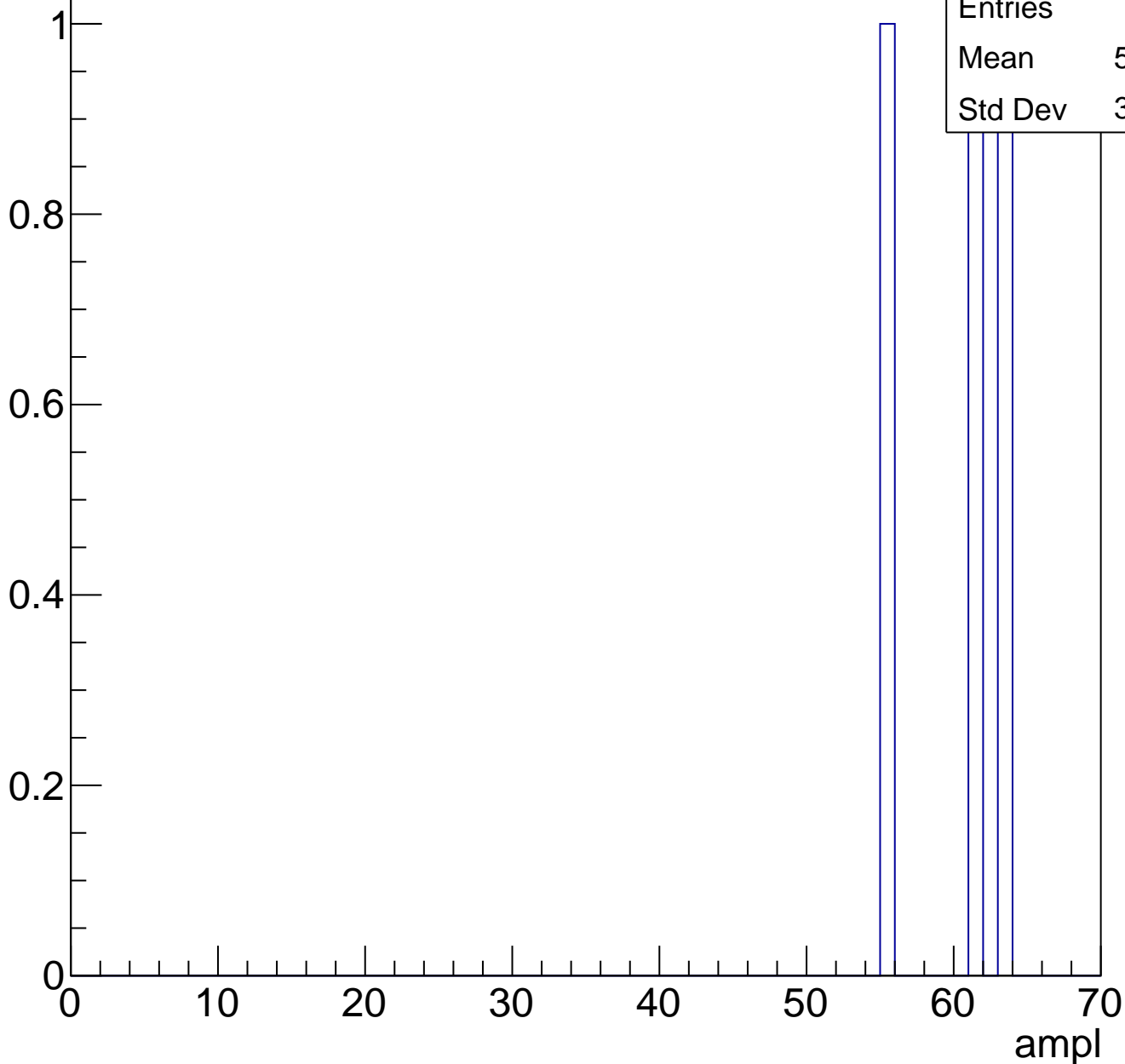
Entries	37
Mean	59.3
Std Dev	10.06



B1L101S, U21-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch46, adc0

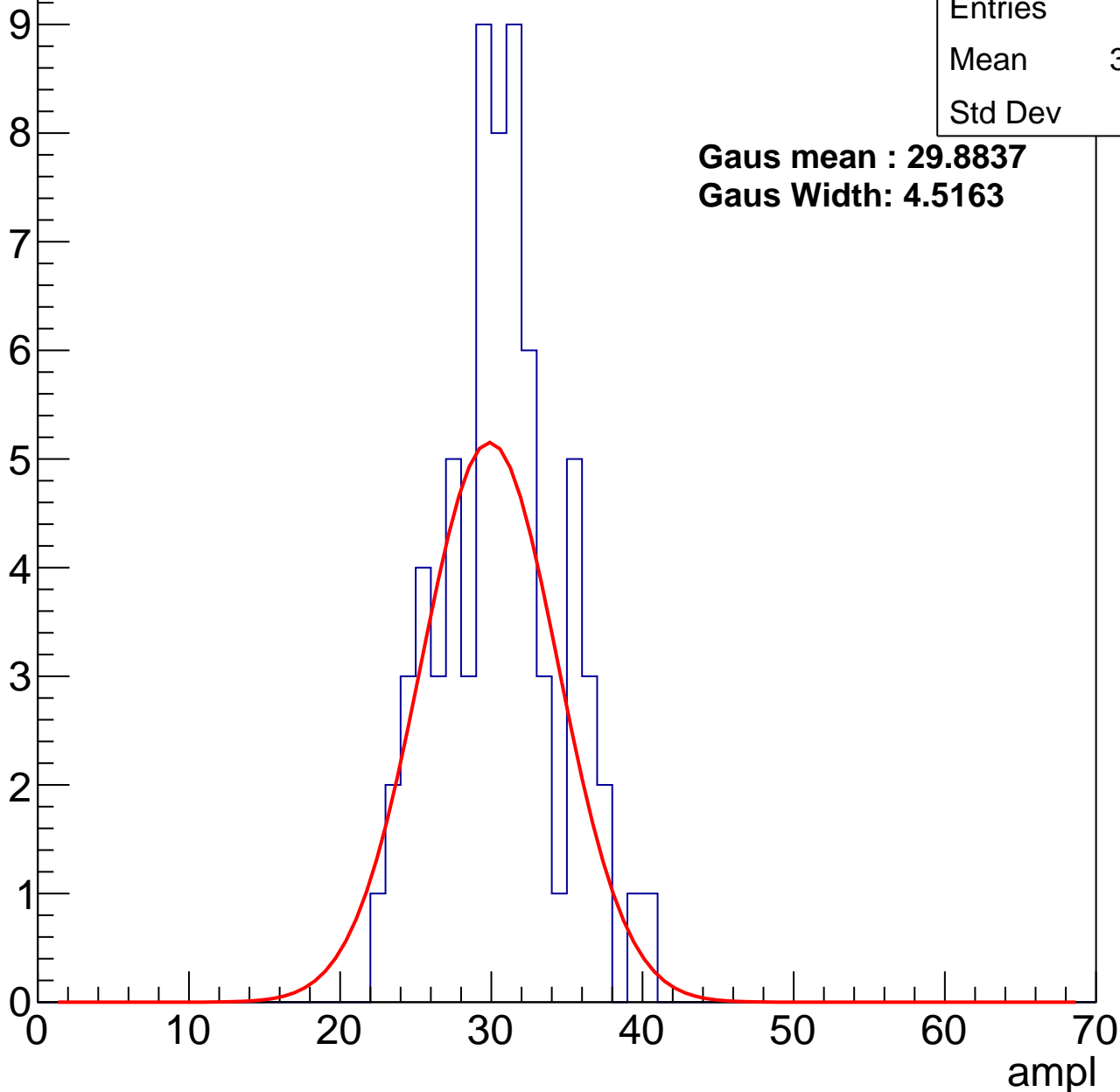
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.12
Std Dev	3.94

Gaus mean : 29.8837

Gaus Width: 4.5163



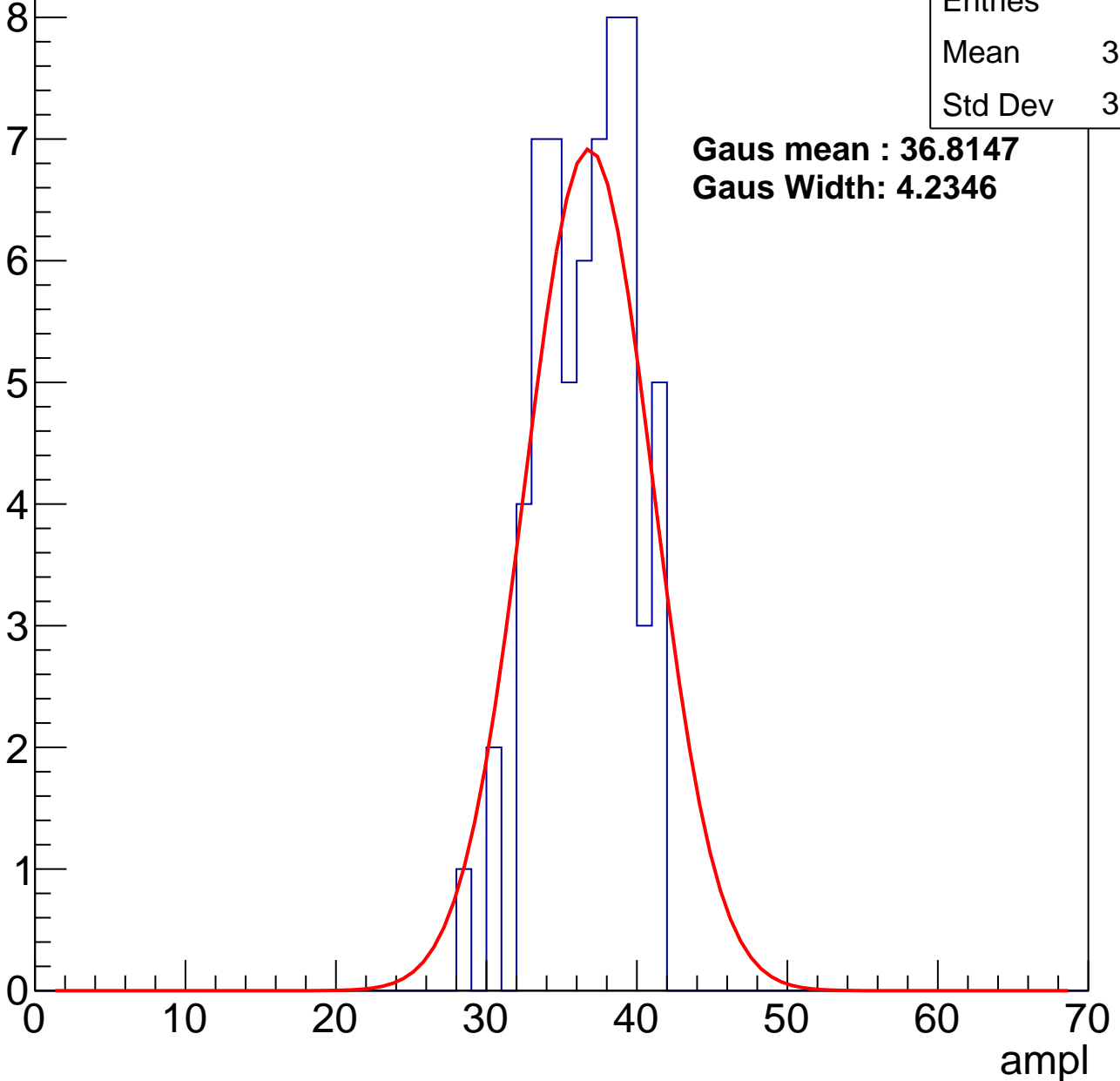
B1L101S, U21-ch46, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.13
Std Dev	3.032

Gaus mean : 36.8147
Gaus Width: 4.2346



B1L101S, U21-ch46, adc2

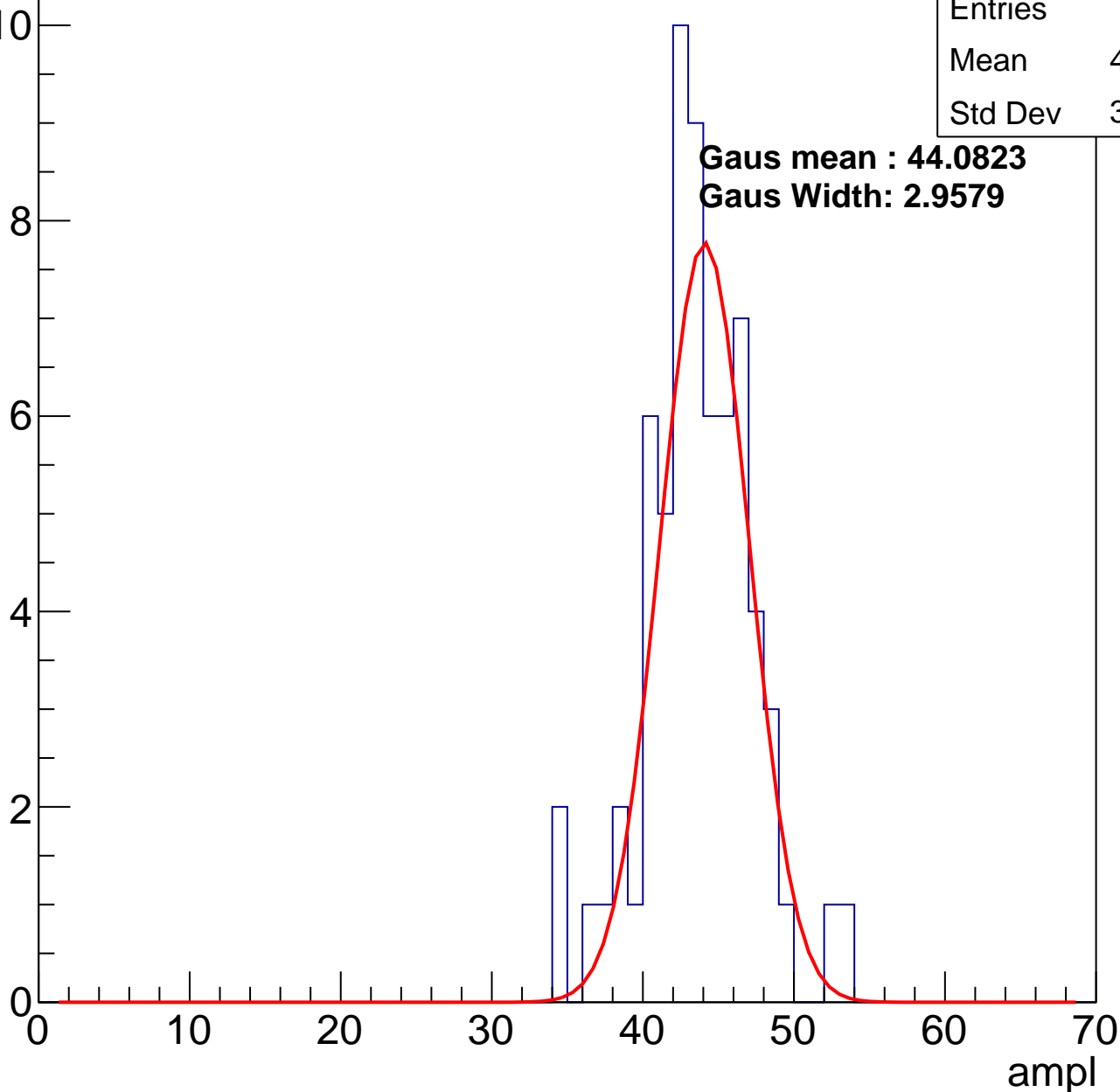
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.18
Std Dev	3.576

Gaus mean : 44.0823

Gaus Width: 2.9579

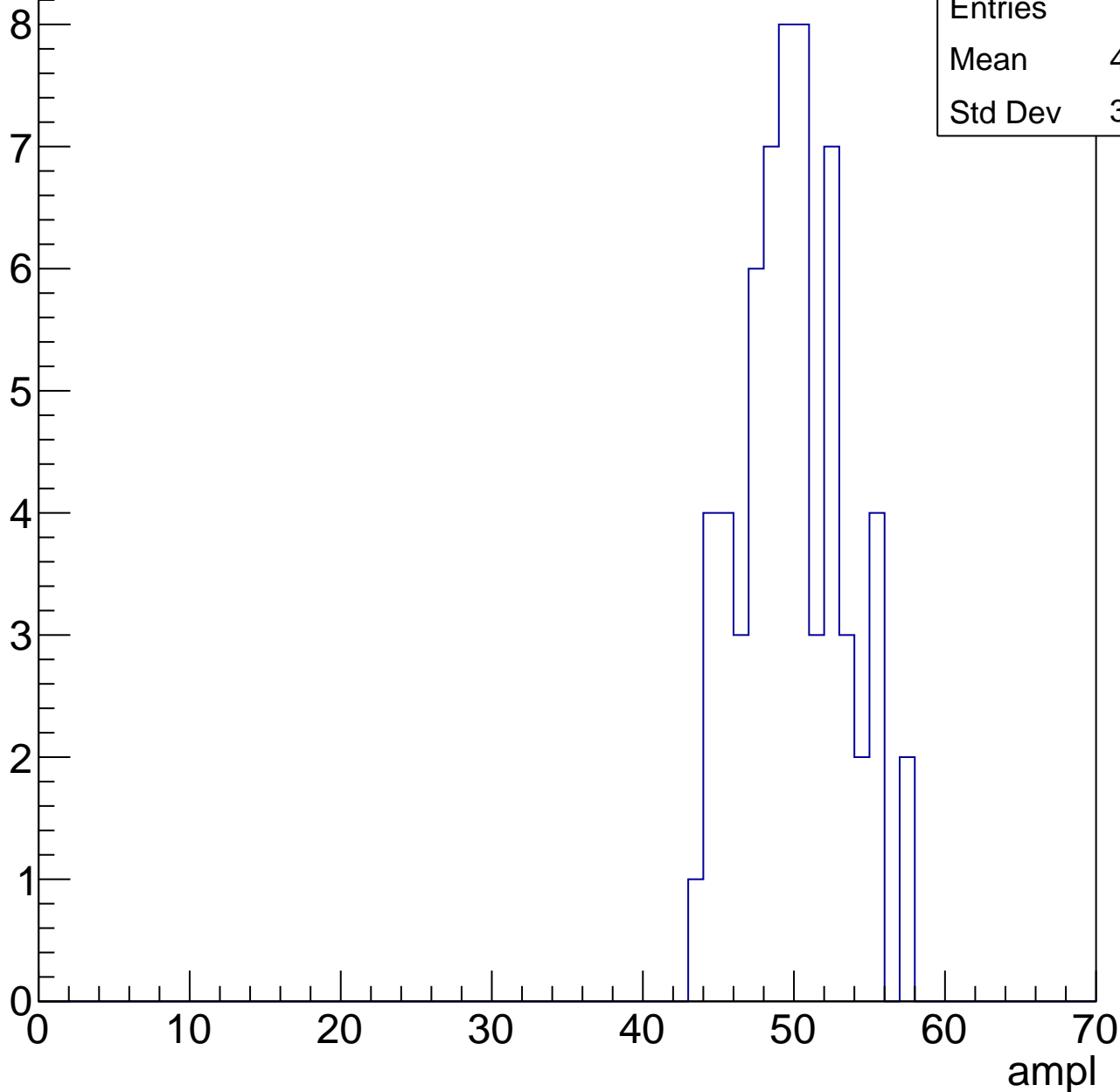


B1L101S, U21-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	49.44
Std Dev	3.358

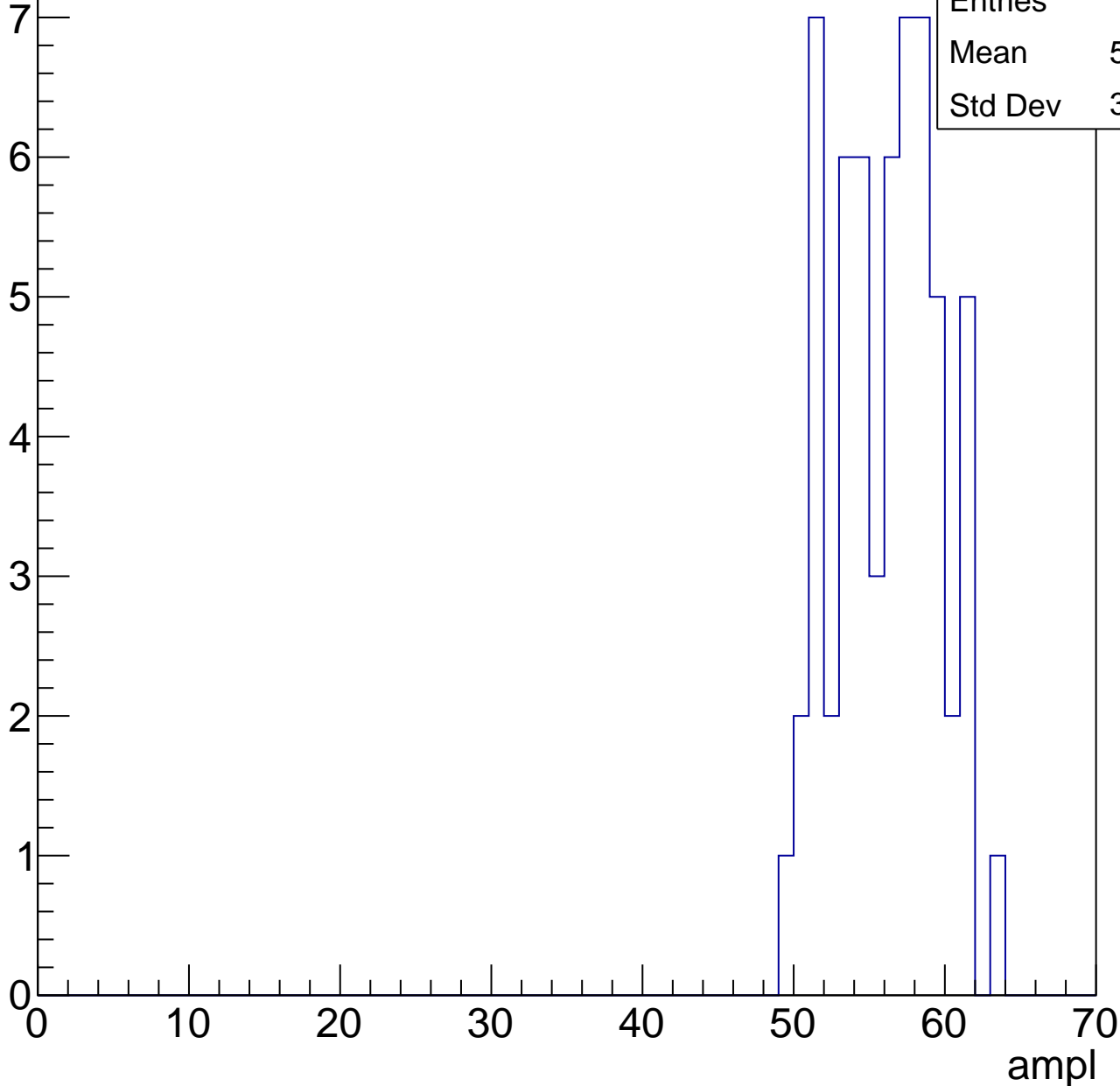


B1L101S, U21-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.68
Std Dev	3.389

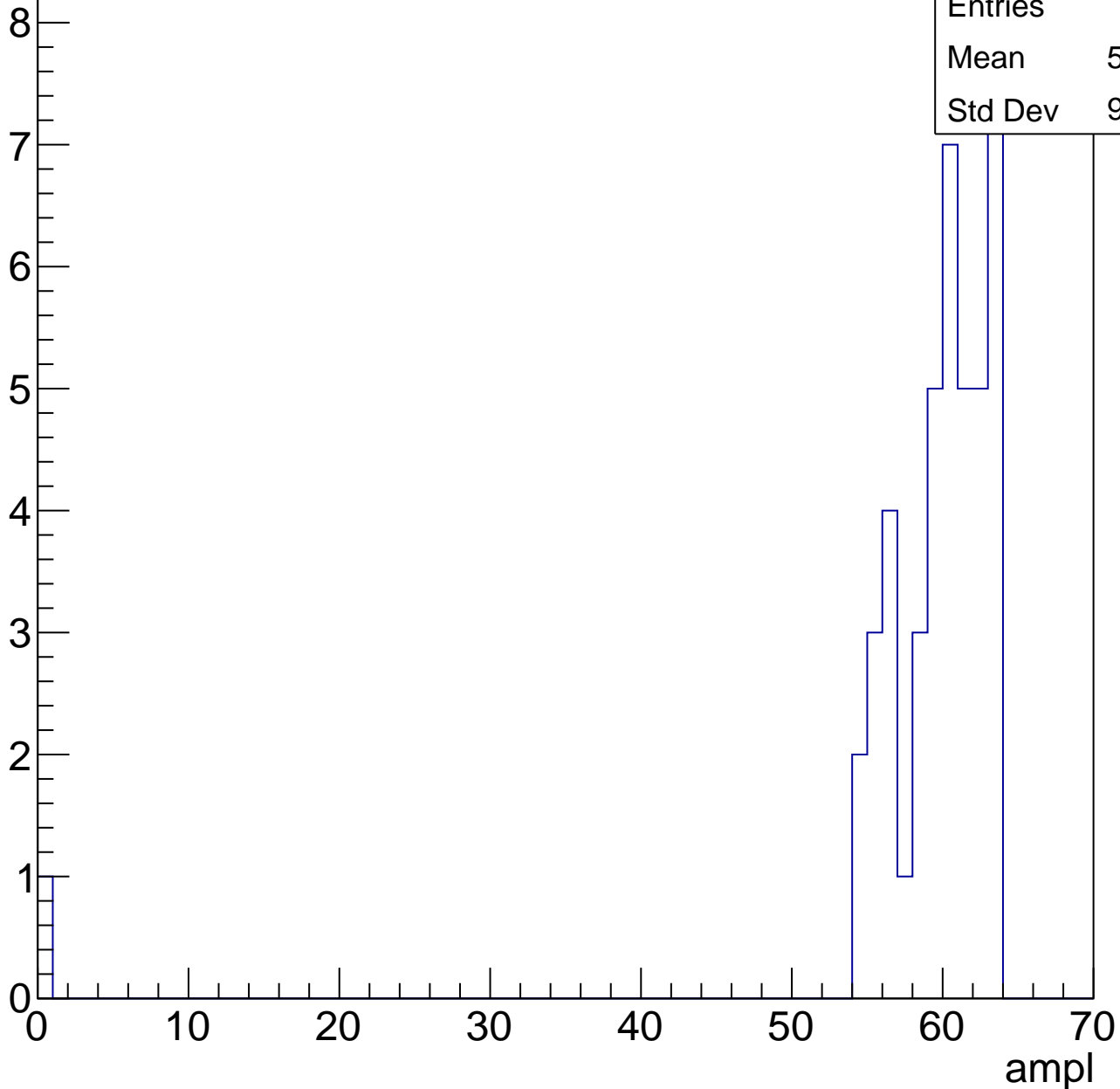


B1L101S, U21-ch46, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.23
Std Dev	9.288

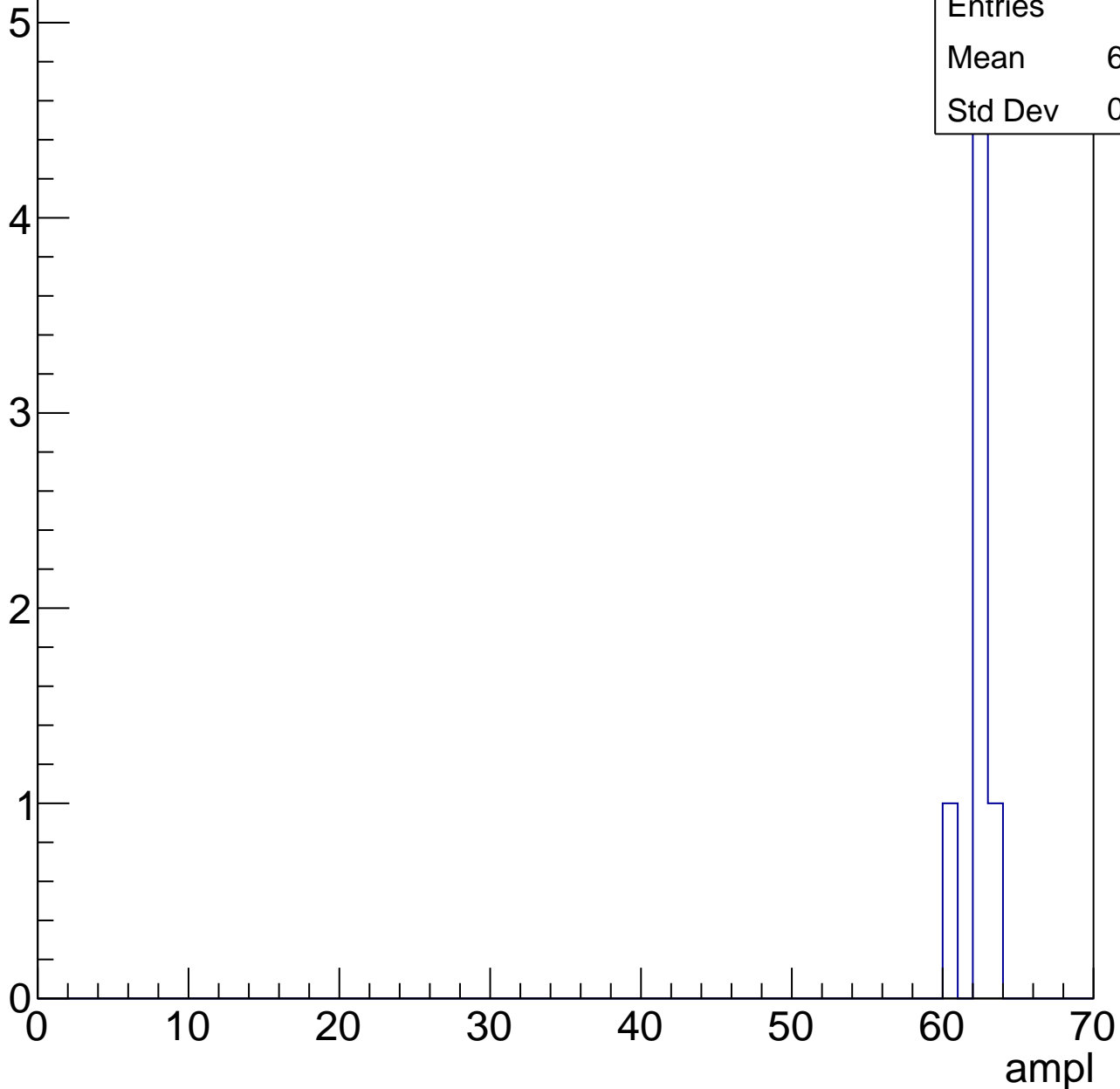


B1L101S, U21-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	7
Mean	61.86
Std Dev	0.833



B1L101S, U21-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch47, adc0

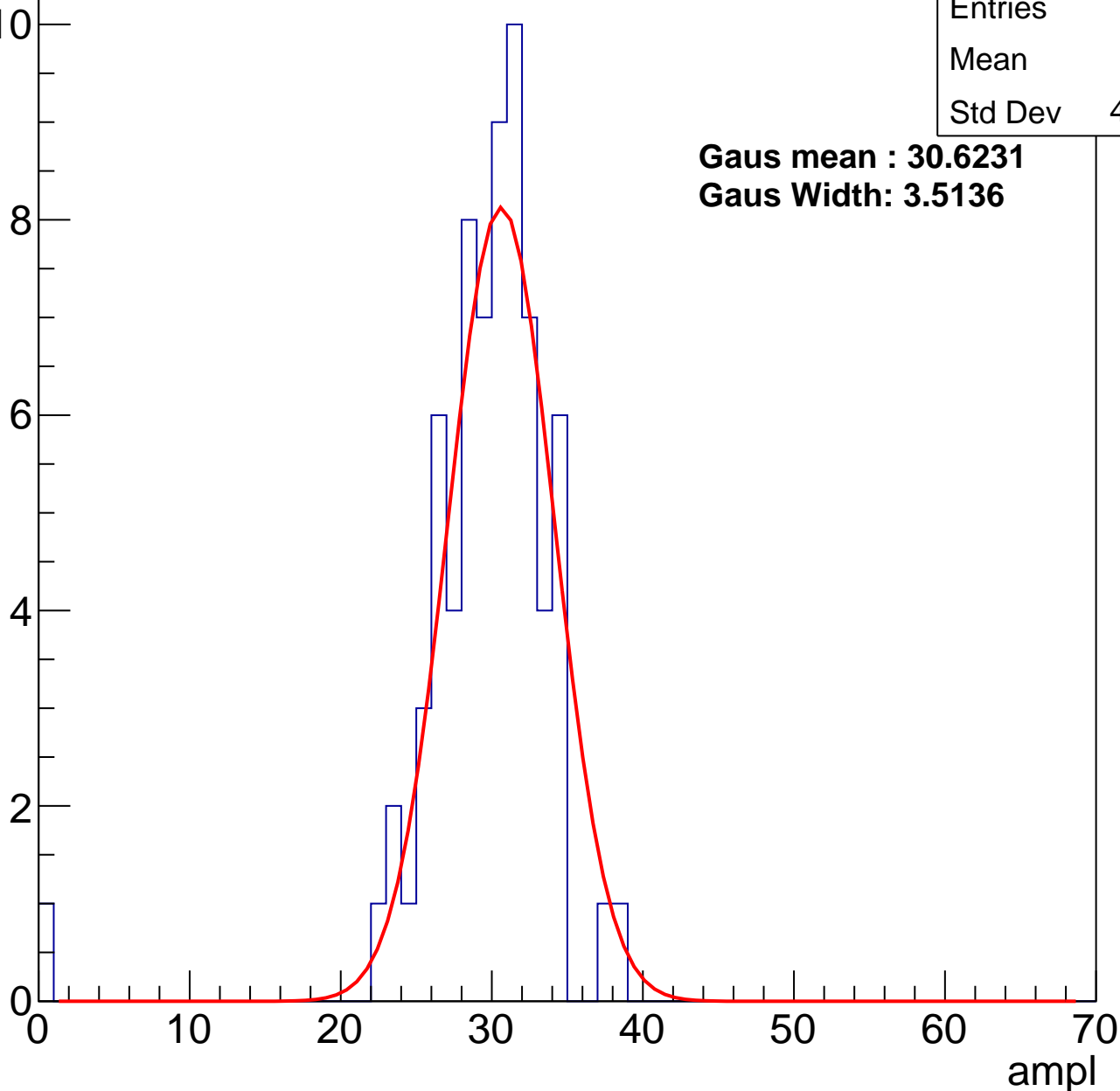
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.2
Std Dev	4.719

Gaus mean : 30.6231

Gaus Width: 3.5136



B1L101S, U21-ch47, adc1

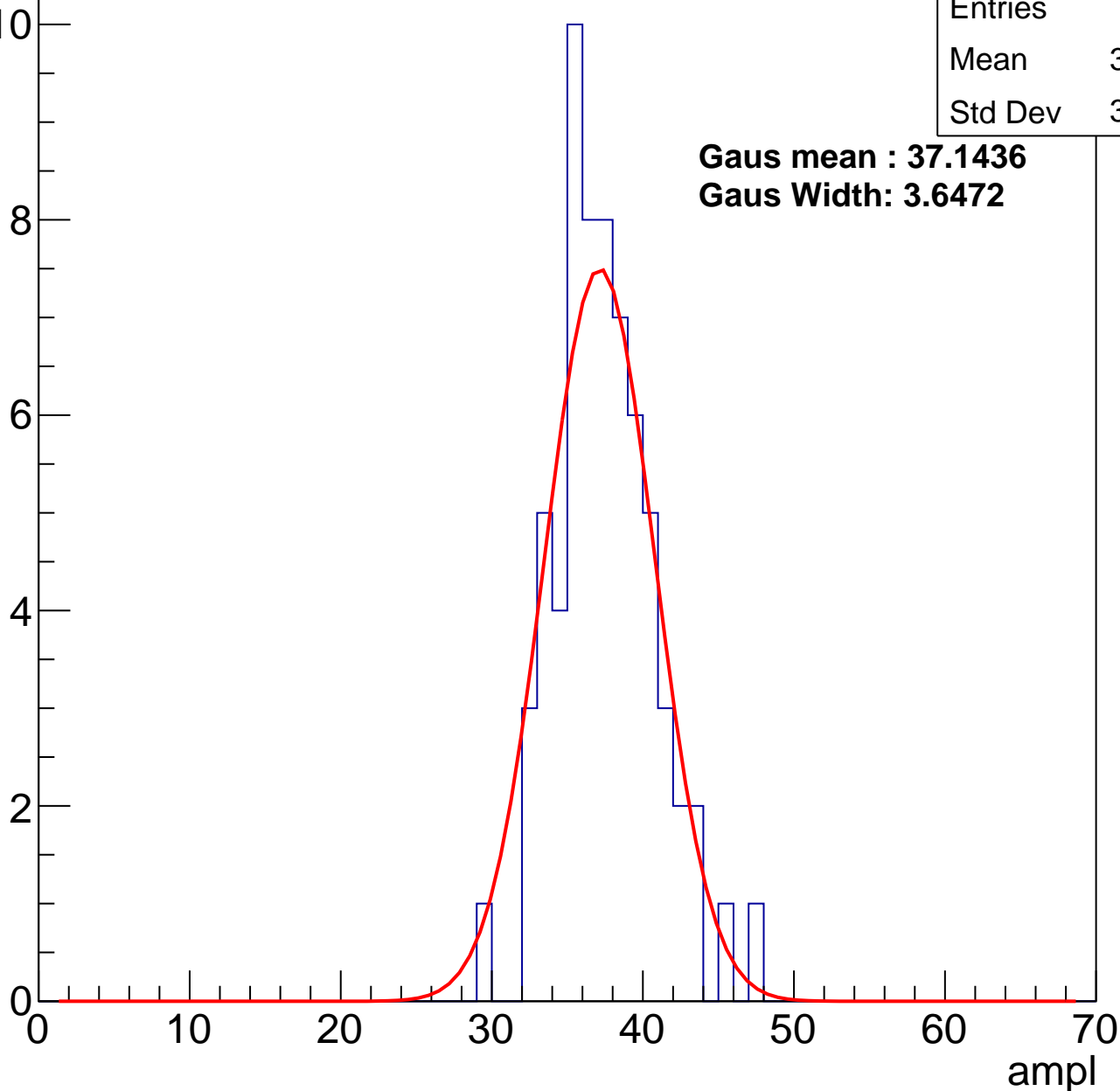
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.05
Std Dev	3.286

Gaus mean : 37.1436

Gaus Width: 3.6472



B1L101S, U21-ch47, adc2

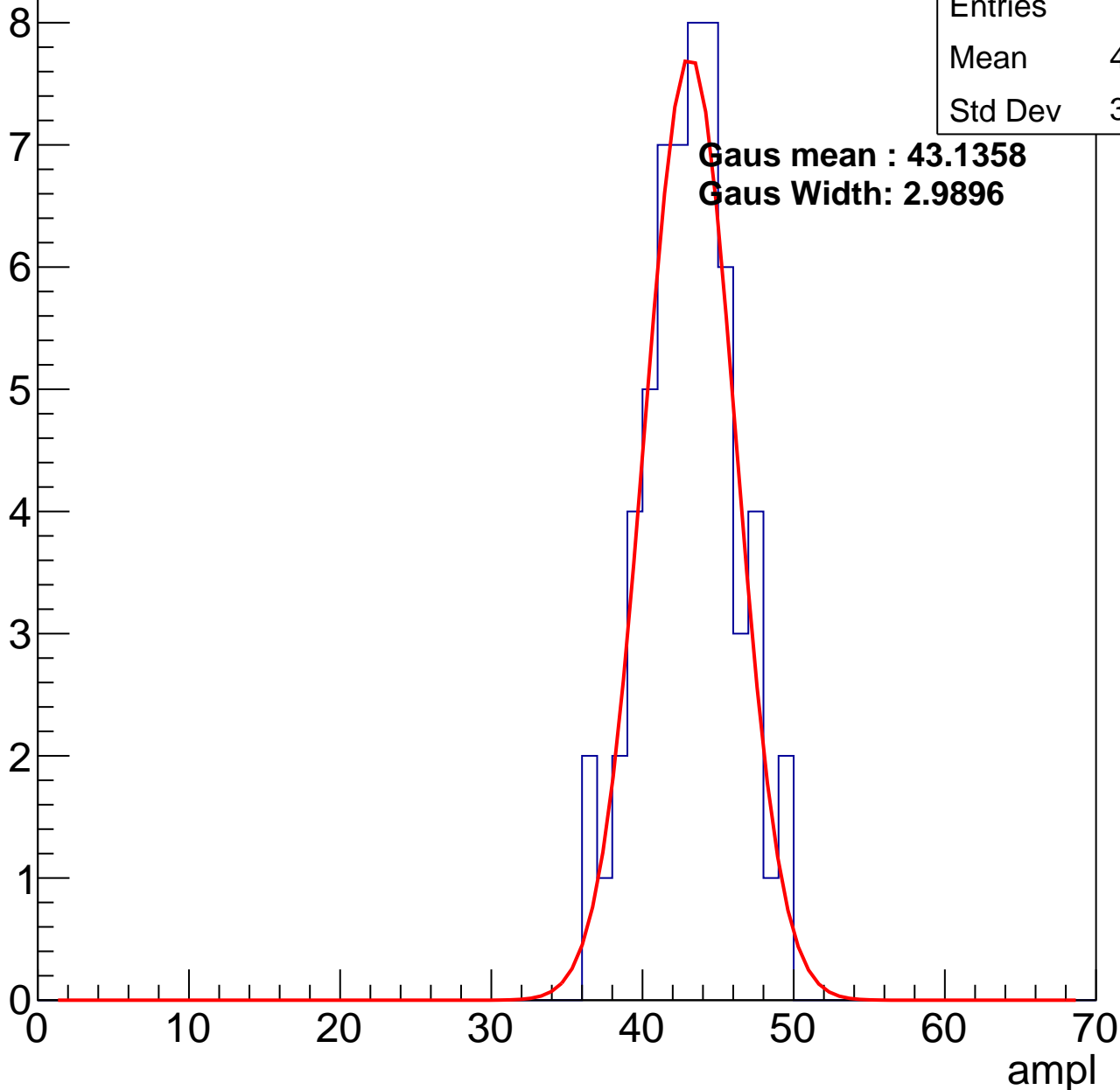
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.67
Std Dev	3.004

Gaus mean : 43.1358

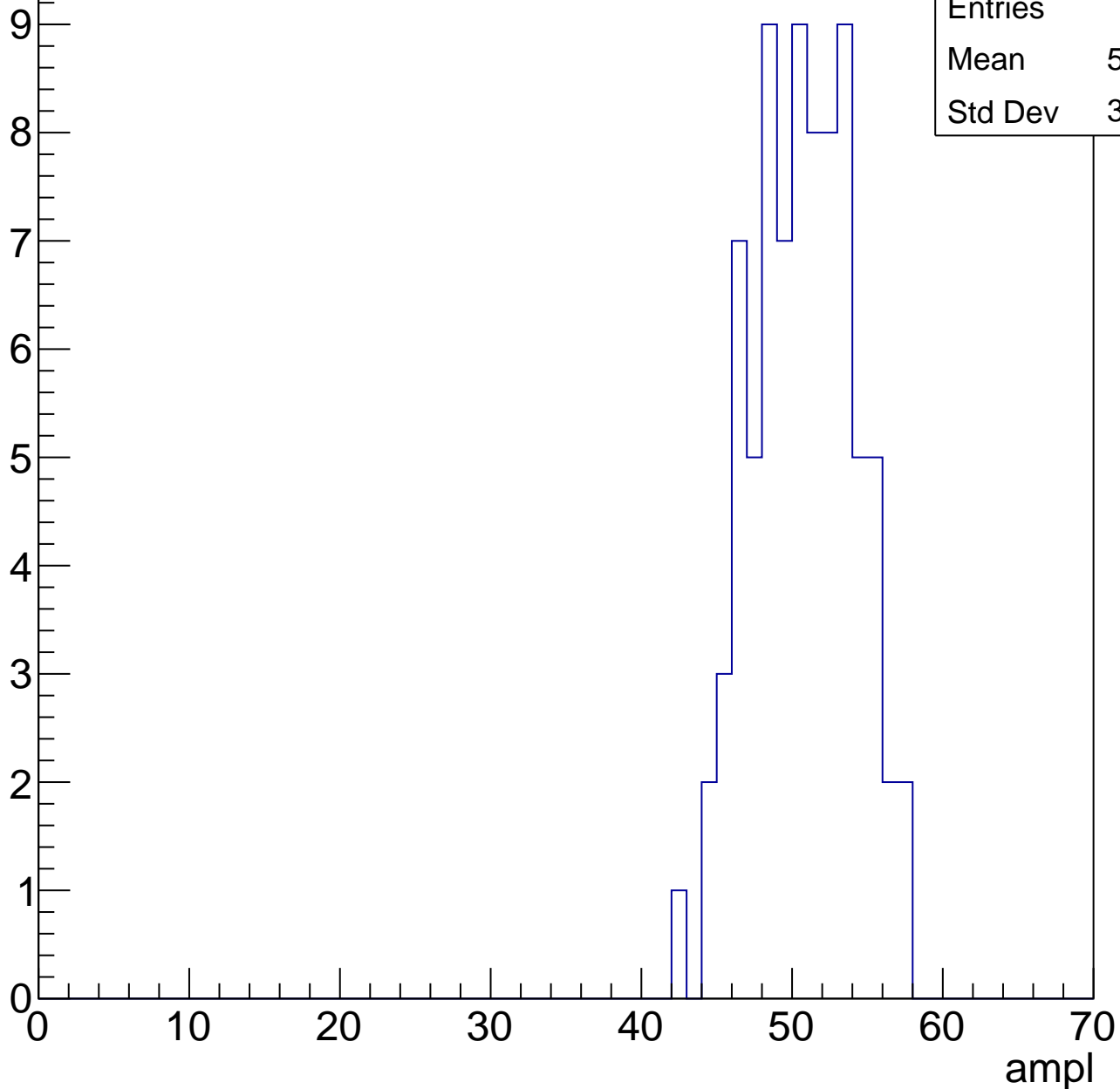
Gaus Width: 2.9896



B1L101S, U21-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



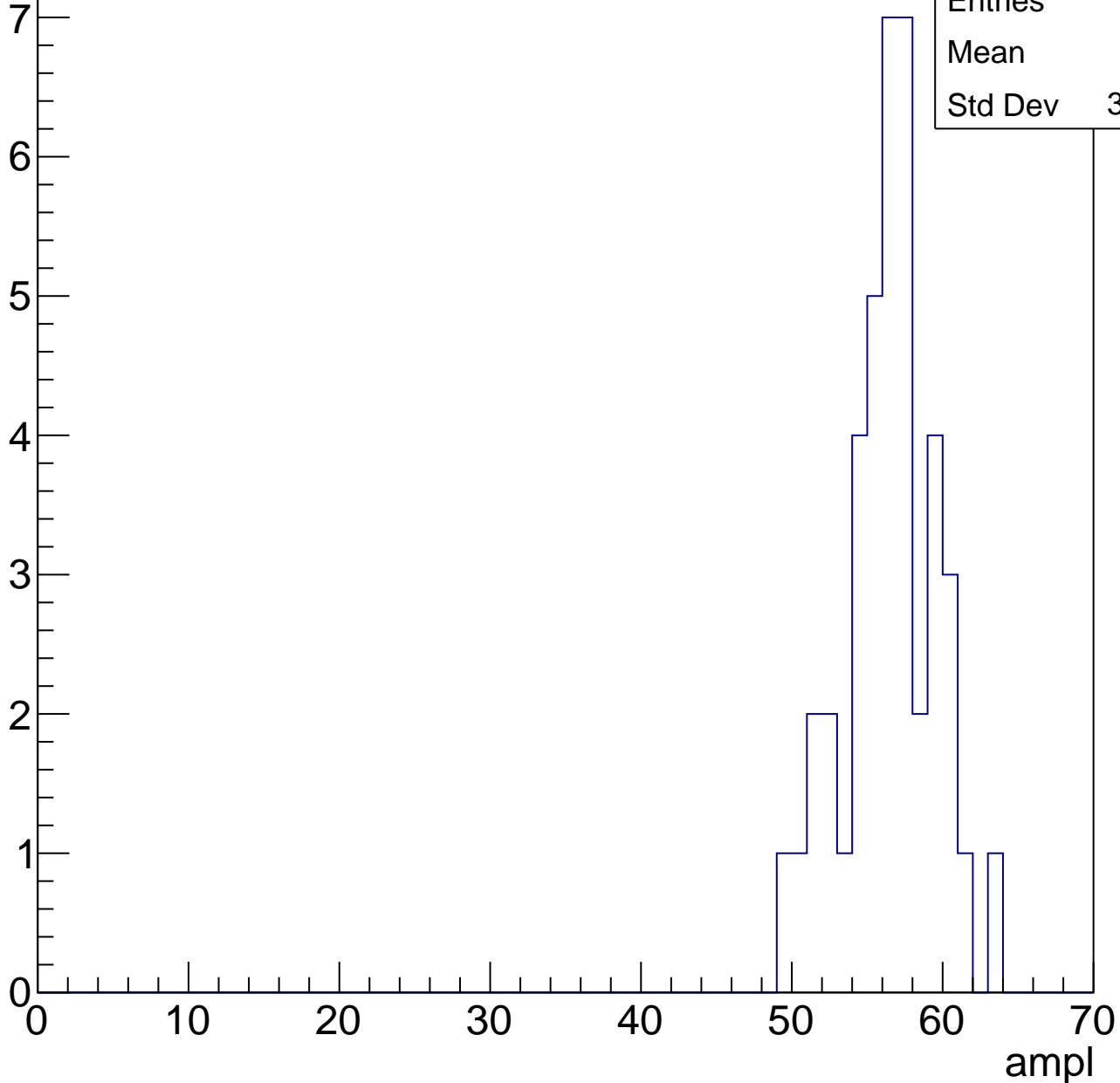
Entries	82
Mean	50.23
Std Dev	3.318

B1L101S, U21-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	56
Std Dev	3.004

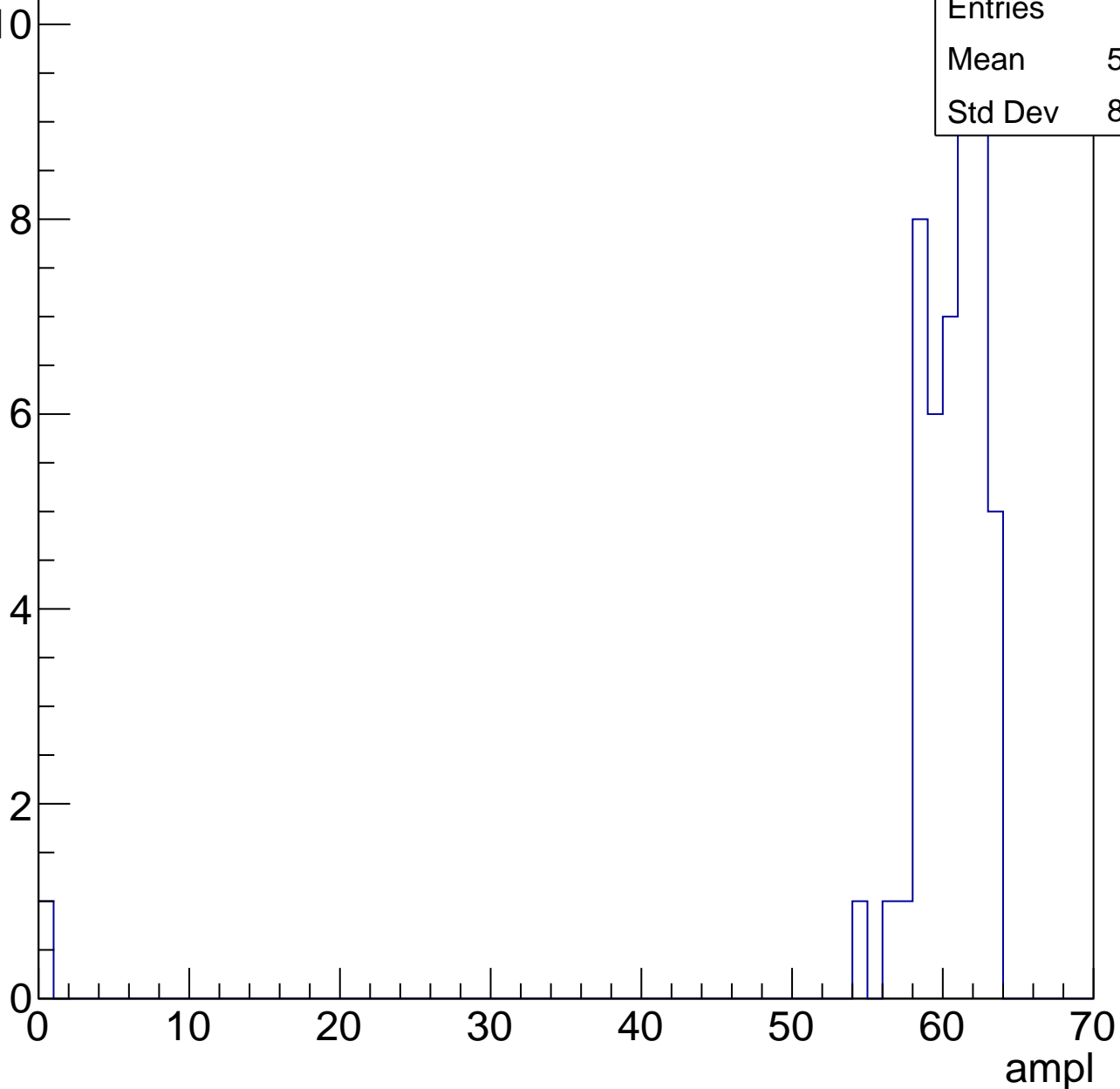


B1L101S, U21-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	58.96
Std Dev	8.736



B1L101S, U21-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch48, adc0

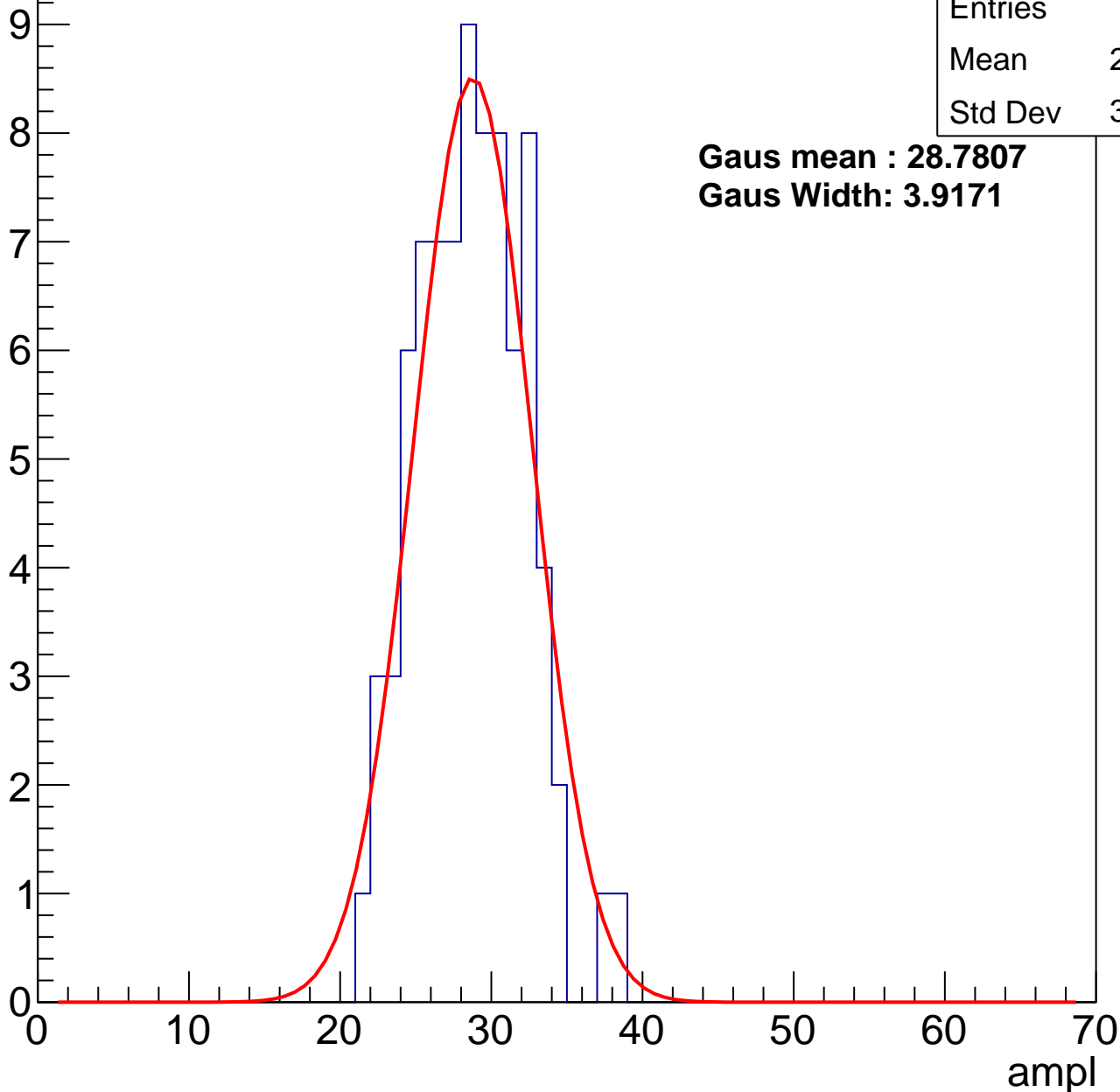
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.23
Std Dev	3.508

Gaus mean : 28.7807

Gaus Width: 3.9171



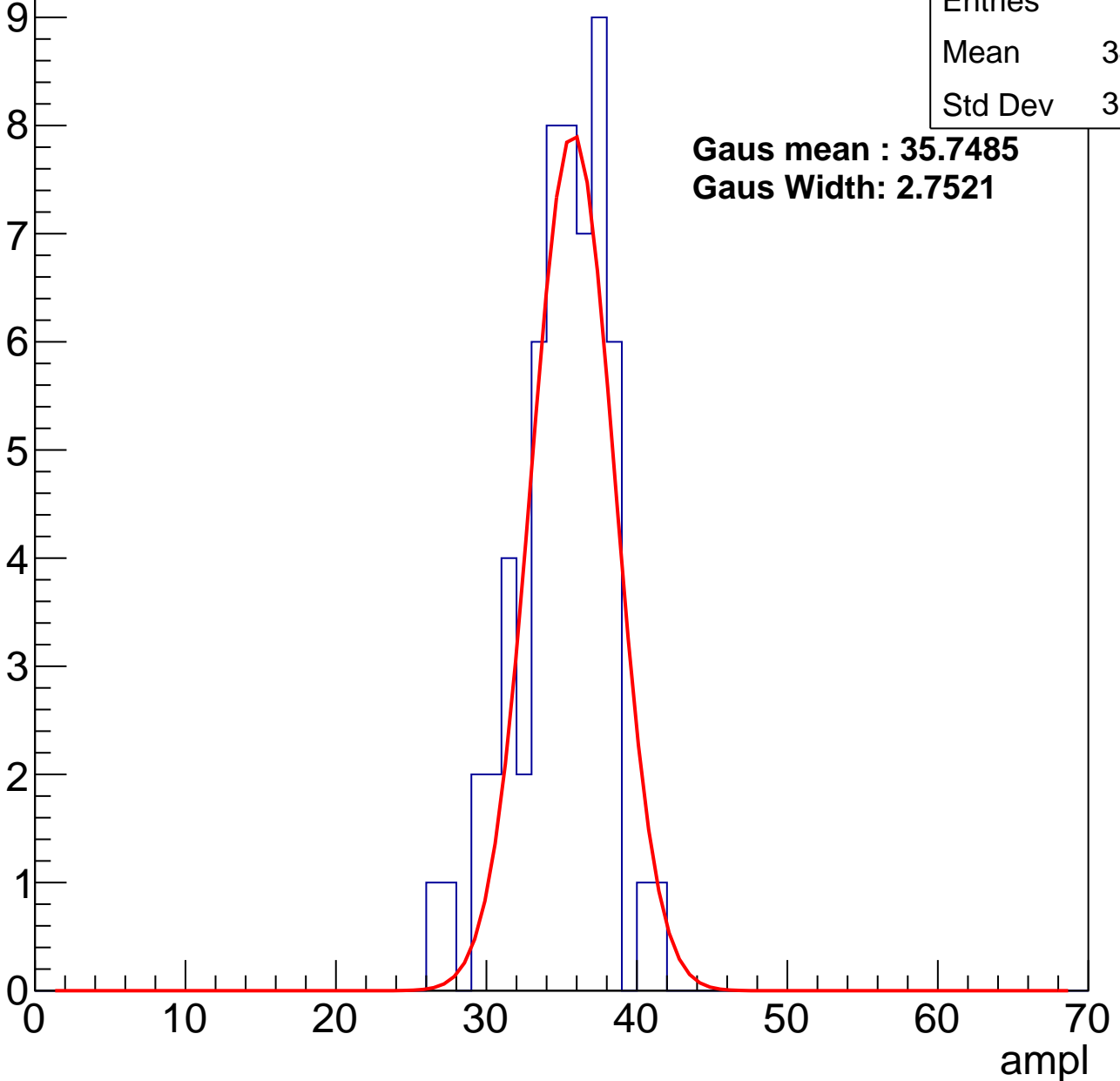
B1L101S, U21-ch48, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	34.53
Std Dev	3.013

Gaus mean : 35.7485
Gaus Width: 2.7521



B1L101S, U21-ch48, adc2

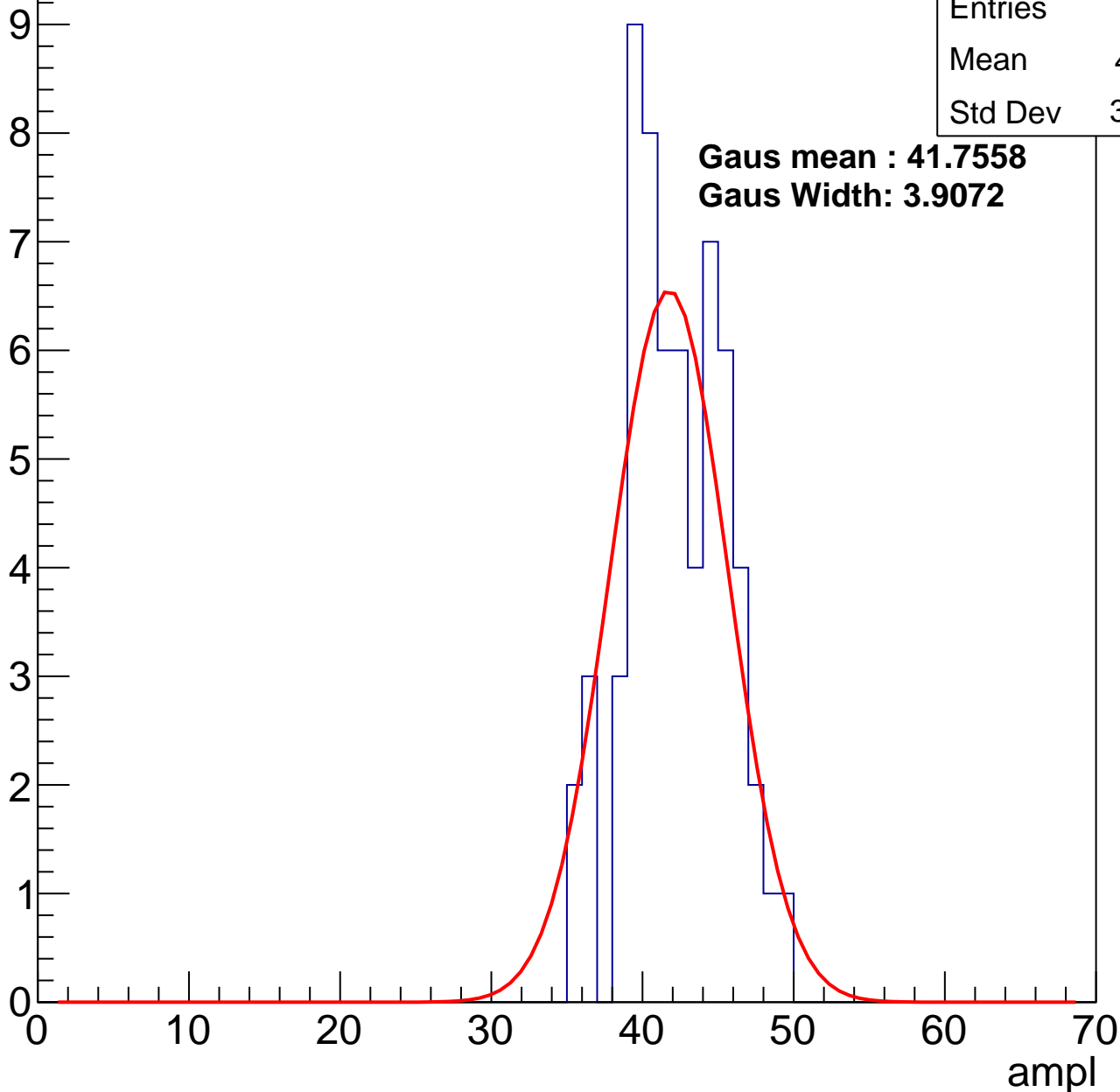
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	41.71
Std Dev	3.245

Gaus mean : 41.7558

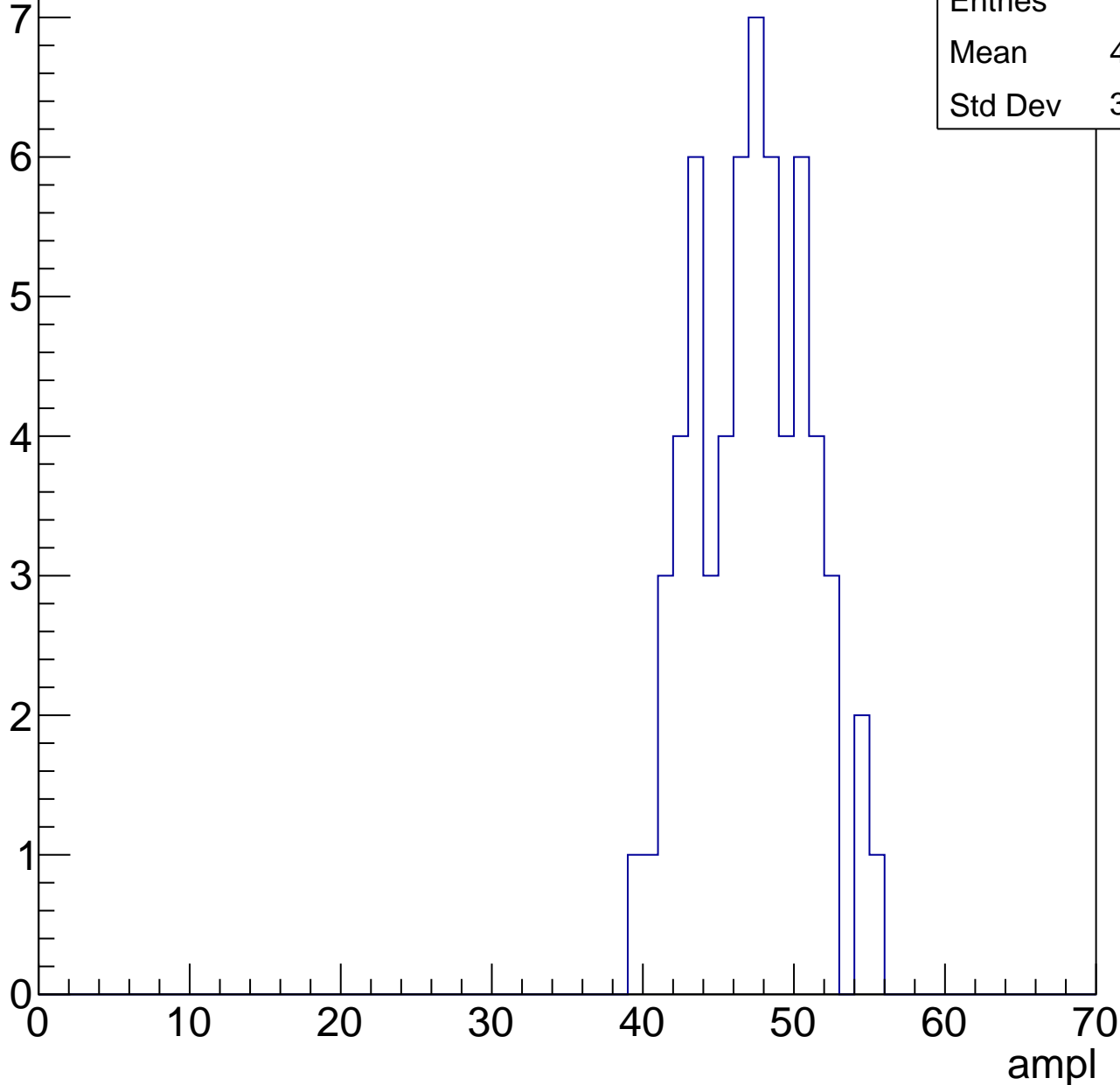
Gaus Width: 3.9072



B1L101S, U21-ch48, adc3

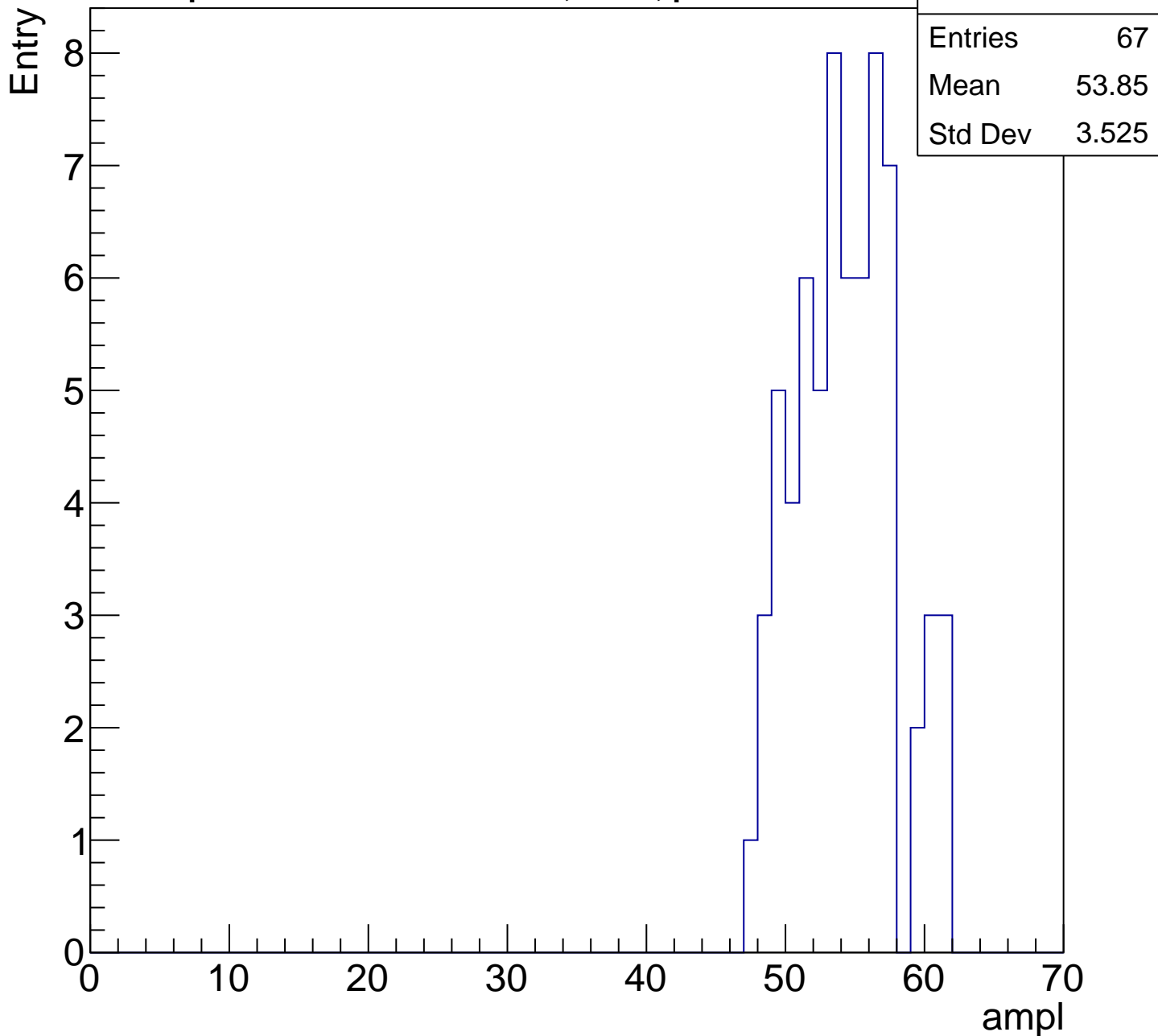
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch48, adc4

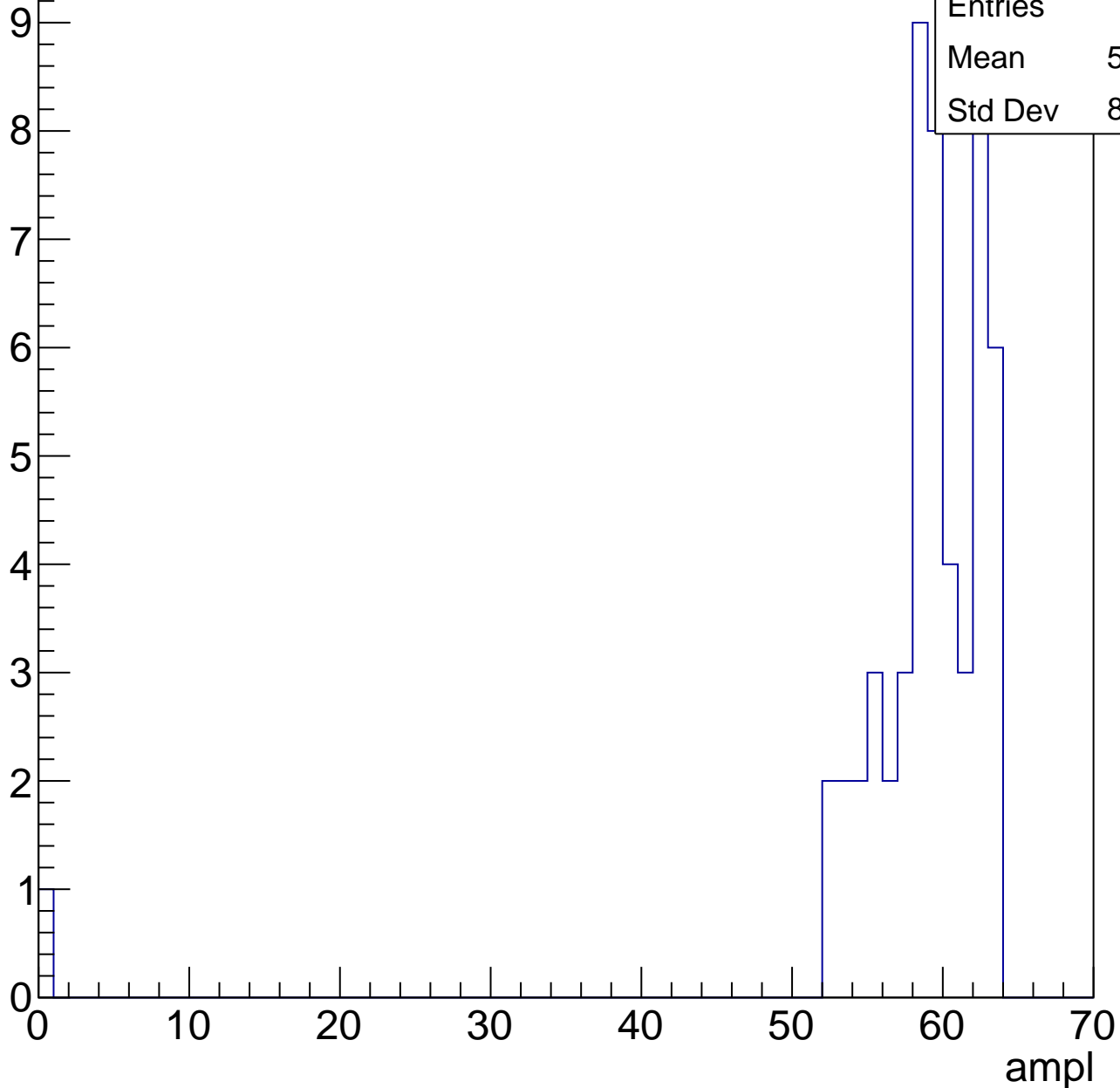
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch48, adc5

calib_packv5_042523_0143.root, FC#0, port D2

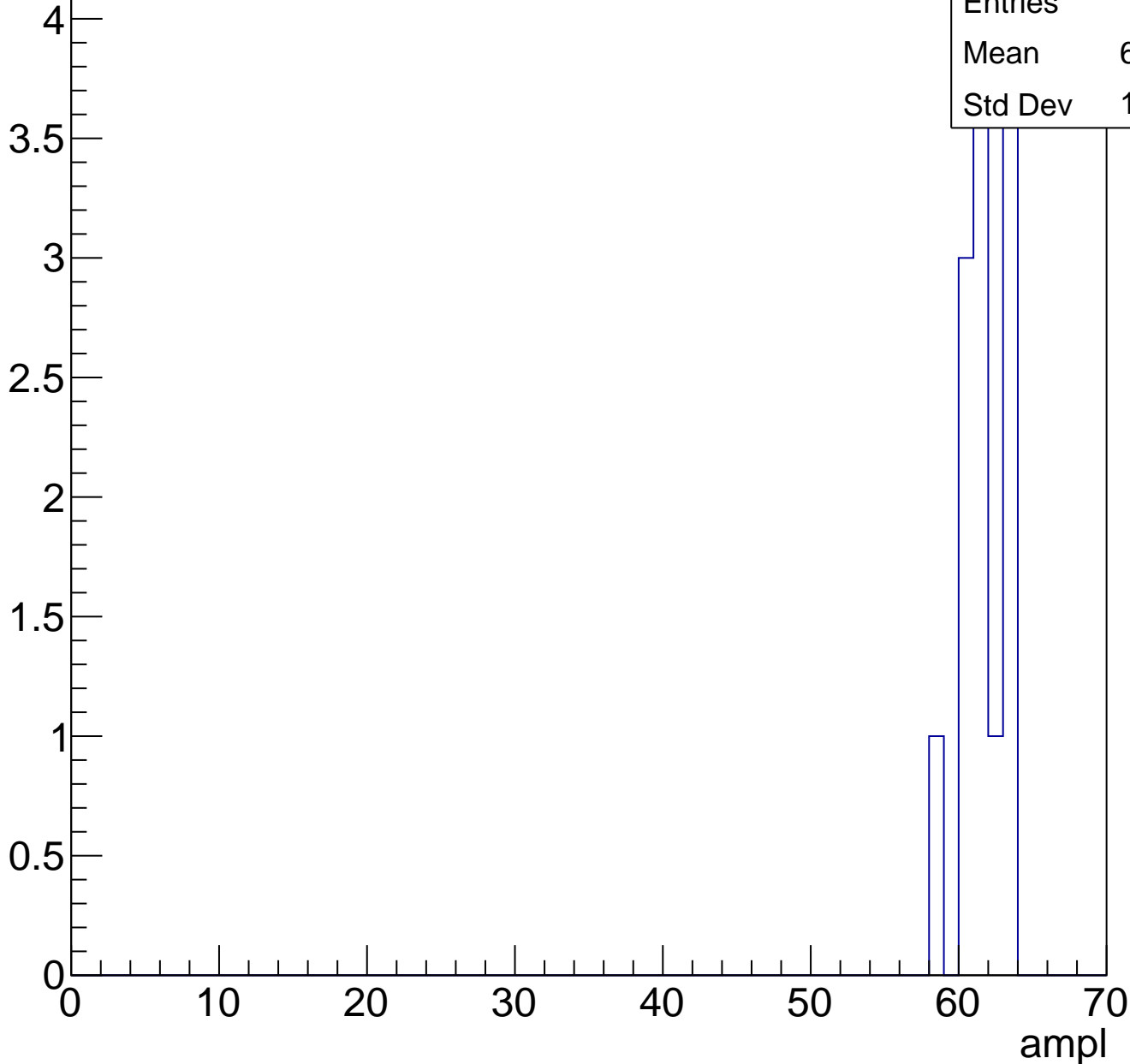
Entry



B1L101S, U21-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

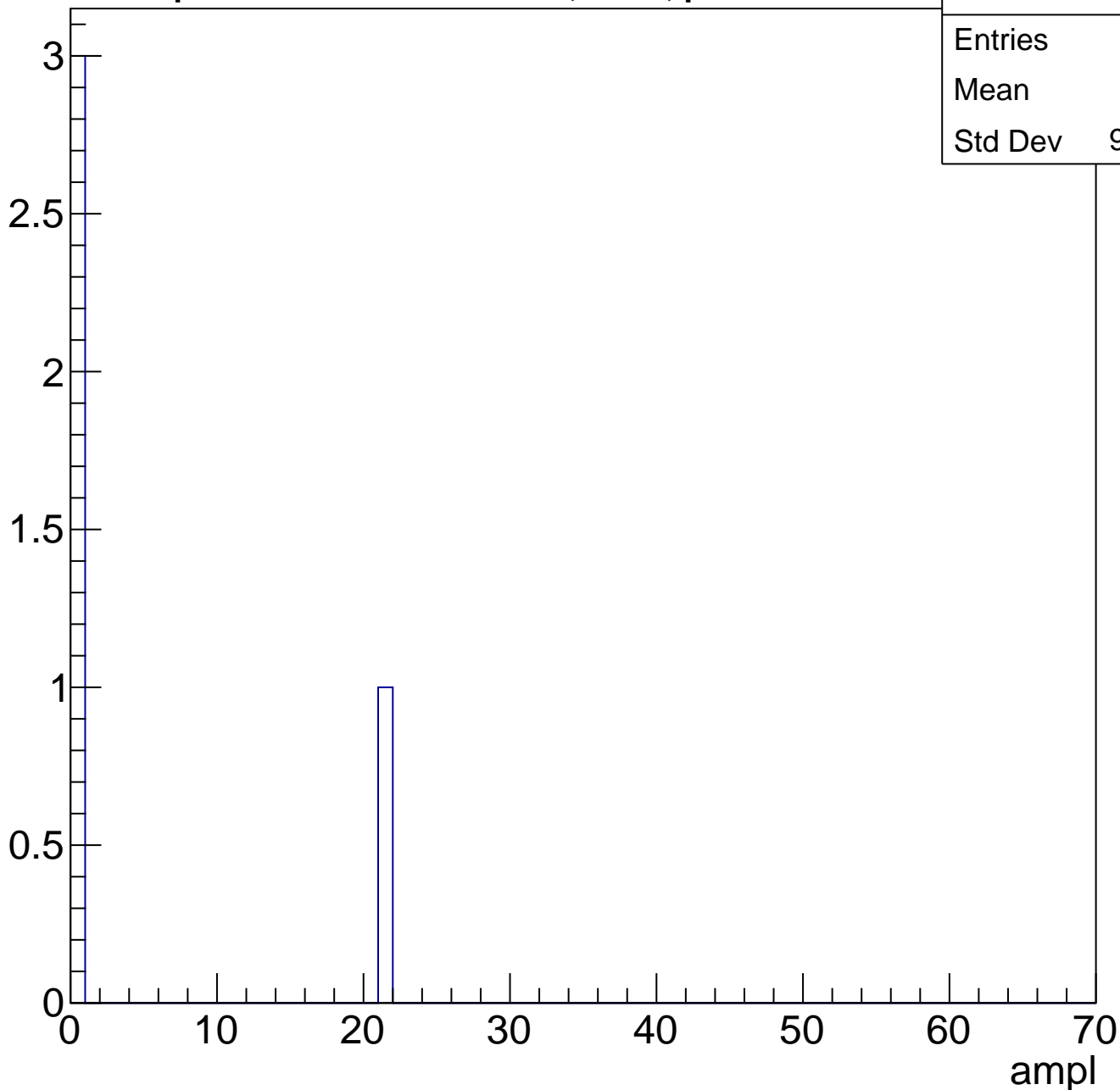
Entry



B1L101S, U21-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	5.25
Std Dev	9.093

B1L101S, U21-ch49, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	31.11
Std Dev	4.006

Gaus mean : 30.9998

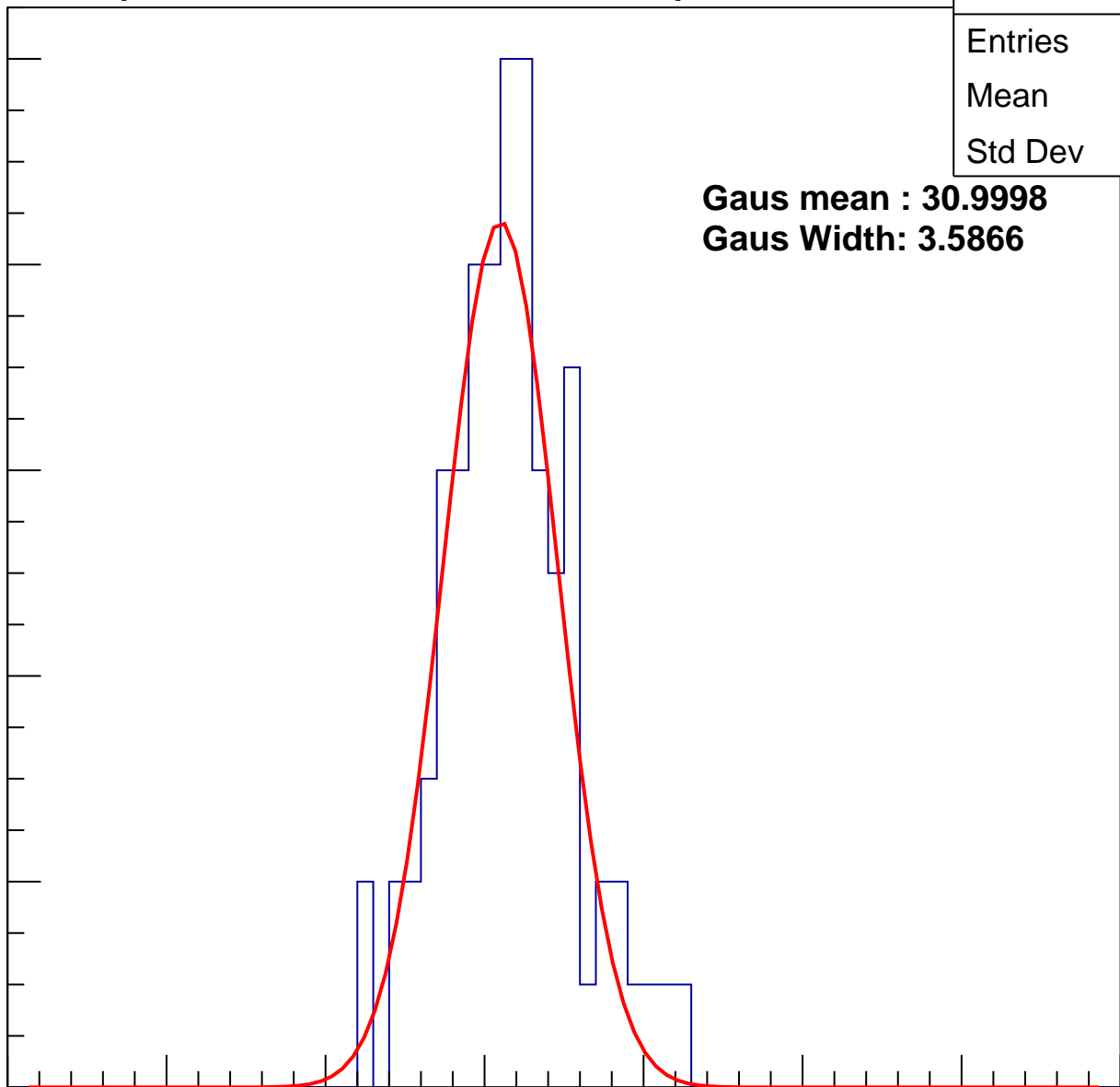
Gaus Width: 3.5866

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



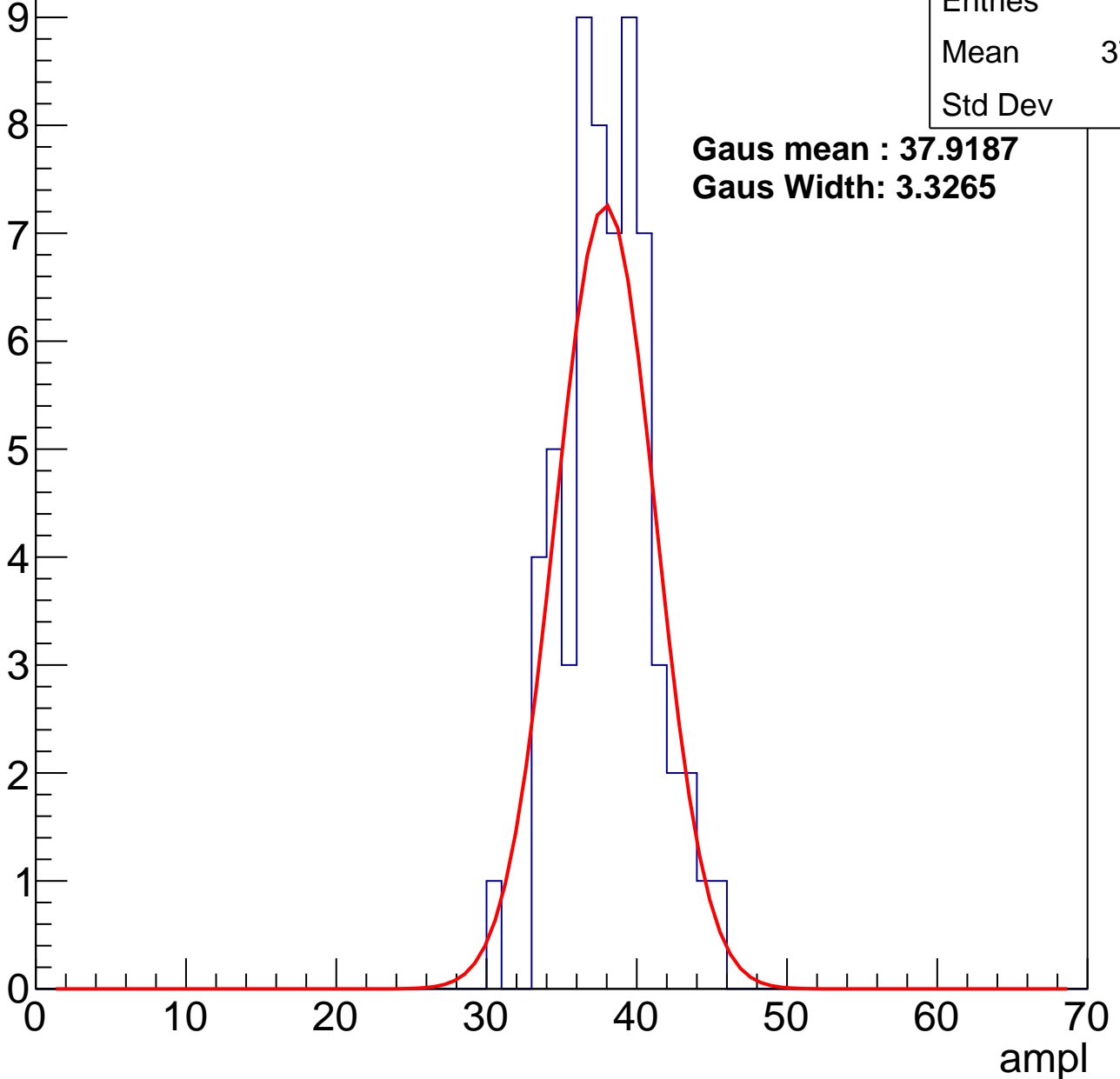
B1L101S, U21-ch49, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.68
Std Dev	2.95

Gaus mean : 37.9187
Gaus Width: 3.3265



B1L101S, U21-ch49, adc2

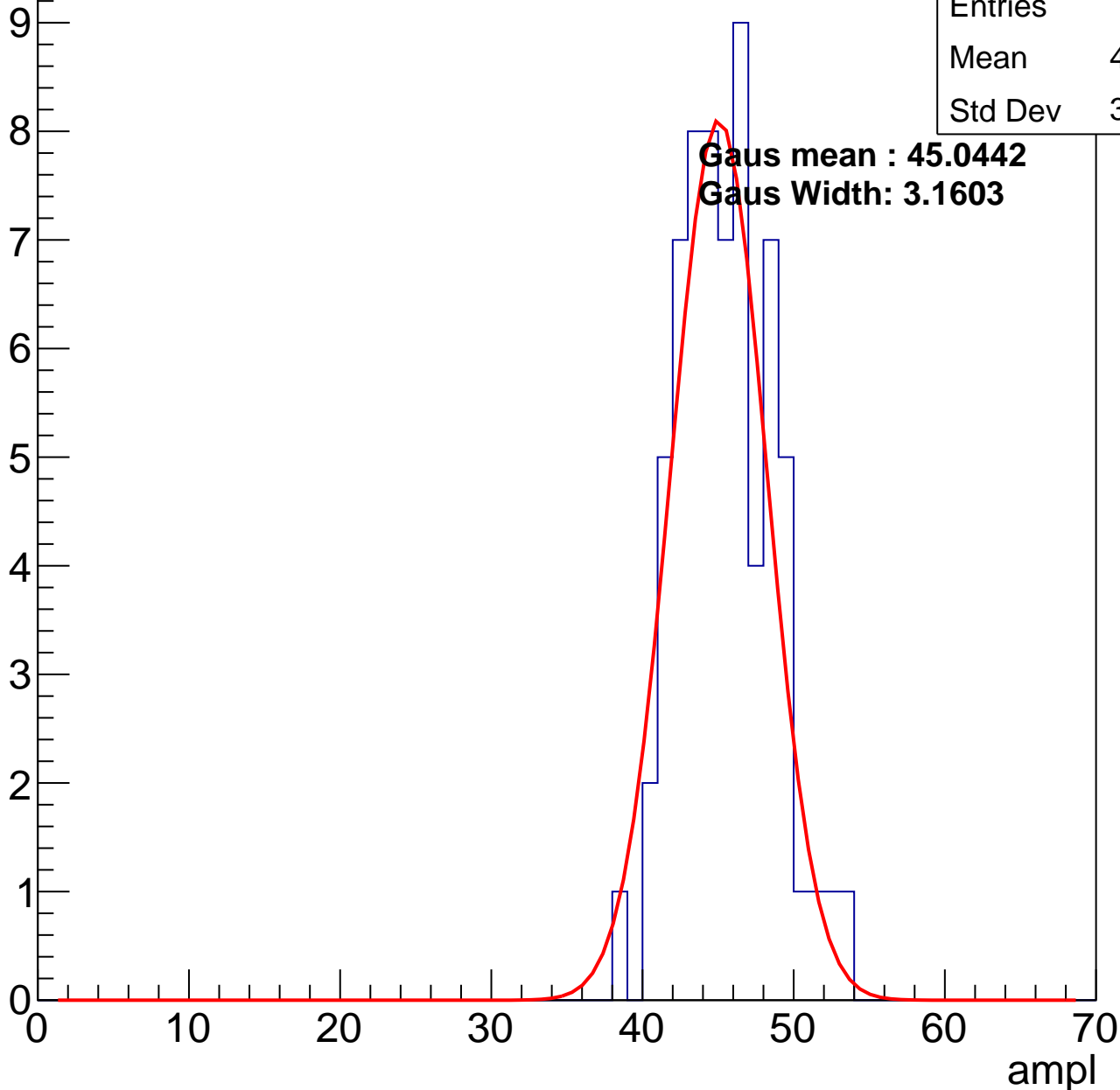
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	45.03
Std Dev	3.052

Gaus mean : 45.0442

Gaus Width: 3.1603

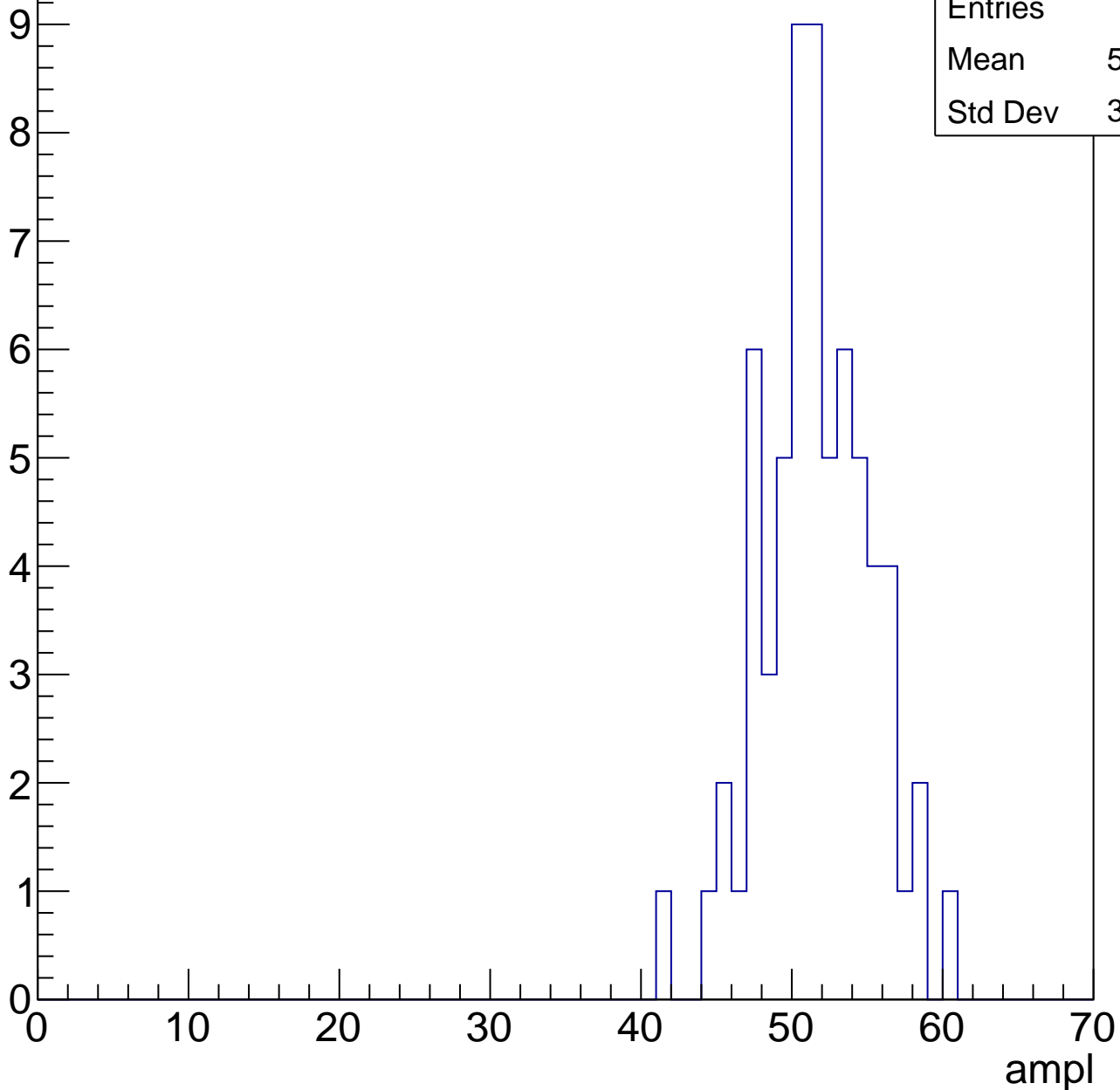


B1L101S, U21-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

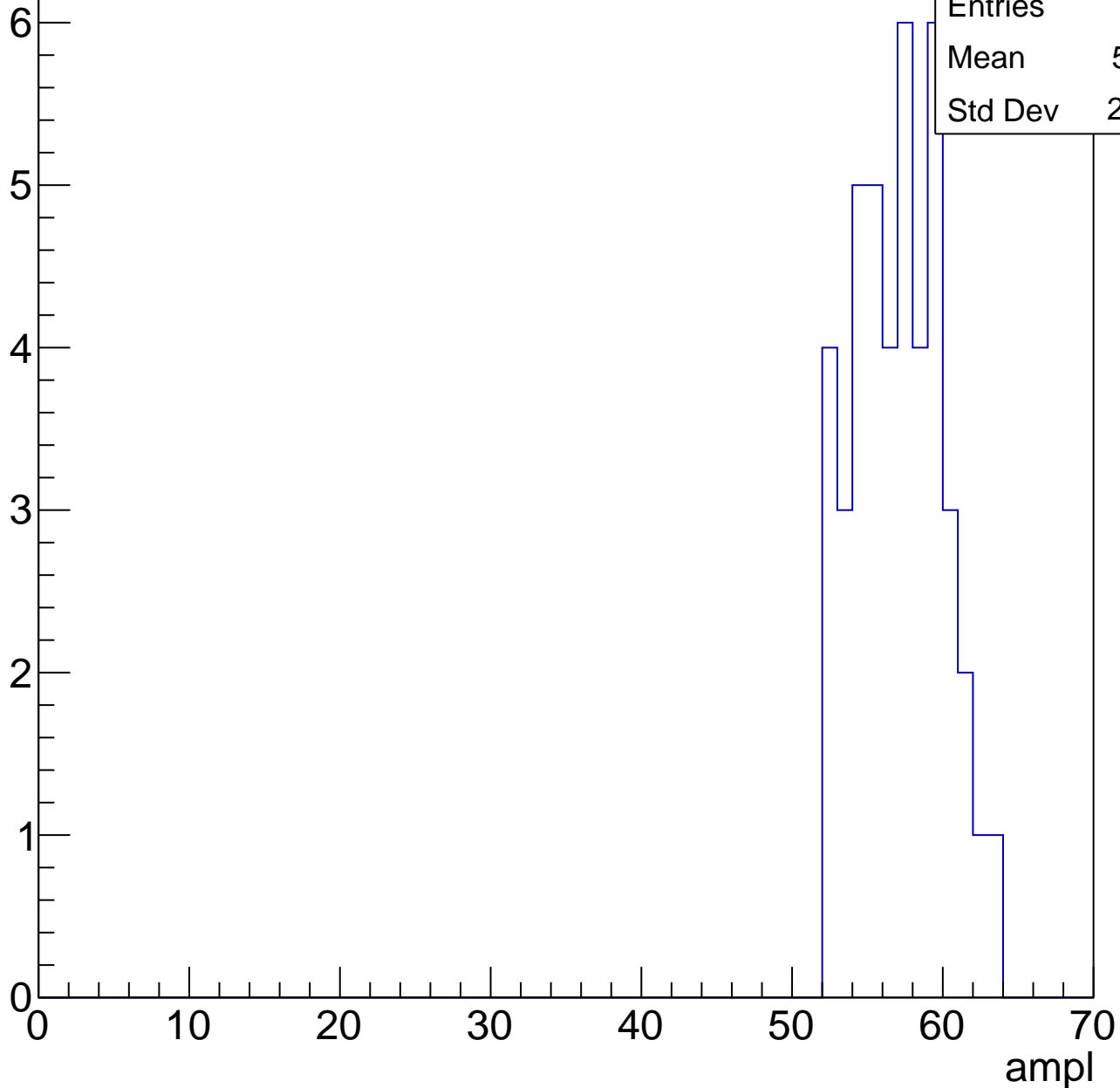
Entries	65
Mean	51.17
Std Dev	3.614



B1L101S, U21-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

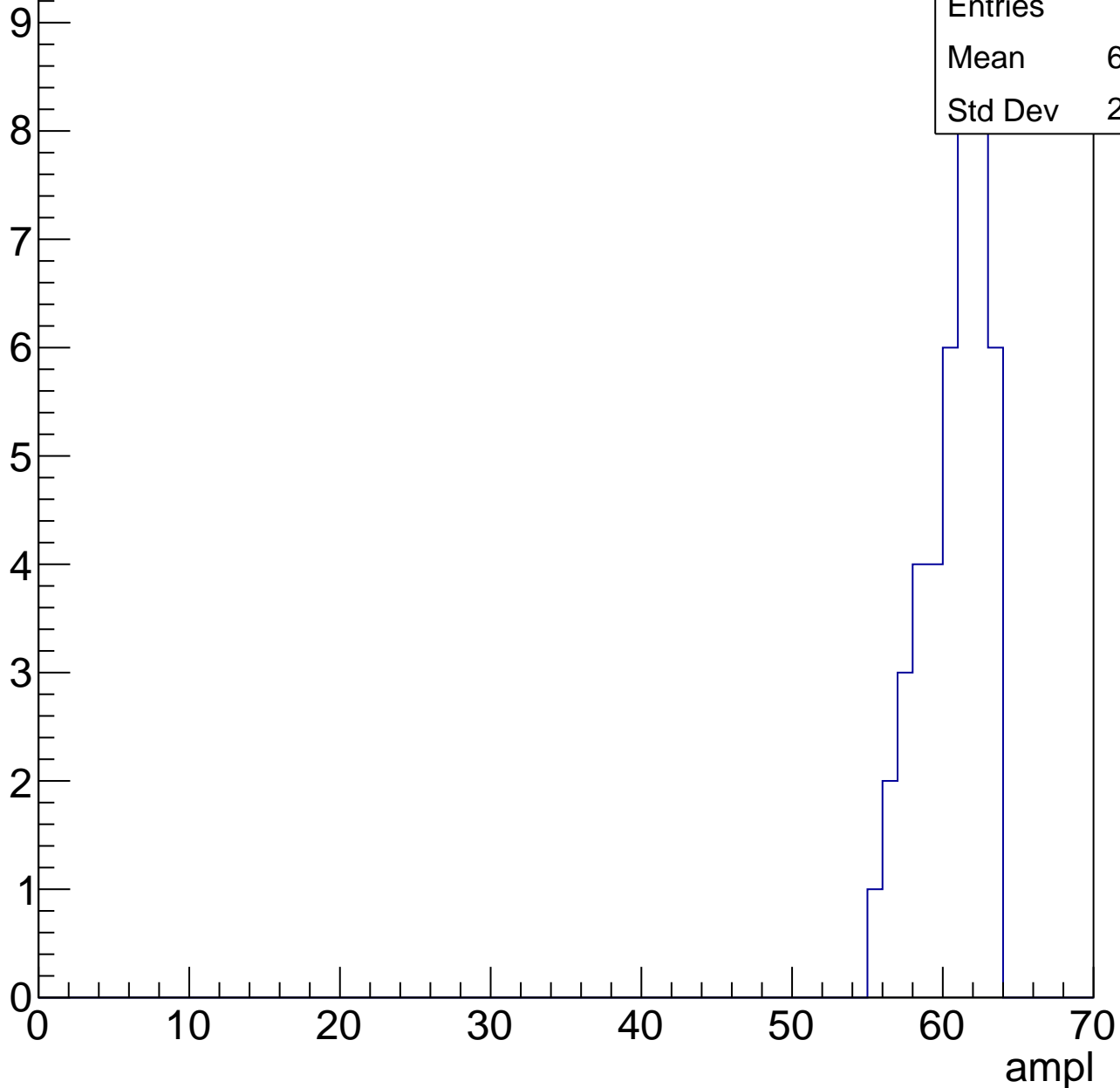


Entries	44
Mean	56.61
Std Dev	2.854

B1L101S, U21-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

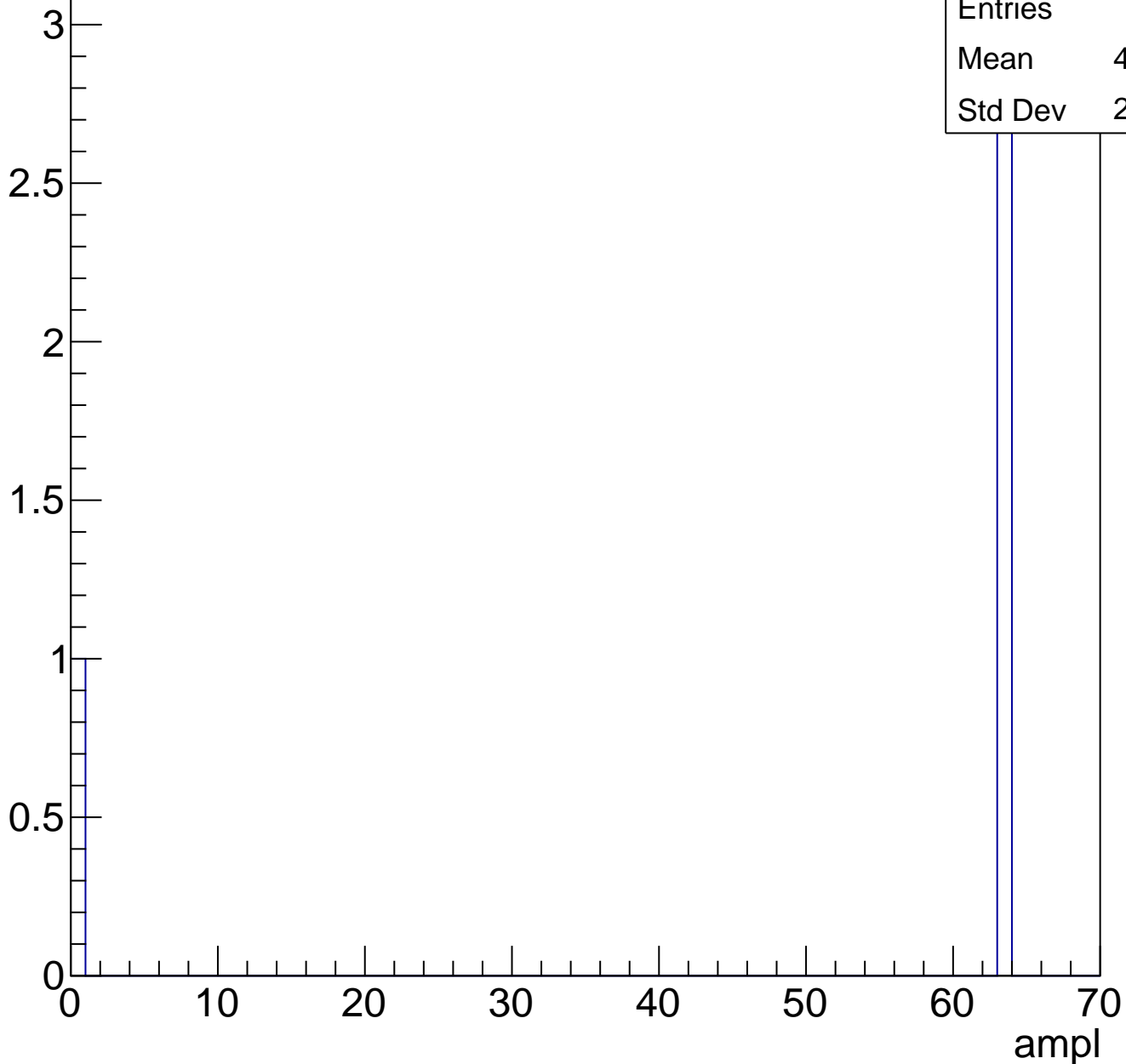
Entry



B1L101S, U21-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch50, adc0

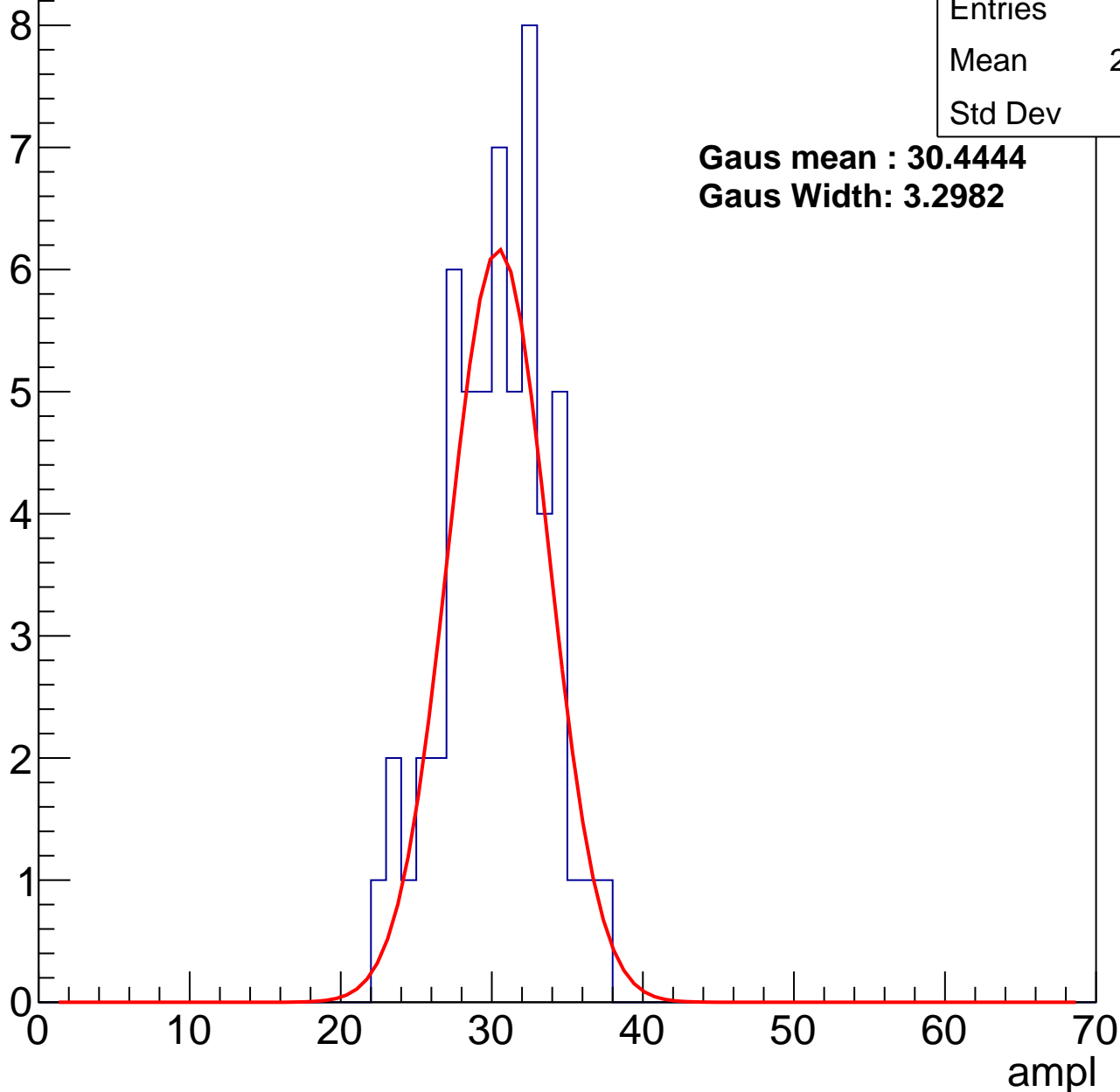
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	29.86
Std Dev	3.33

Gaus mean : 30.4444

Gaus Width: 3.2982



B1L101S, U21-ch50, adc1

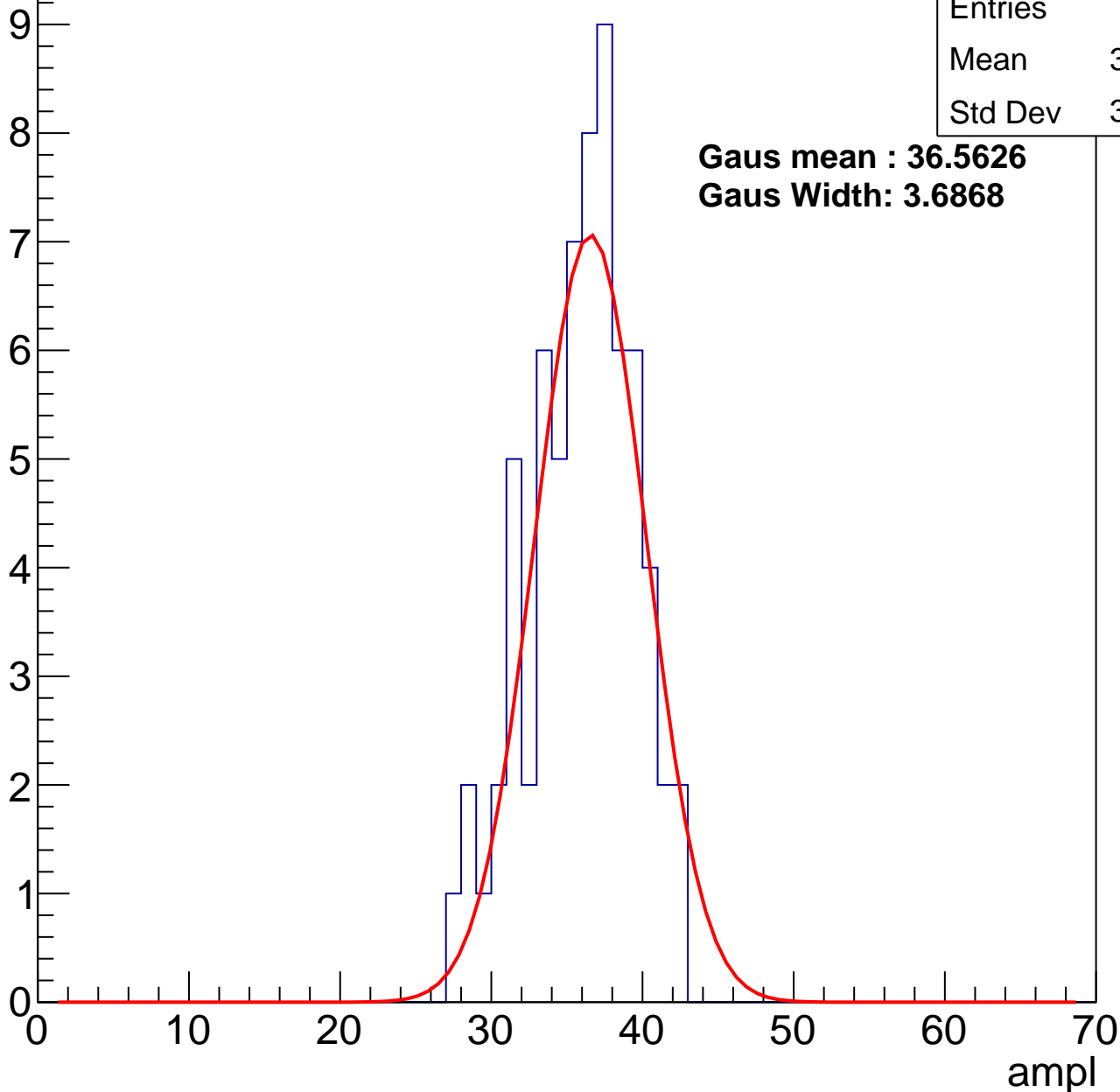
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.49
Std Dev	3.479

Gaus mean : 36.5626

Gaus Width: 3.6868



B1L101S, U21-ch50, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

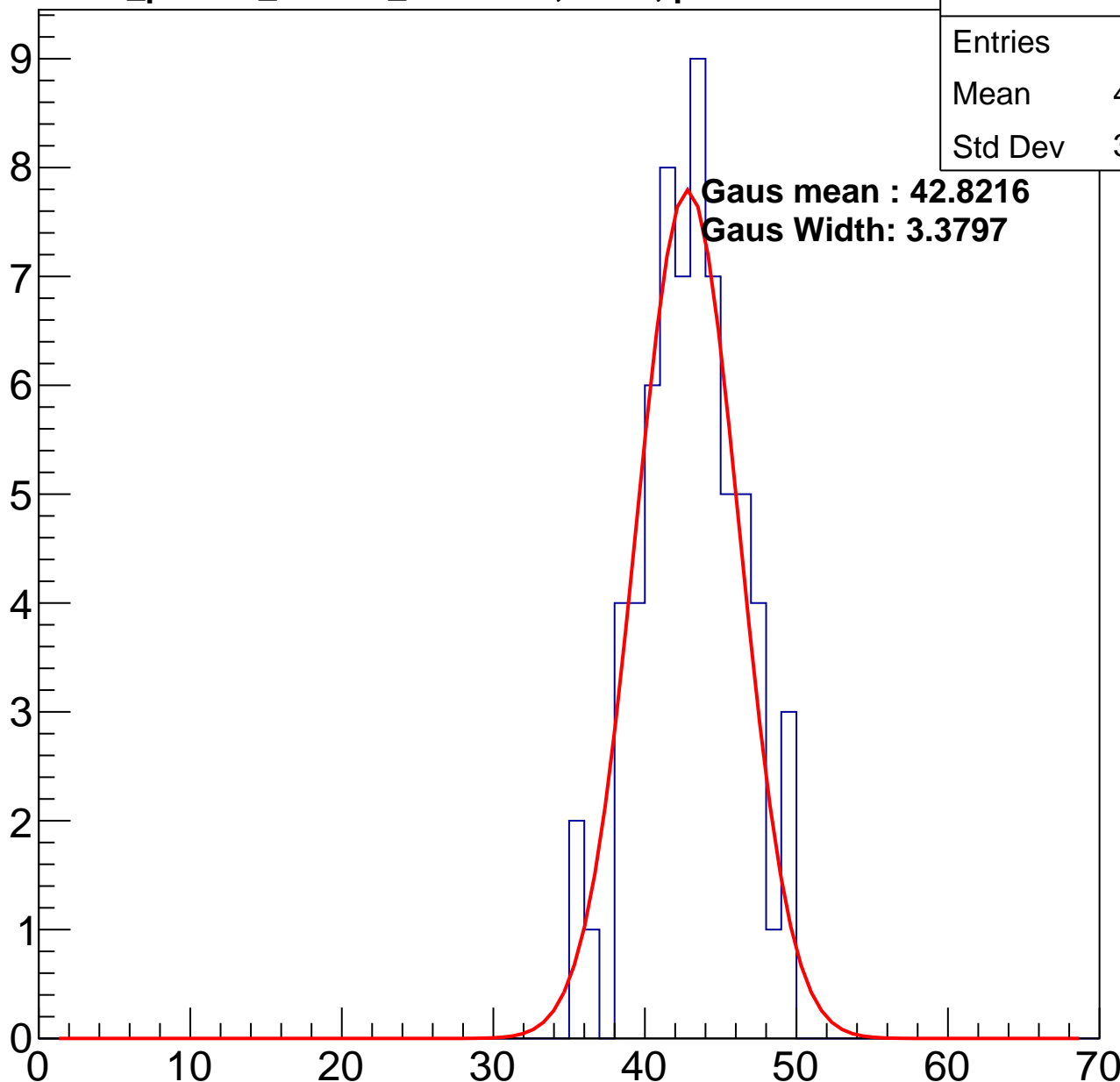
9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	42.56
Std Dev	3.234

Gaus mean : 42.8216

Gaus Width: 3.3797

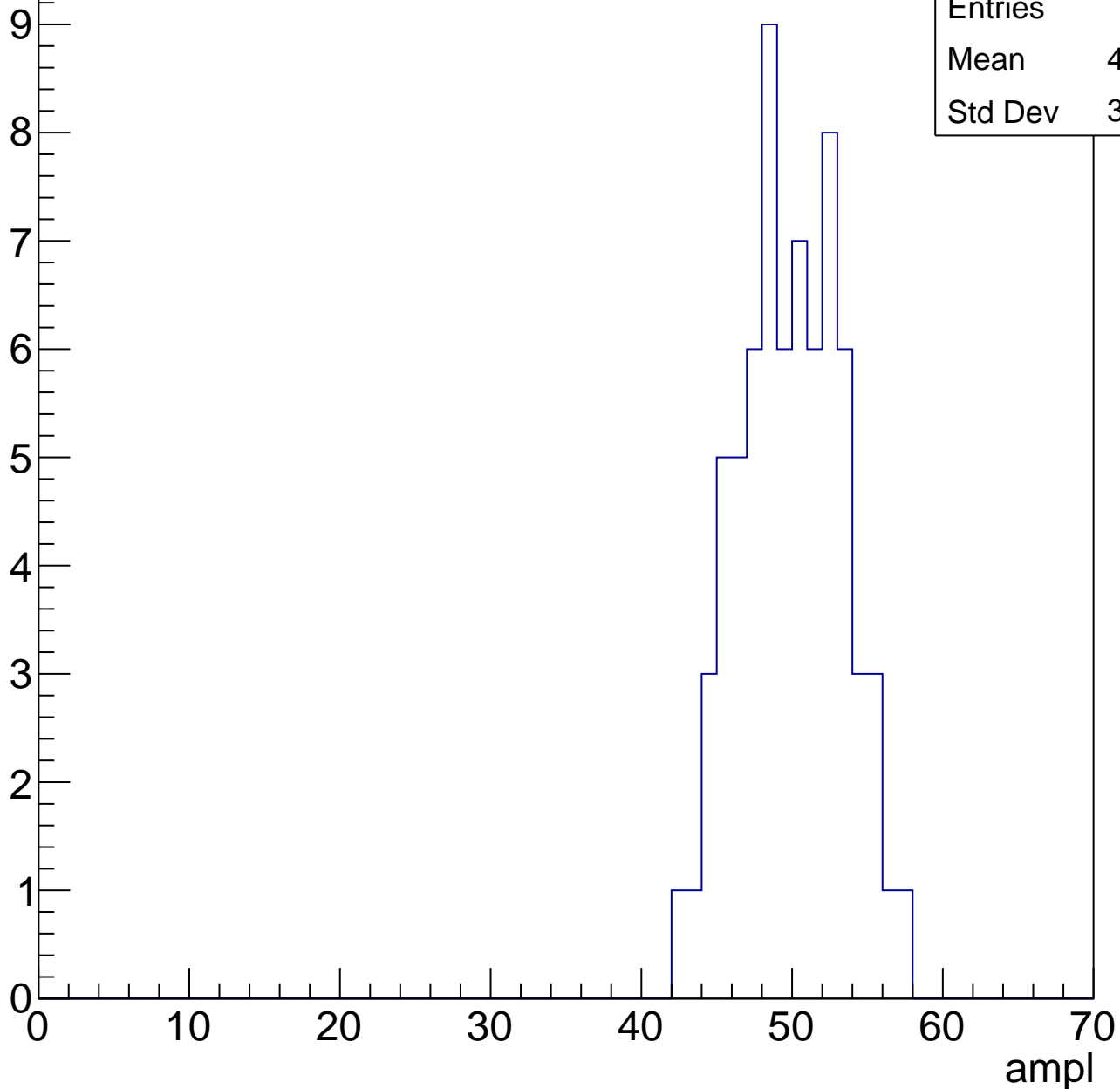
ampl



B1L101S, U21-ch50, adc3

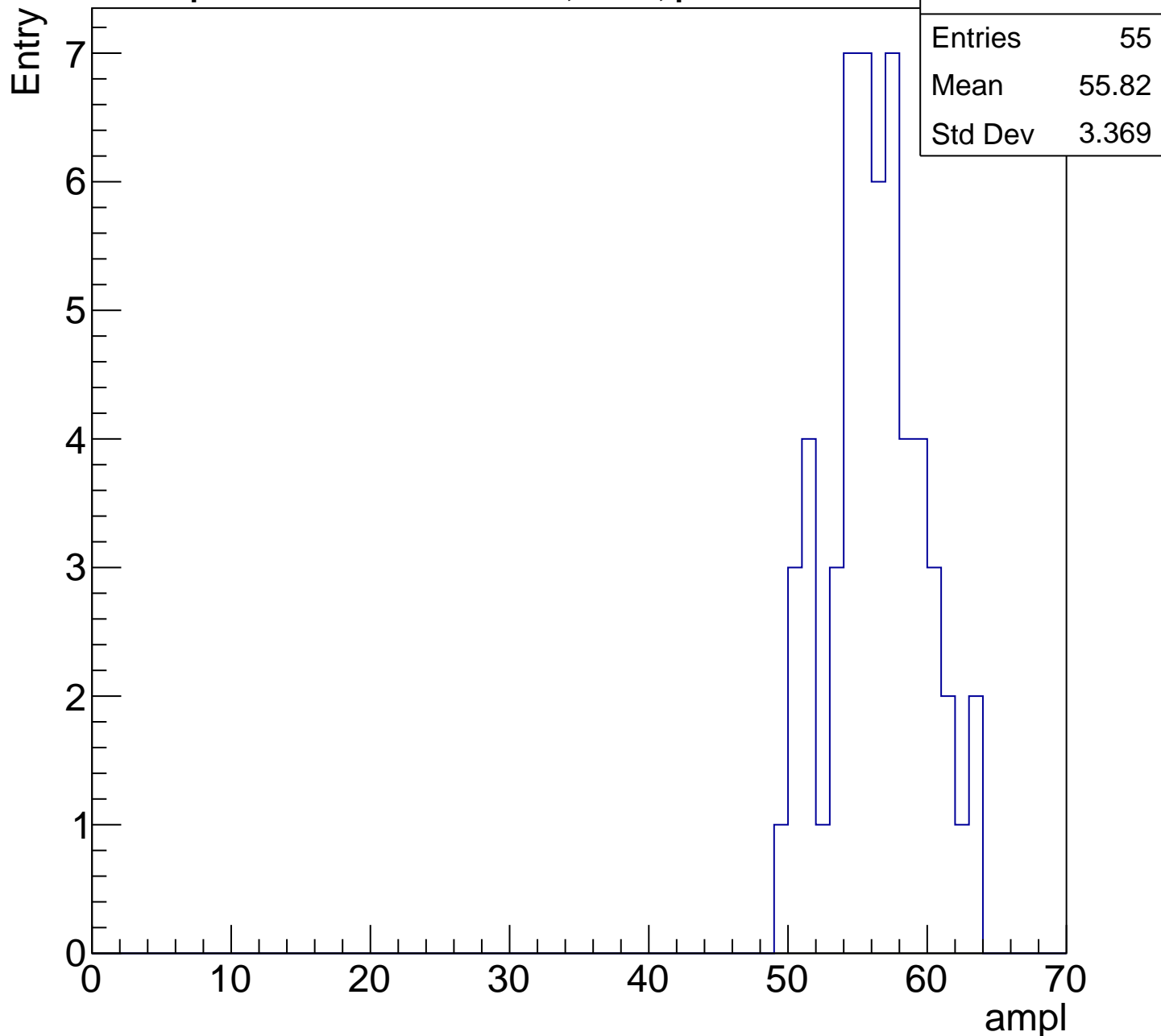
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	43
Mean	59.58
Std Dev	2.545

10

20

30

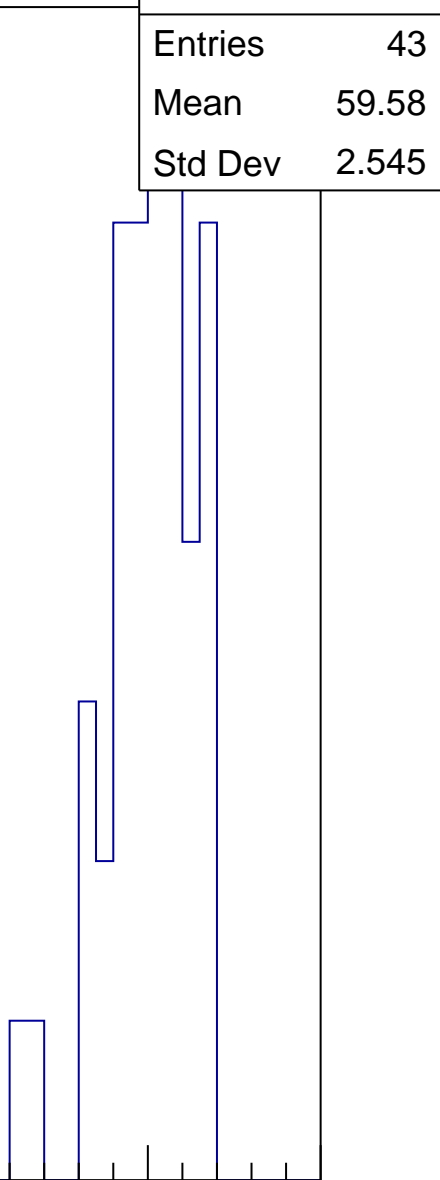
40

50

60

70

ampl

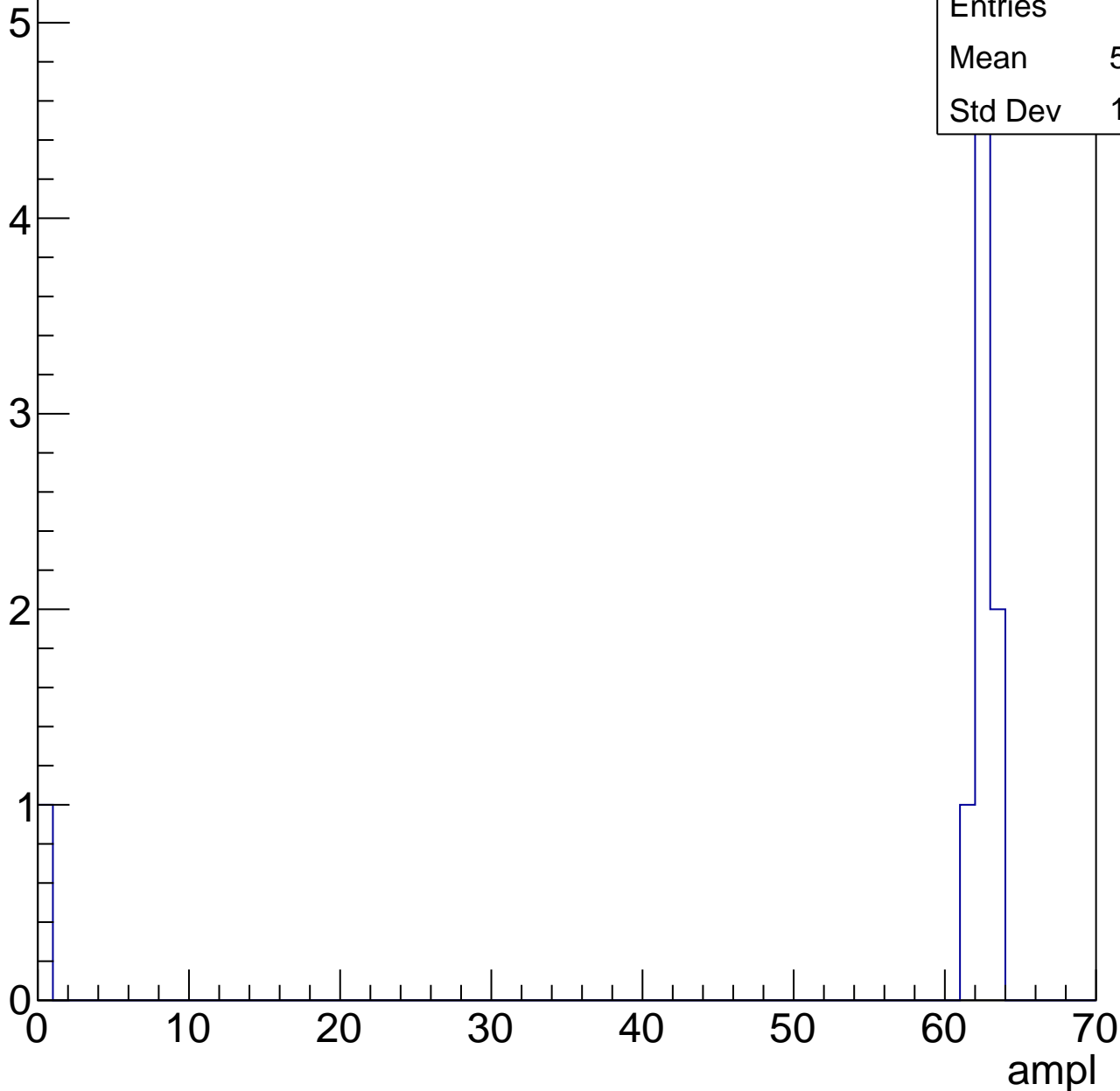


B1L101S, U21-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	55.22
Std Dev	19.53



B1L101S, U21-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch51, adc0

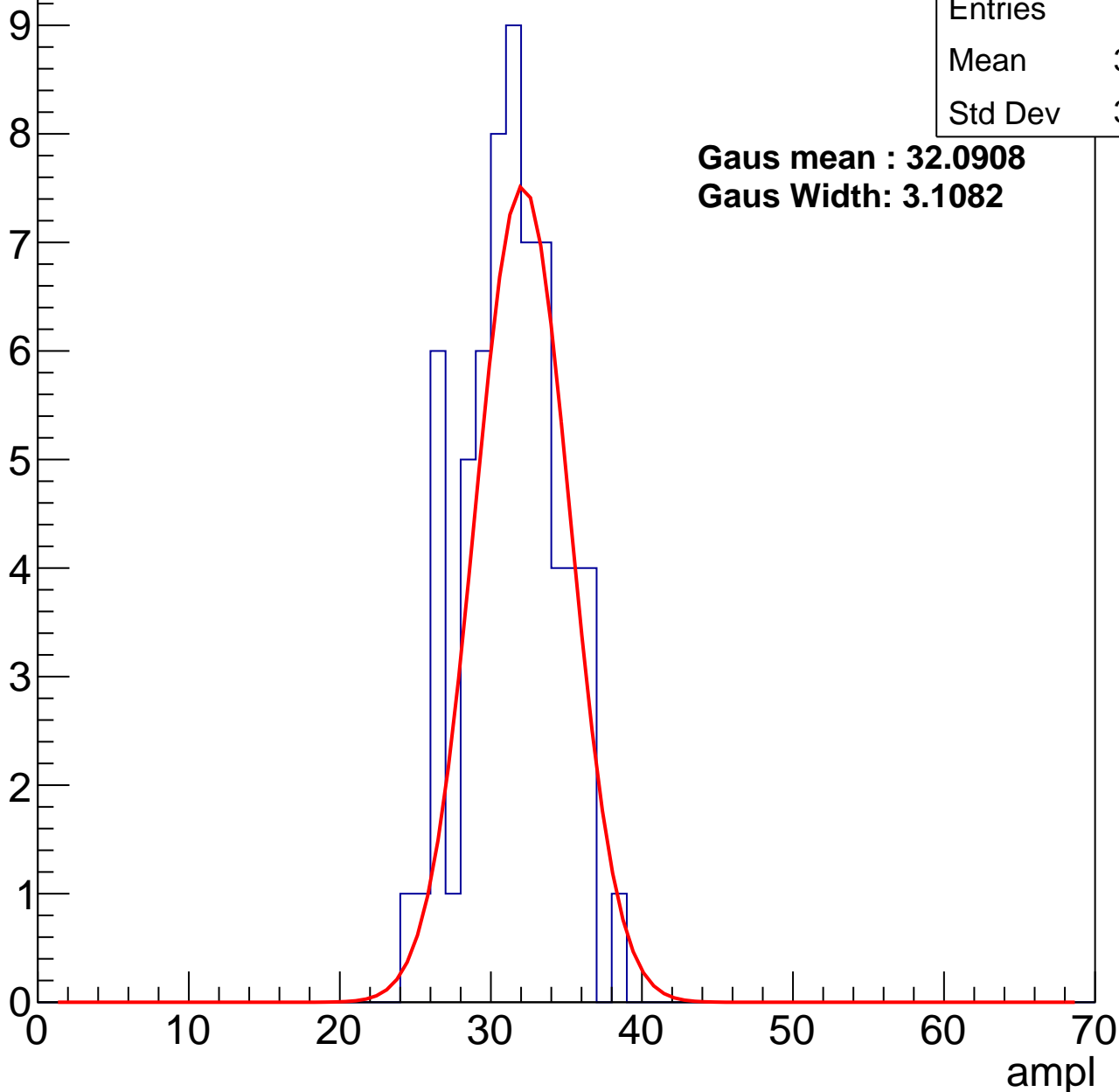
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.91
Std Dev	3.091

Gaus mean : 32.0908

Gaus Width: 3.1082



B1L101S, U21-ch51, adc1

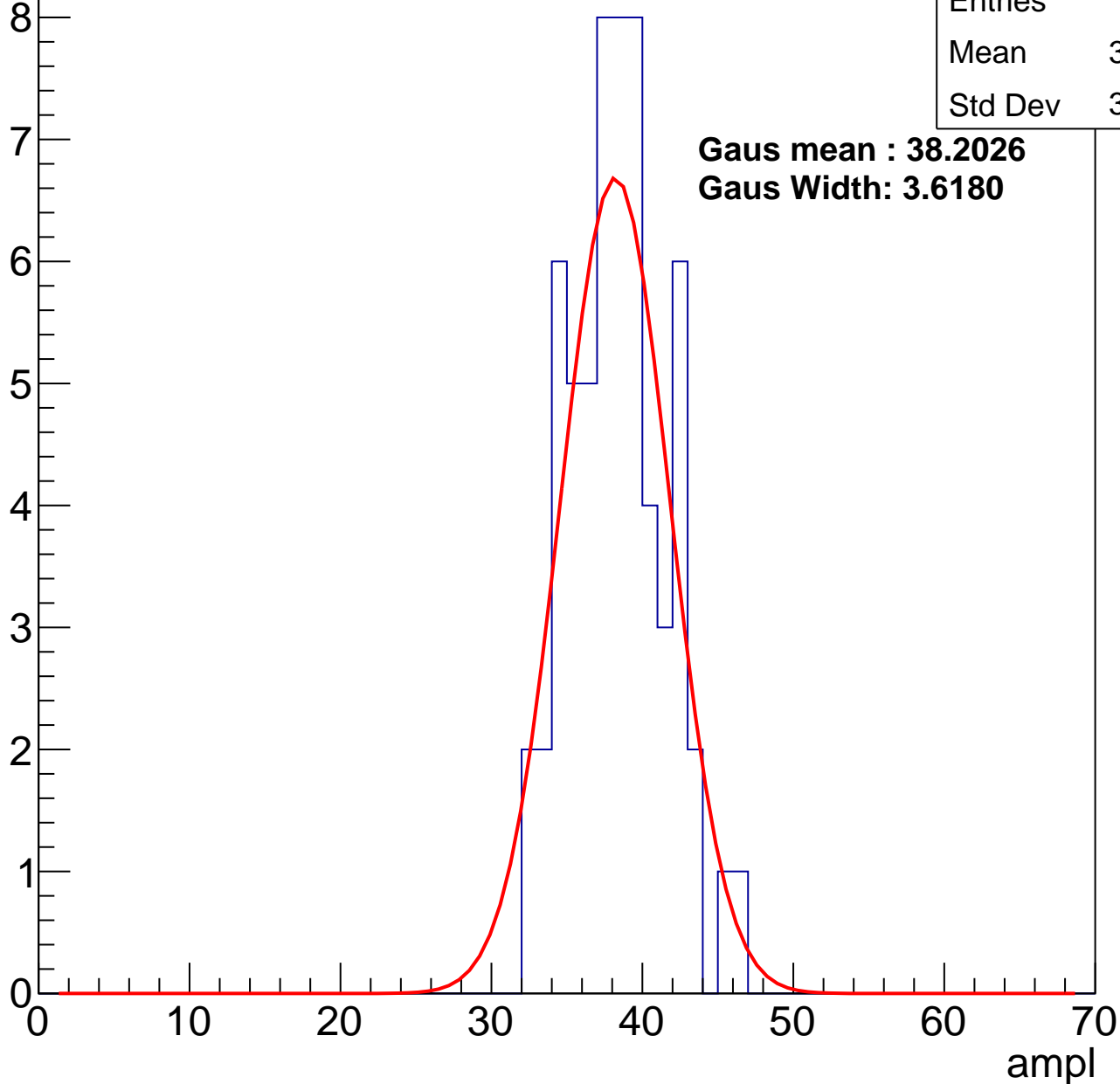
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.92
Std Dev	3.138

Gaus mean : 38.2026

Gaus Width: 3.6180



B1L101S, U21-ch51, adc2

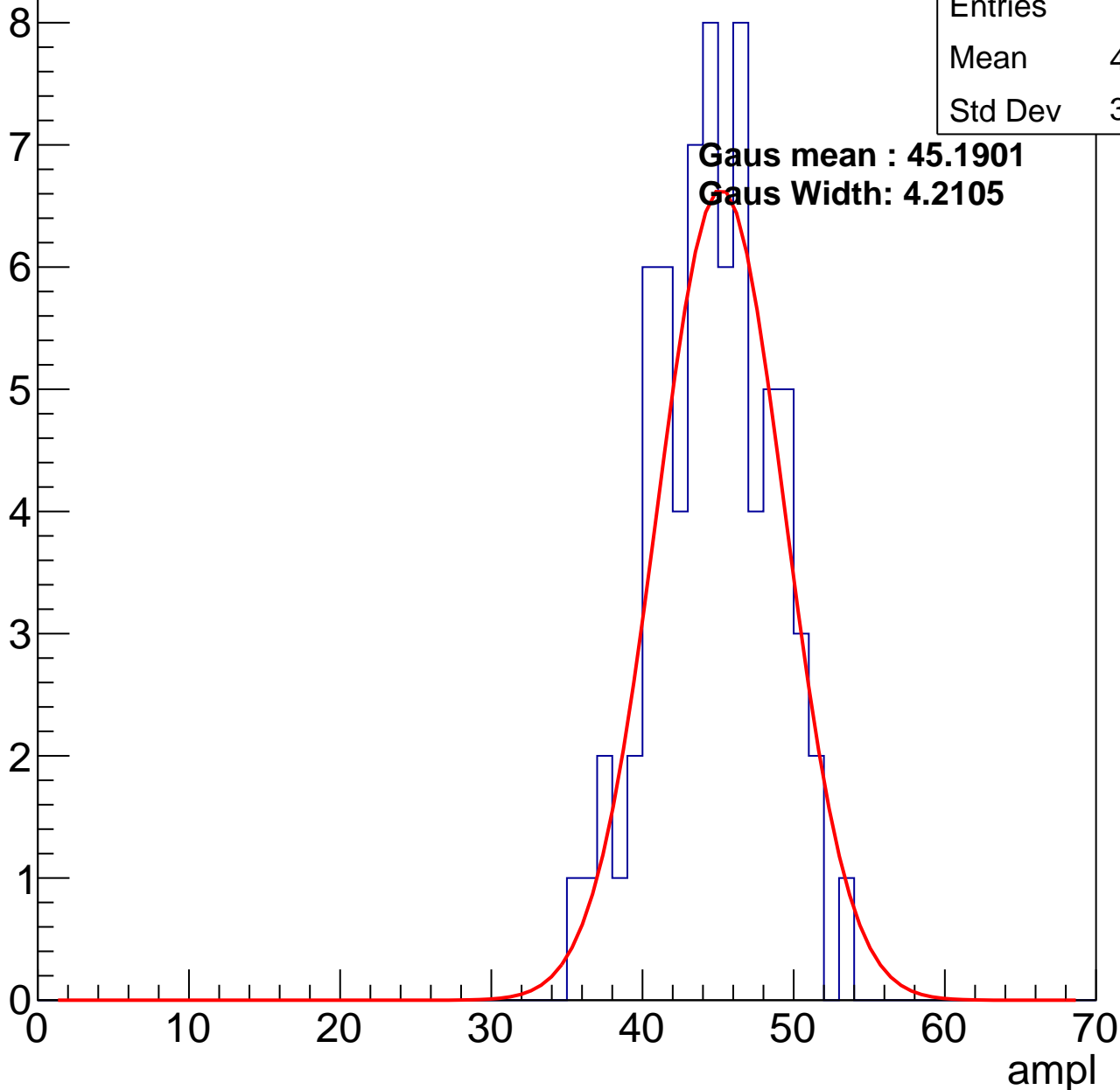
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	44.22
Std Dev	3.845

Gaus mean : 45.1901

Gaus Width: 4.2105

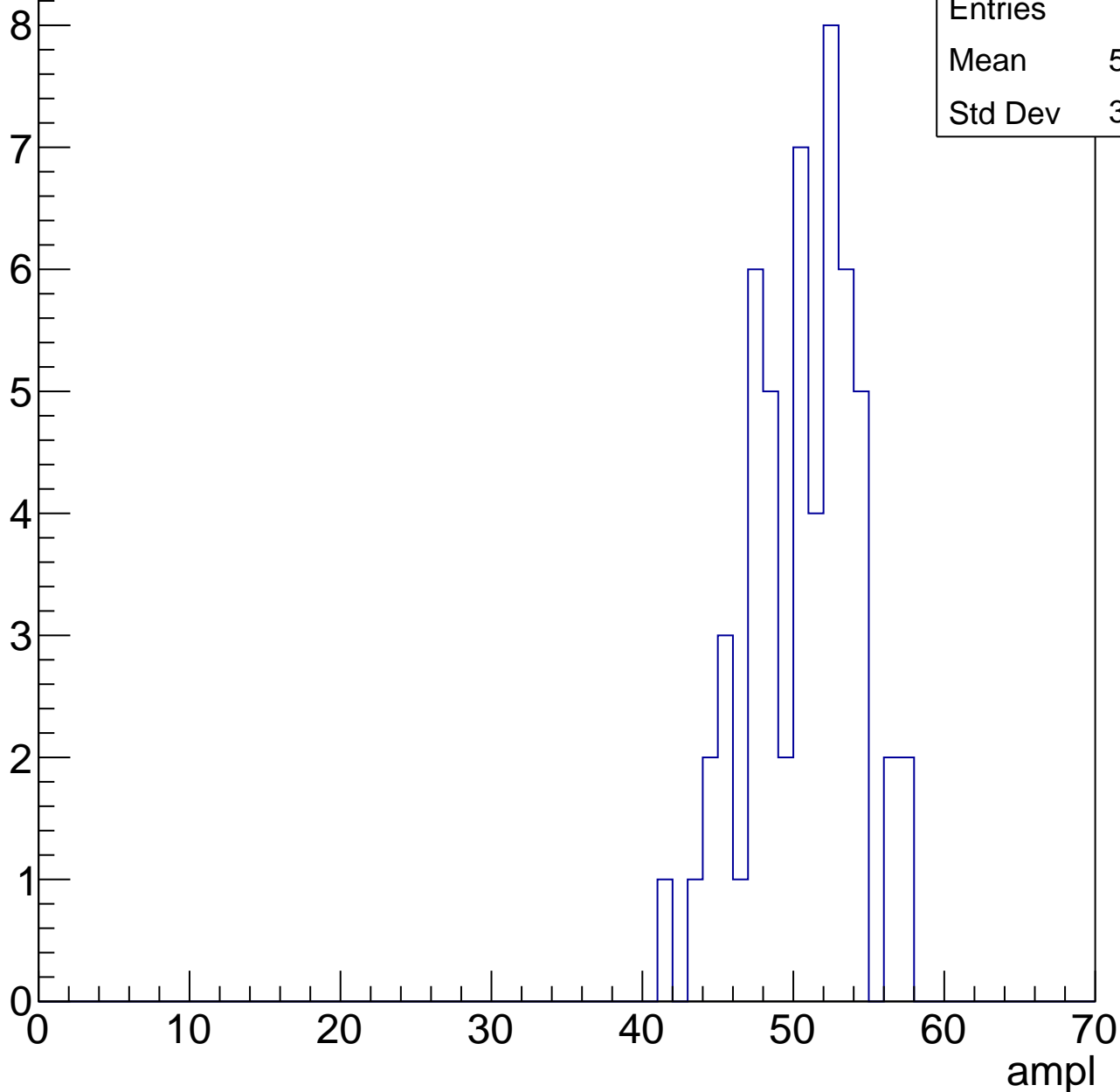


B1L101S, U21-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	50.13
Std Dev	3.588

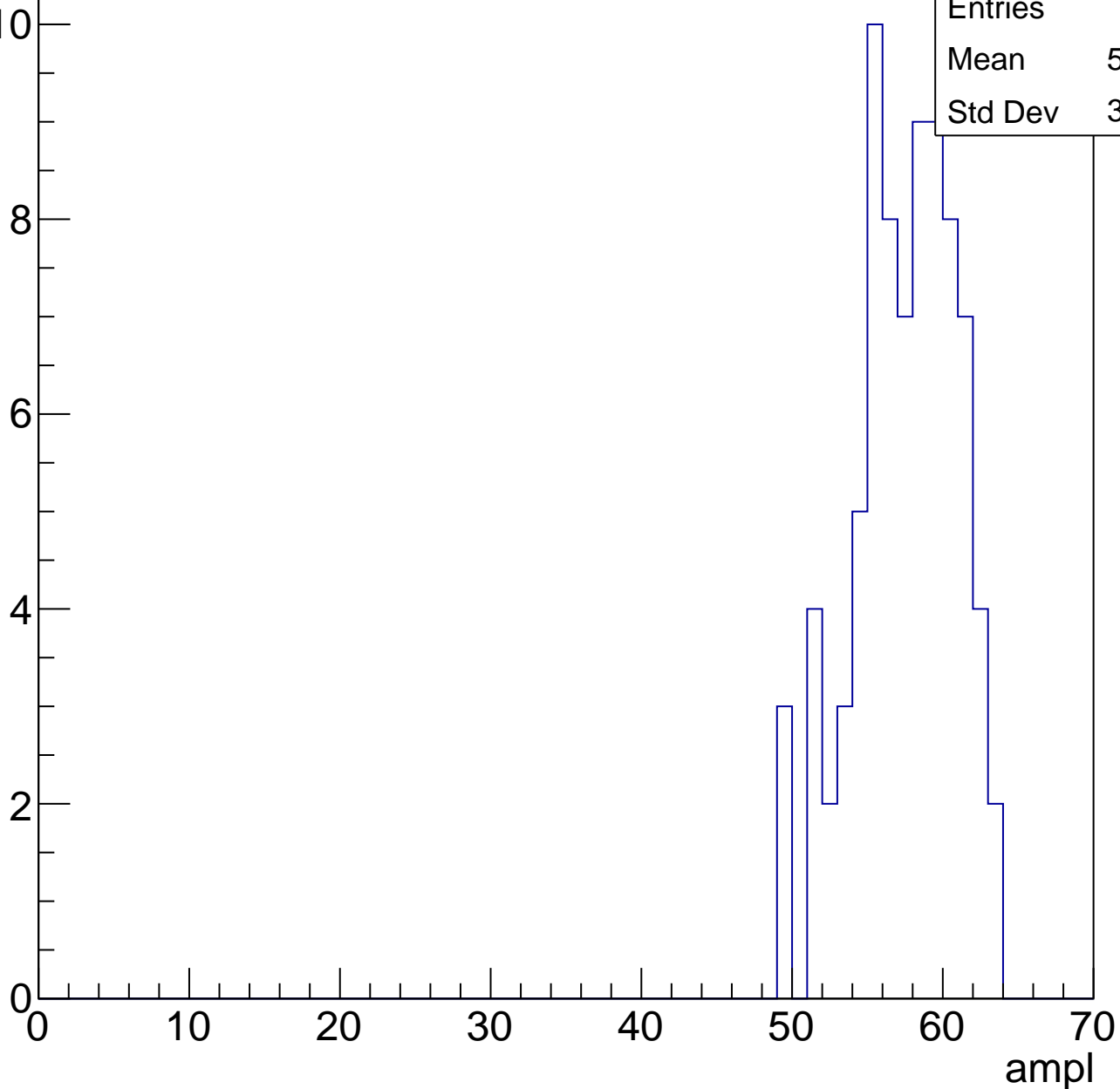


B1L101S, U21-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	56.98
Std Dev	3.385

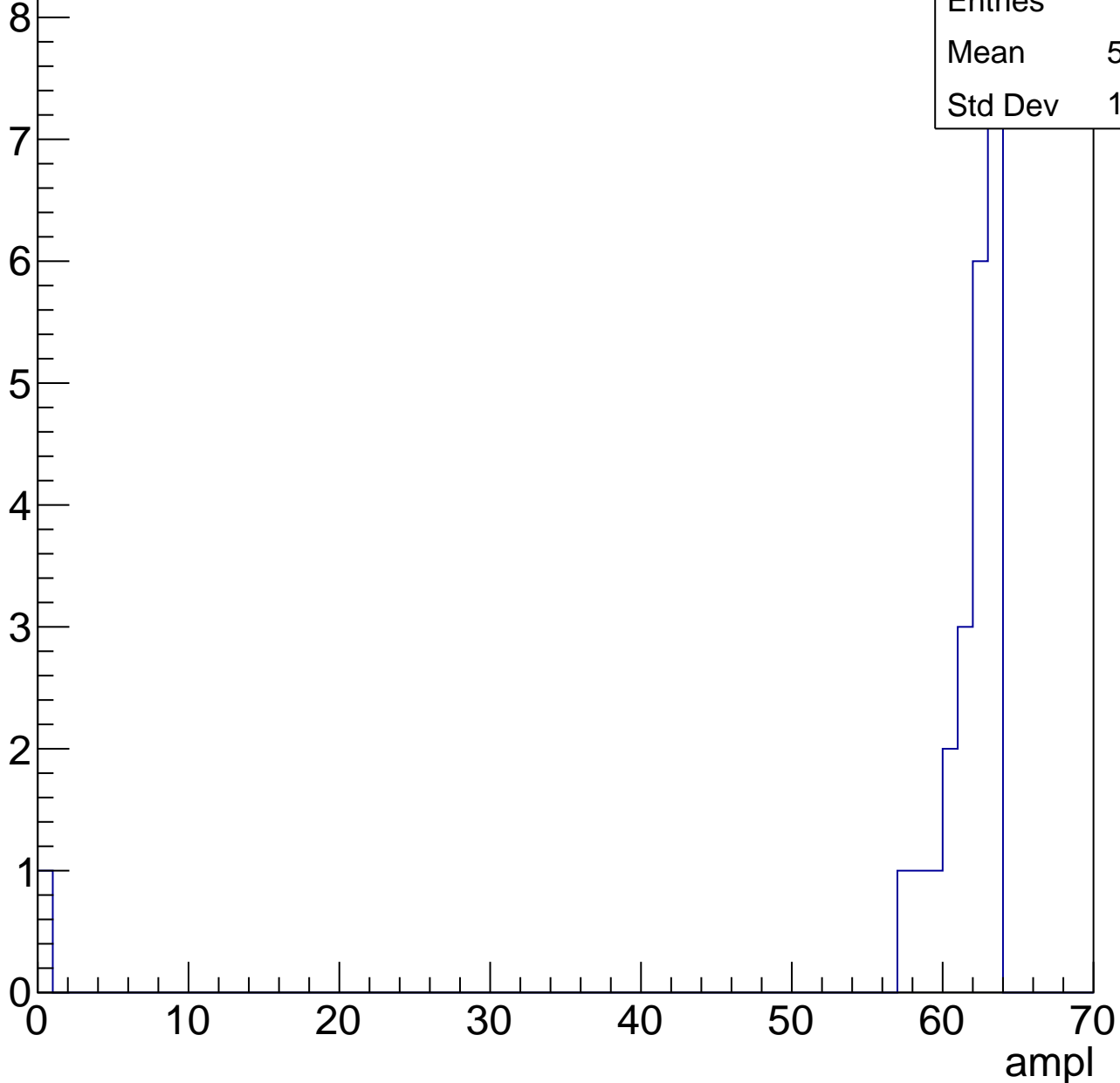


B1L101S, U21-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.83
Std Dev	12.65



B1L101S, U21-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch52, adc0

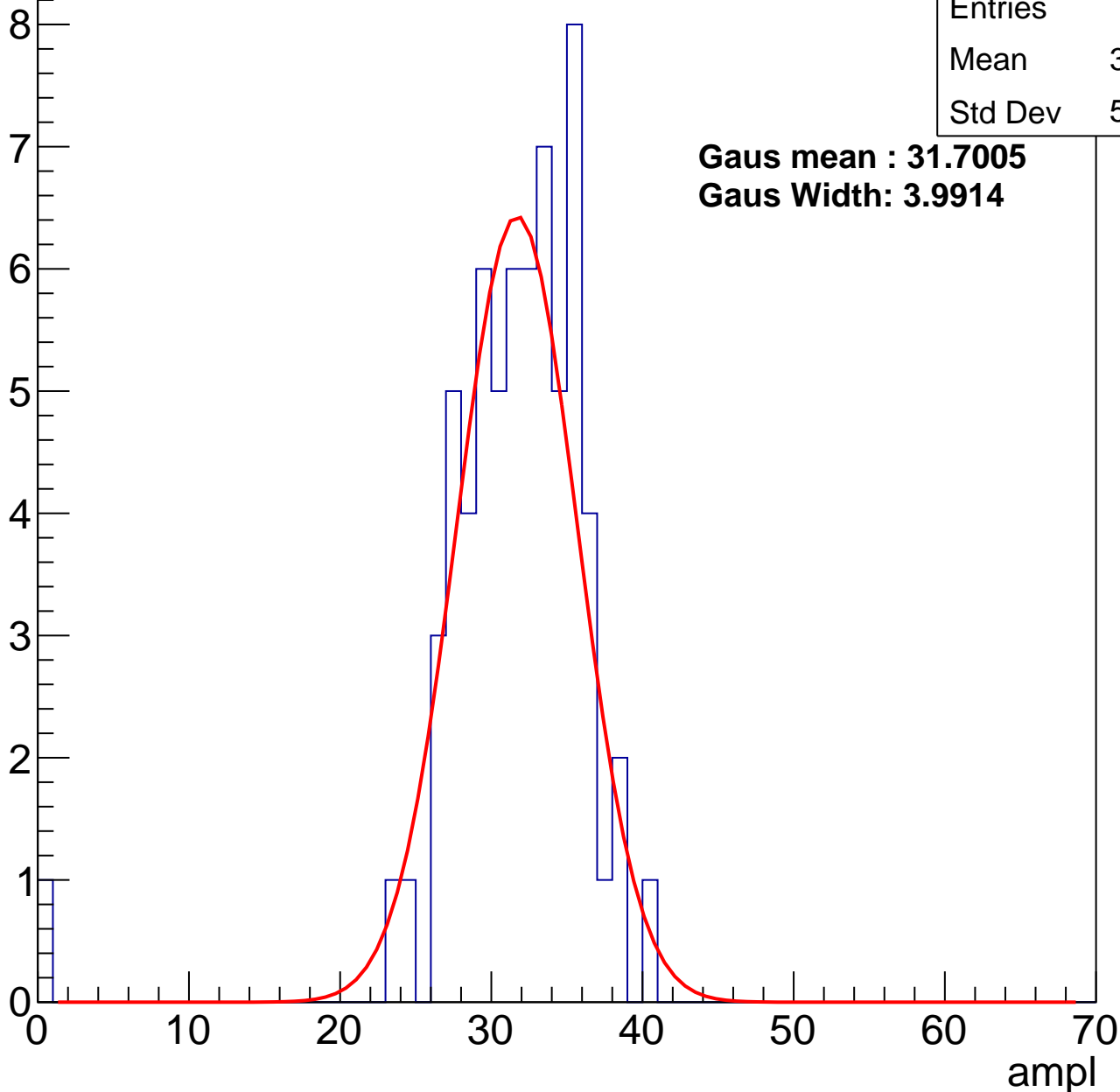
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	31.09
Std Dev	5.245

Gaus mean : 31.7005

Gaus Width: 3.9914



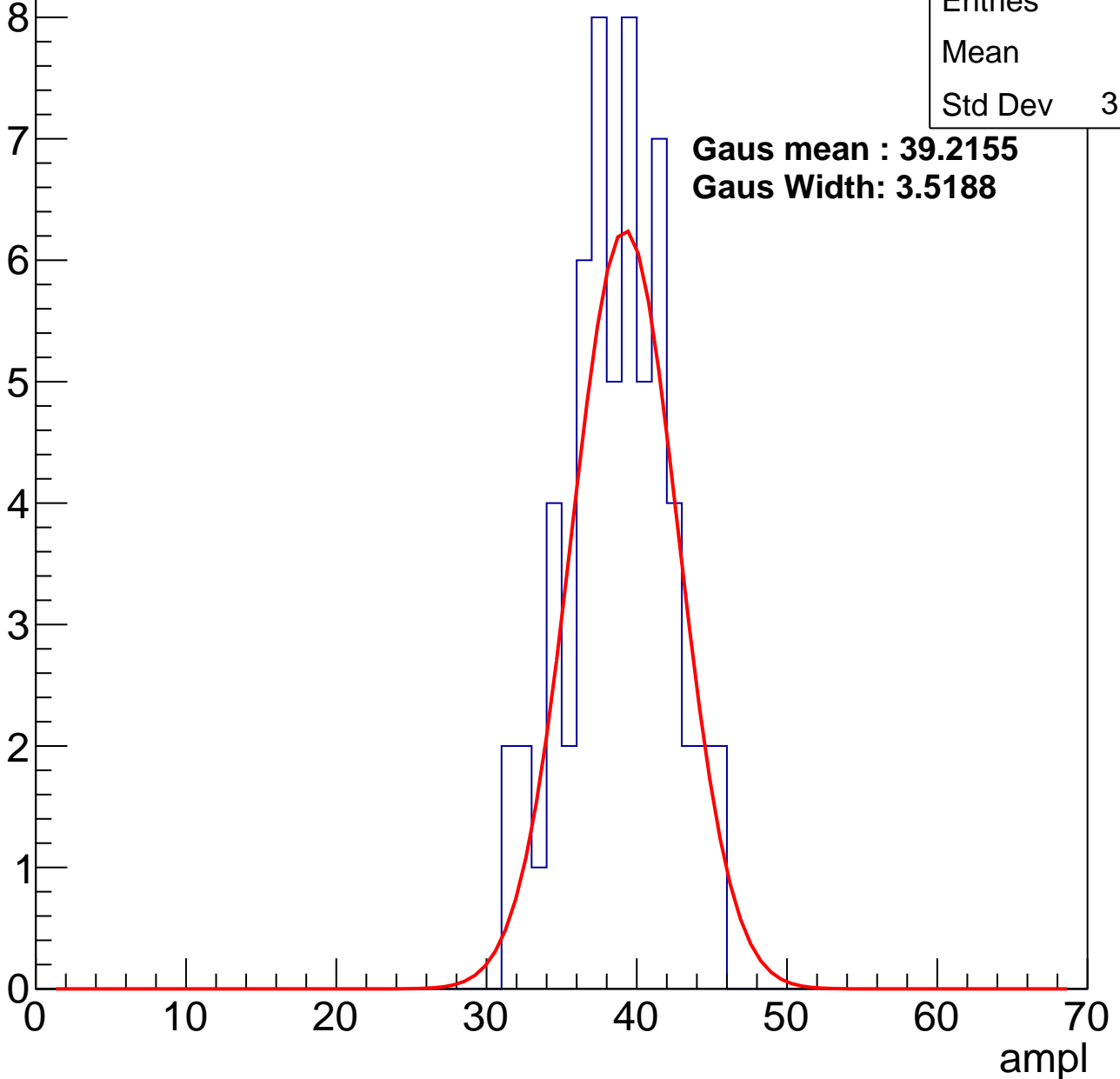
B1L101S, U21-ch52, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	38.3
Std Dev	3.363

Gaus mean : 39.2155
Gaus Width: 3.5188



B1L101S, U21-ch52, adc2

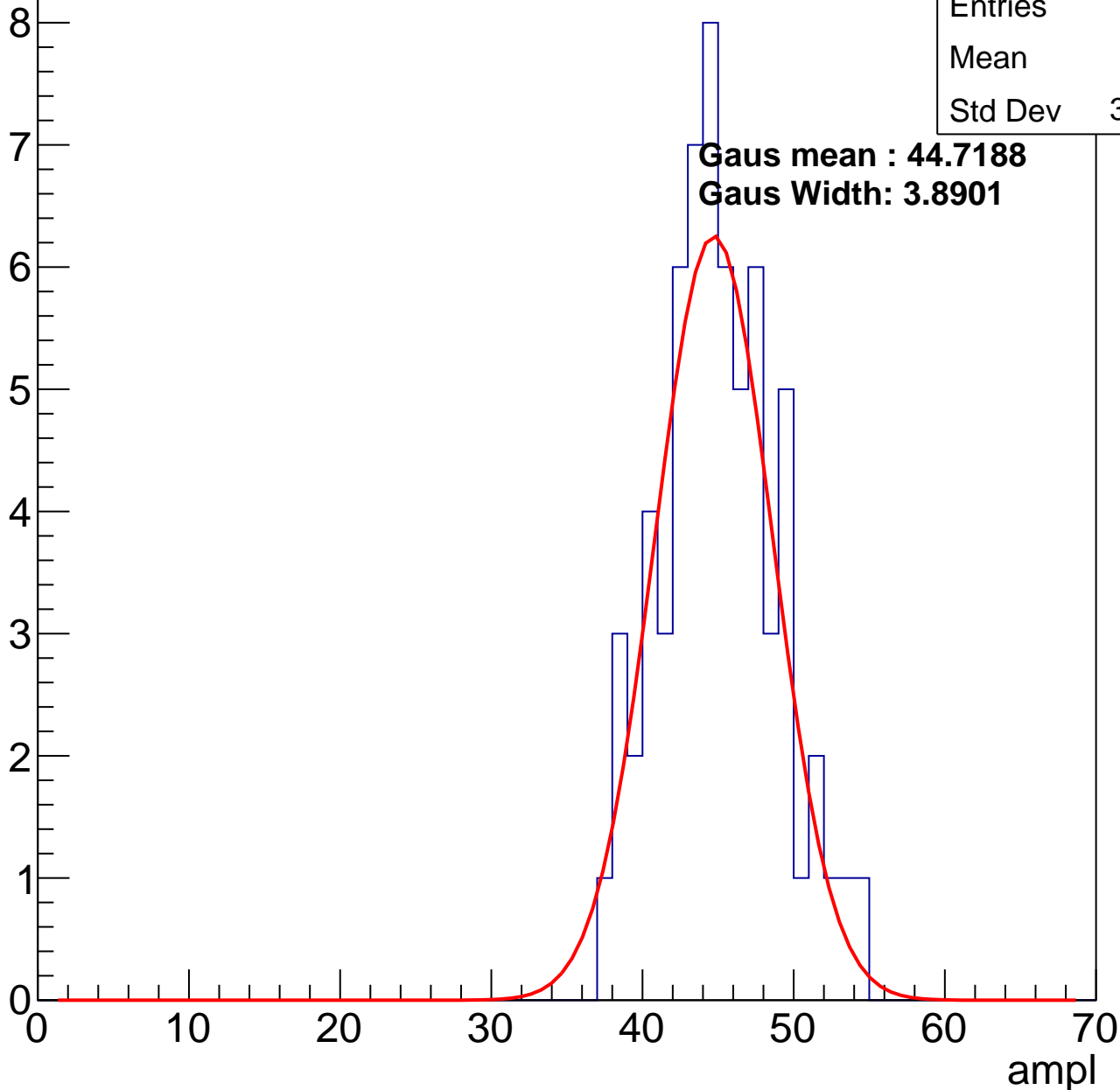
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.6
Std Dev	3.798

Gaus mean : 44.7188

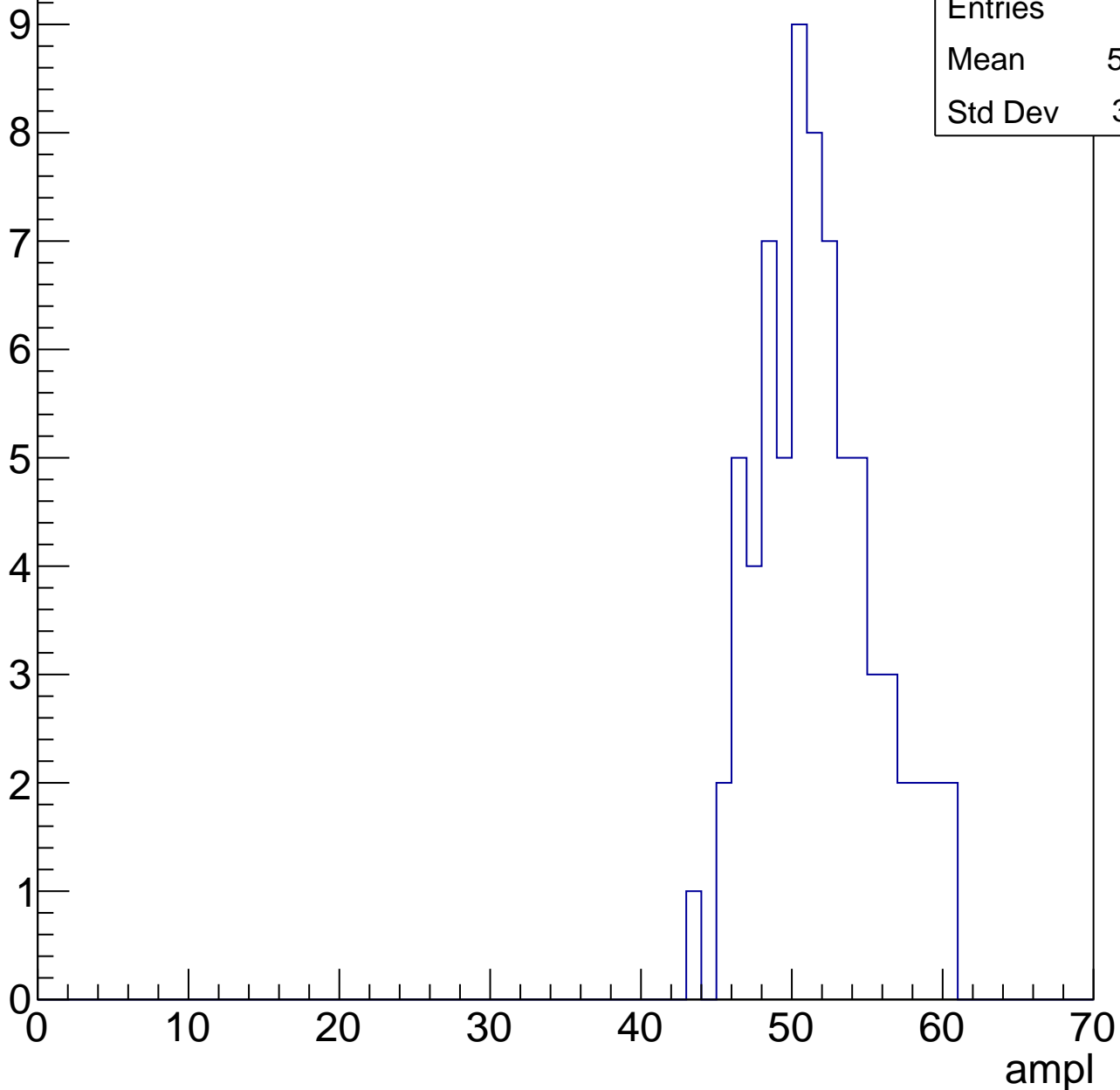
Gaus Width: 3.8901



B1L101S, U21-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



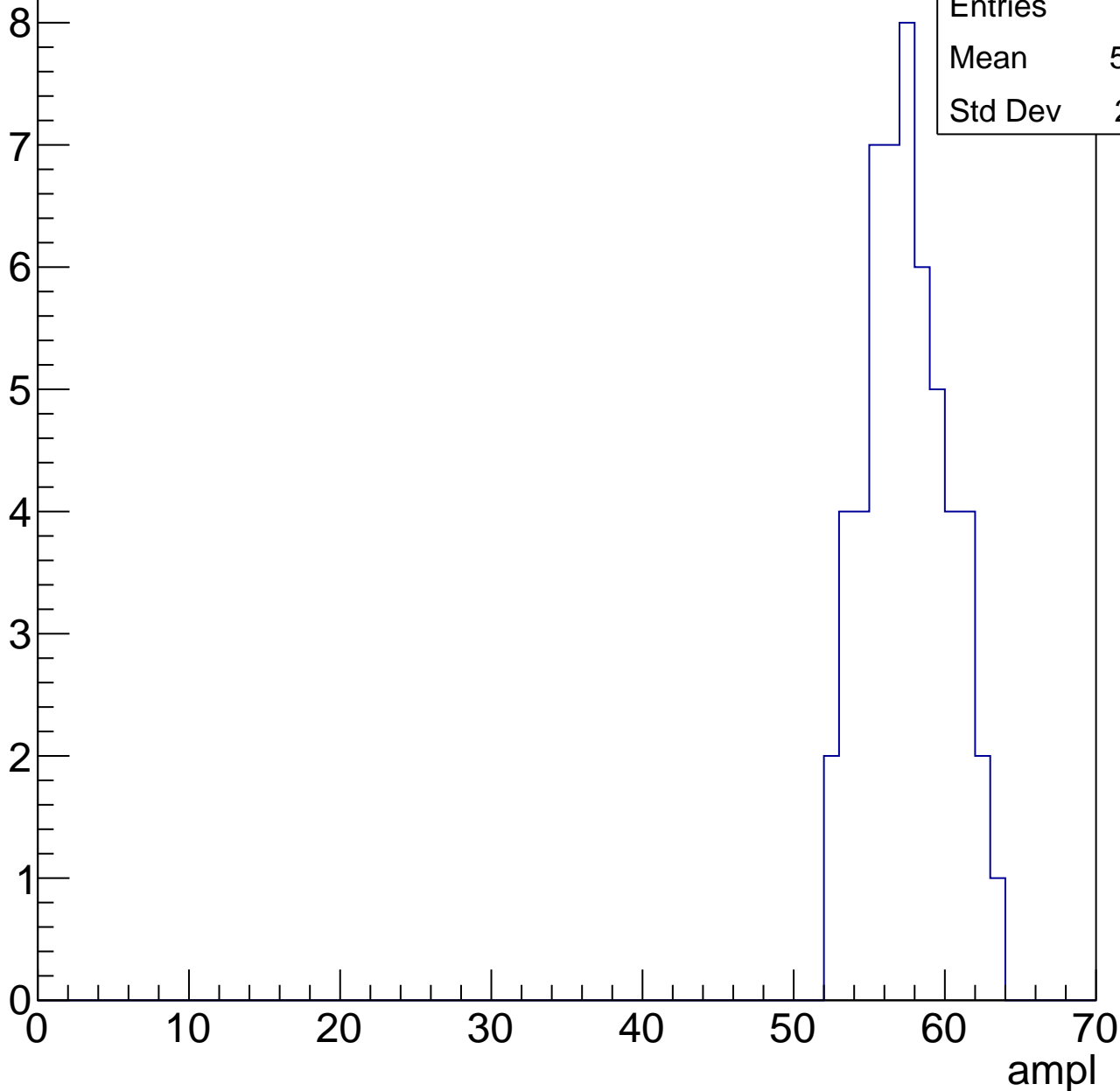
Entries	72
Mean	51.25
Std Dev	3.851

B1L101S, U21-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

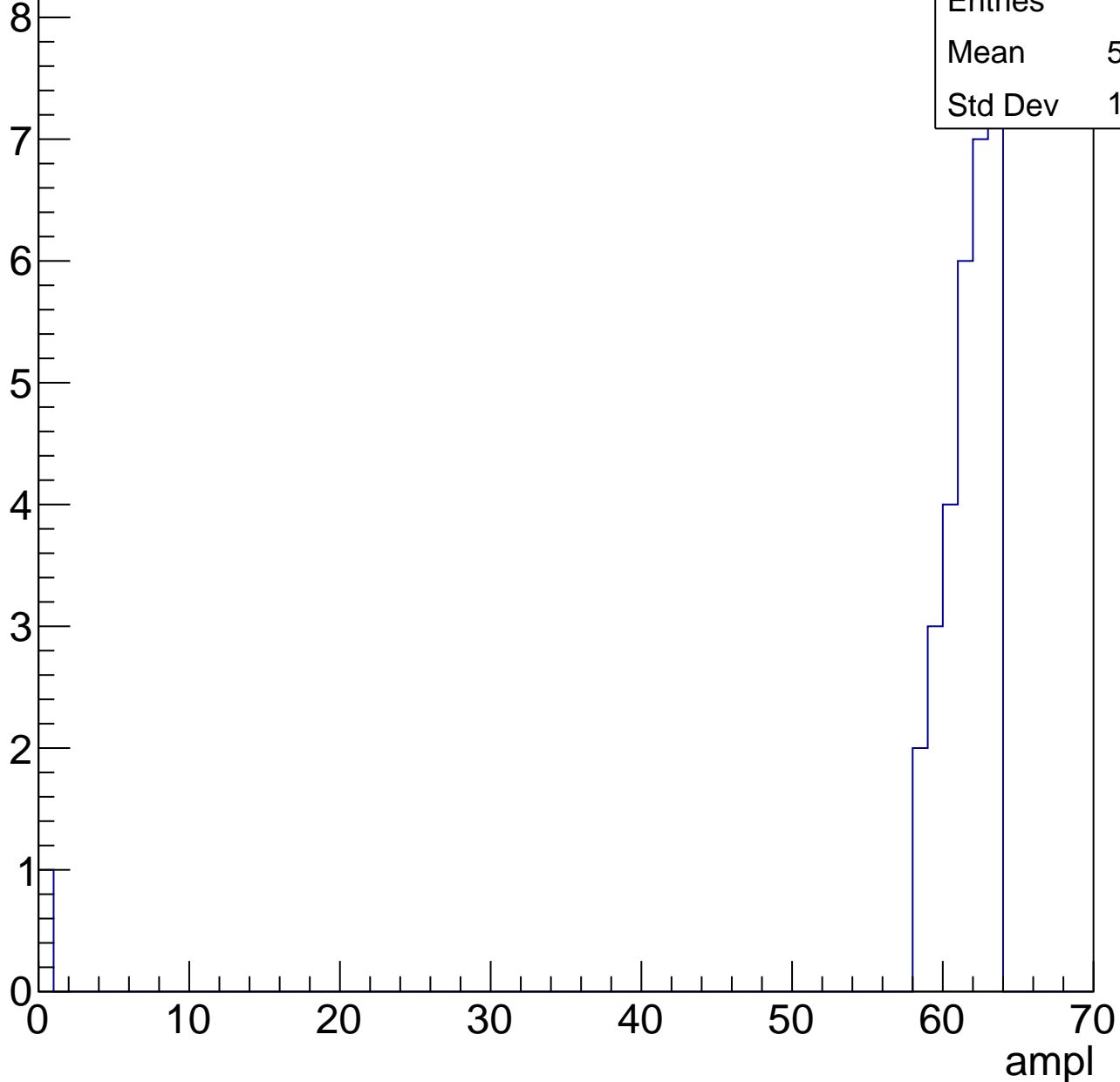
Entries	54
Mean	57.02
Std Dev	2.711



B1L101S, U21-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	31
Mean	59.26
Std Dev	10.92

B1L101S, U21-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch53, adc0

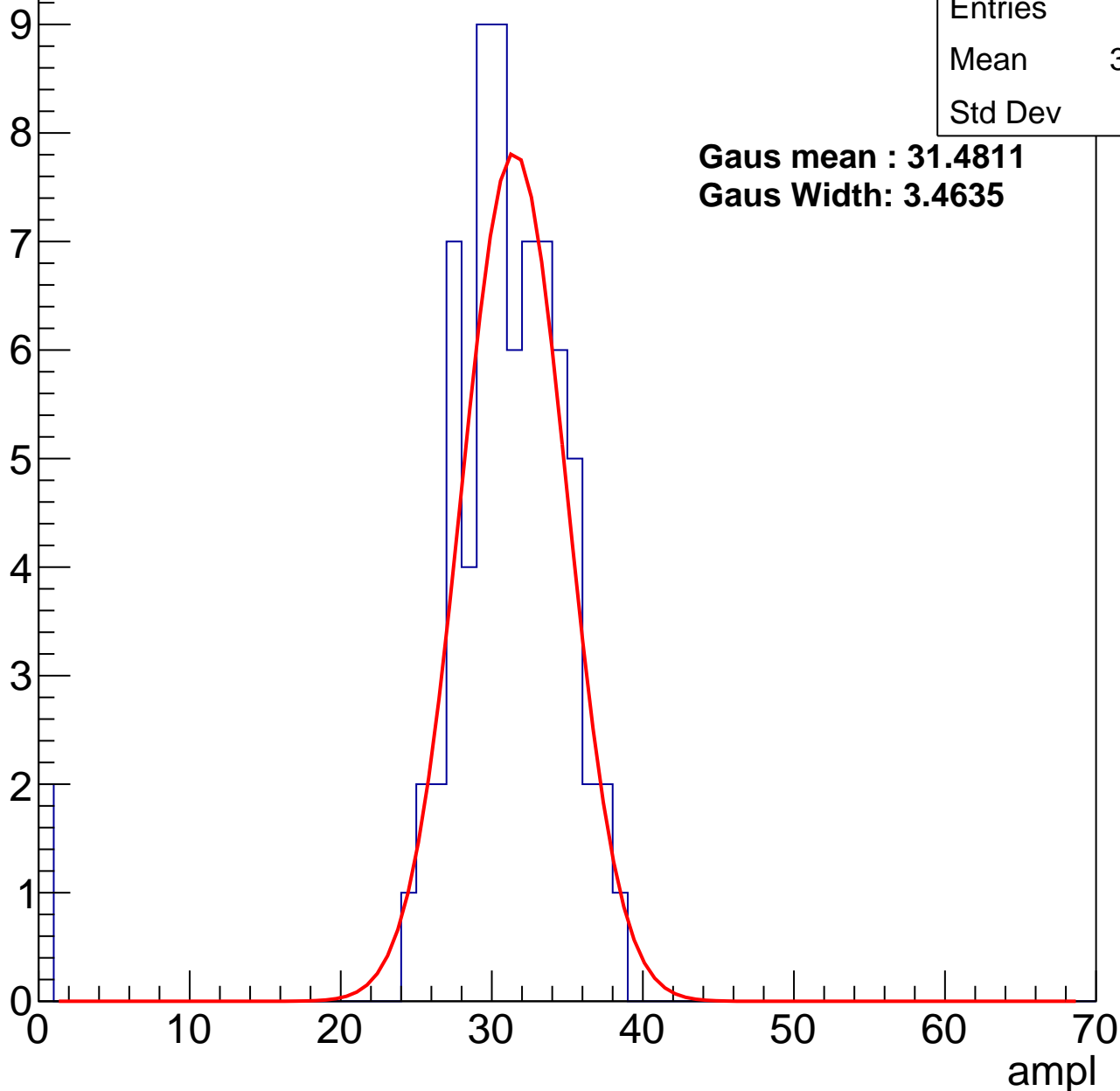
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.03
Std Dev	5.96

Gaus mean : 31.4811

Gaus Width: 3.4635



B1L101S, U21-ch53, adc1

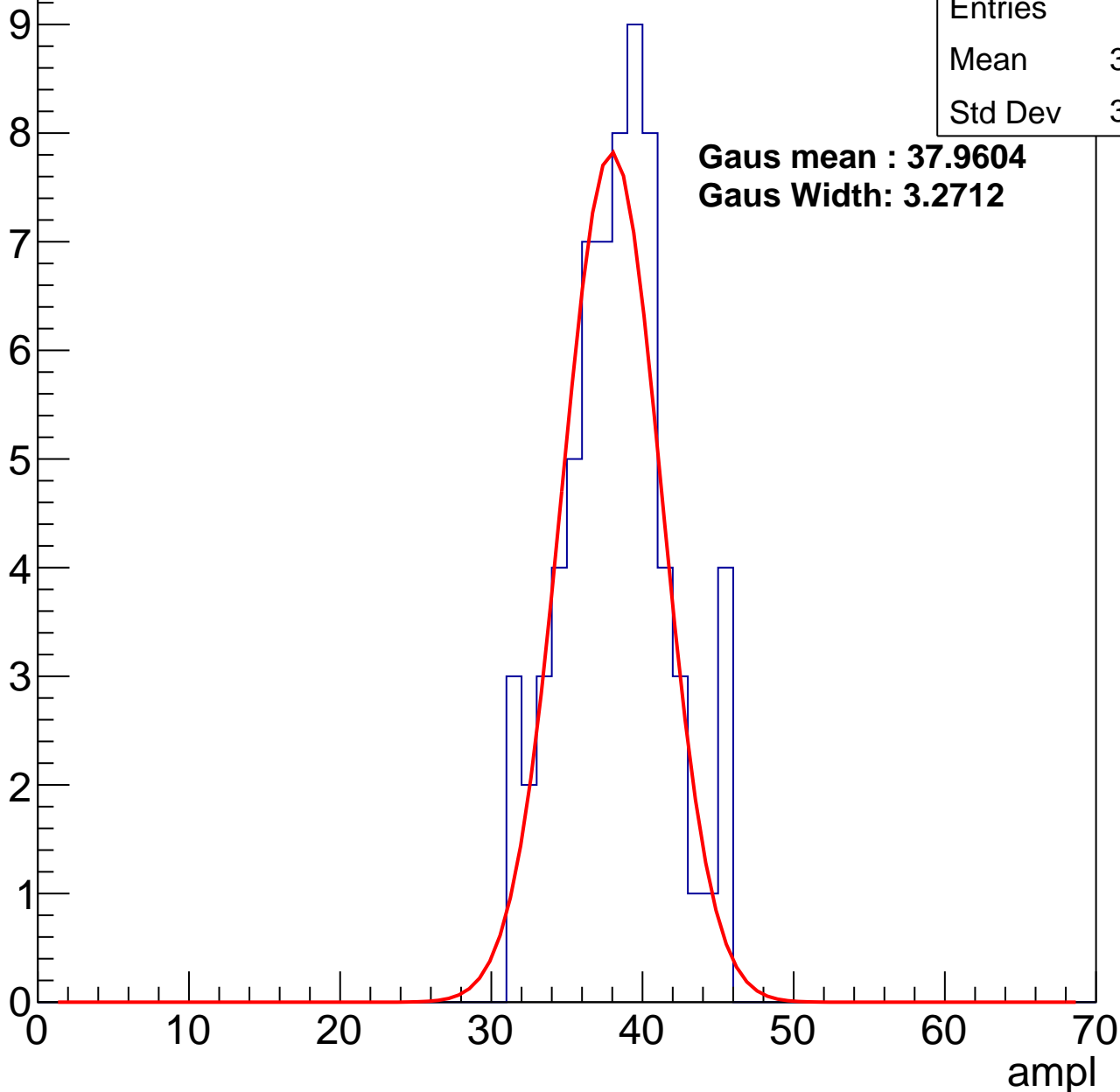
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.83
Std Dev	3.443

Gaus mean : 37.9604

Gaus Width: 3.2712



B1L101S, U21-ch53, adc2

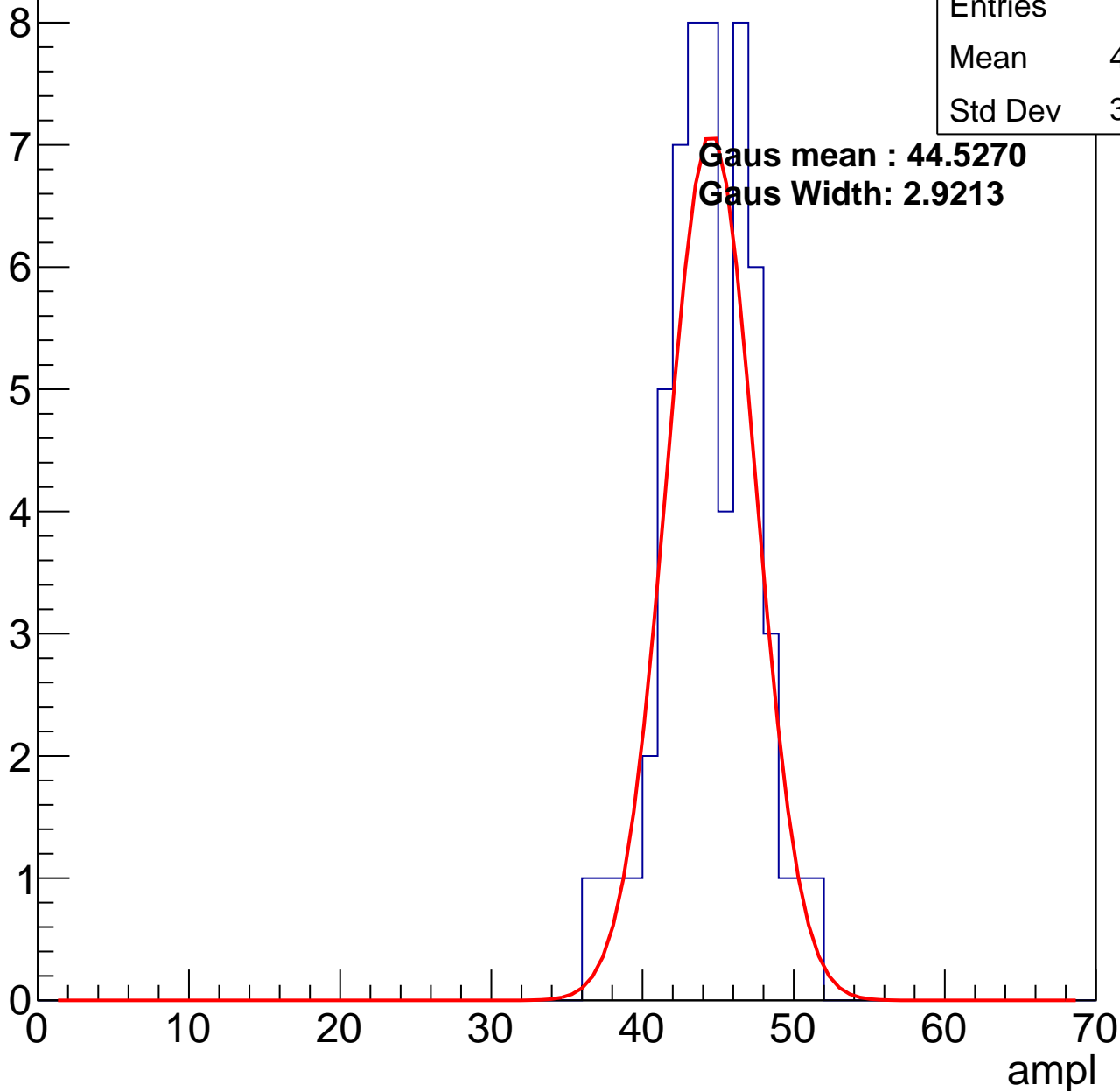
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	43.95
Std Dev	3.037

Gaus mean : 44.5270

Gaus Width: 2.9213

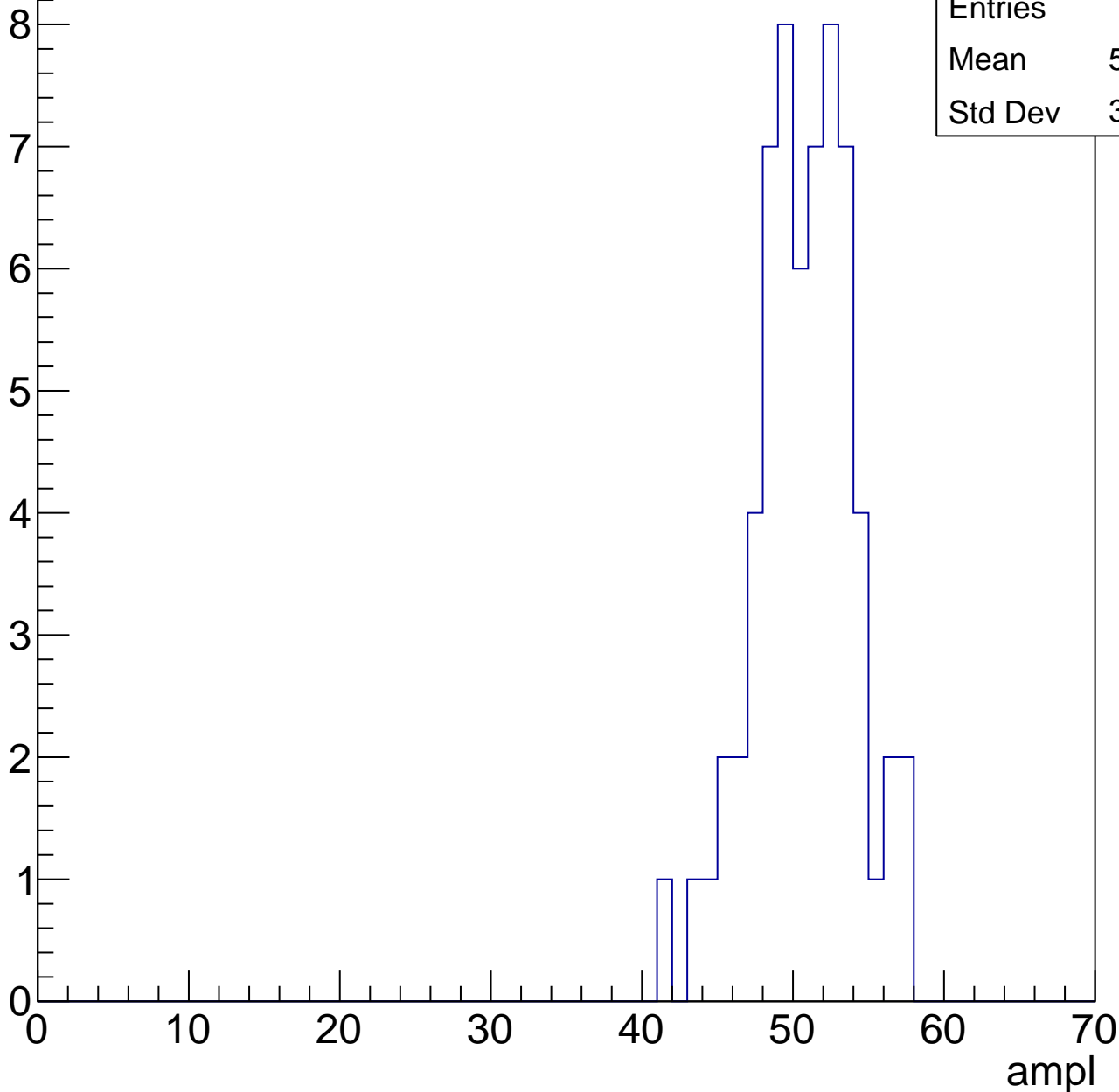


B1L101S, U21-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.27
Std Dev	3.277

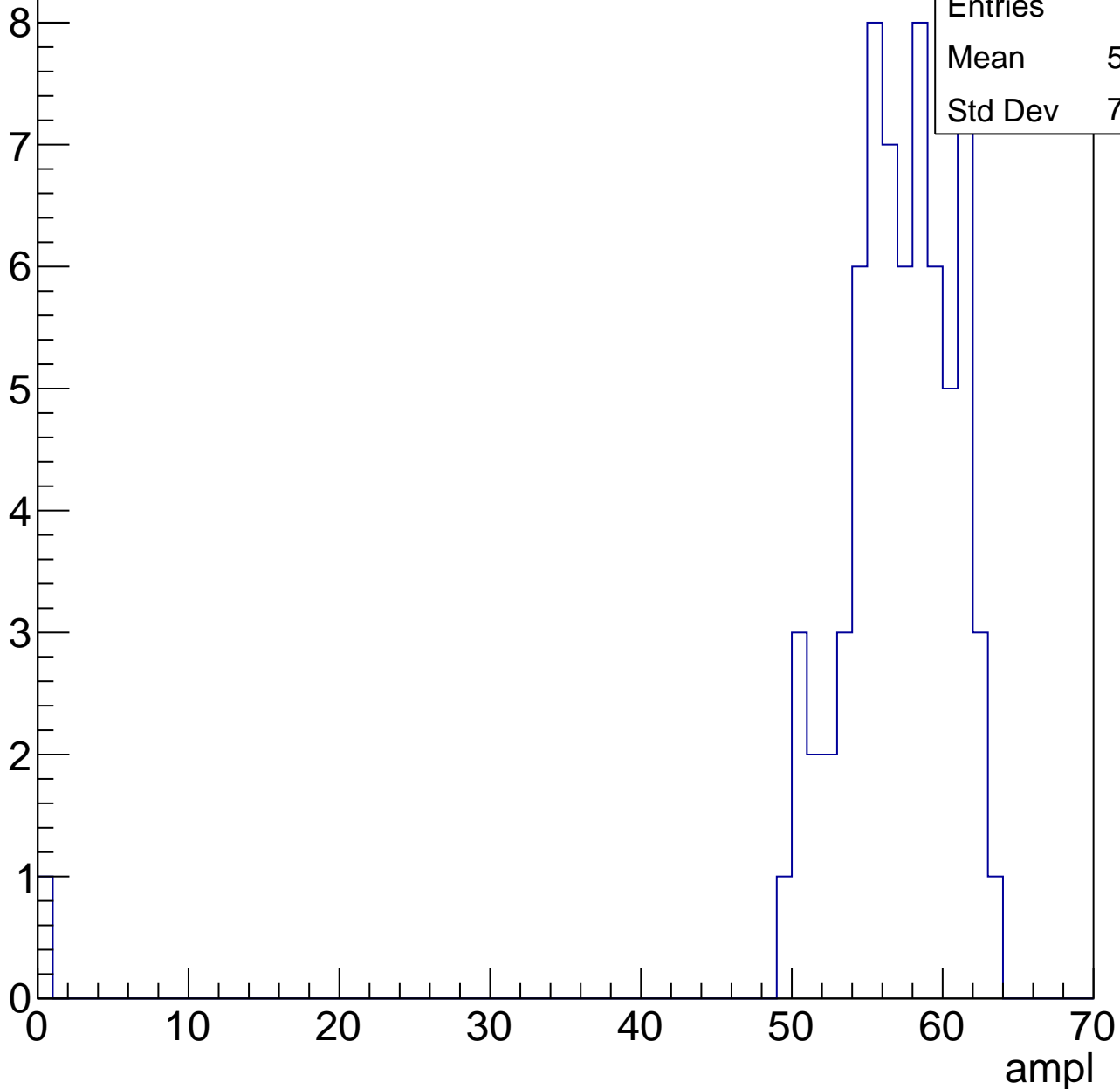


B1L101S, U21-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.96
Std Dev	7.524

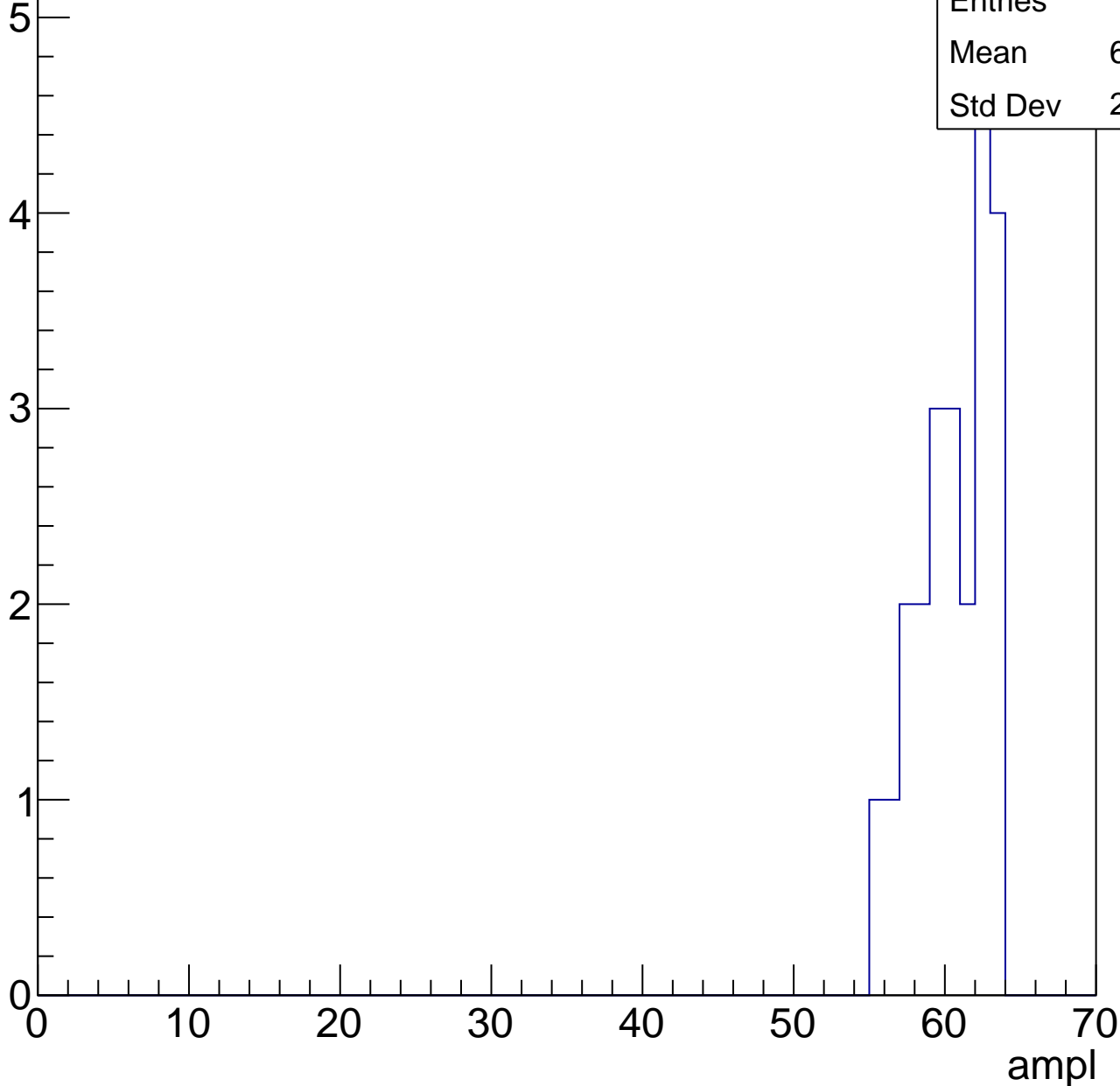


B1L101S, U21-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	60.09
Std Dev	2.357

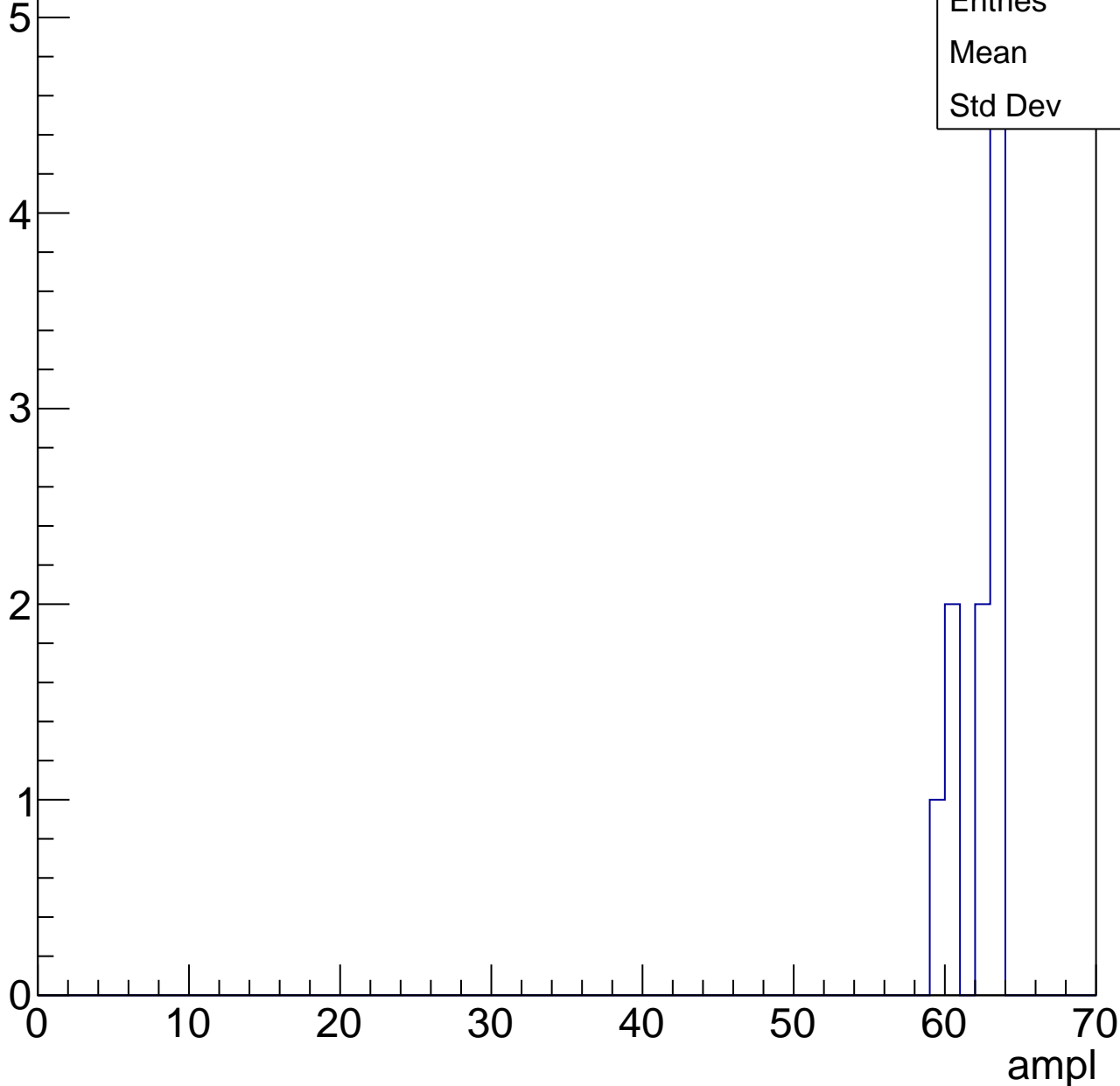


B1L101S, U21-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	61.8
Std Dev	1.47



B1L101S, U21-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch54, adc0

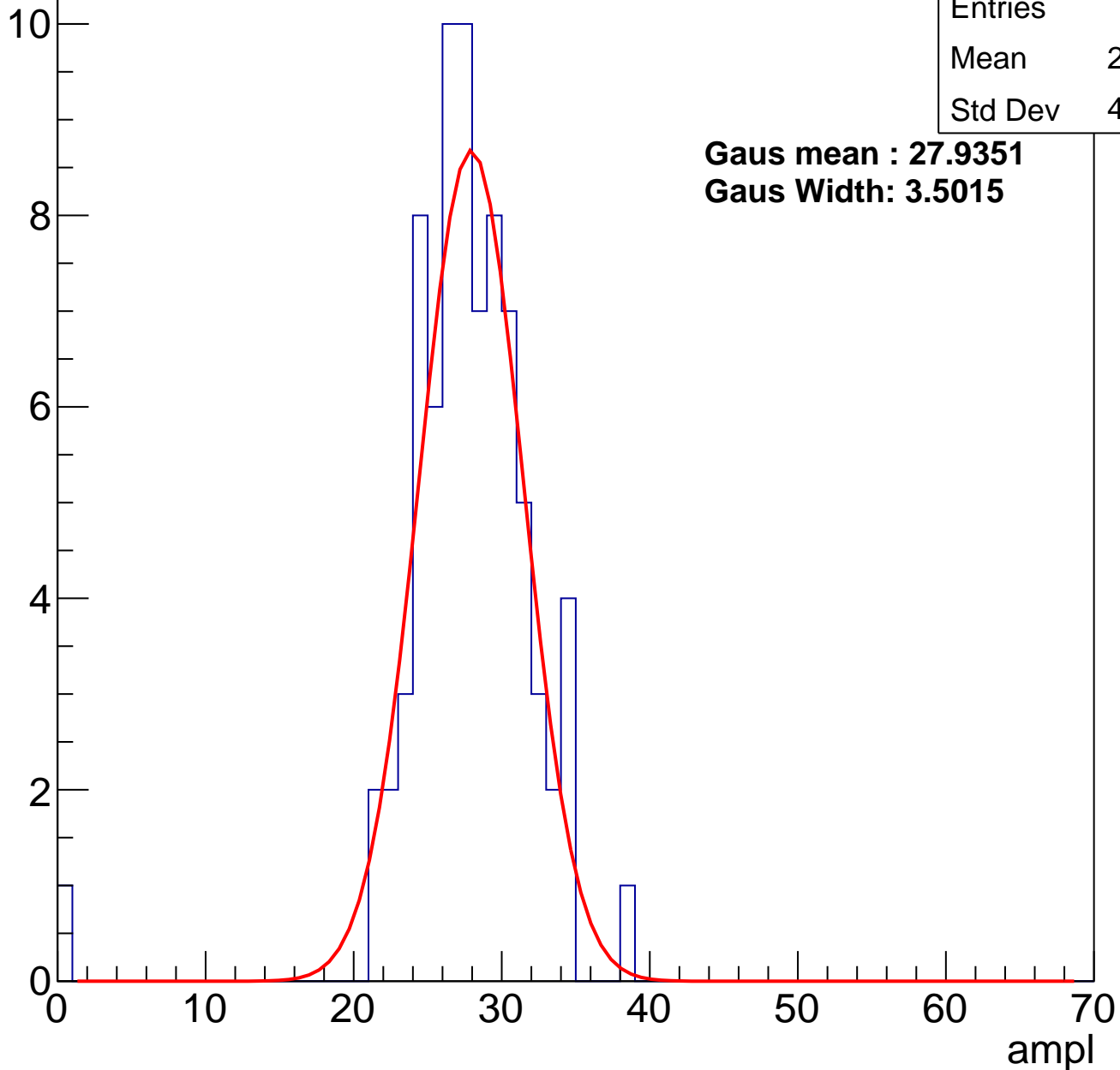
calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	27.29
Std Dev	4.562

Gaus mean : 27.9351

Gaus Width: 3.5015

Entry



B1L101S, U21-ch54, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	35.33
Std Dev	3.629

Gaus mean : 35.7588

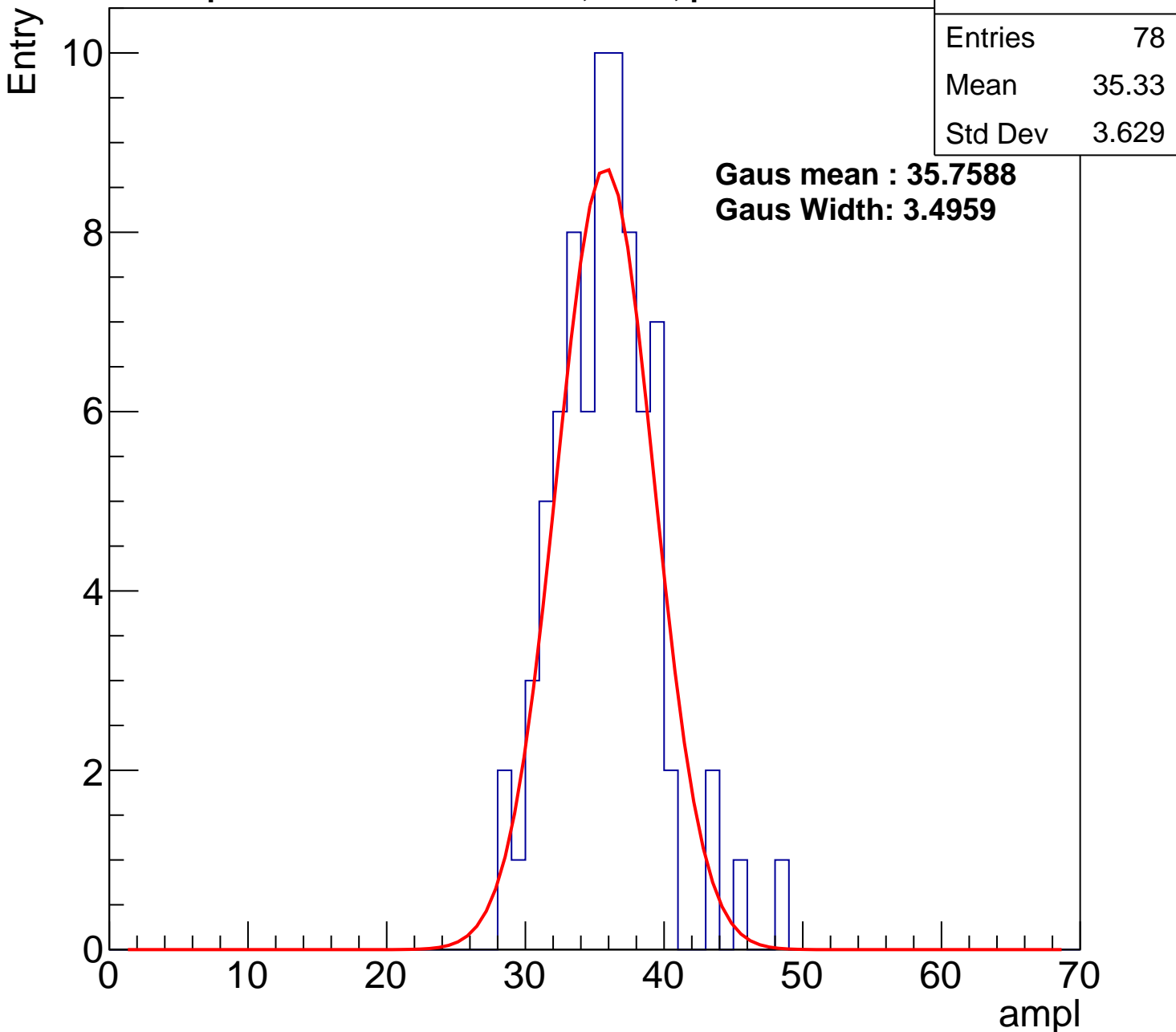
Gaus Width: 3.4959

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch54, adc2

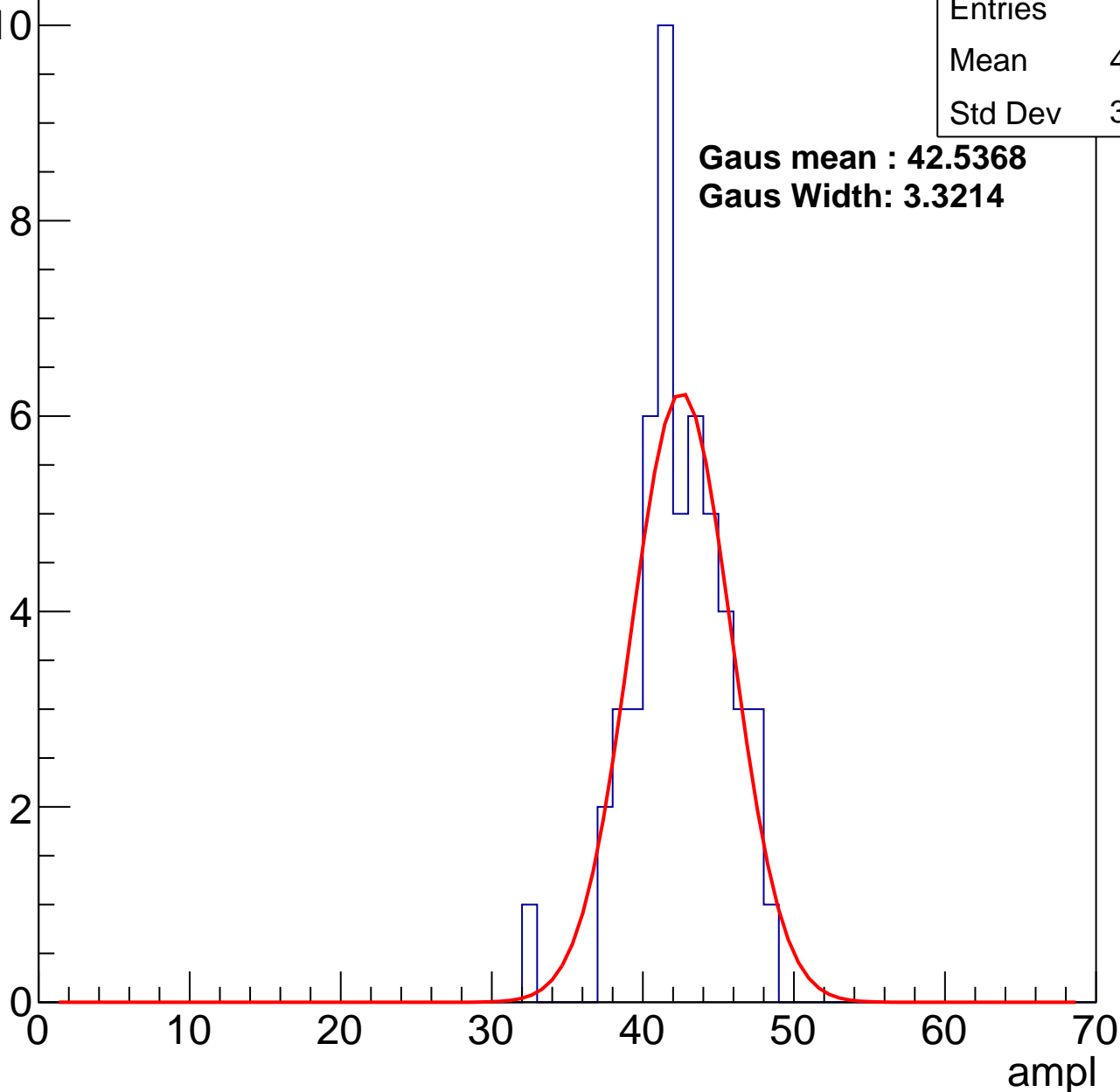
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.96
Std Dev	3.038

Gaus mean : 42.5368

Gaus Width: 3.3214

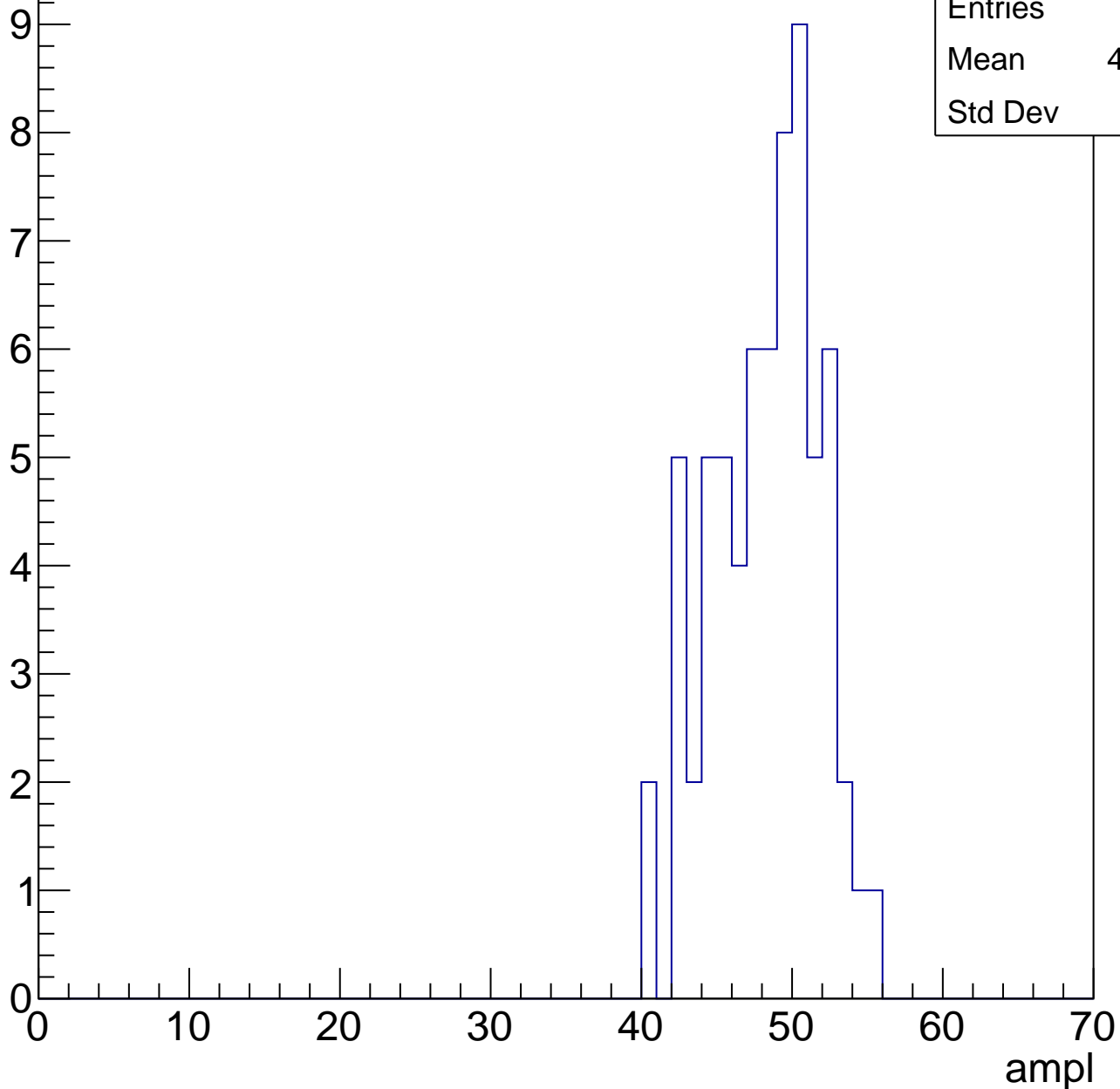


B1L101S, U21-ch54, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	47.75
Std Dev	3.5

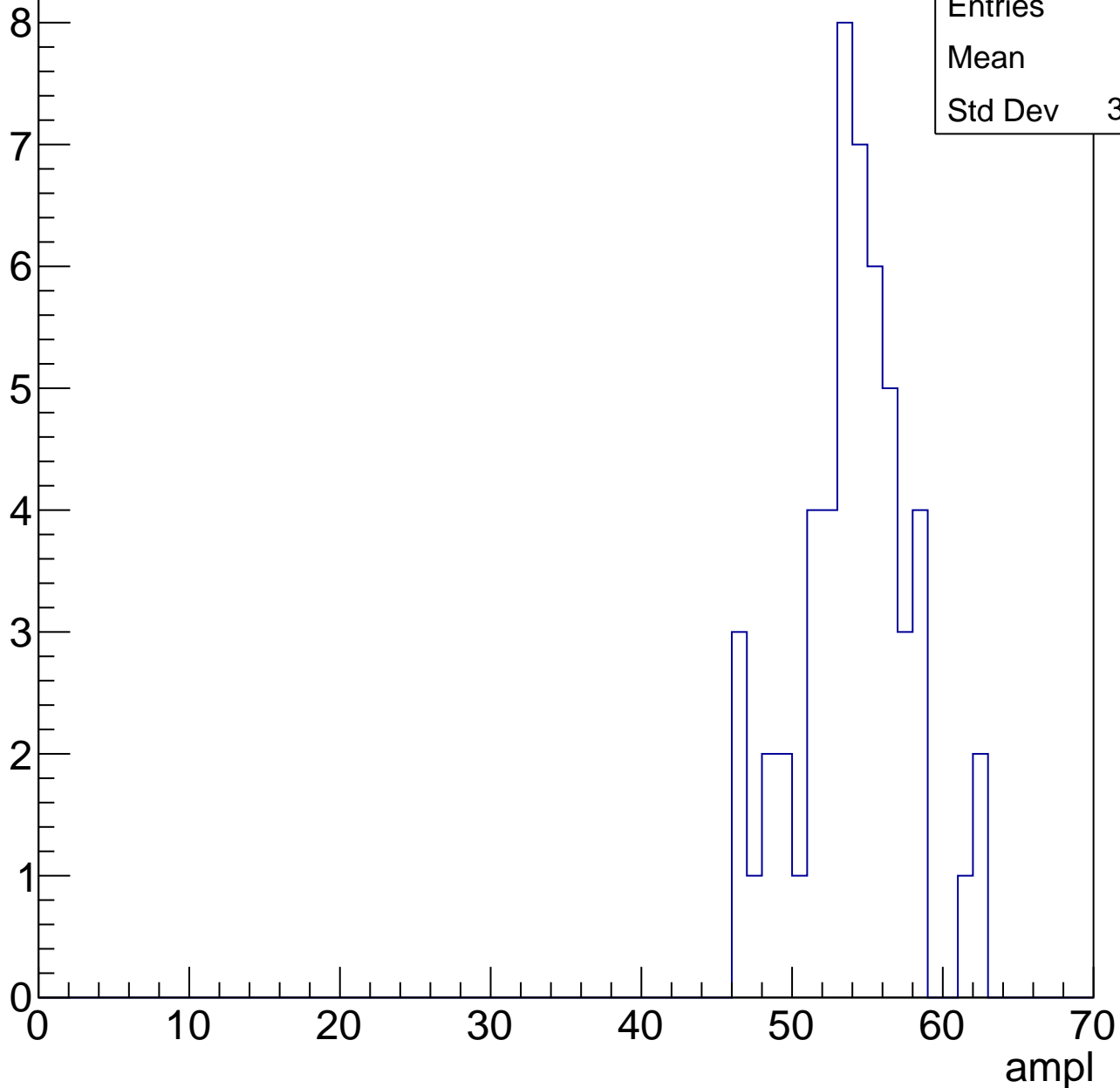


B1L101S, U21-ch54, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	53.6
Std Dev	3.698



B1L101S, U21-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	59.26
Std Dev	2.66

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

0

2

4

6

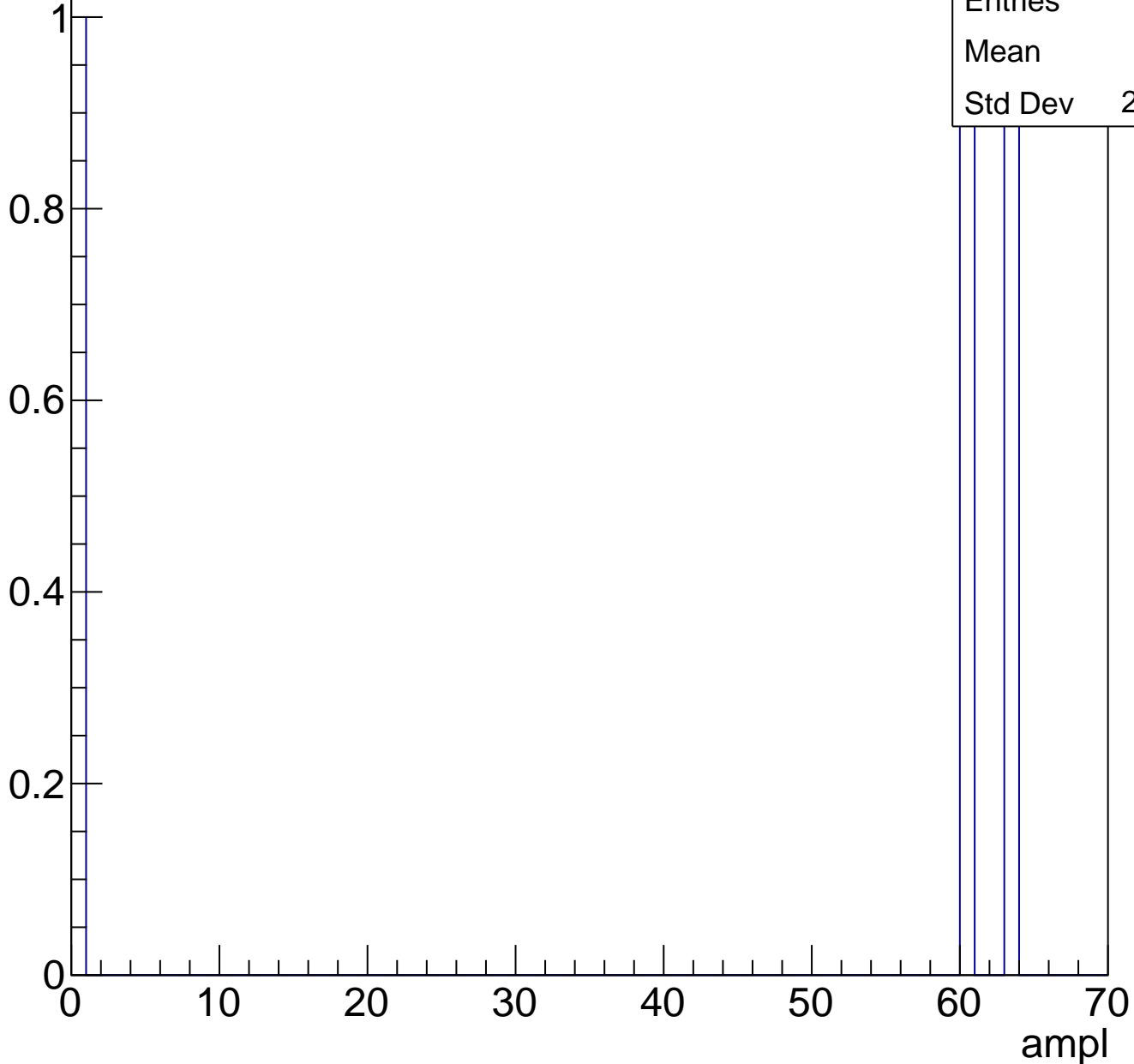
8

10

B1L101S, U21-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch55, adc0

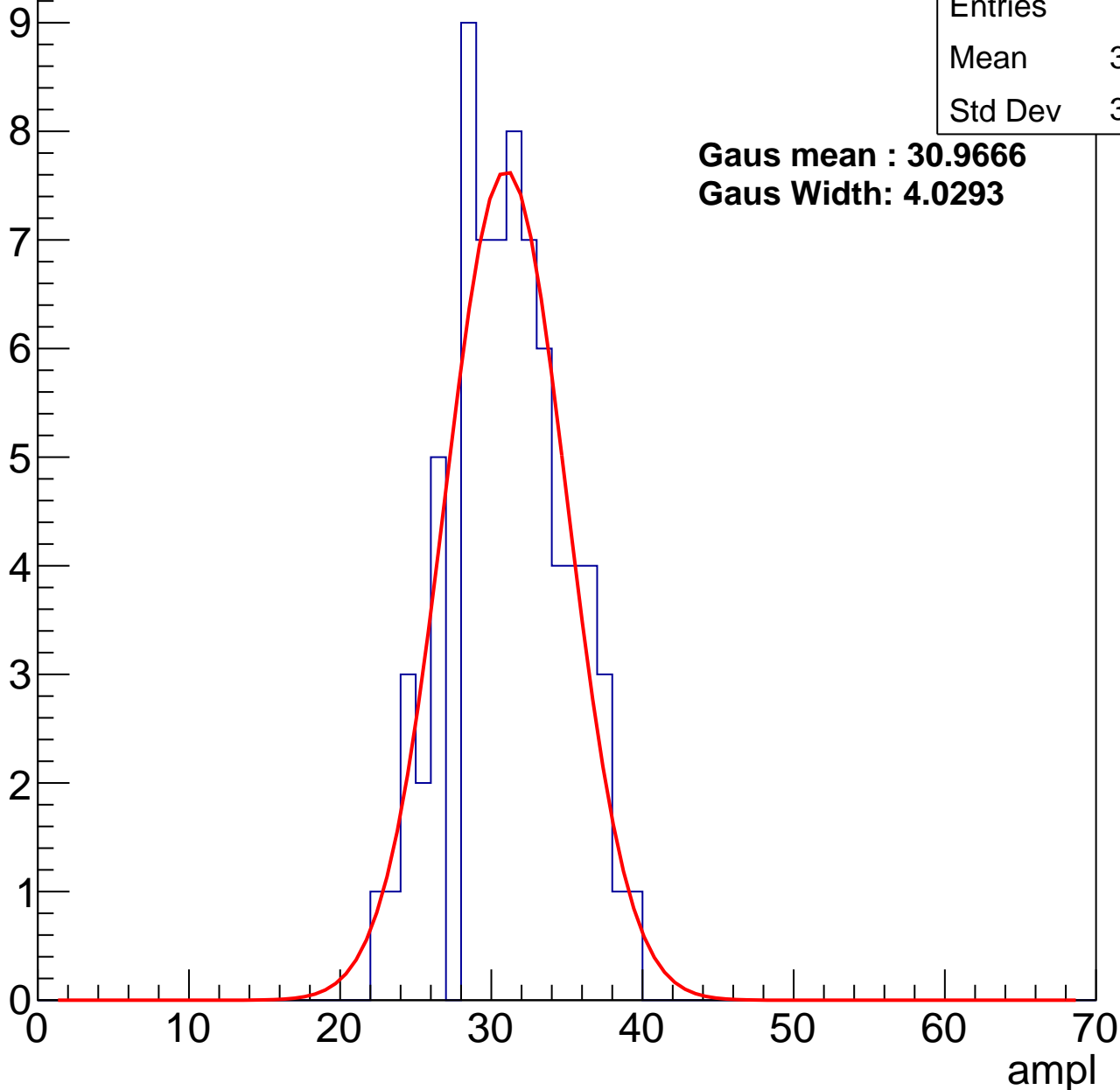
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.68
Std Dev	3.792

Gaus mean : 30.9666

Gaus Width: 4.0293



B1L101S, U21-ch55, adc1

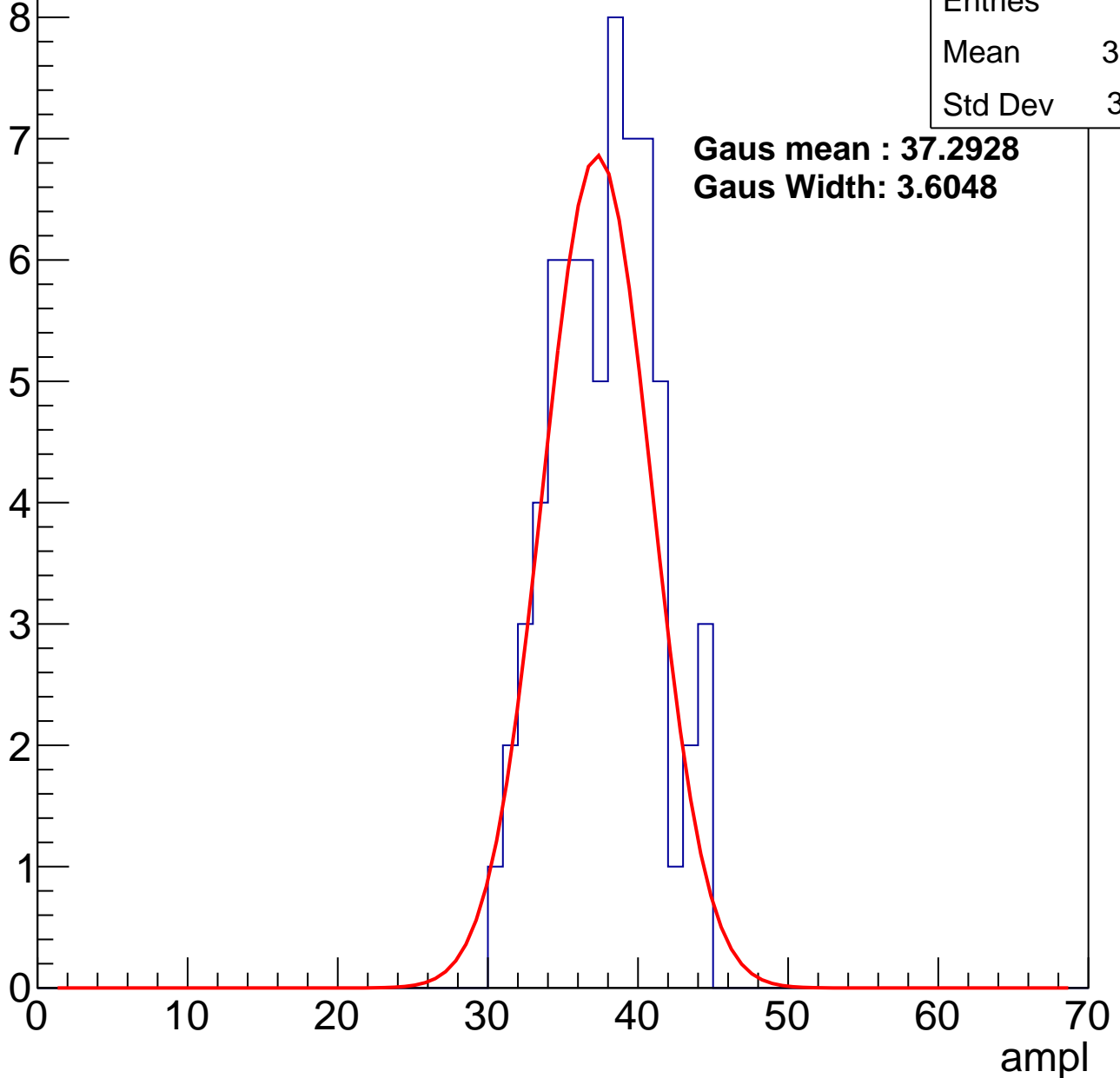
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.23
Std Dev	3.401

Gaus mean : 37.2928

Gaus Width: 3.6048



B1L101S, U21-ch55, adc2

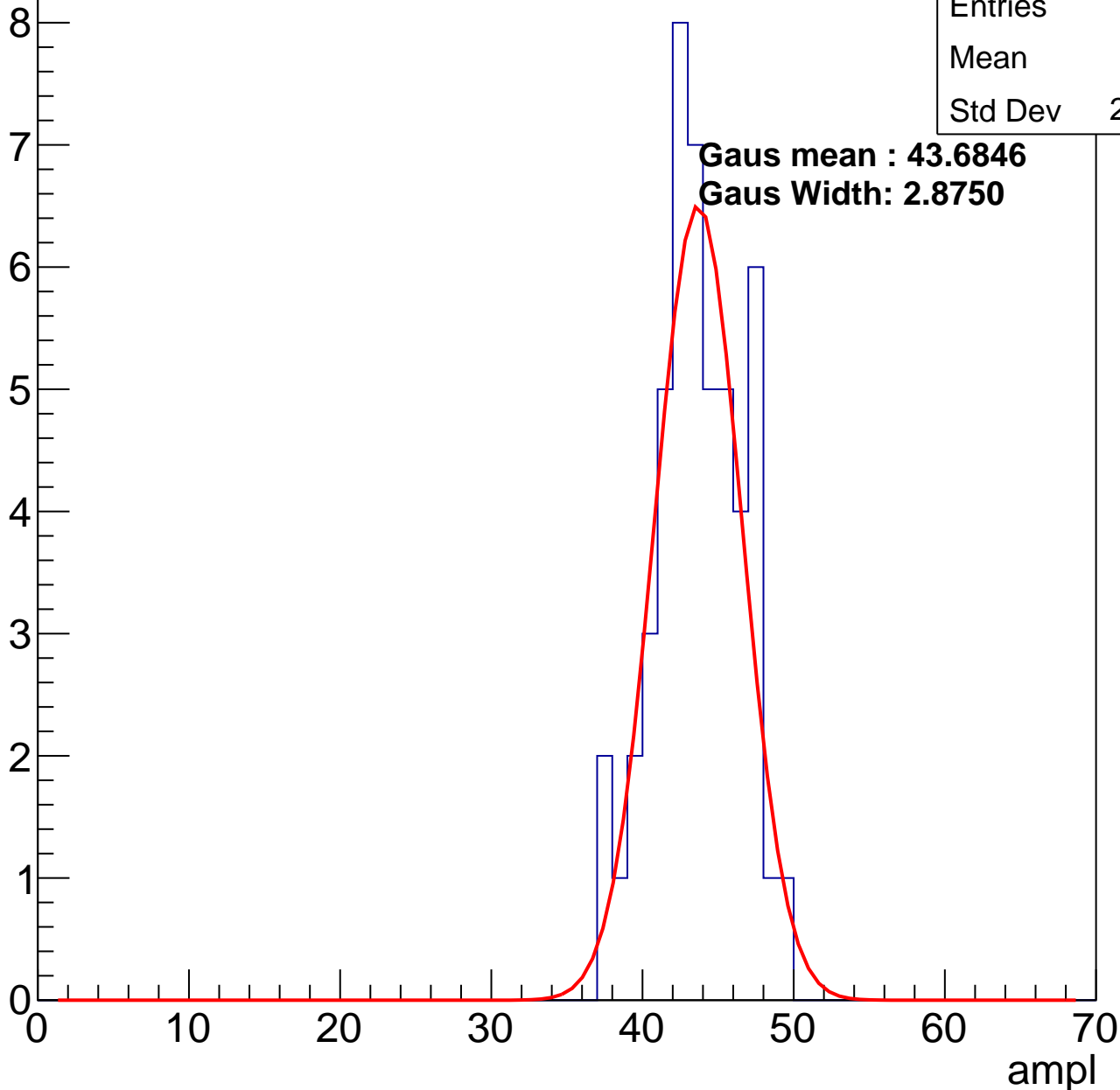
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	43.2
Std Dev	2.828

Gaus mean : 43.6846

Gaus Width: 2.8750



B1L101S, U21-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	83
Mean	50.14
Std Dev	3.567

Entry

10

8

6

4

2

0

0

10

20

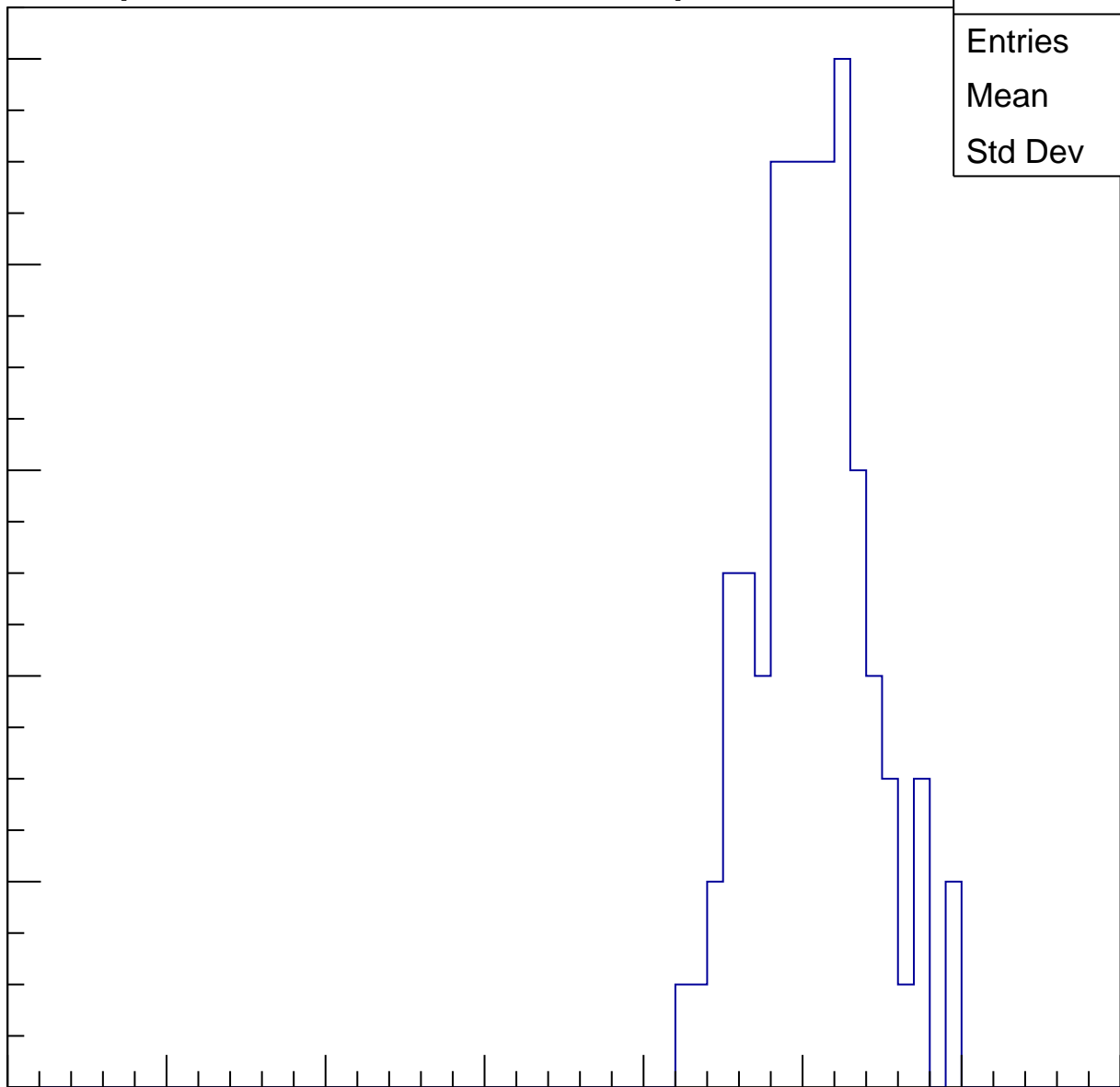
30

40

50

60

ampl

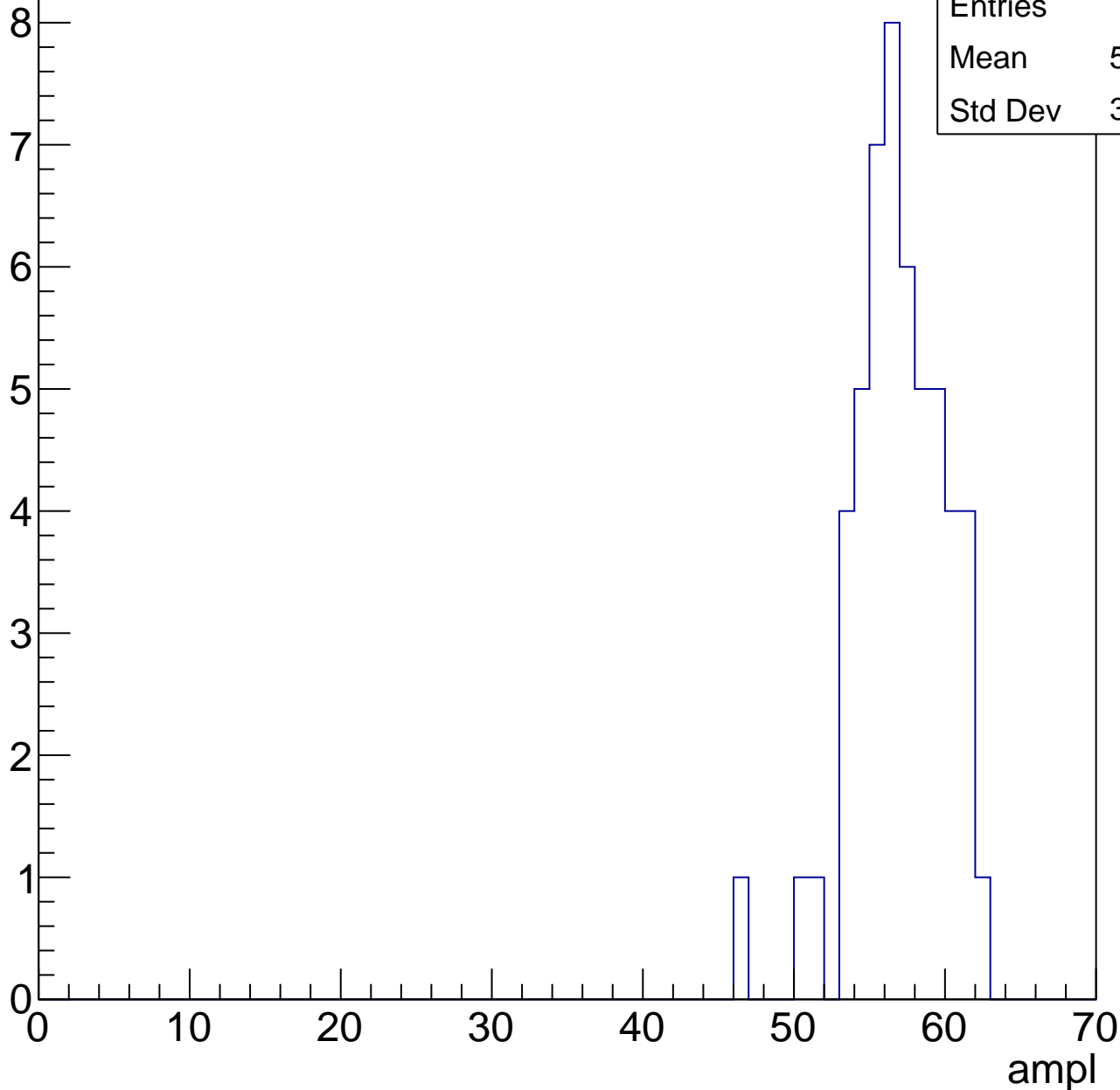


B1L101S, U21-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.44
Std Dev	3.053



B1L101S, U21-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	43
Mean	59.26
Std Dev	9.391

Entry

10

8

6

4

2

0

0

10

20

30

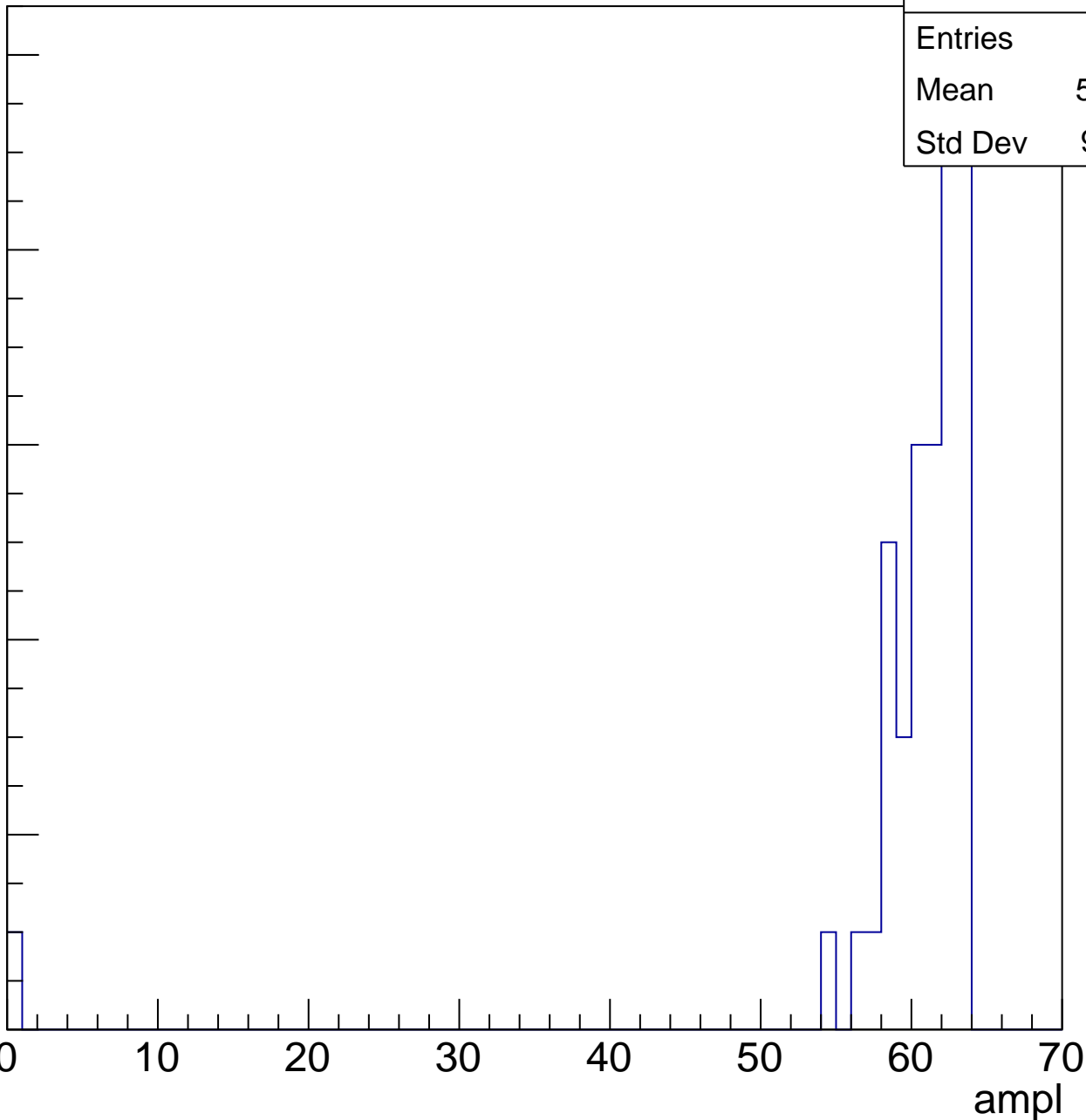
40

50

60

70

ampl



B1L101S, U21-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch56, adc0

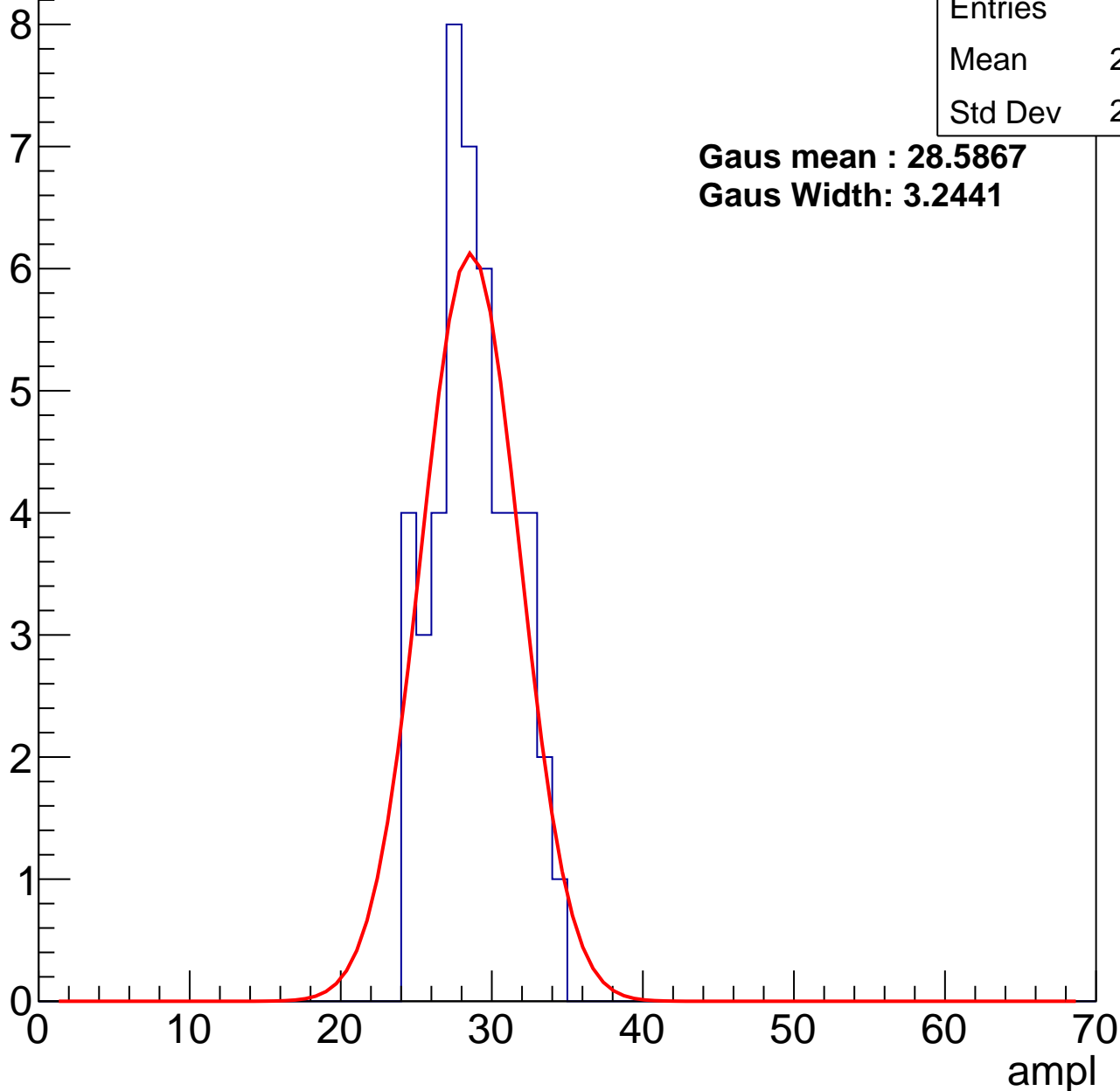
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	28.36
Std Dev	2.596

Gaus mean : 28.5867

Gaus Width: 3.2441



B1L101S, U21-ch56, adc1

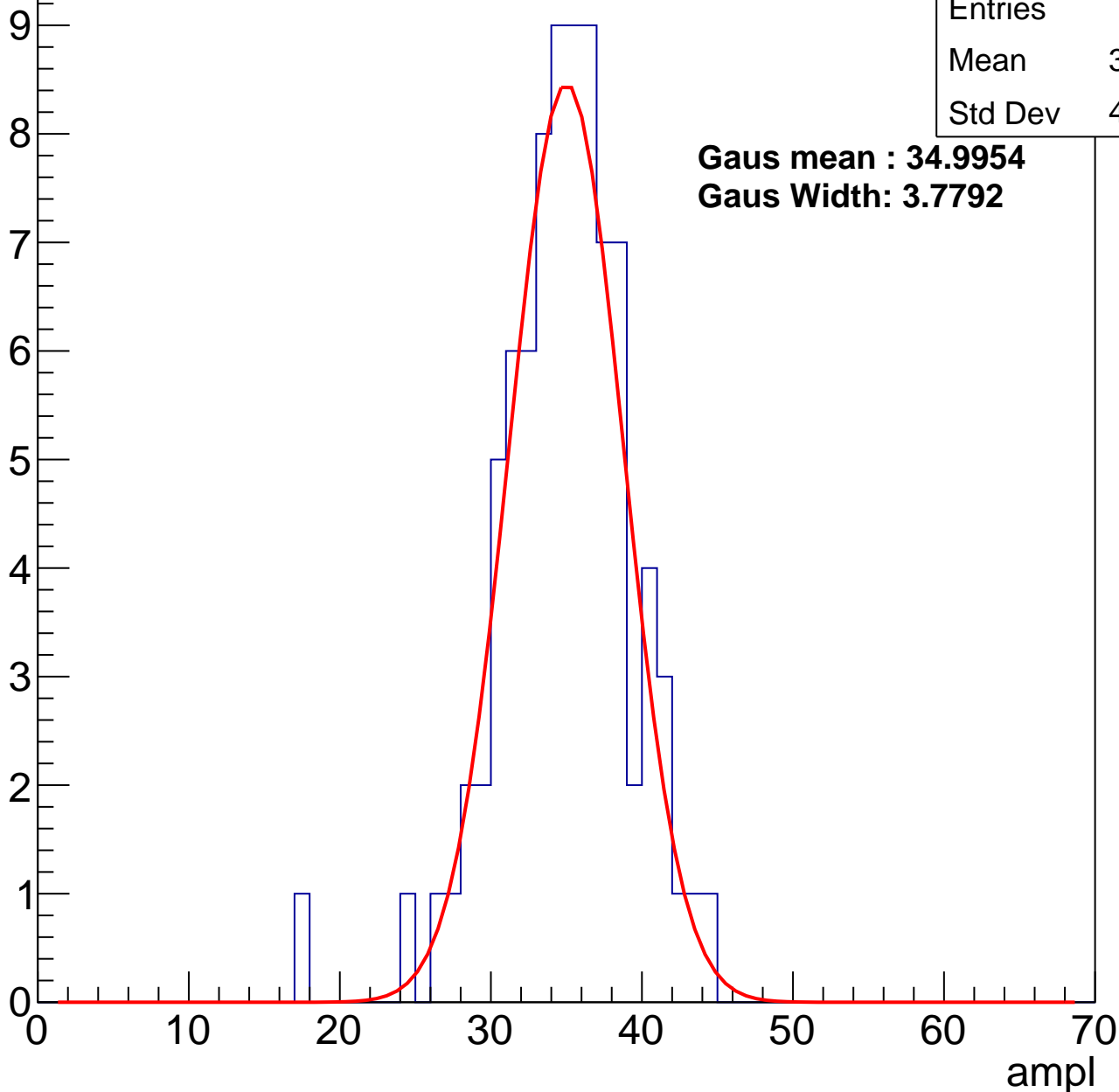
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	34.42
Std Dev	4.288

Gaus mean : 34.9954

Gaus Width: 3.7792



B1L101S, U21-ch56, adc2

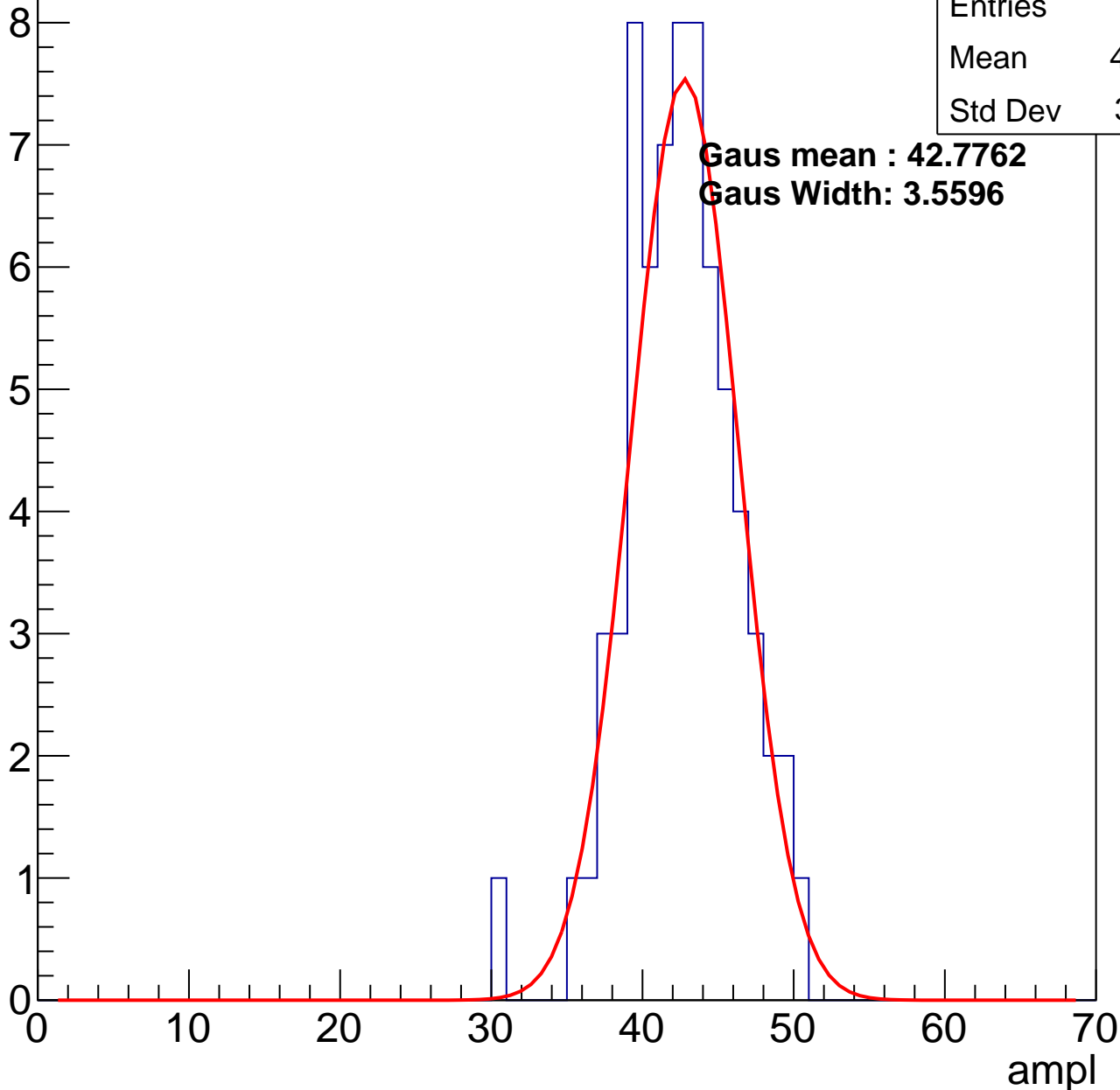
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.07
Std Dev	3.621

Gaus mean : 42.7762

Gaus Width: 3.5596

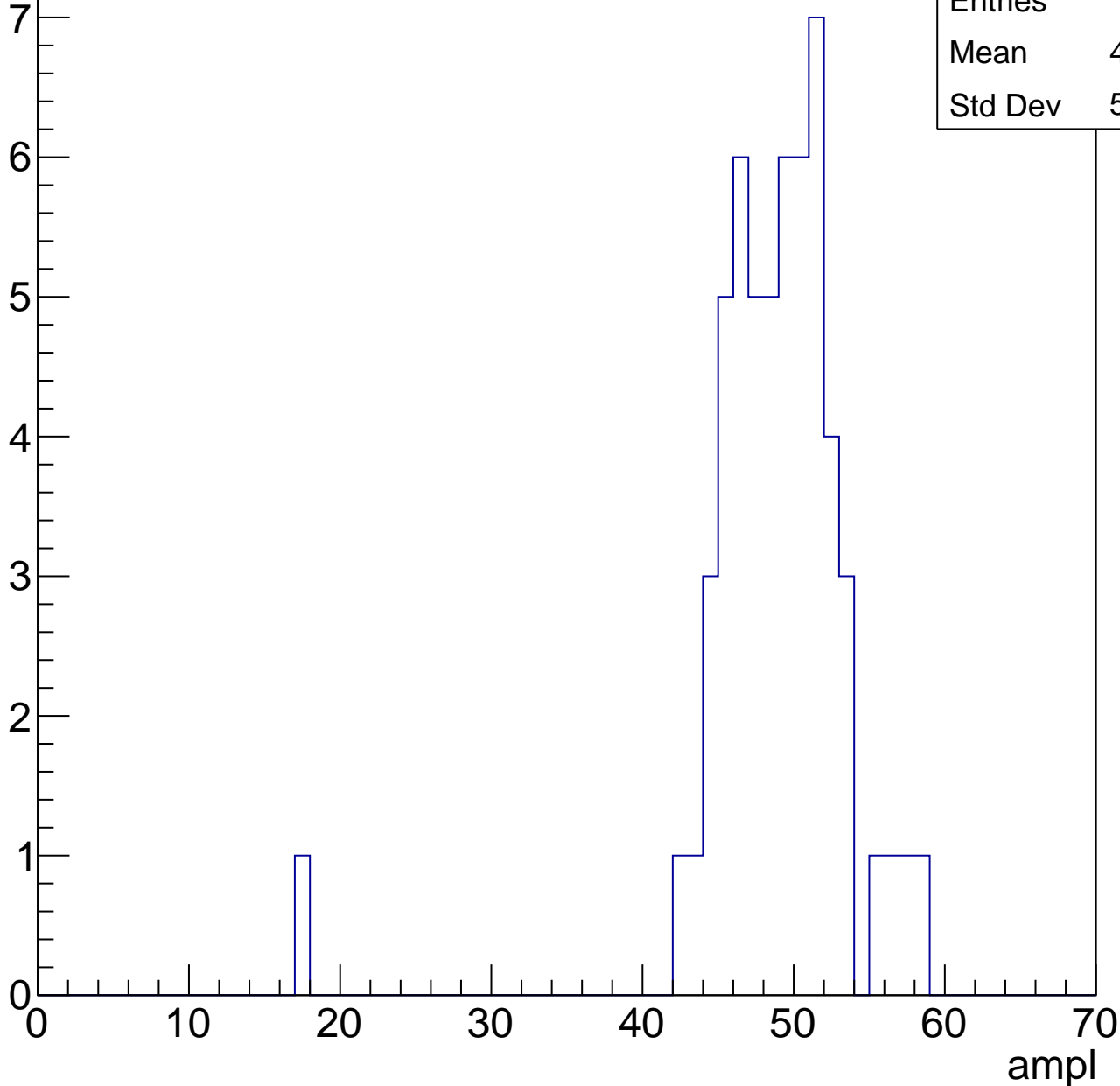


B1L101S, U21-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	48.32
Std Dev	5.404

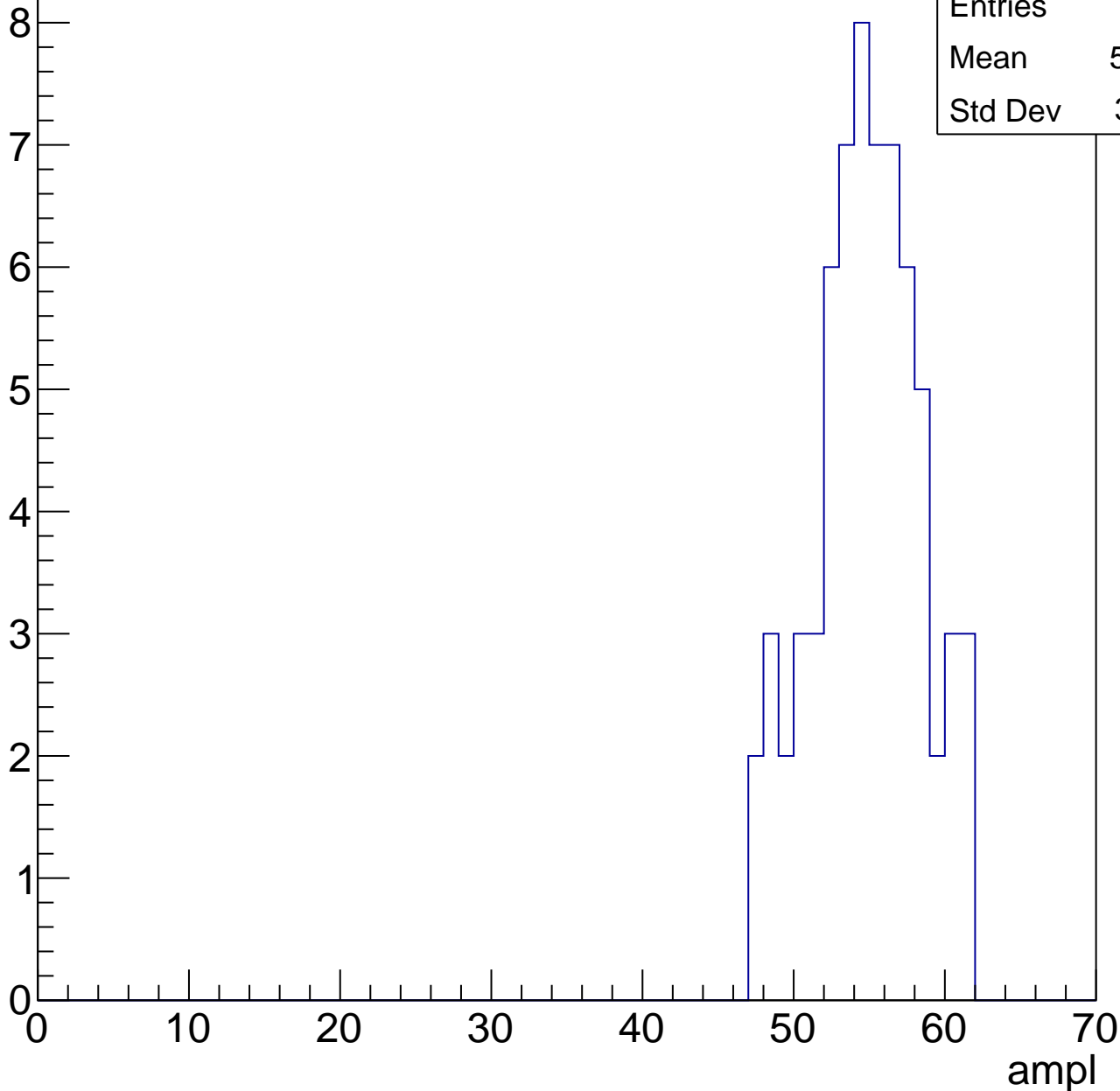


B1L101S, U21-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

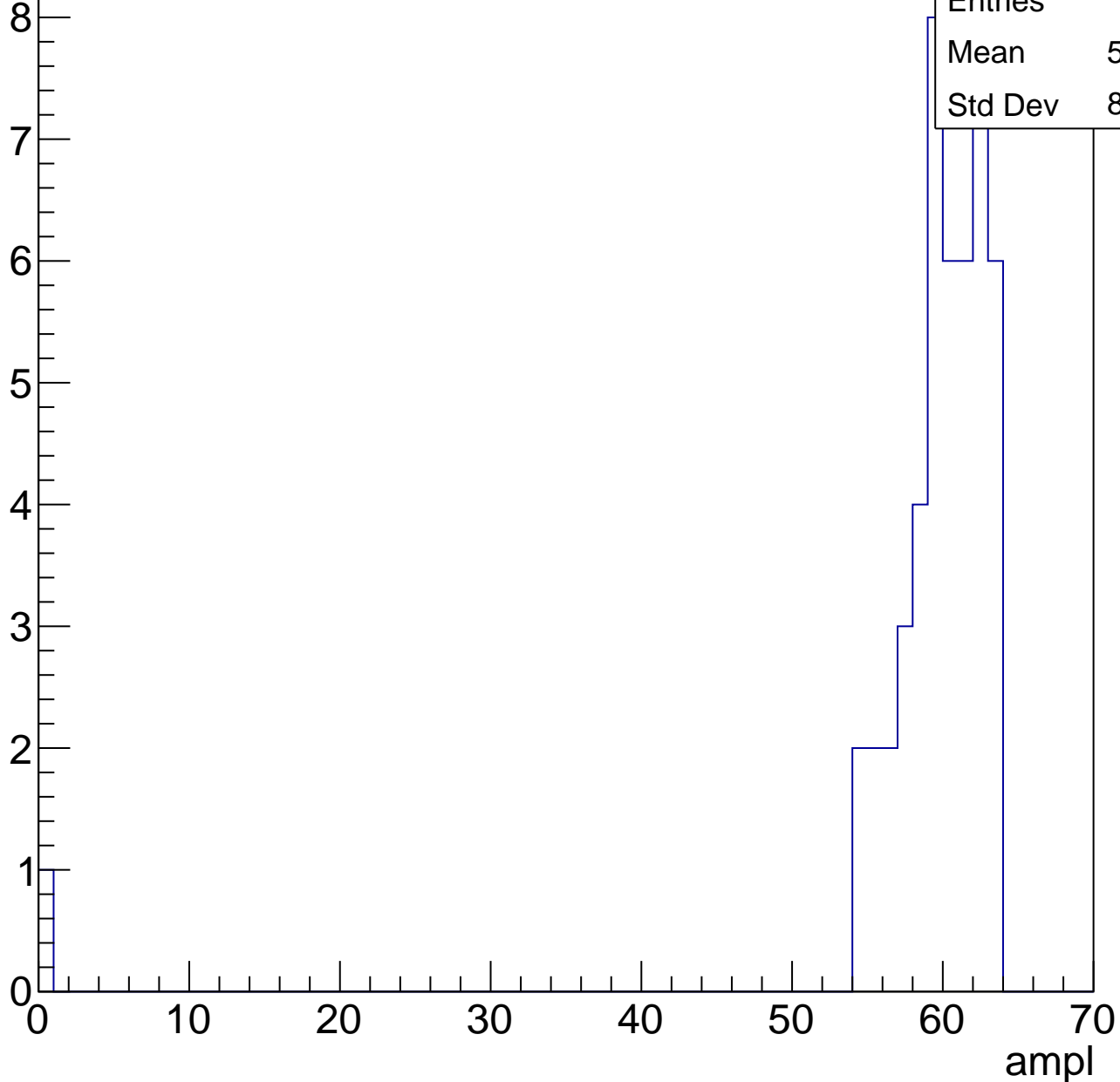
Entries	67
Mean	54.39
Std Dev	3.511



B1L101S, U21-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch57, adc0

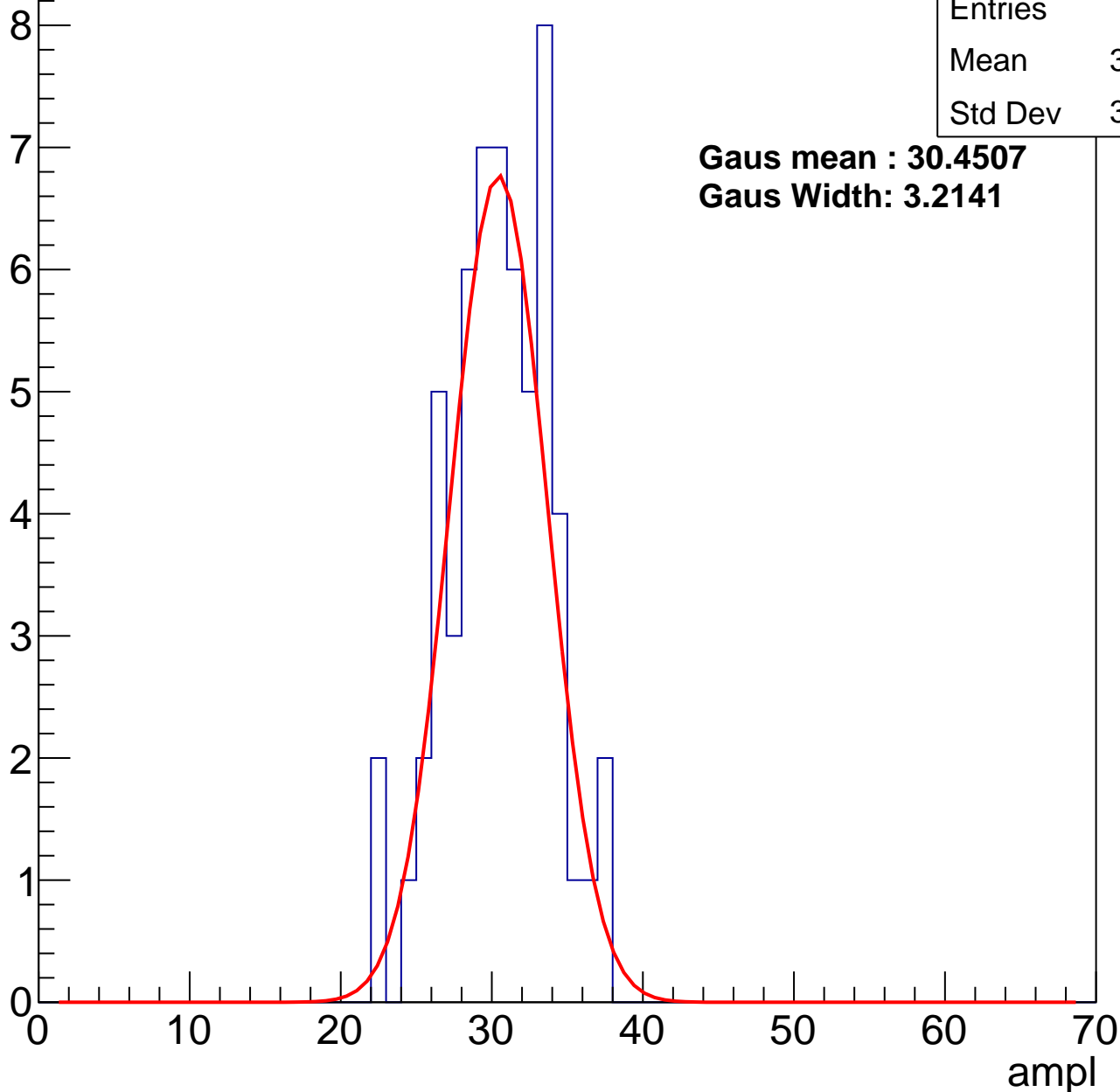
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	30.02
Std Dev	3.349

Gaus mean : 30.4507

Gaus Width: 3.2141



B1L101S, U21-ch57, adc1

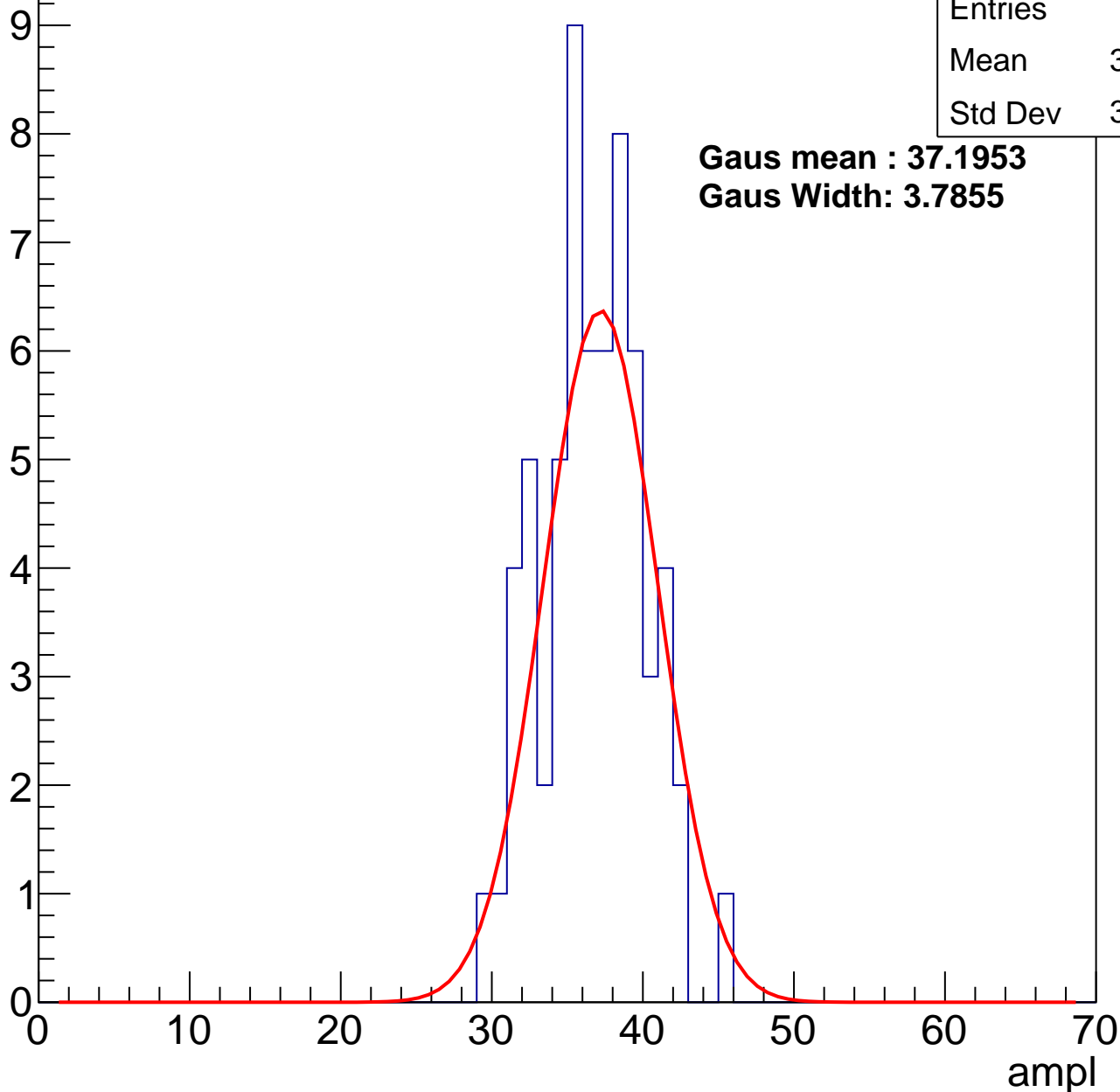
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.24
Std Dev	3.337

Gaus mean : 37.1953

Gaus Width: 3.7855



B1L101S, U21-ch57, adc2

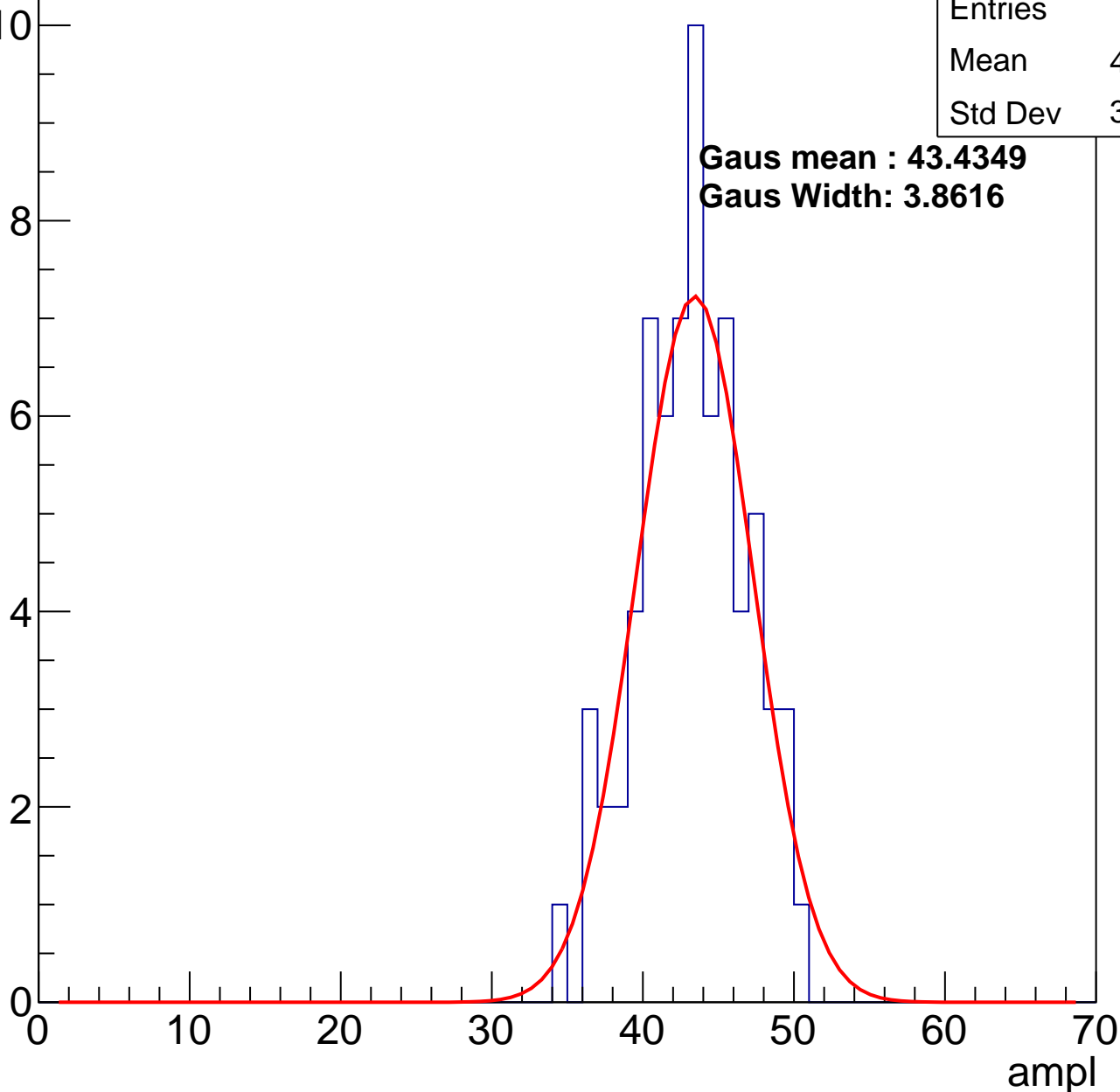
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.77
Std Dev	3.533

Gaus mean : 43.4349

Gaus Width: 3.8616

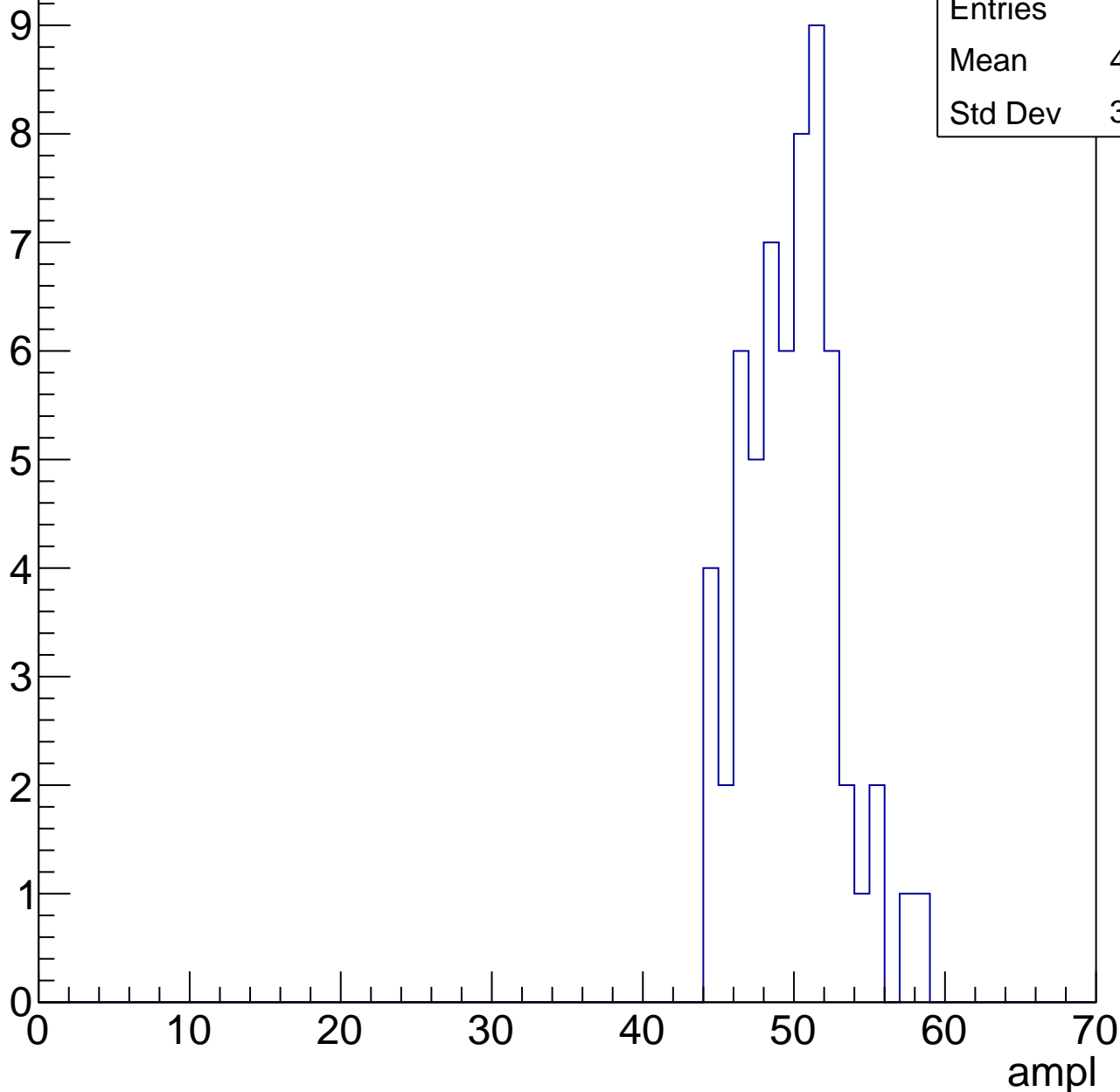


B1L101S, U21-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	49.38
Std Dev	3.099

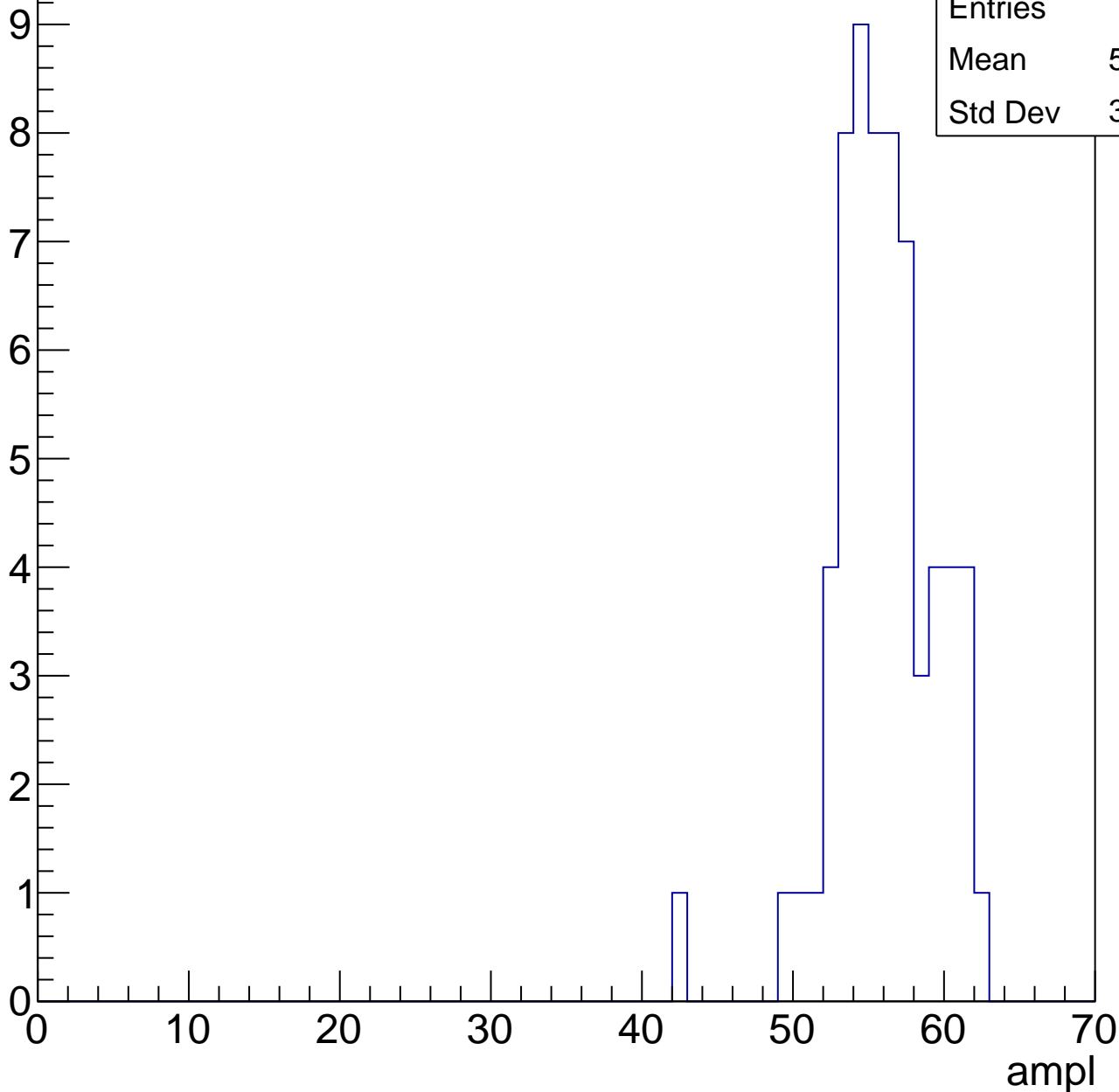


B1L101S, U21-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

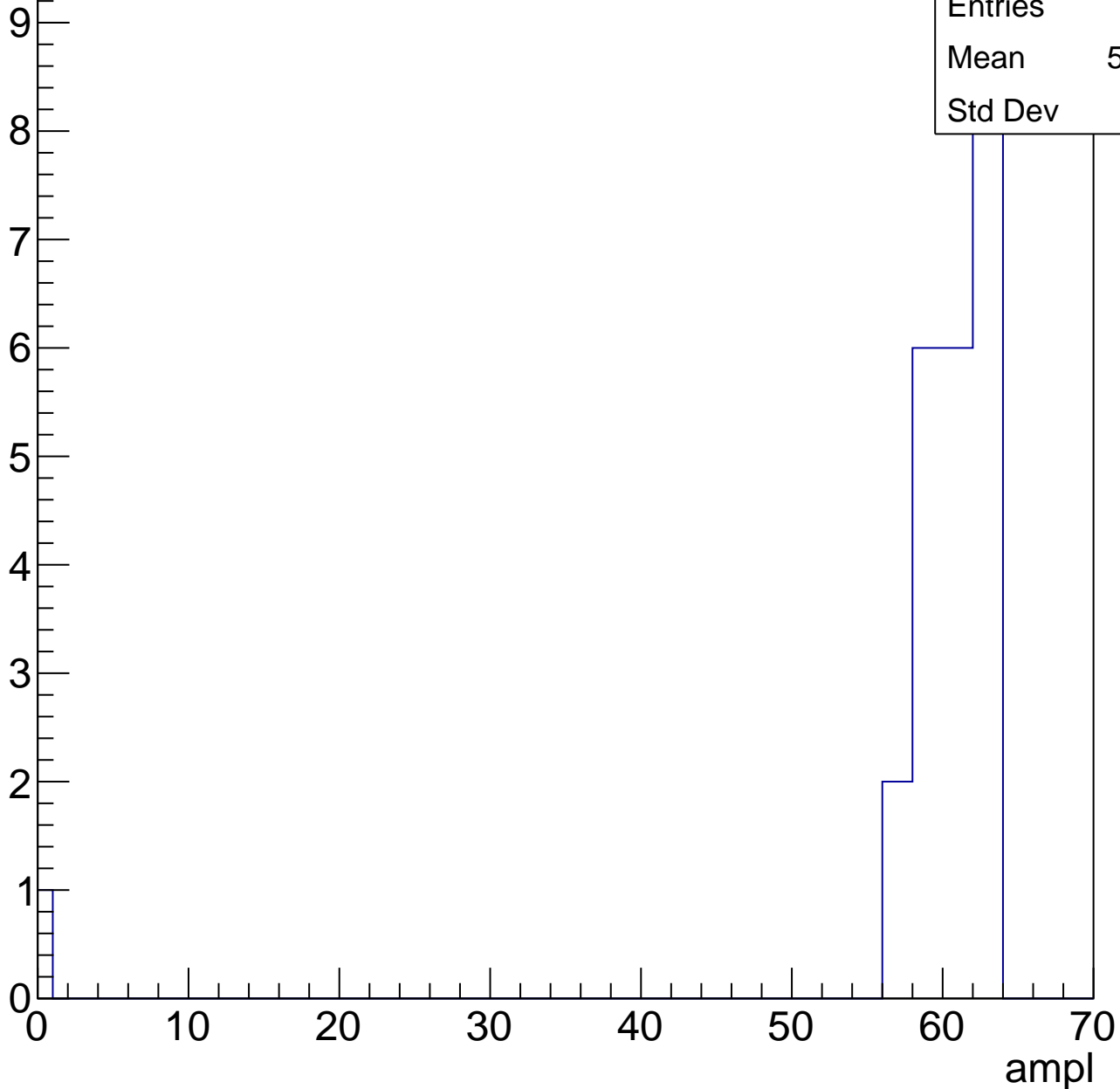
Entries	64
Mean	55.52
Std Dev	3.368



B1L101S, U21-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch58, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

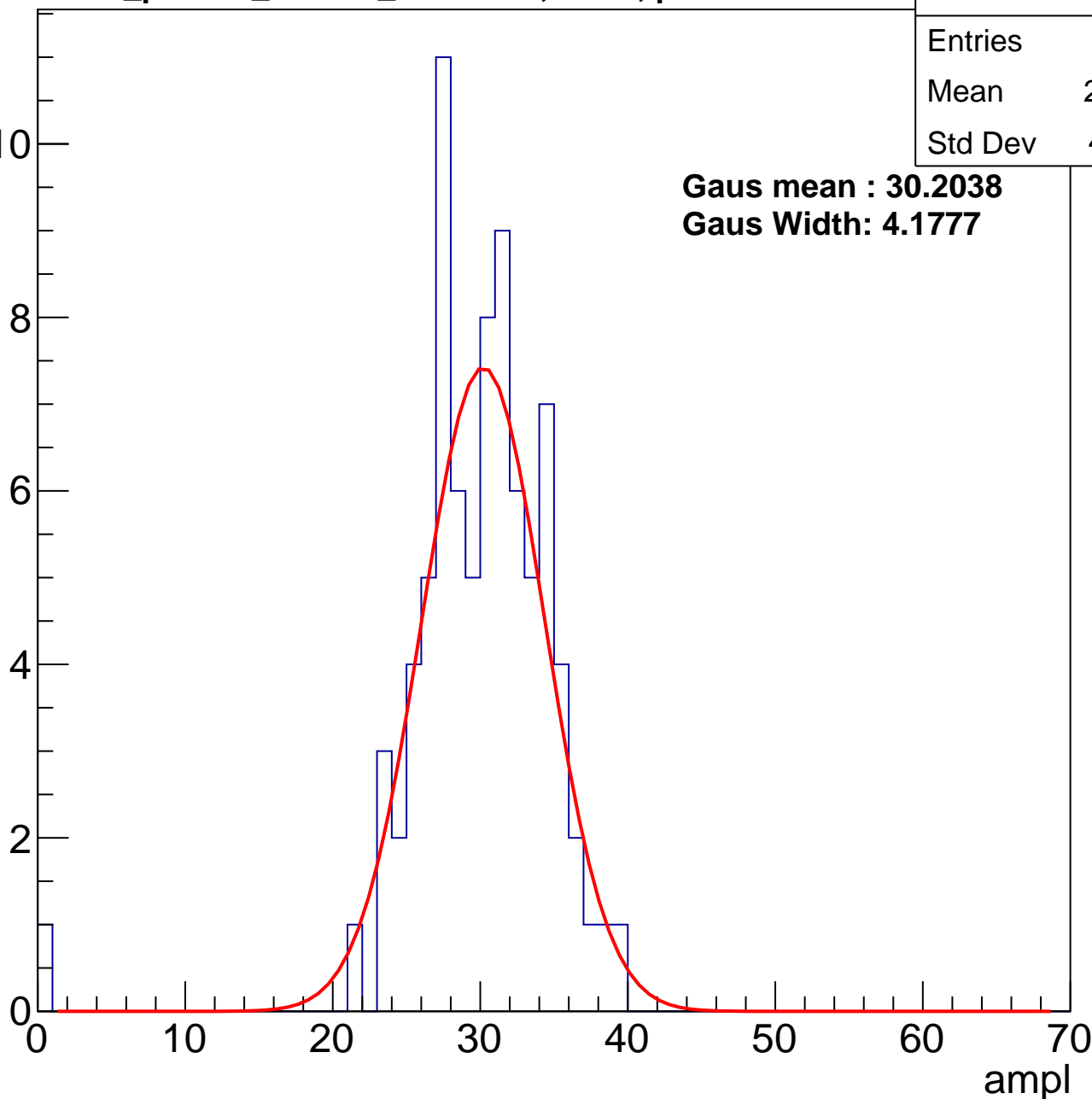
70

ampl

Entries	82
Mean	29.49
Std Dev	4.991

Gaus mean : 30.2038

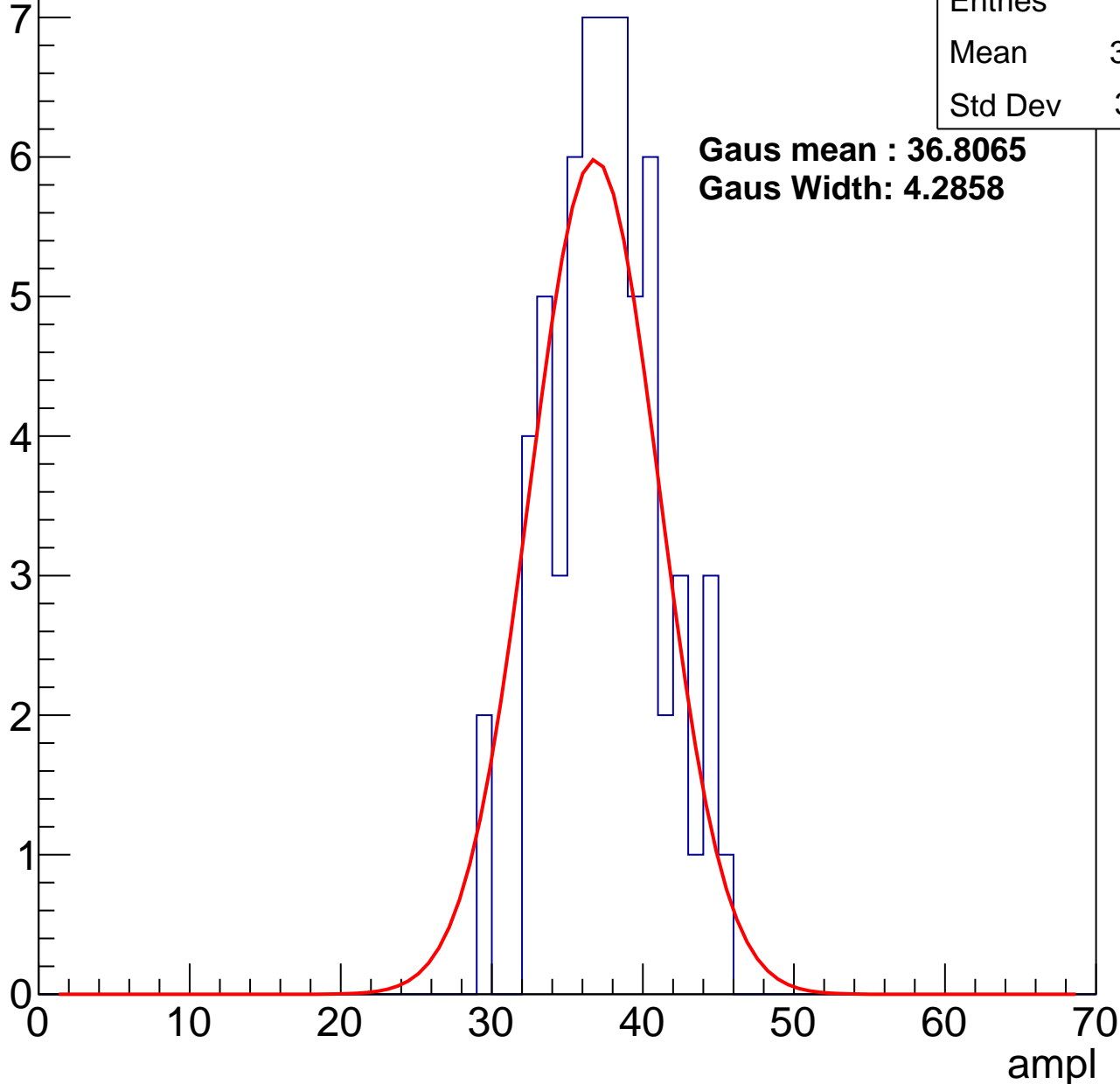
Gaus Width: 4.1777



B1L101S, U21-ch58, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch58, adc2

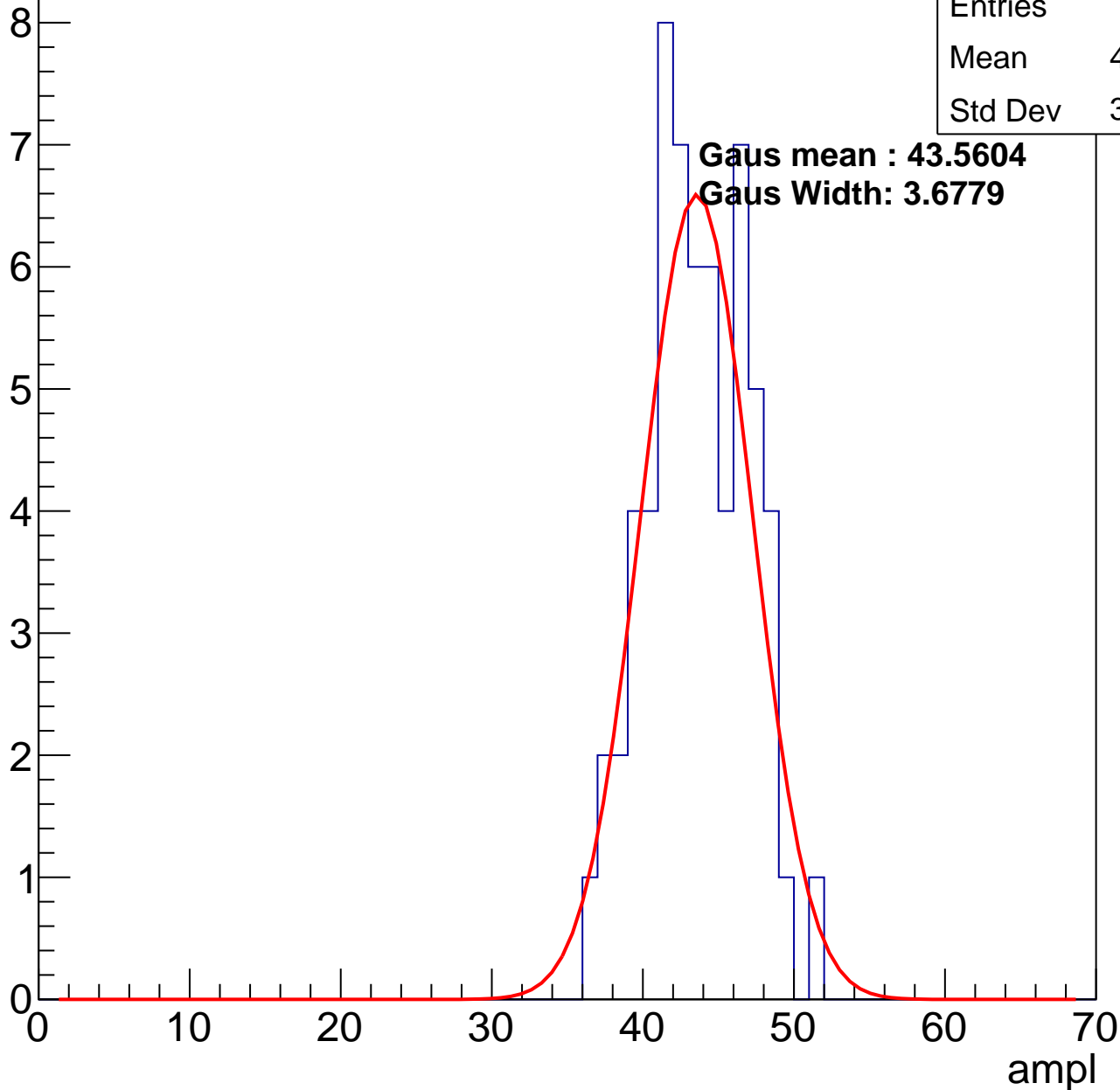
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	43.15
Std Dev	3.296

Gaus mean : 43.5604

Gaus Width: 3.6779

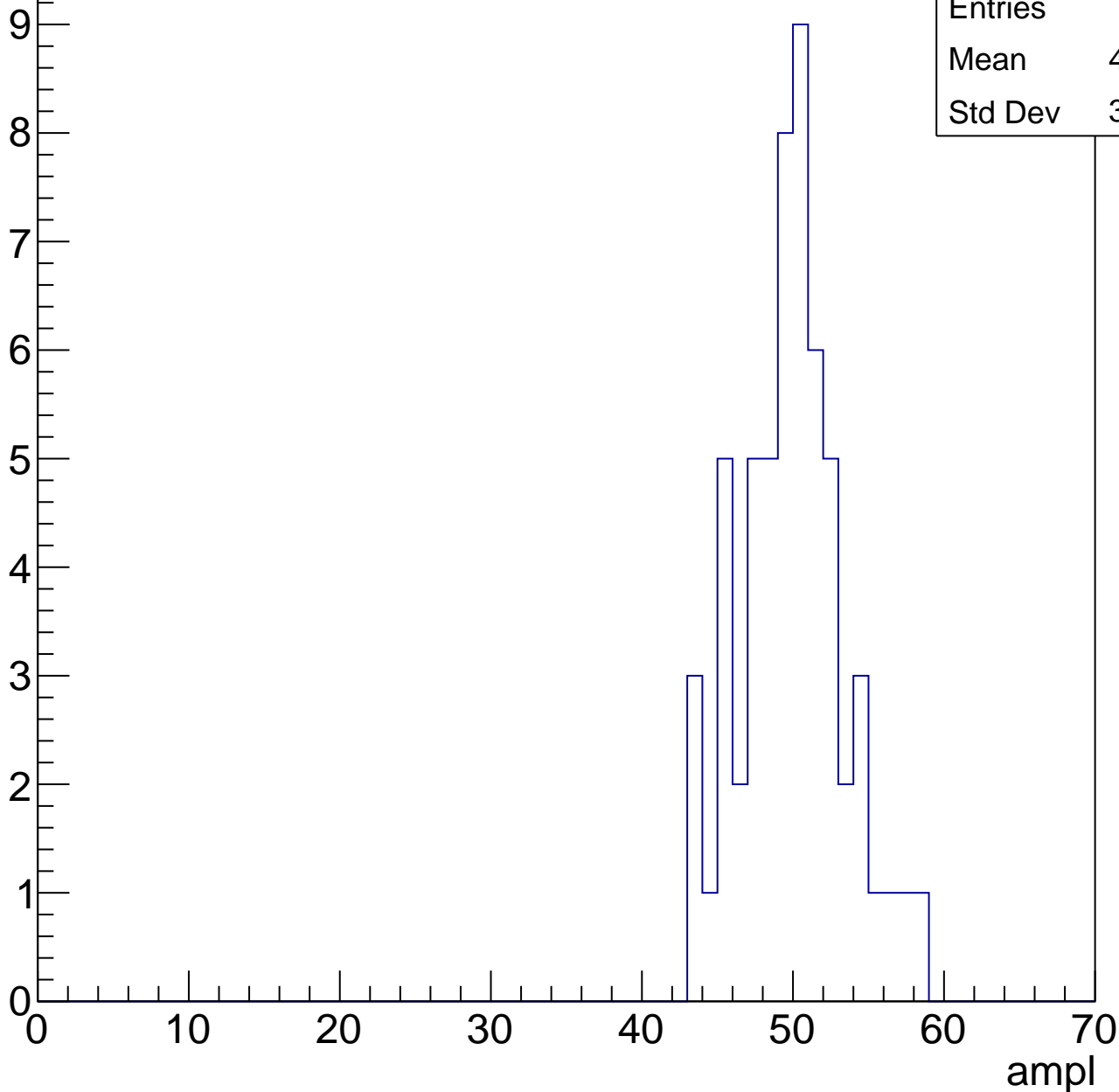


B1L101S, U21-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

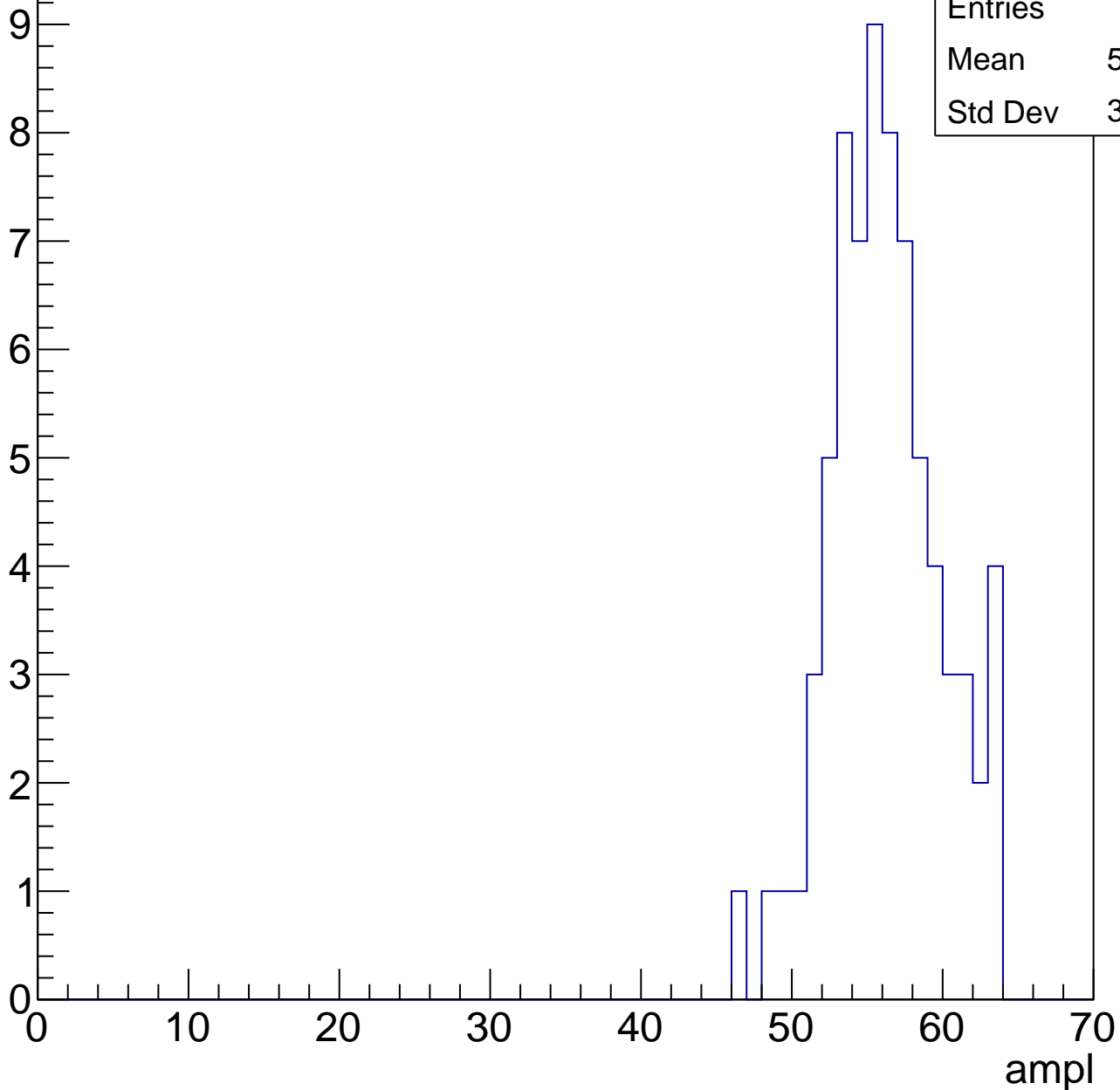
Entries	58
Mean	49.43
Std Dev	3.374



B1L101S, U21-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



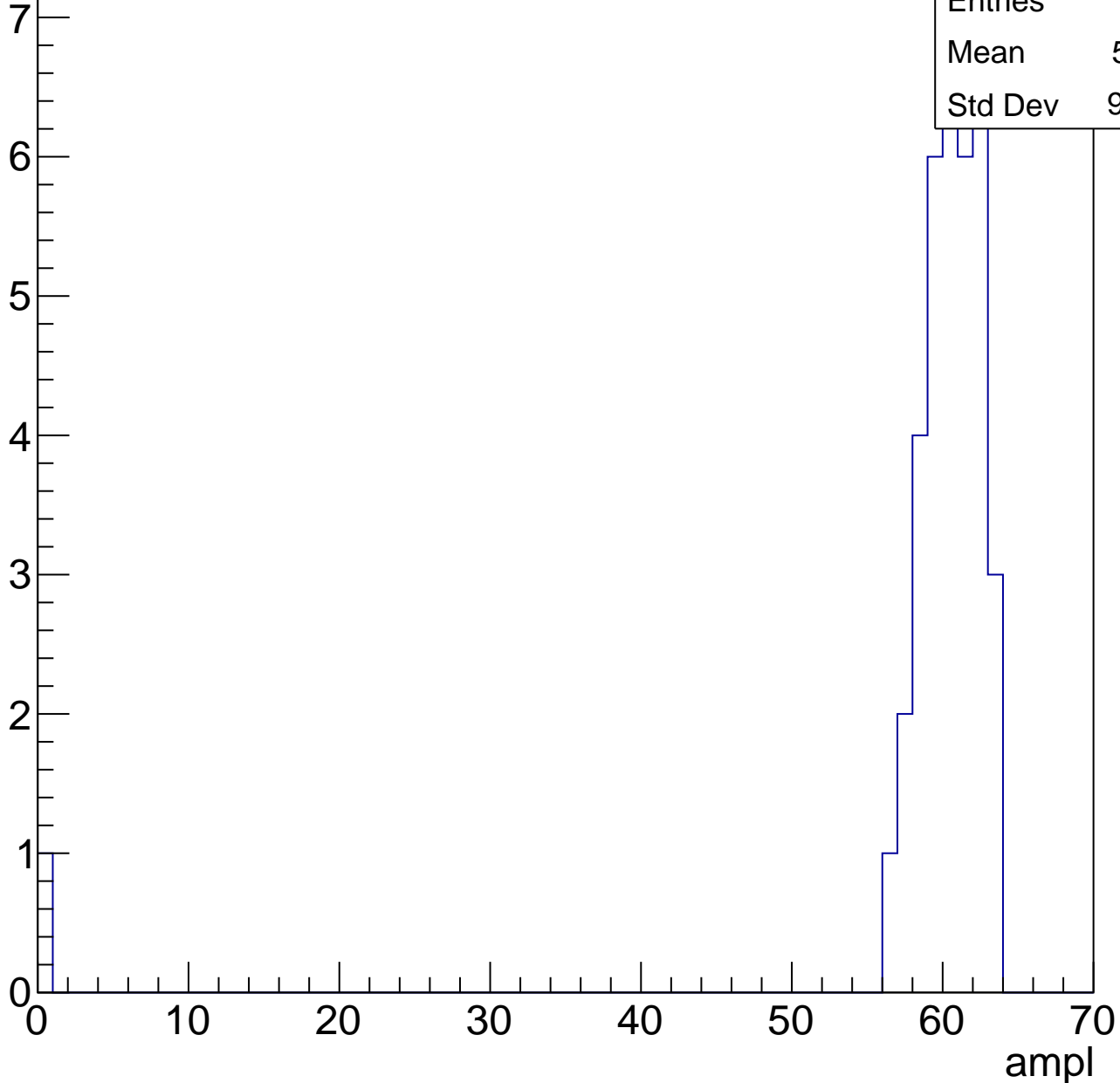
Entries	72
Mean	55.76
Std Dev	3.657

B1L101S, U21-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.51
Std Dev	9.912



B1L101S, U21-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch59, adc0

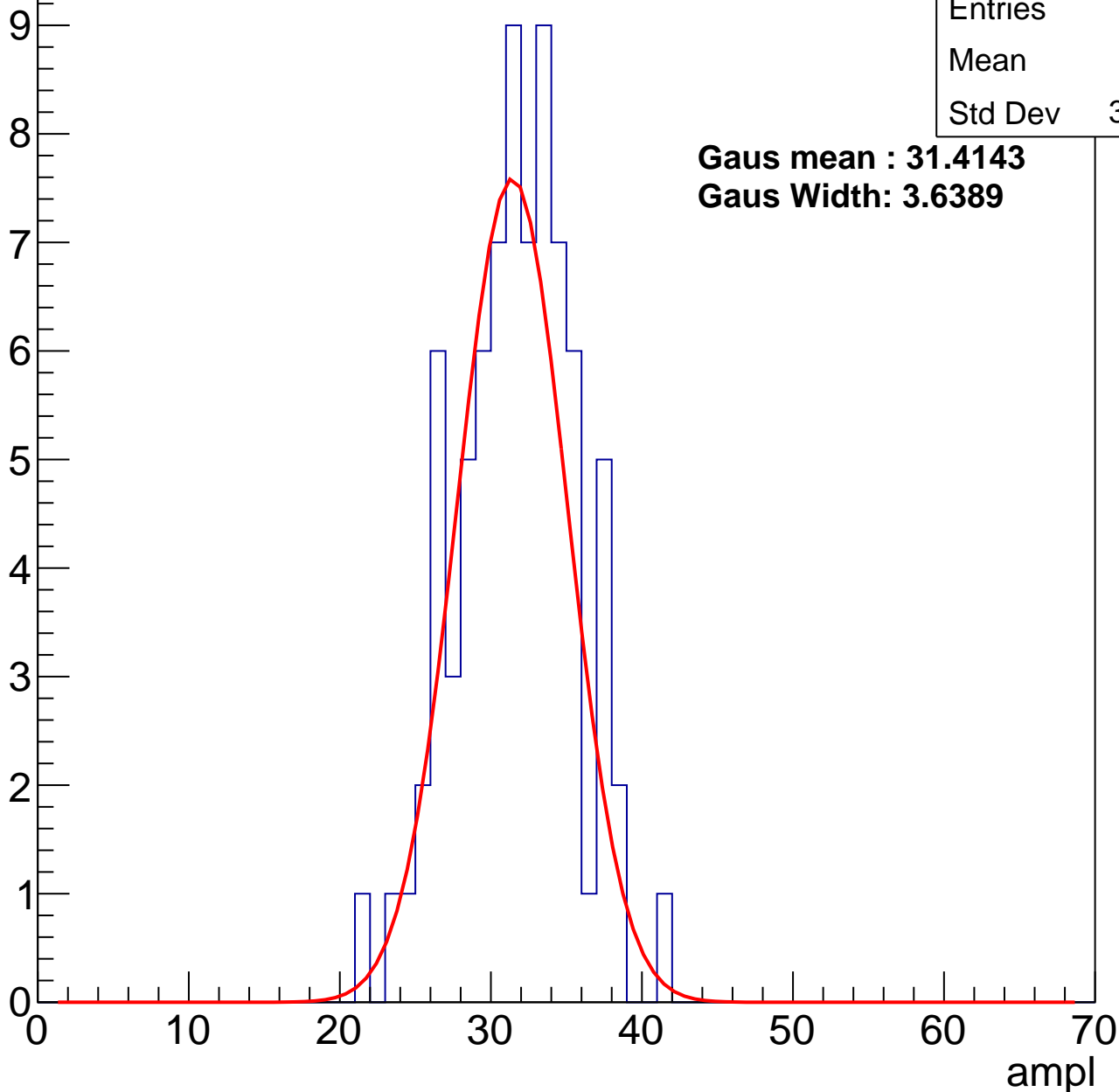
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	31.2
Std Dev	3.833

Gaus mean : 31.4143

Gaus Width: 3.6389



B1L101S, U21-ch59, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	38.56
Std Dev	3.211

Gaus mean : 39.4591

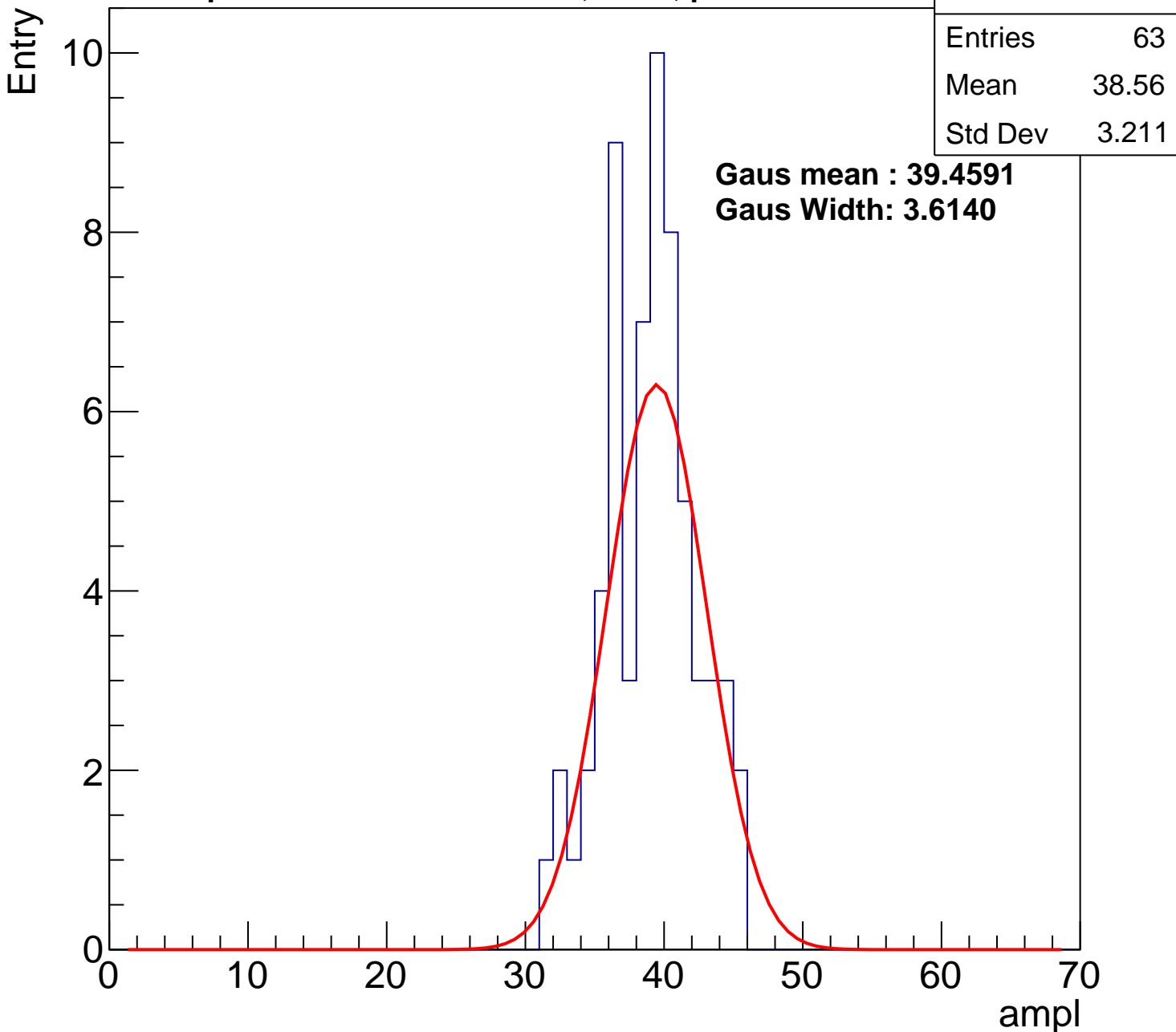
Gaus Width: 3.6140

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch59, adc2

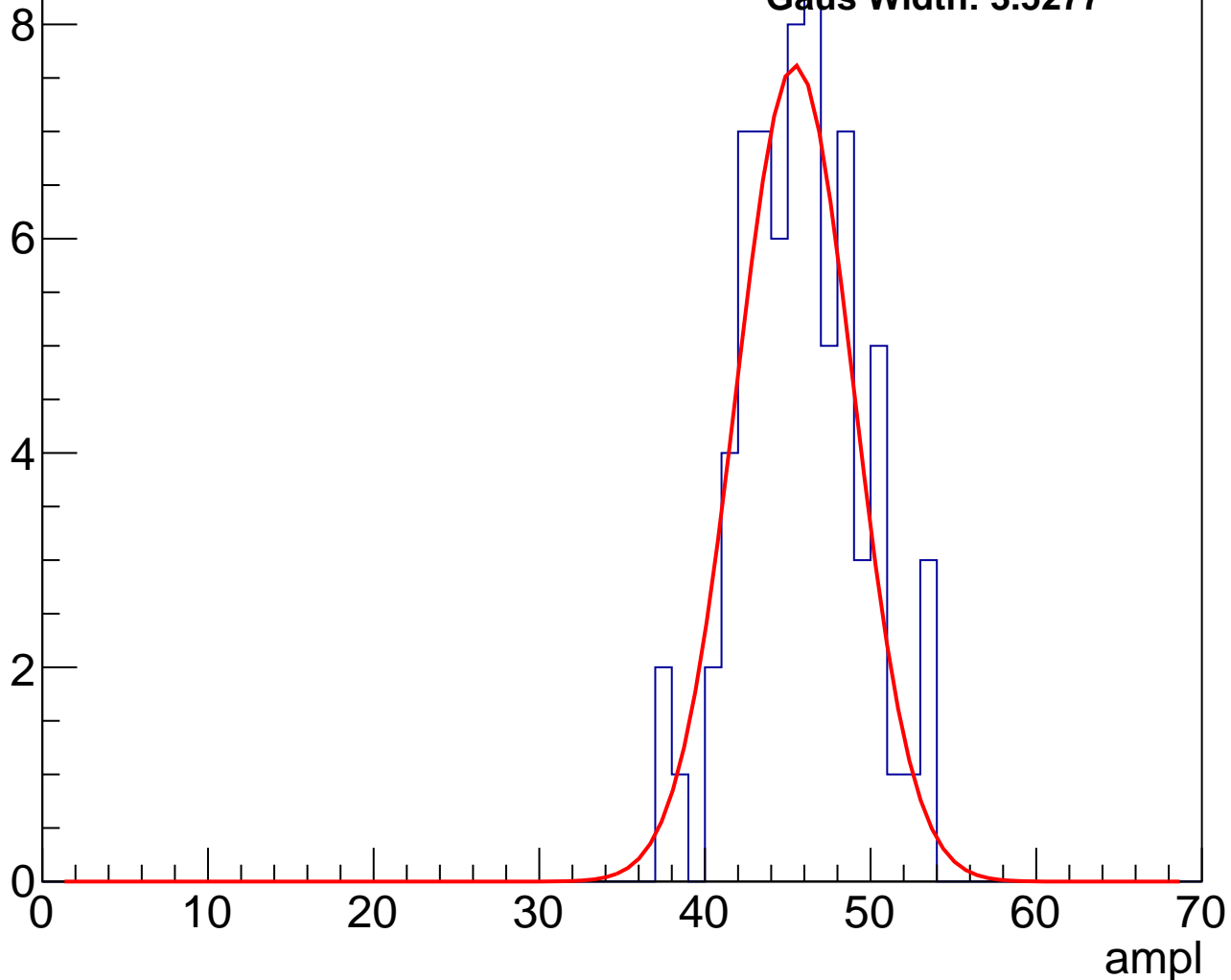
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	45.35
Std Dev	3.579

Gaus mean : 45.4447

Gaus Width: 3.5277

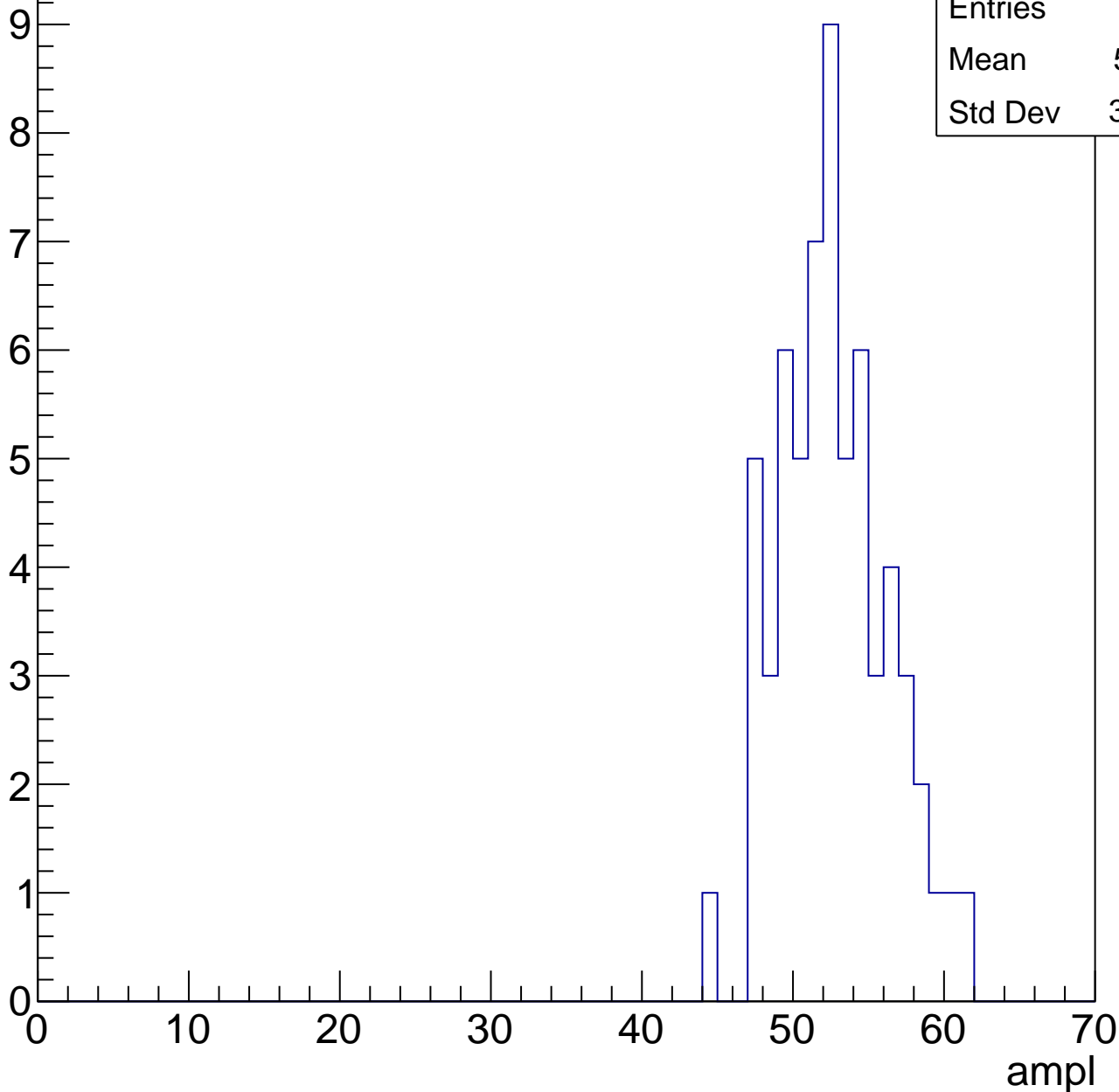


B1L101S, U21-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	52.21
Std Dev	3.538



B1L101S, U21-ch59, adc4

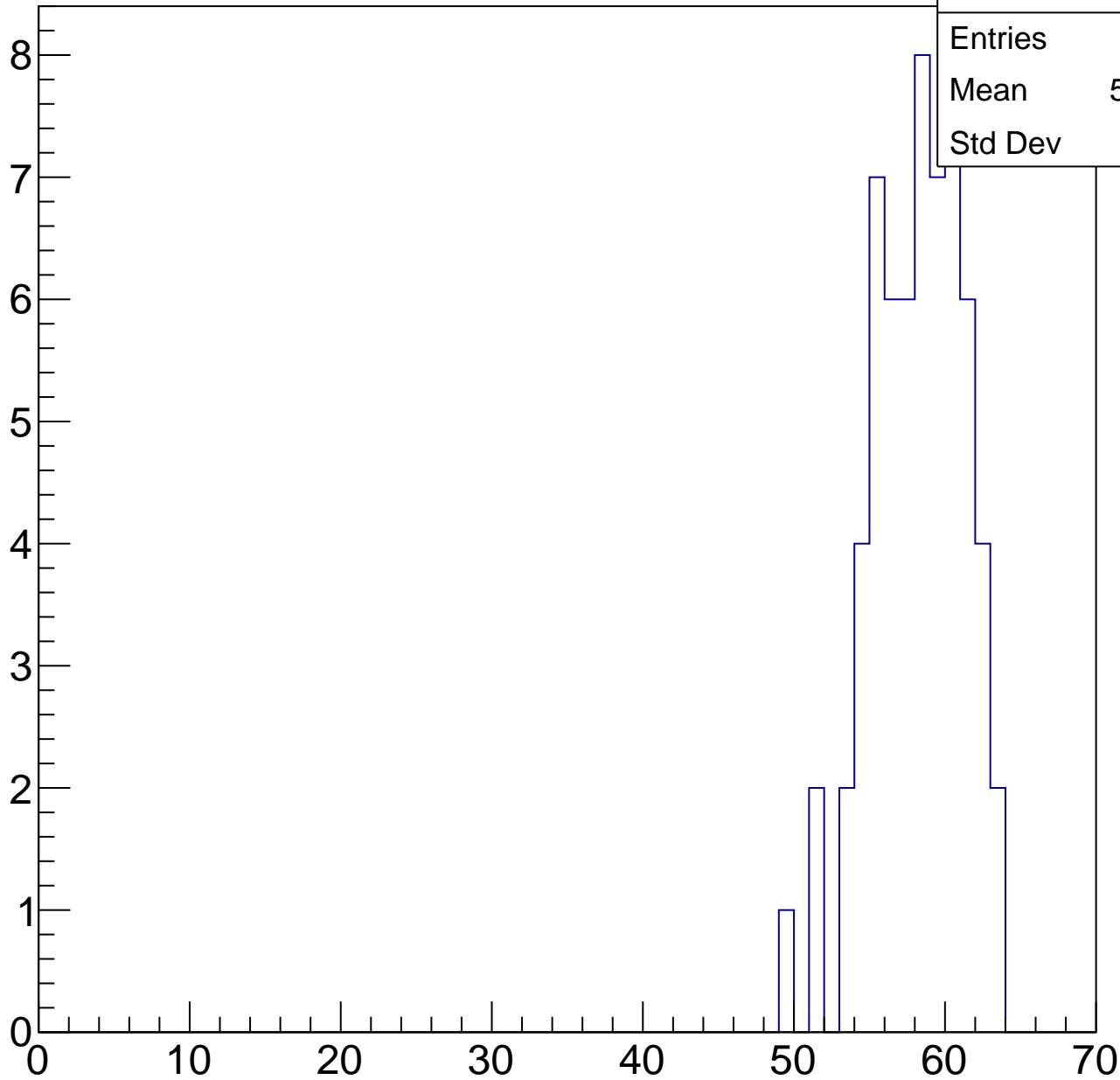
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	63
Mean	57.67
Std Dev	3.05

ampl

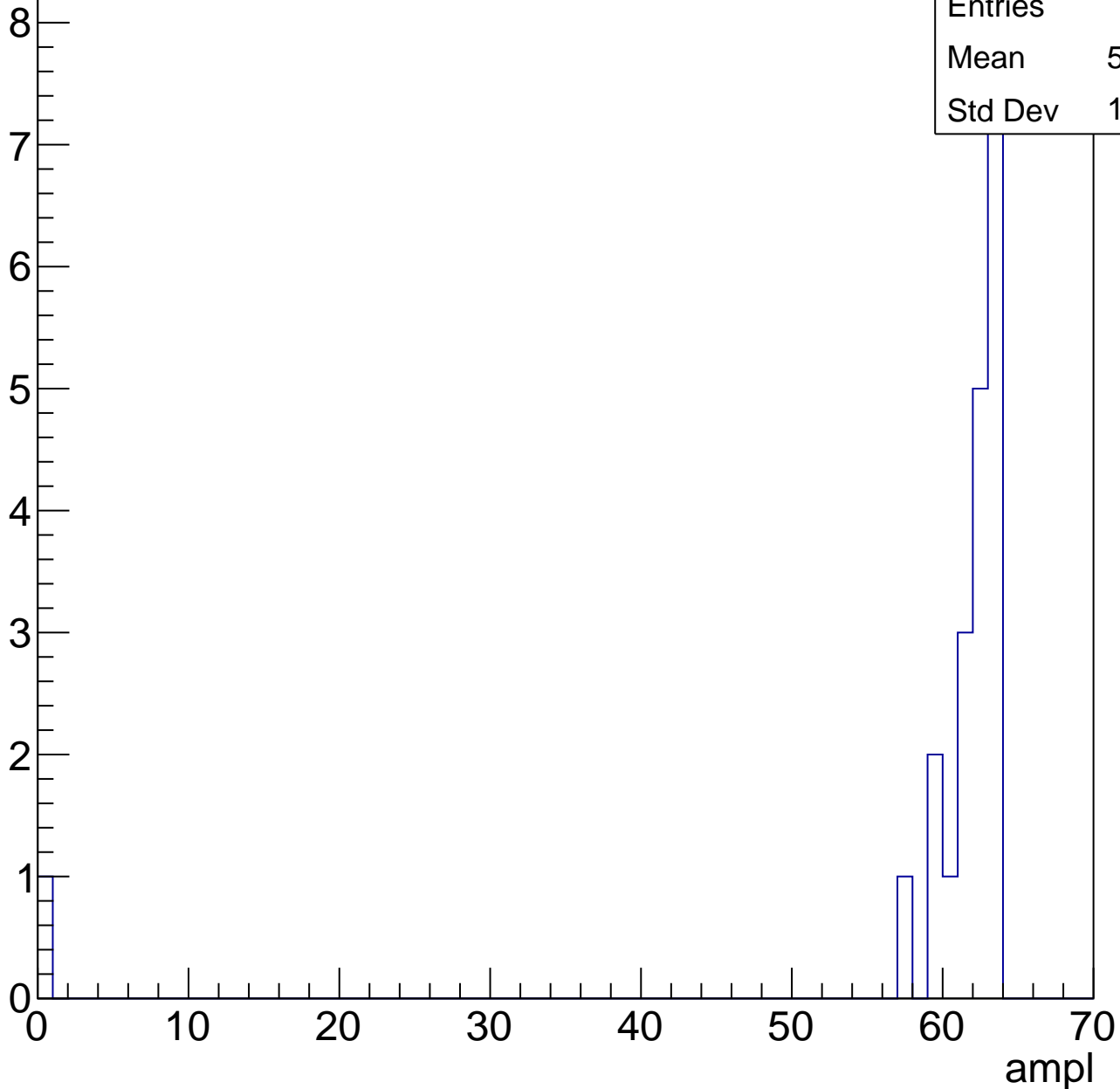


B1L101S, U21-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	21
Mean	58.67
Std Dev	13.22



B1L101S, U21-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch60, adc0

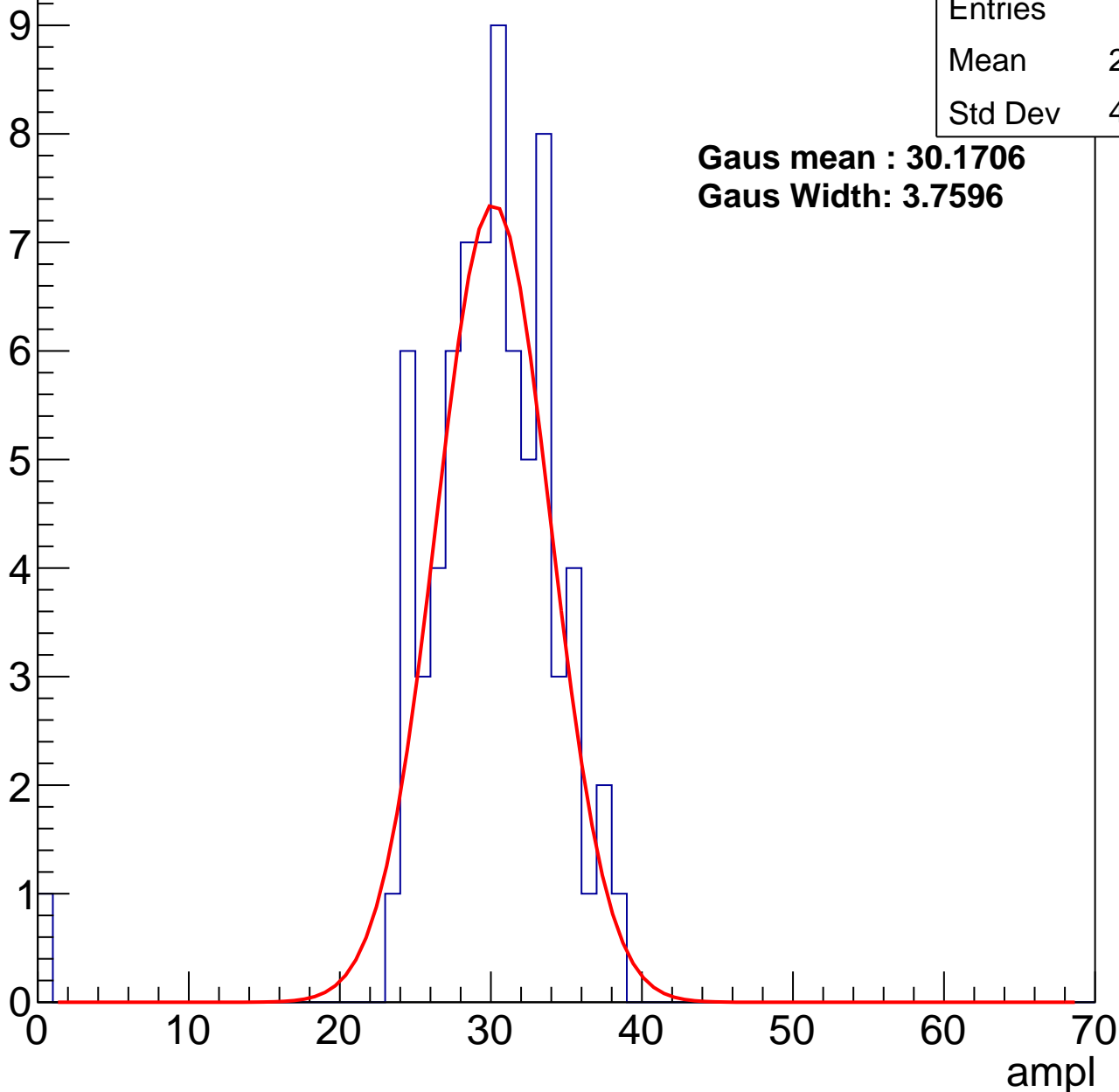
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.42
Std Dev	4.948

Gaus mean : 30.1706

Gaus Width: 3.7596



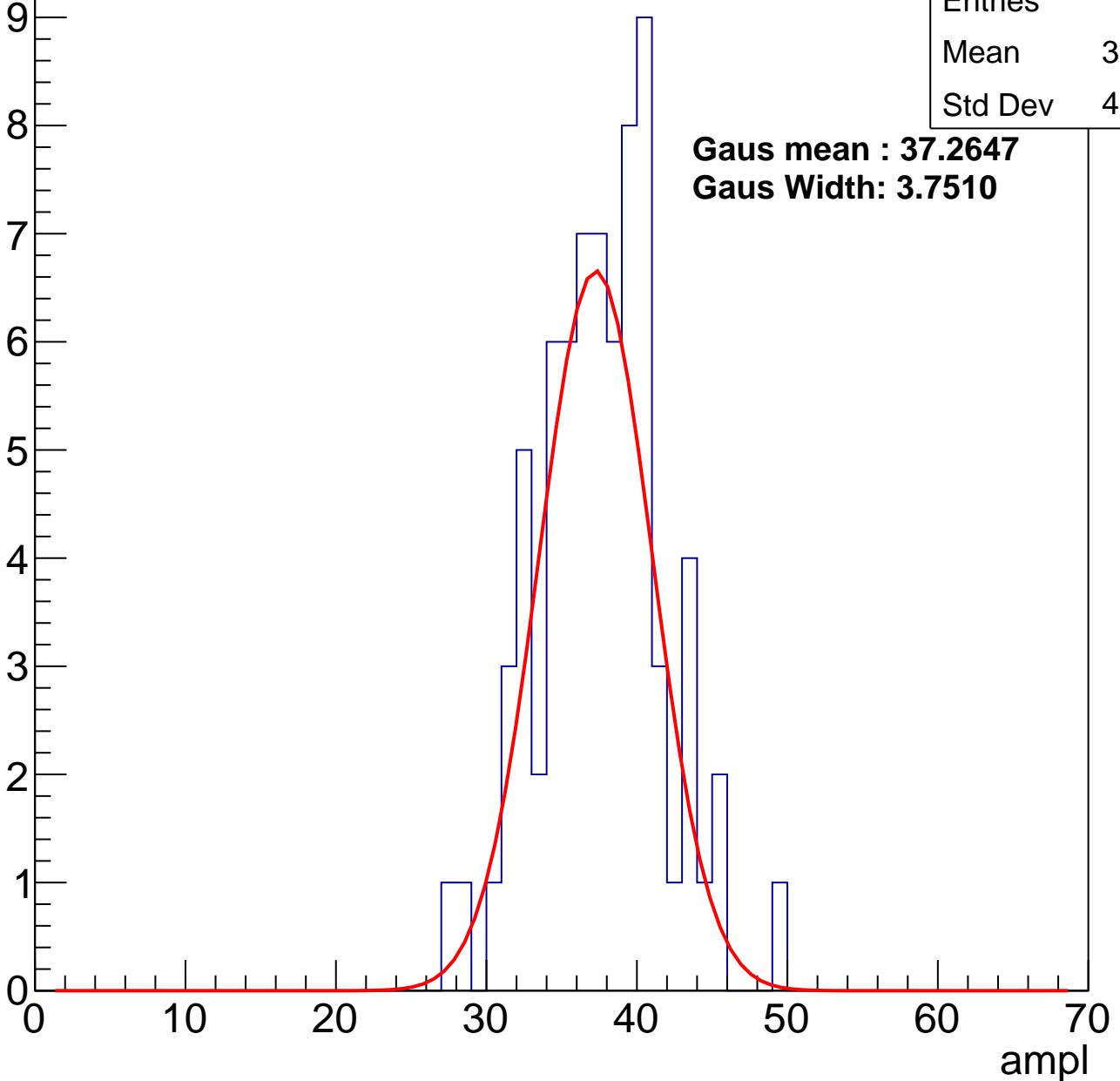
B1L101S, U21-ch60, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	37.15
Std Dev	4.086

Gaus mean : 37.2647
Gaus Width: 3.7510



B1L101S, U21-ch60, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	44.73
Std Dev	3.986

Gaus mean : 45.4654

Gaus Width: 3.8059

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

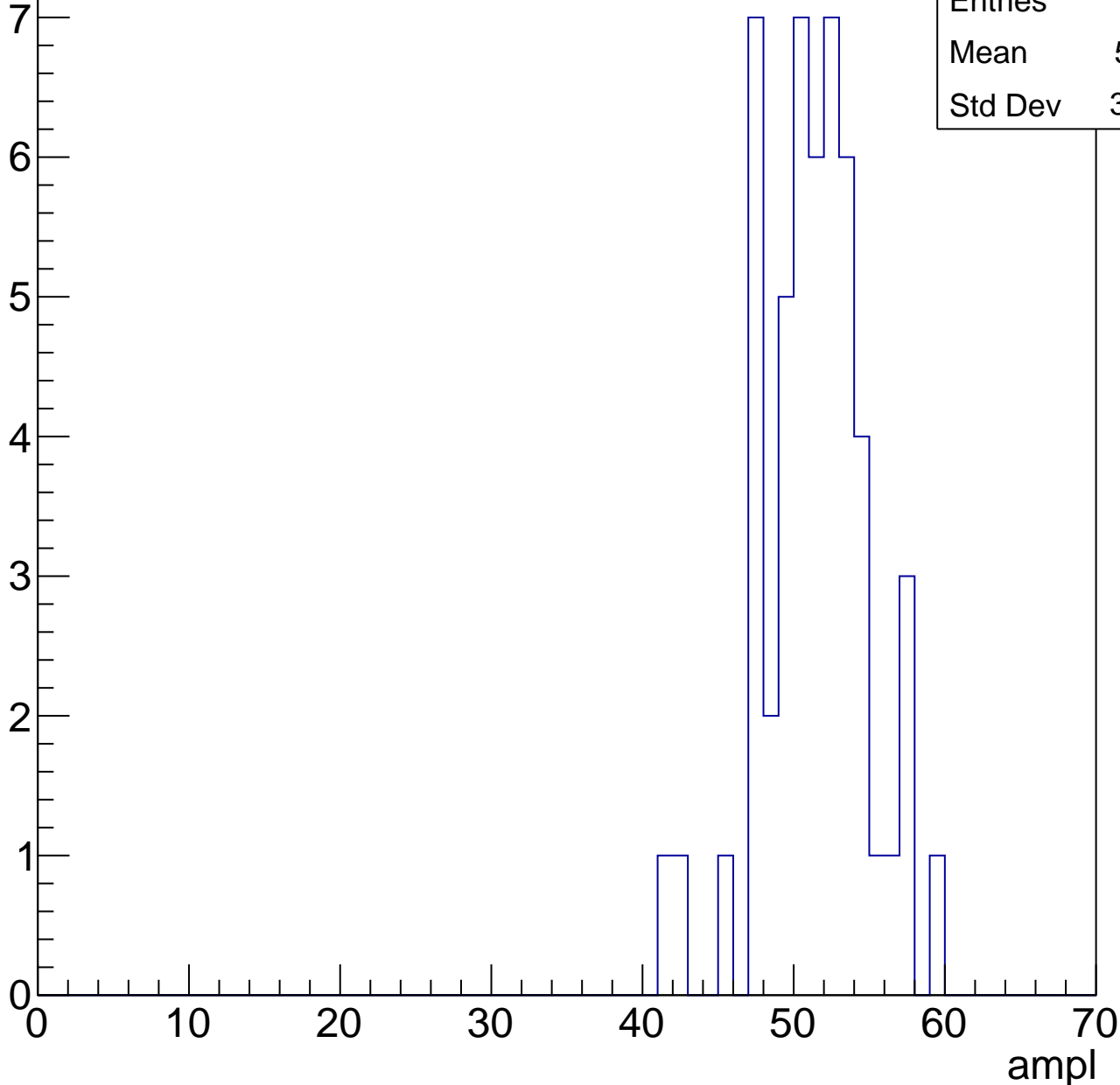
70

B1L101S, U21-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	50.81
Std Dev	3.508

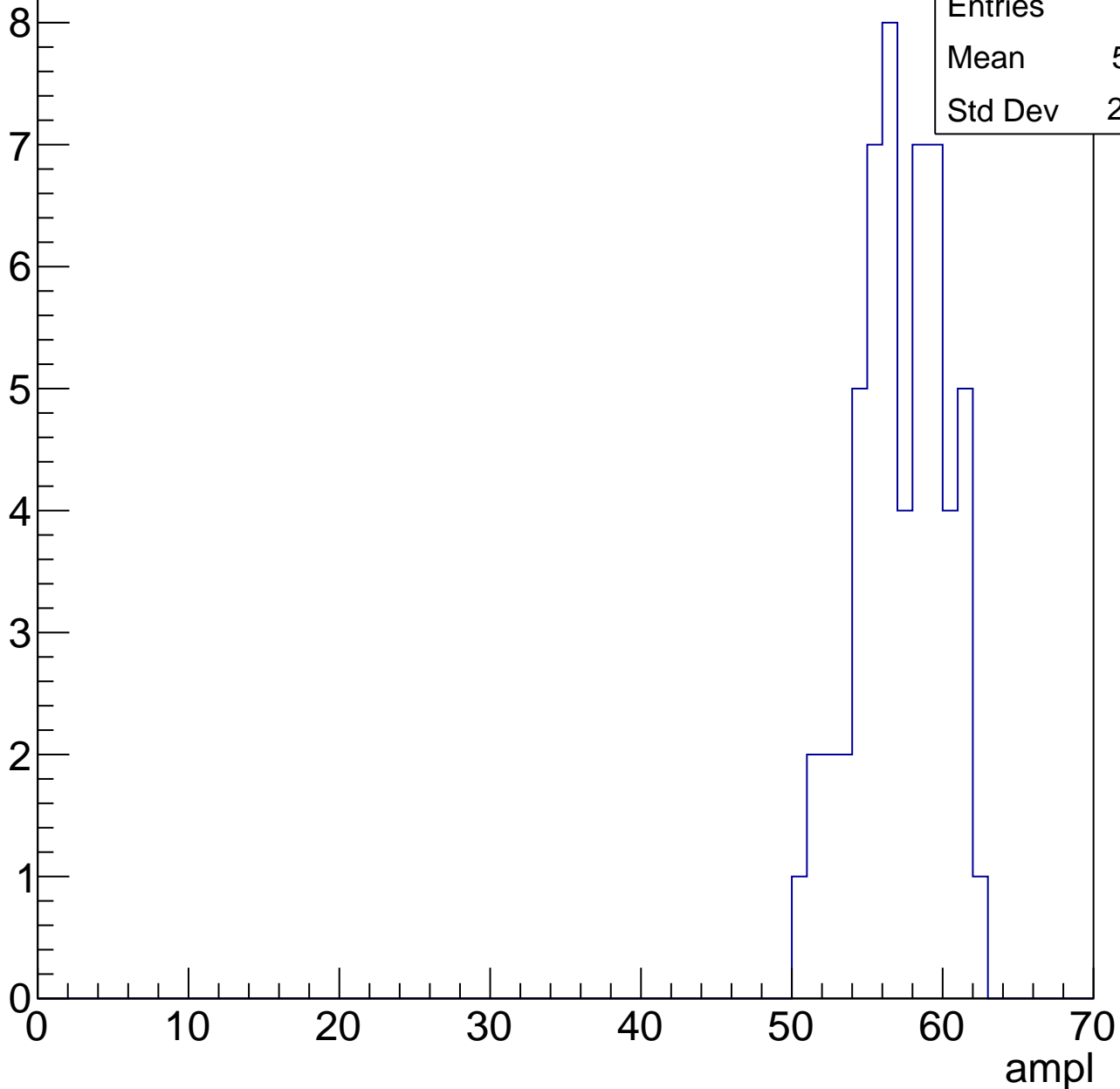


B1L101S, U21-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.71
Std Dev	2.877

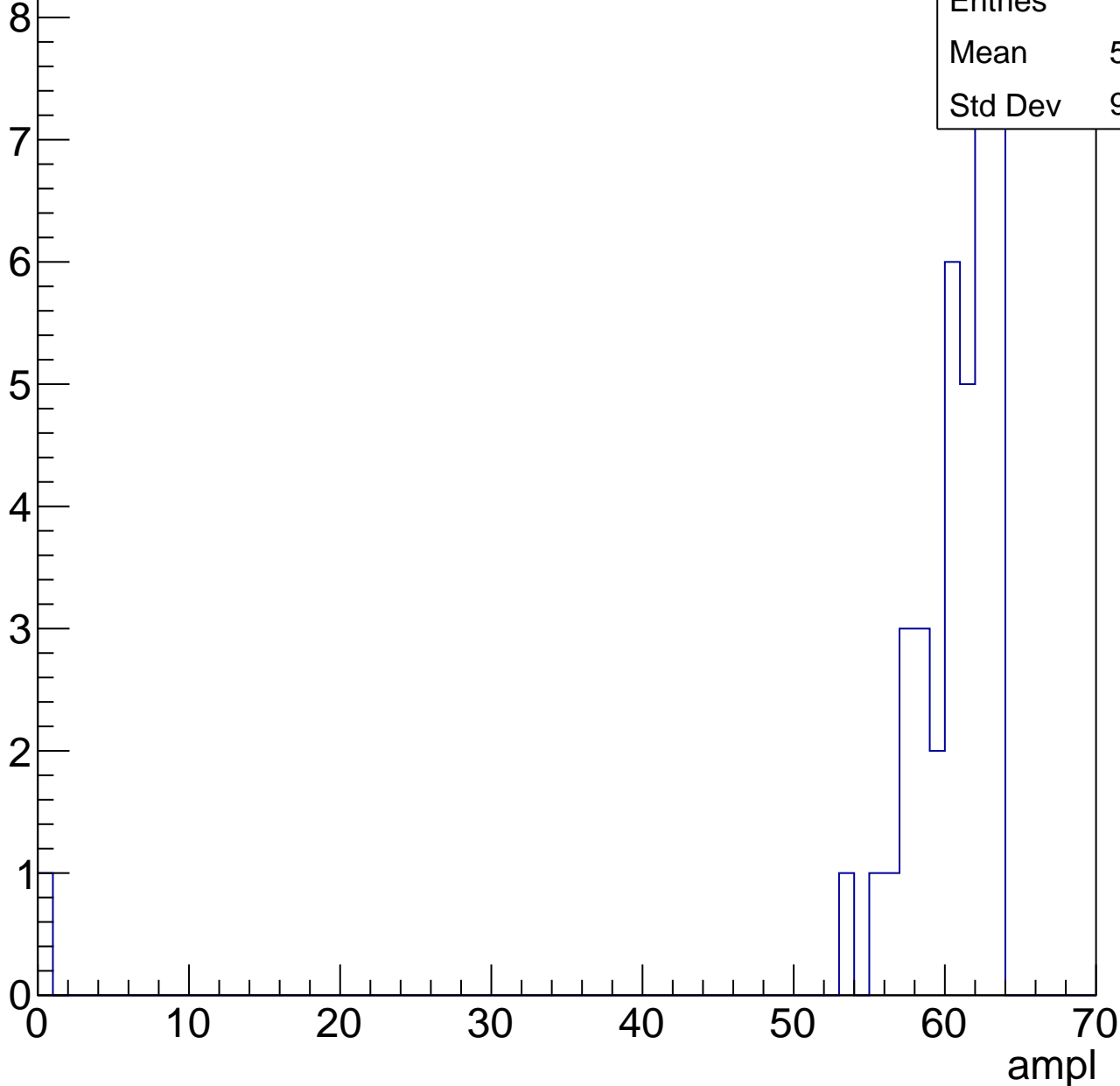


B1L101S, U21-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.77
Std Dev	9.846



B1L101S, U21-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L101S, U21-ch61, adc0

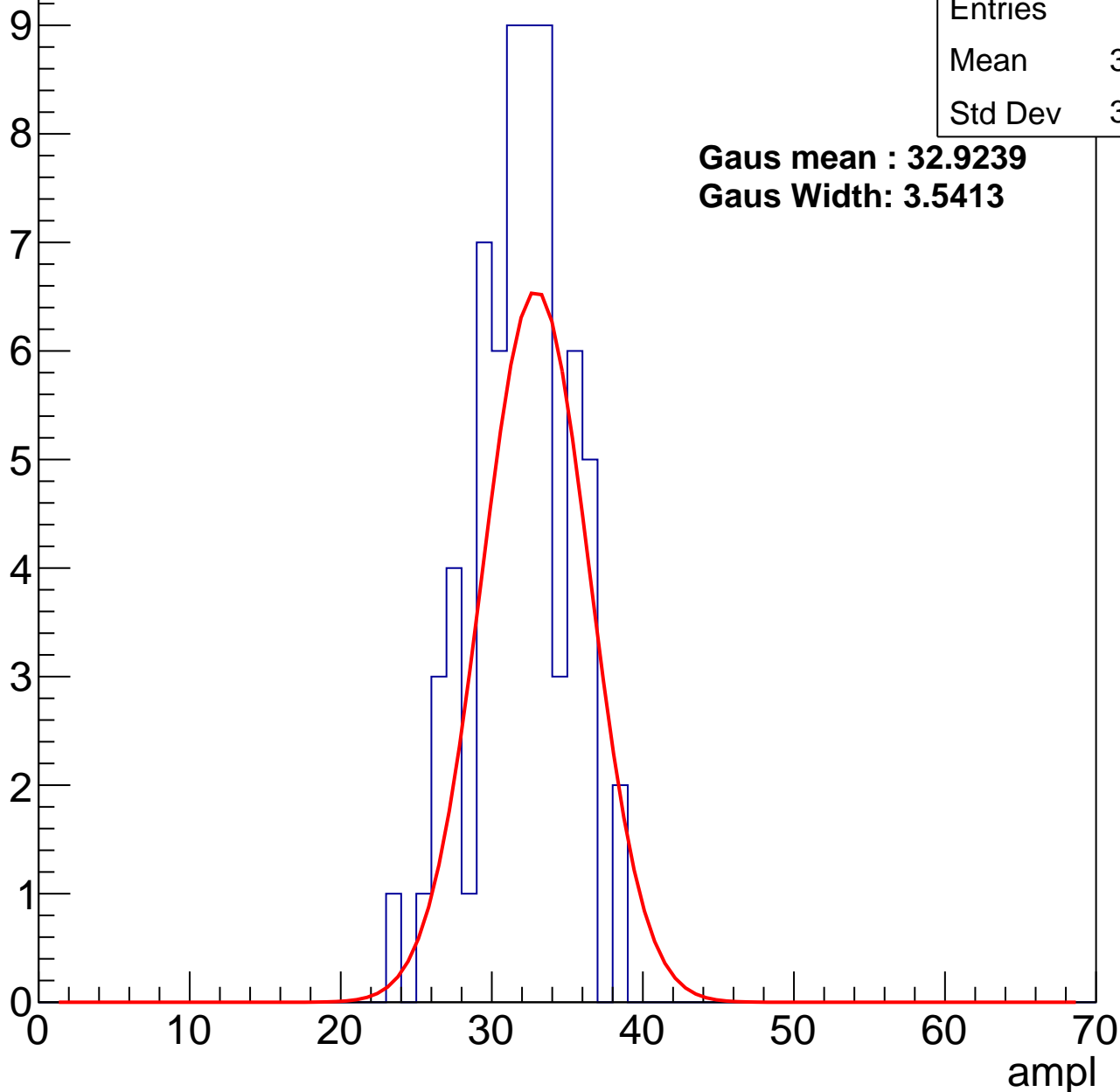
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	31.47
Std Dev	3.159

Gaus mean : 32.9239

Gaus Width: 3.5413



B1L101S, U21-ch61, adc1

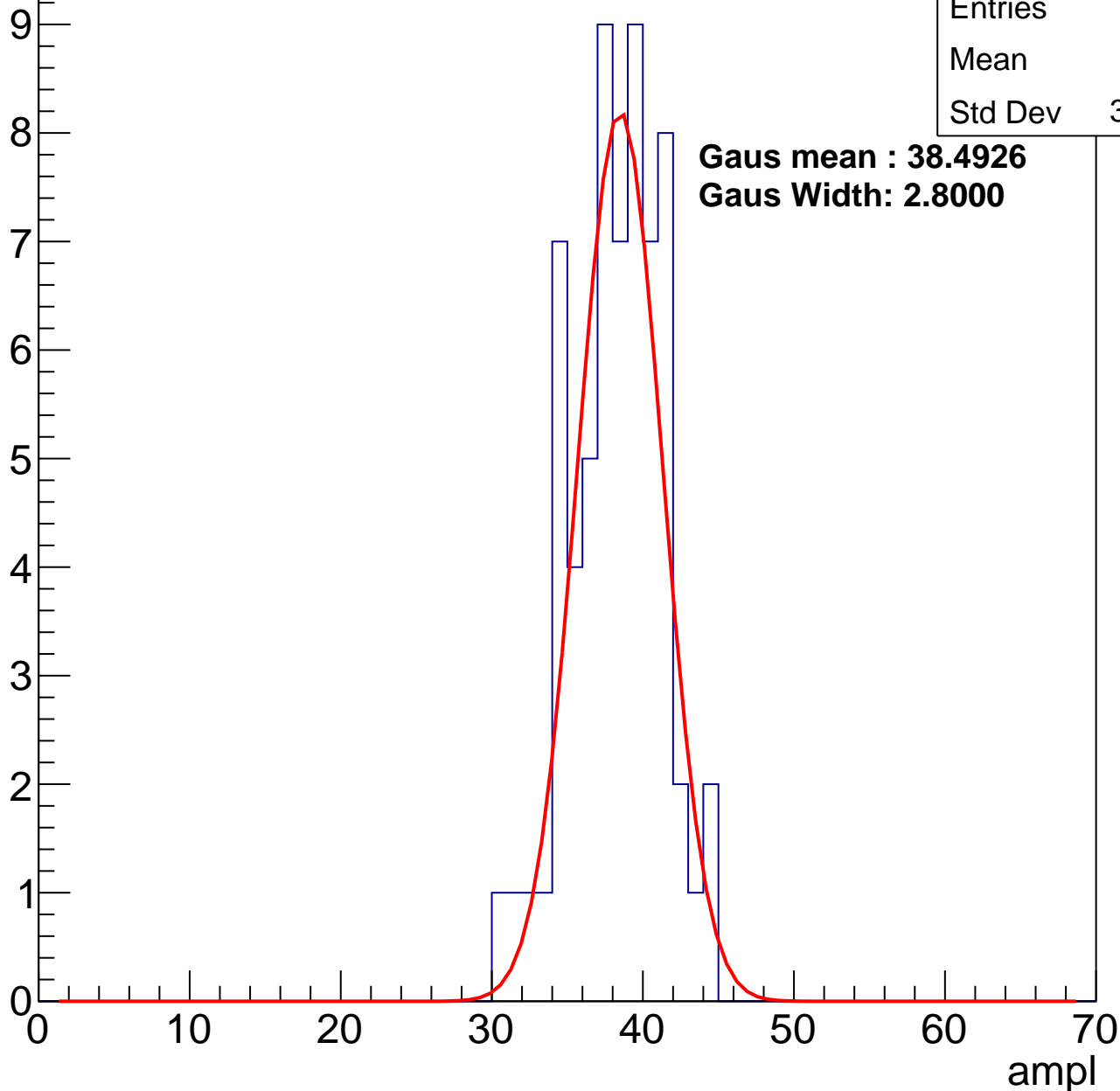
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	37.8
Std Dev	3.004

Gaus mean : 38.4926

Gaus Width: 2.8000



B1L101S, U21-ch61, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	45.04
Std Dev	3.215

Gaus mean : 45.4356

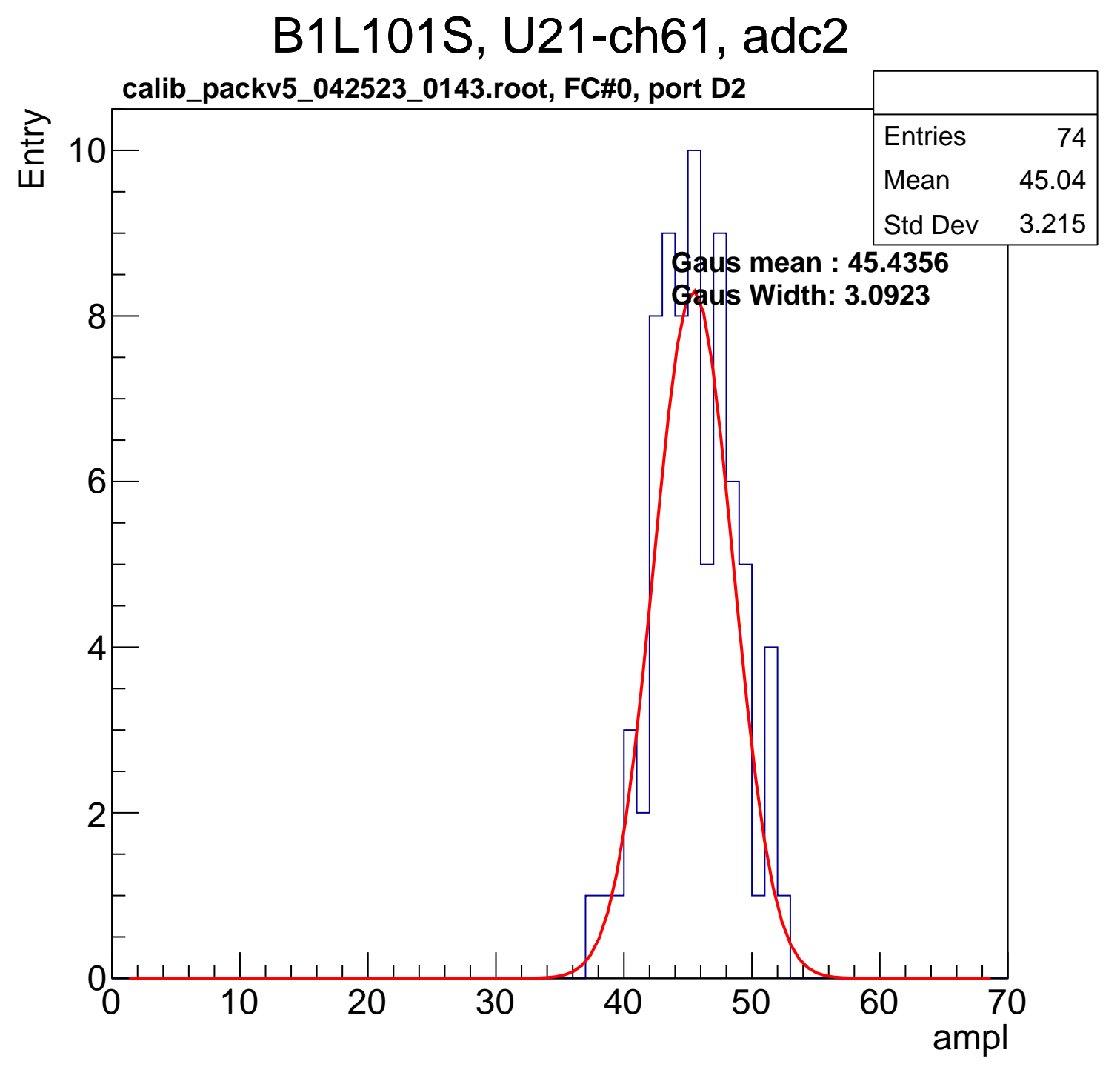
Gaus Width: 3.0923

Entry

10
8
6
4
2
0

ampl

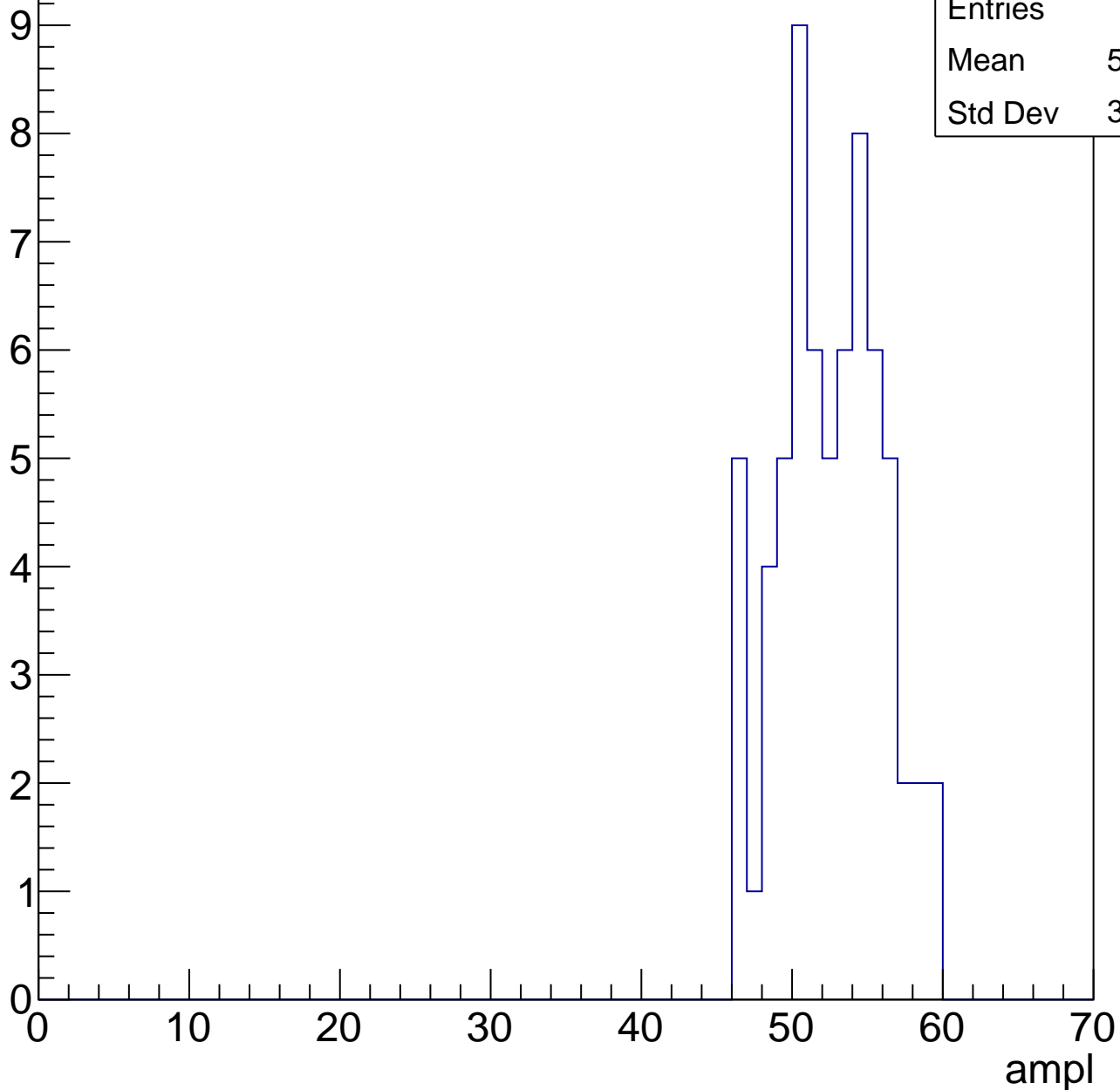
0 10 20 30 40 50 60 70



B1L101S, U21-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



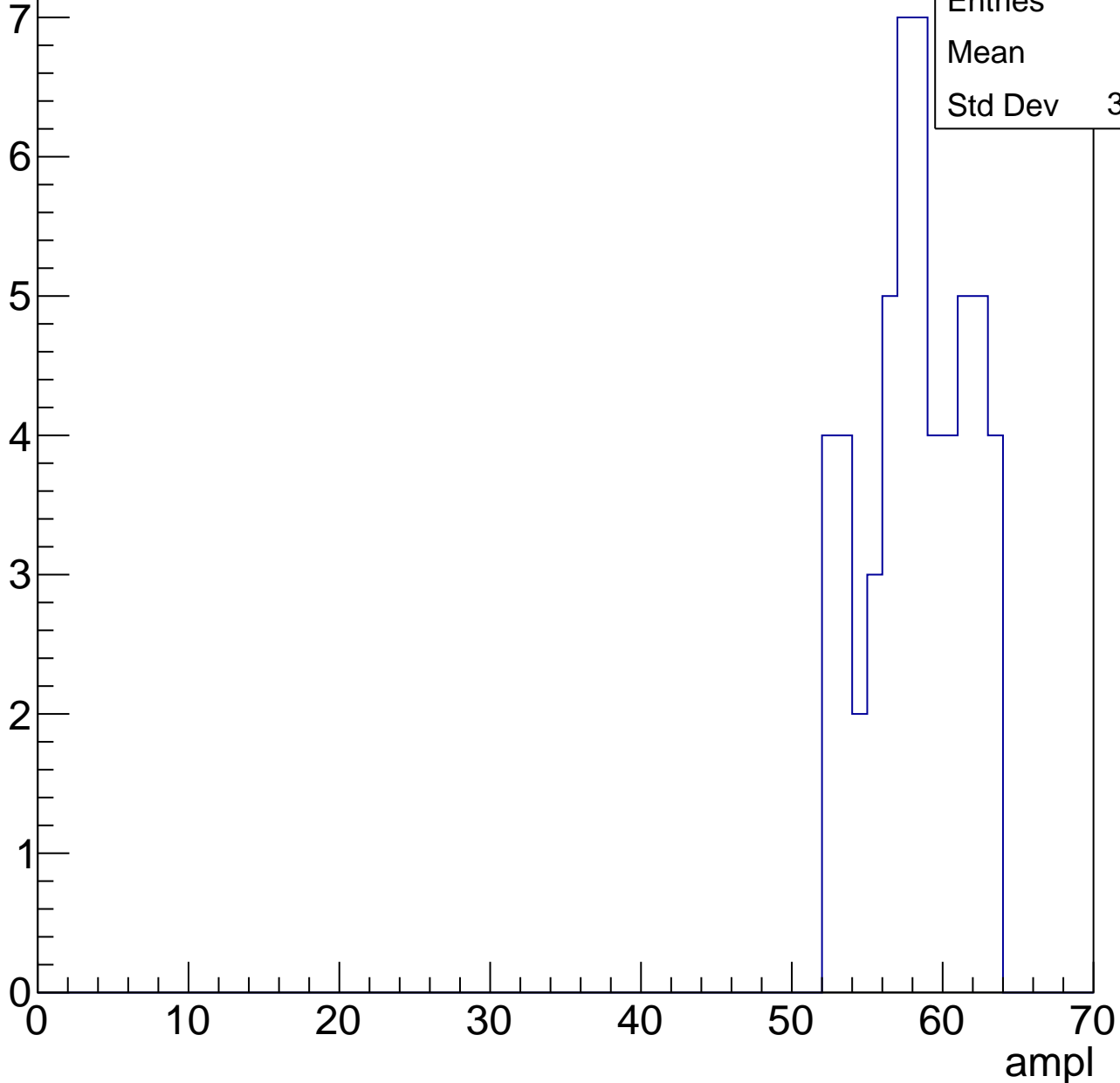
Entries	66
Mean	52.09
Std Dev	3.365

B1L101S, U21-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.8
Std Dev	3.257



B1L101S, U21-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	28
Mean	58.32
Std Dev	11.38

ampl

0

10

20

30

40

50

60

70

B1L101S, U21-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch62, adc0

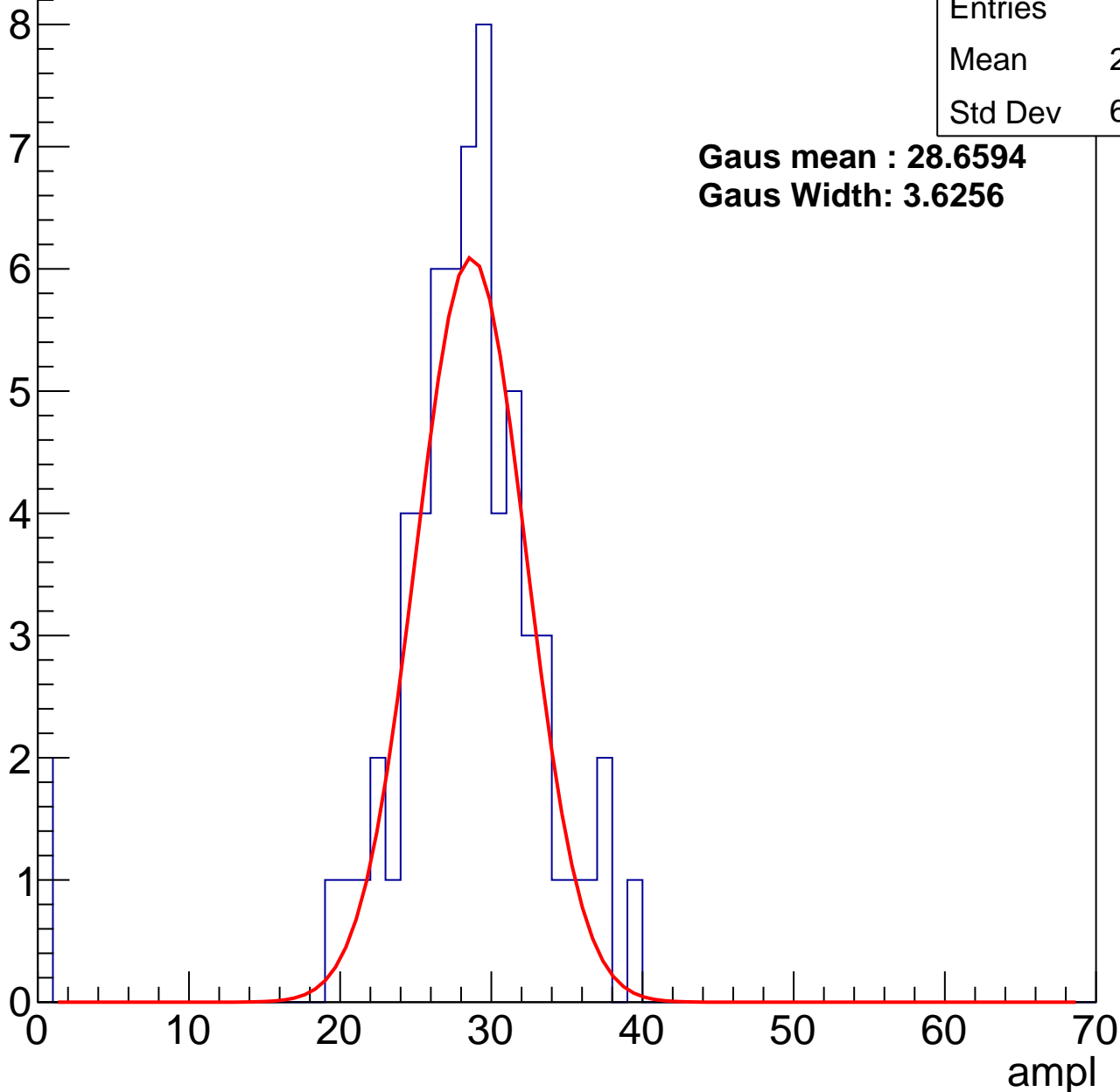
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	27.45
Std Dev	6.366

Gaus mean : 28.6594

Gaus Width: 3.6256



B1L101S, U21-ch62, adc1

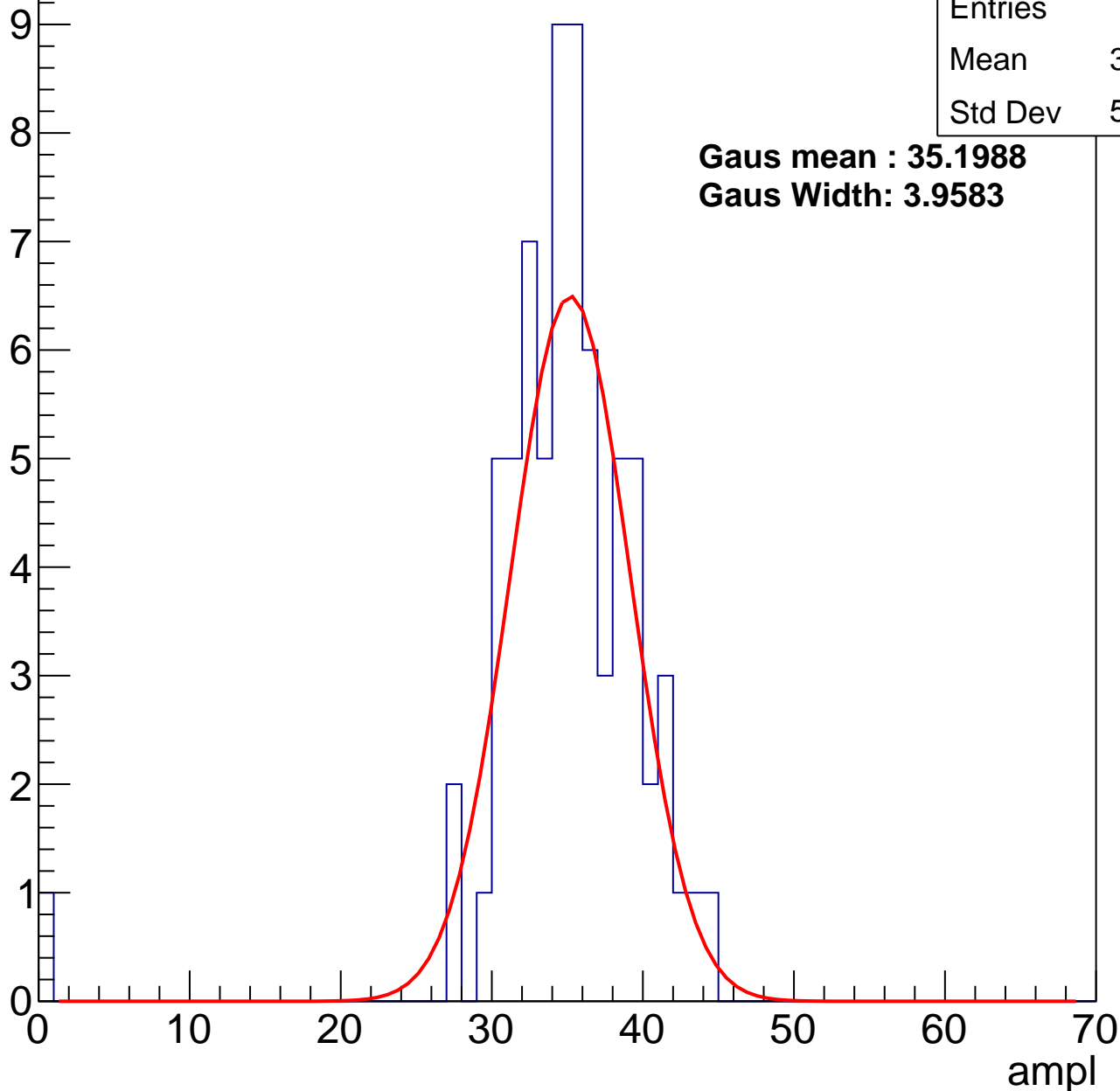
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	34.39
Std Dev	5.509

Gaus mean : 35.1988

Gaus Width: 3.9583



B1L101S, U21-ch62, adc2

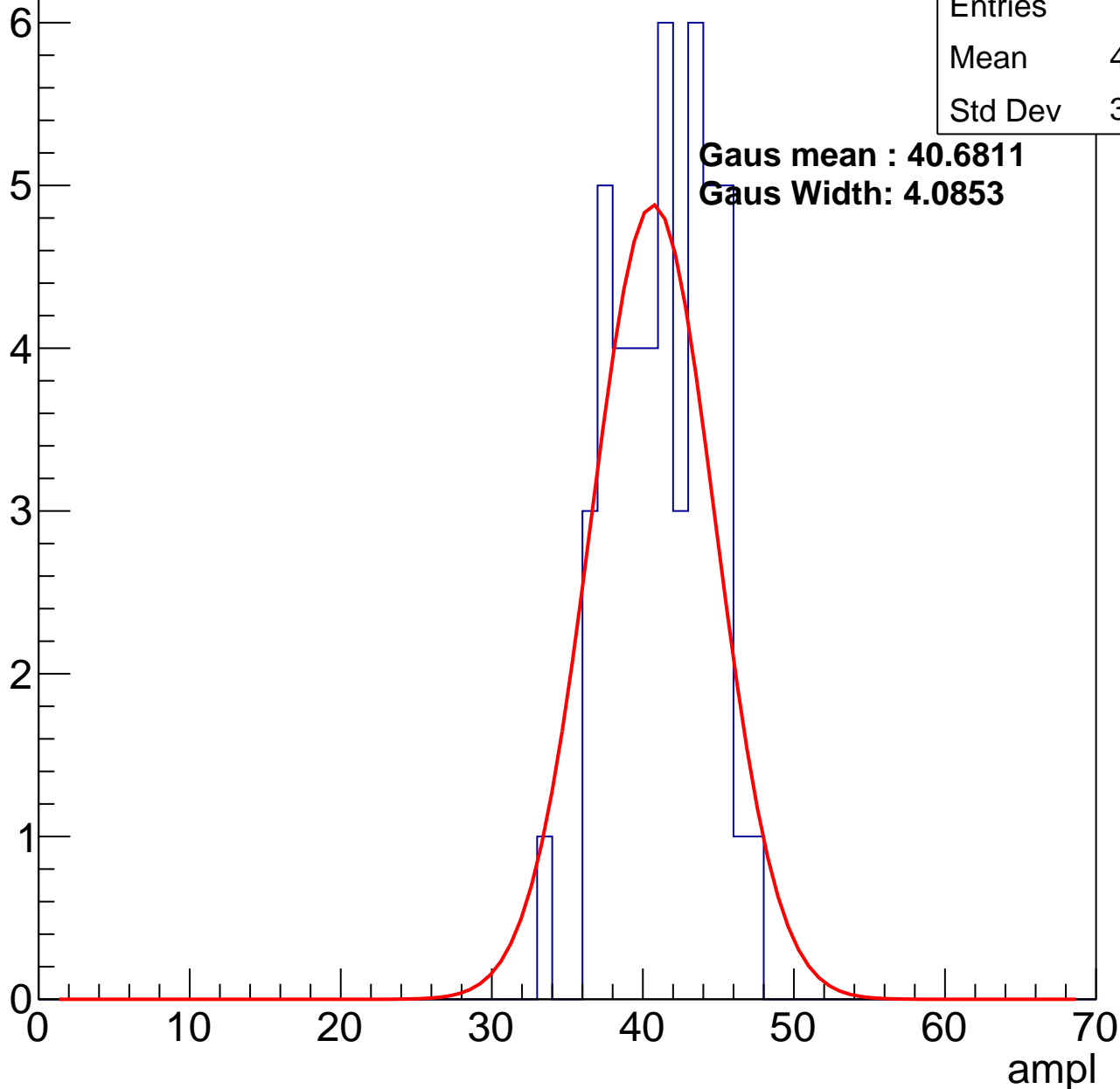
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	40.88
Std Dev	3.186

Gaus mean : 40.6811

Gaus Width: 4.0853

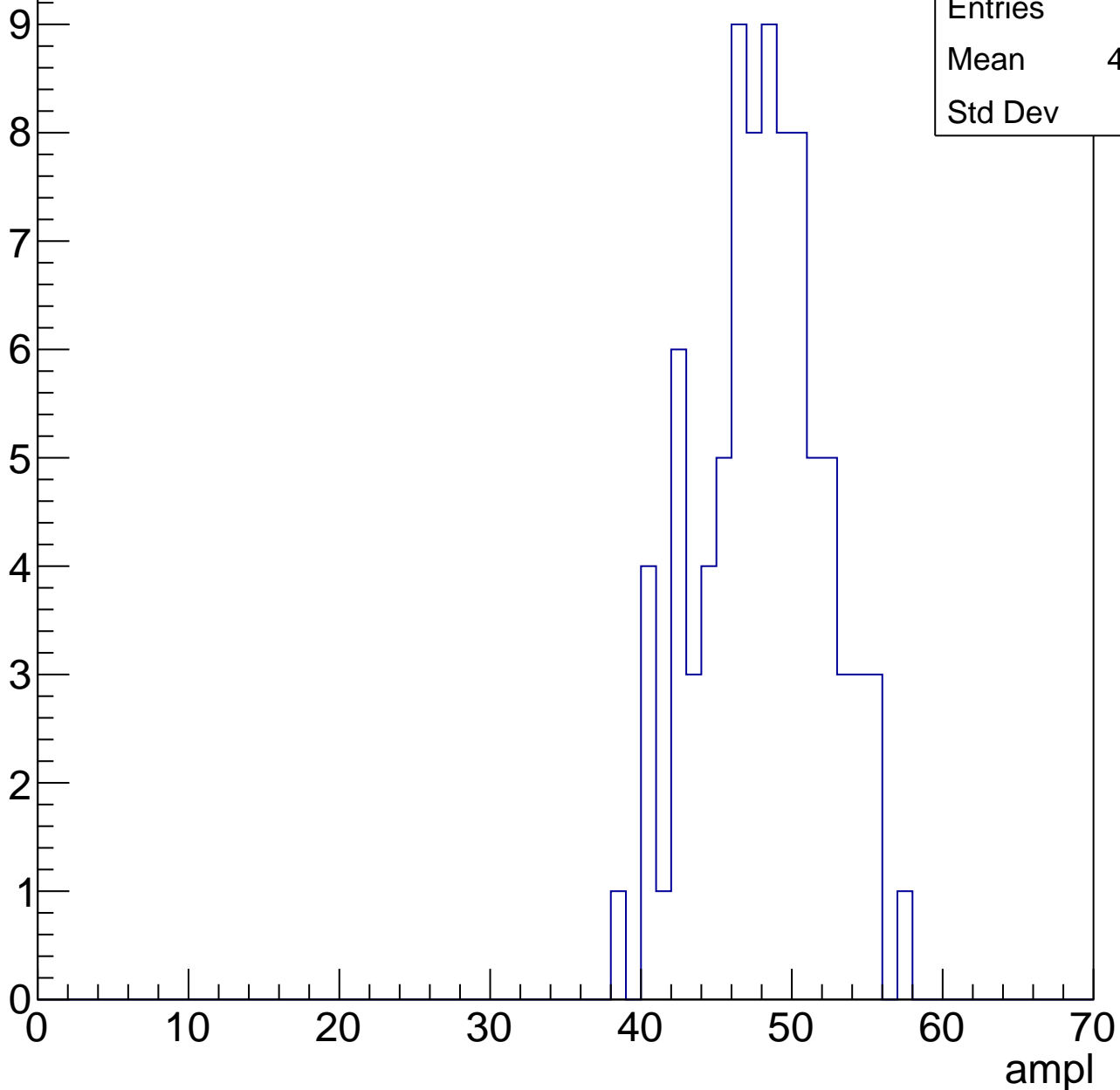


B1L101S, U21-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	47.59
Std Dev	4.05



B1L101S, U21-ch62, adc4

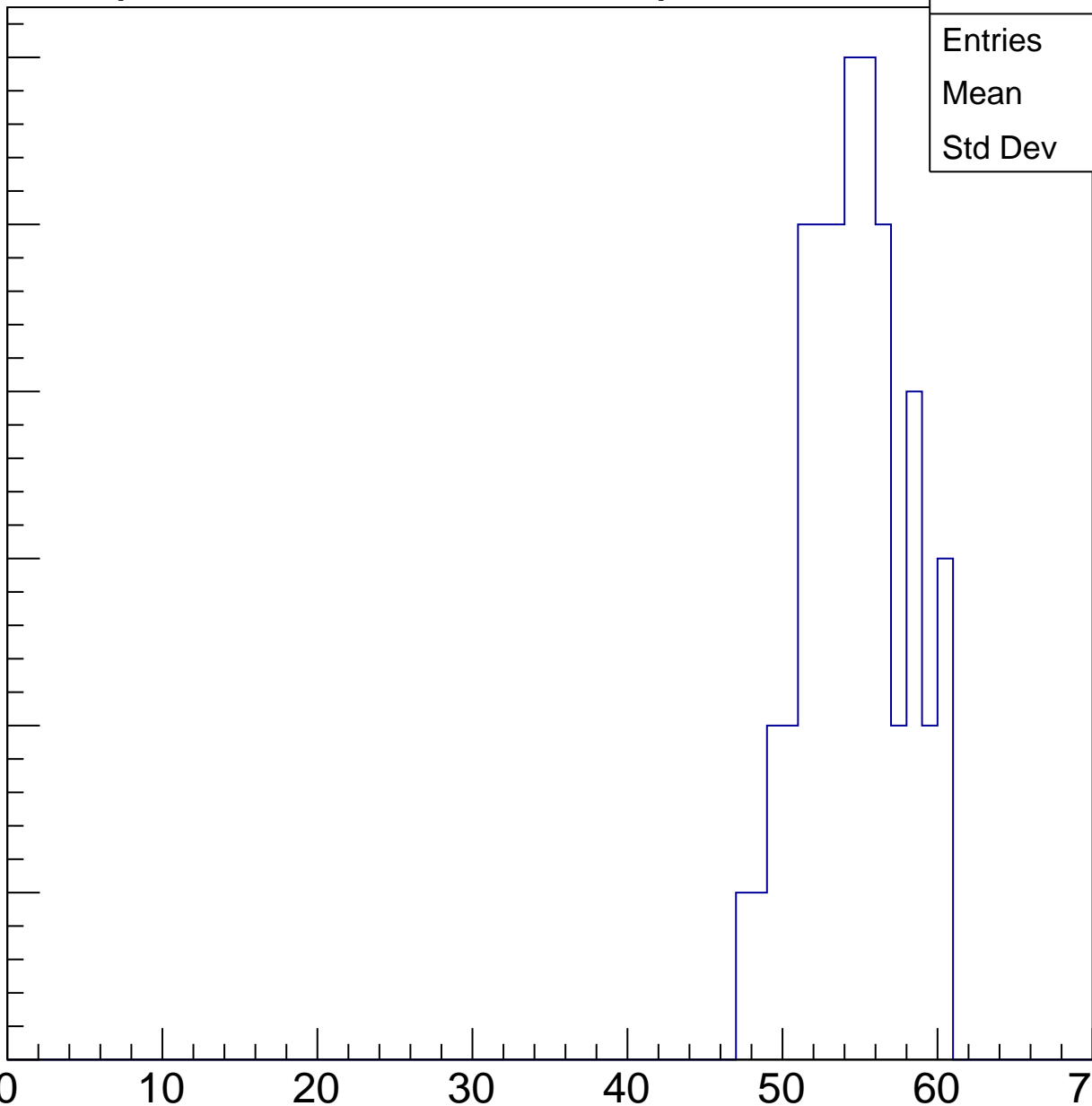
calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	49
Mean	54.1
Std Dev	3.202

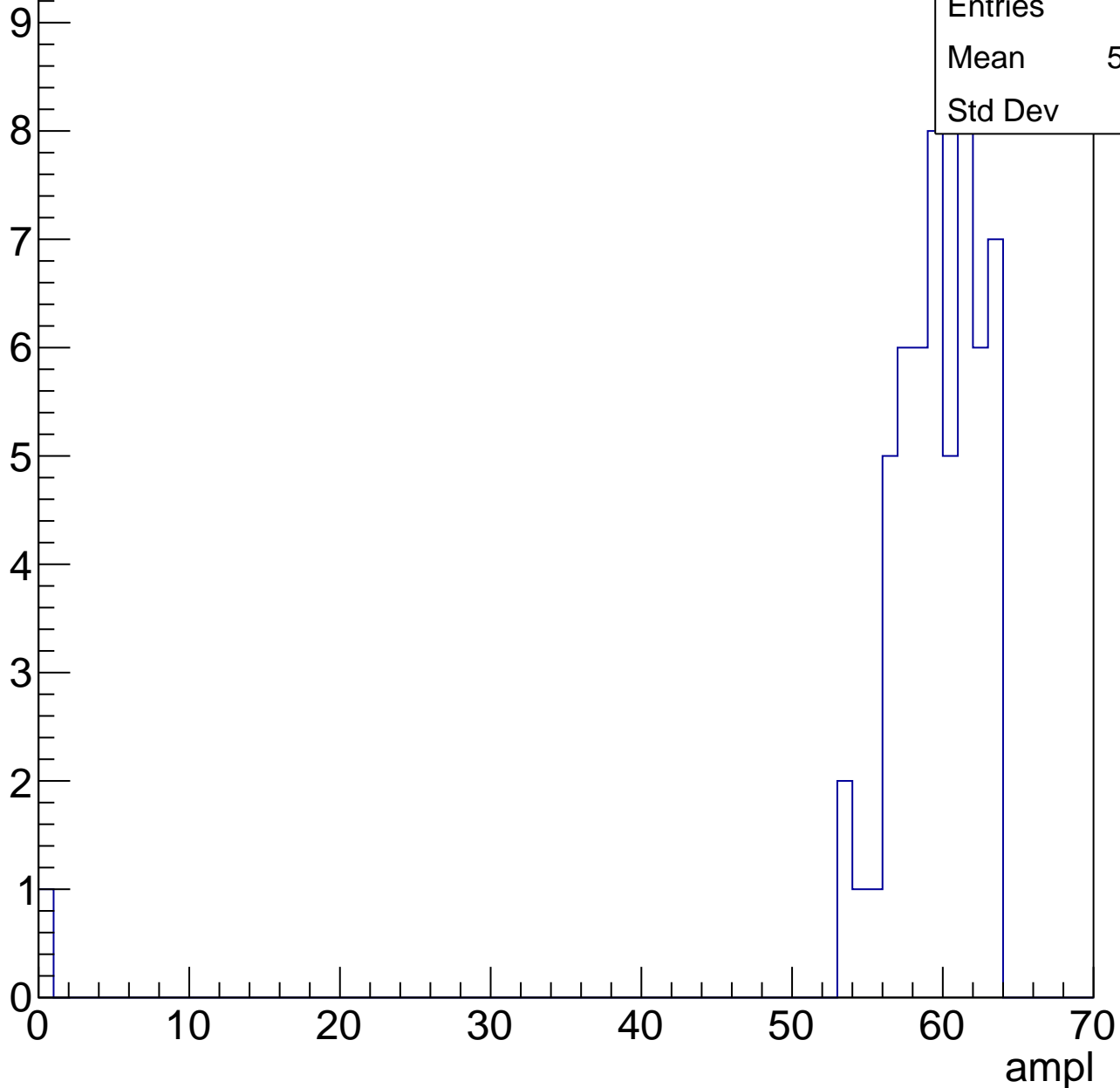
ampl



B1L101S, U21-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

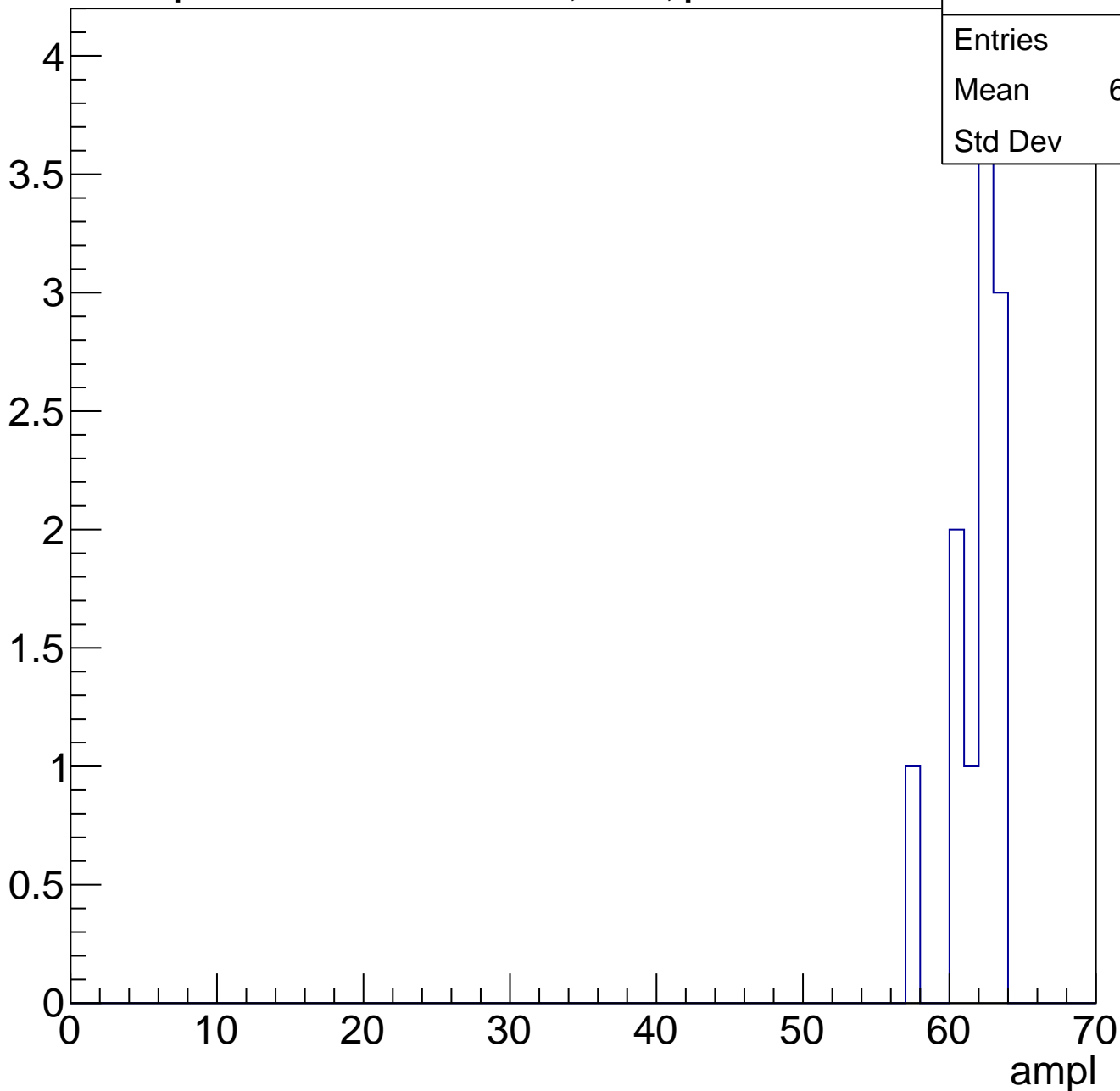
Entry



B1L101S, U21-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



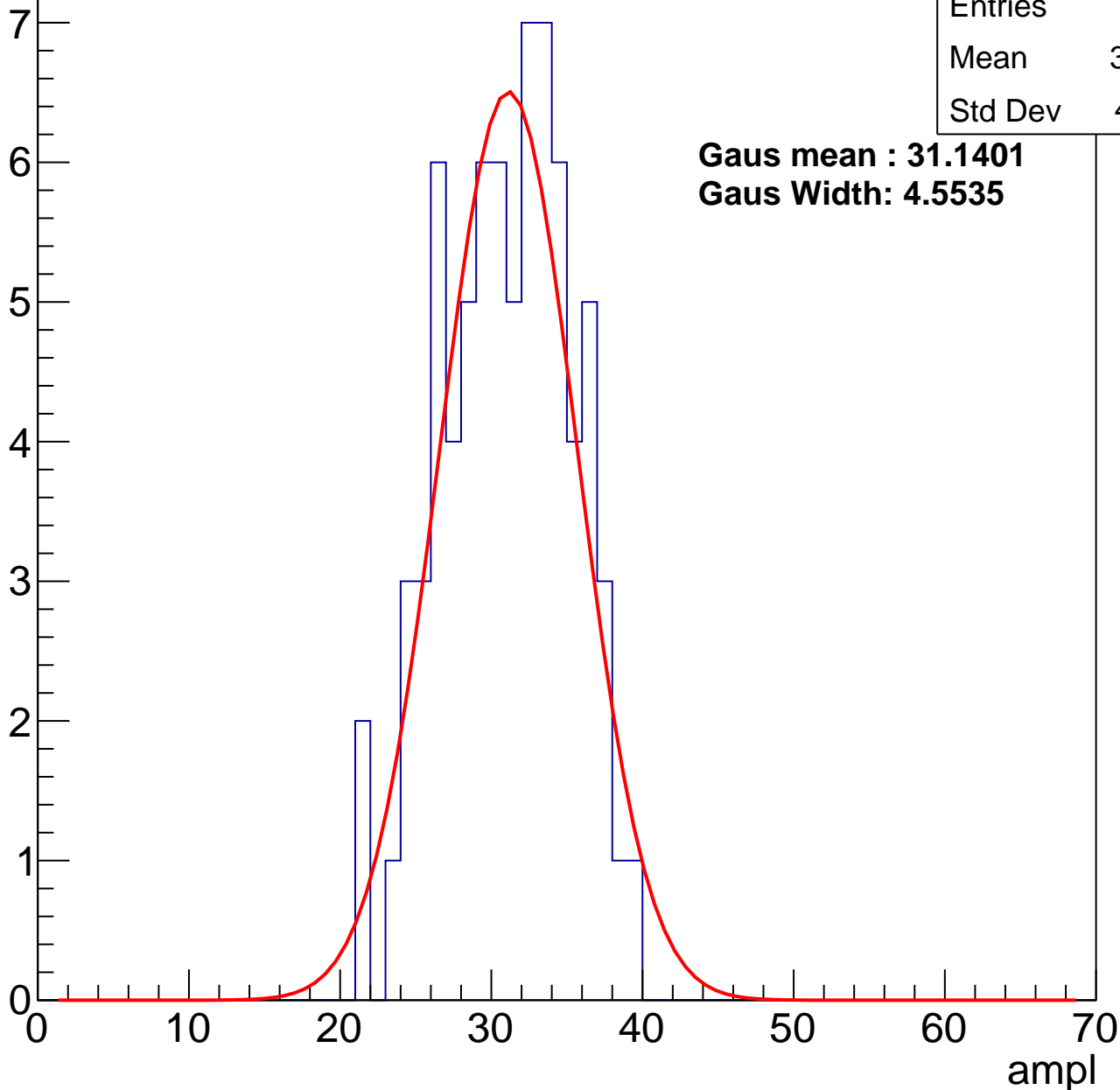
B1L101S, U21-ch63, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.56
Std Dev	4.151

Gaus mean : 31.1401
Gaus Width: 4.5535



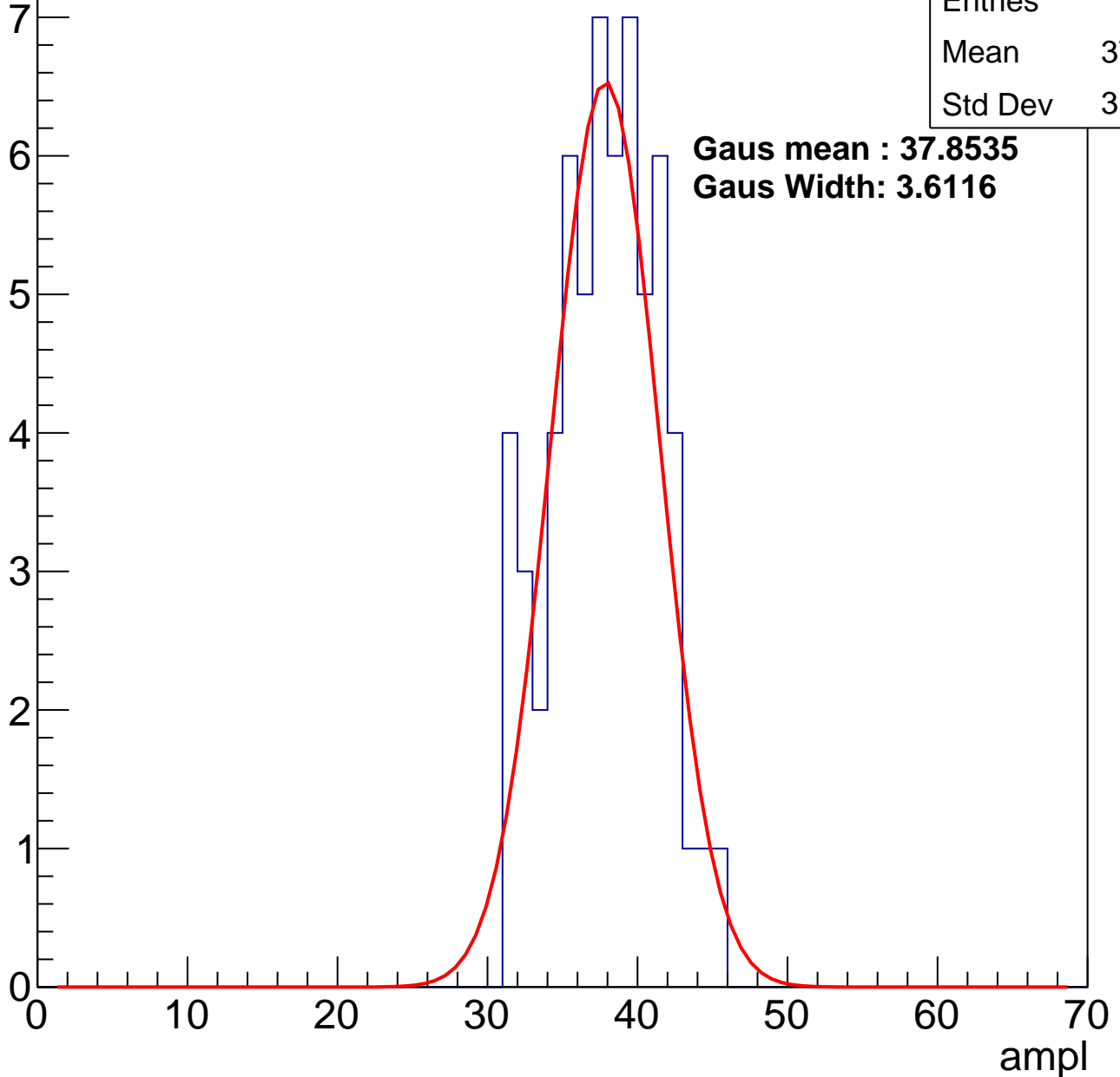
B1L101S, U21-ch63, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.39
Std Dev	3.433

Gaus mean : 37.8535
Gaus Width: 3.6116



B1L101S, U21-ch63, adc2

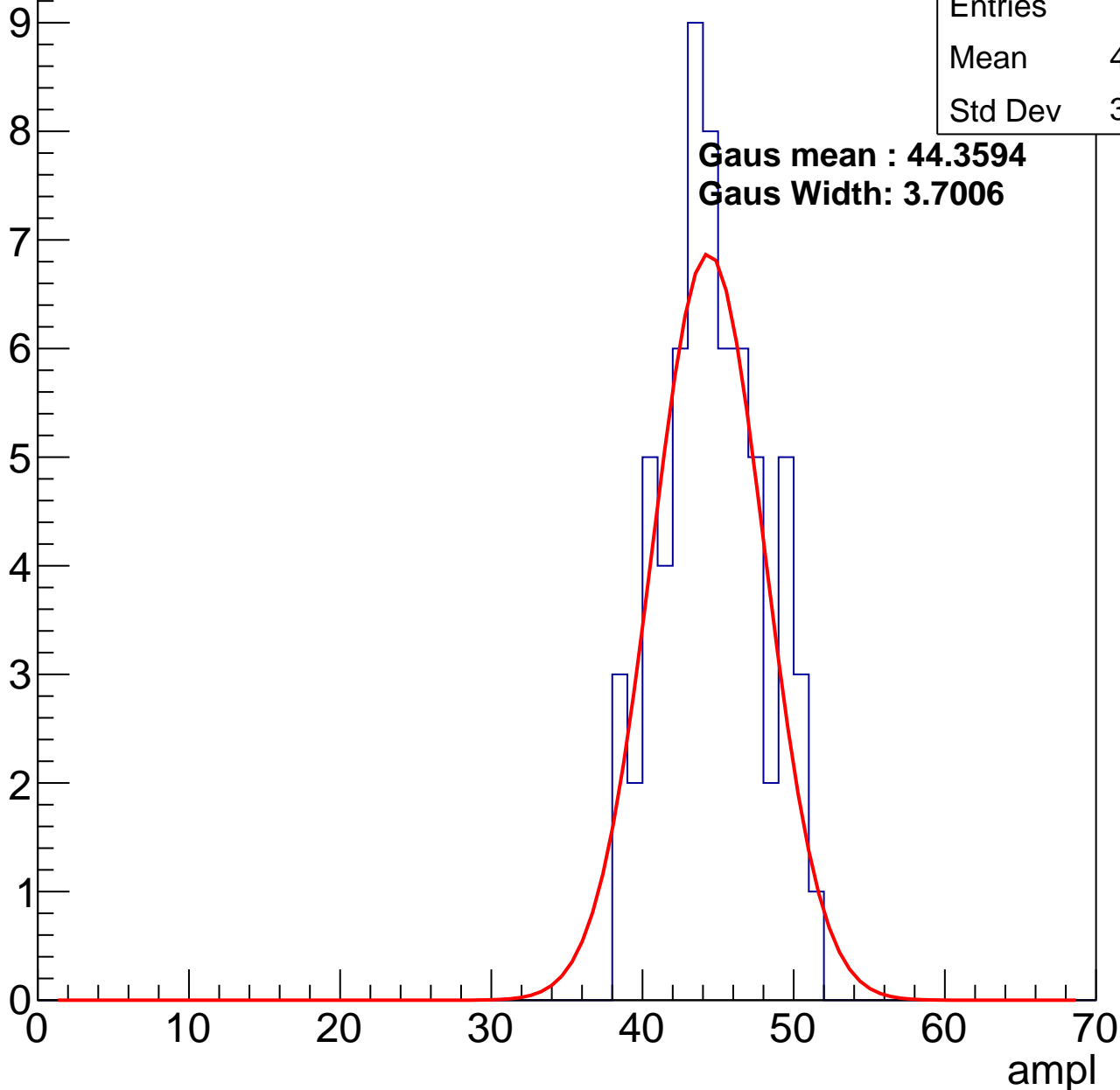
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	44.15
Std Dev	3.269

Gaus mean : 44.3594

Gaus Width: 3.7006

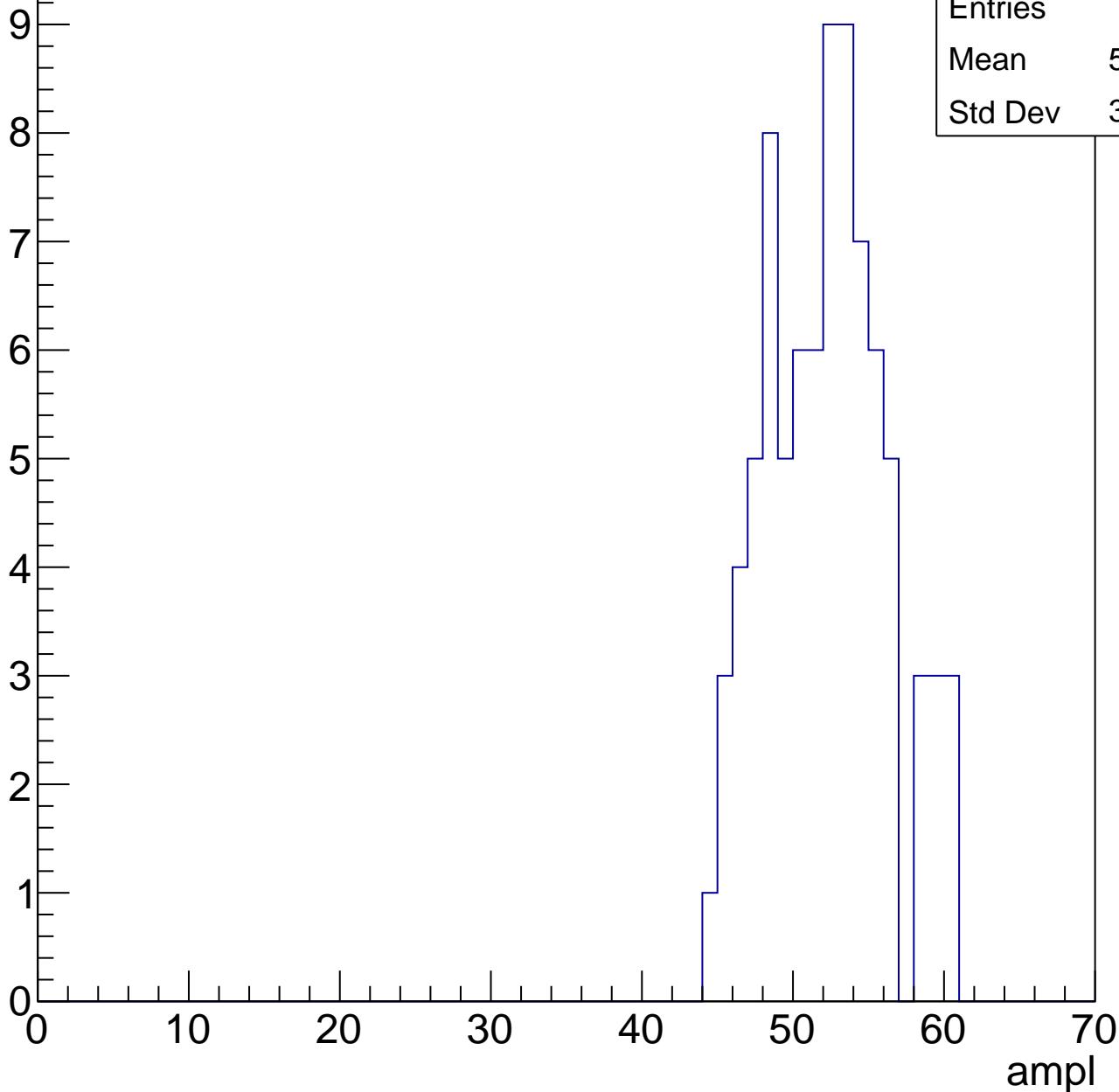


B1L101S, U21-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	83
Mean	51.77
Std Dev	3.953

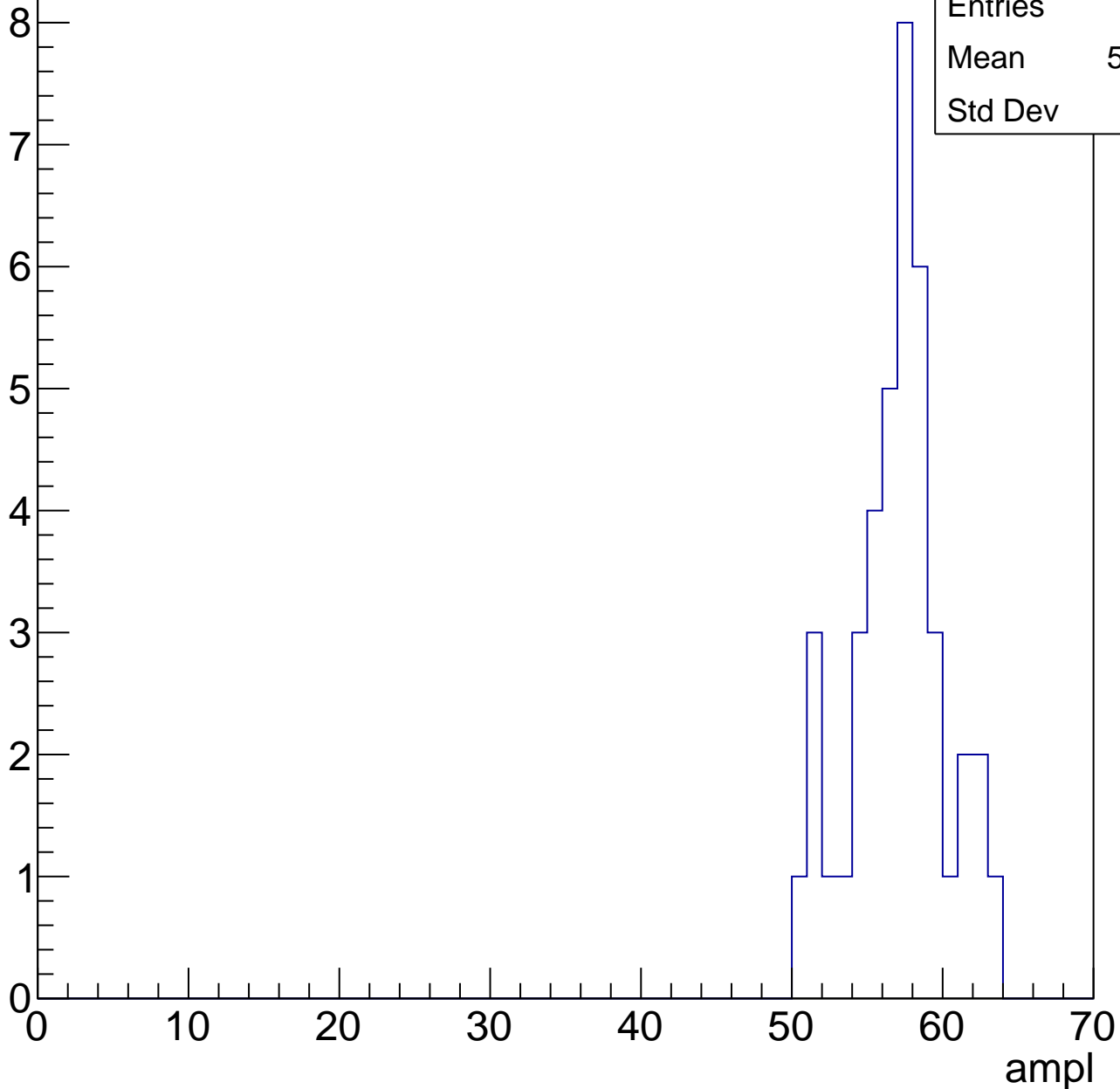


B1L101S, U21-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

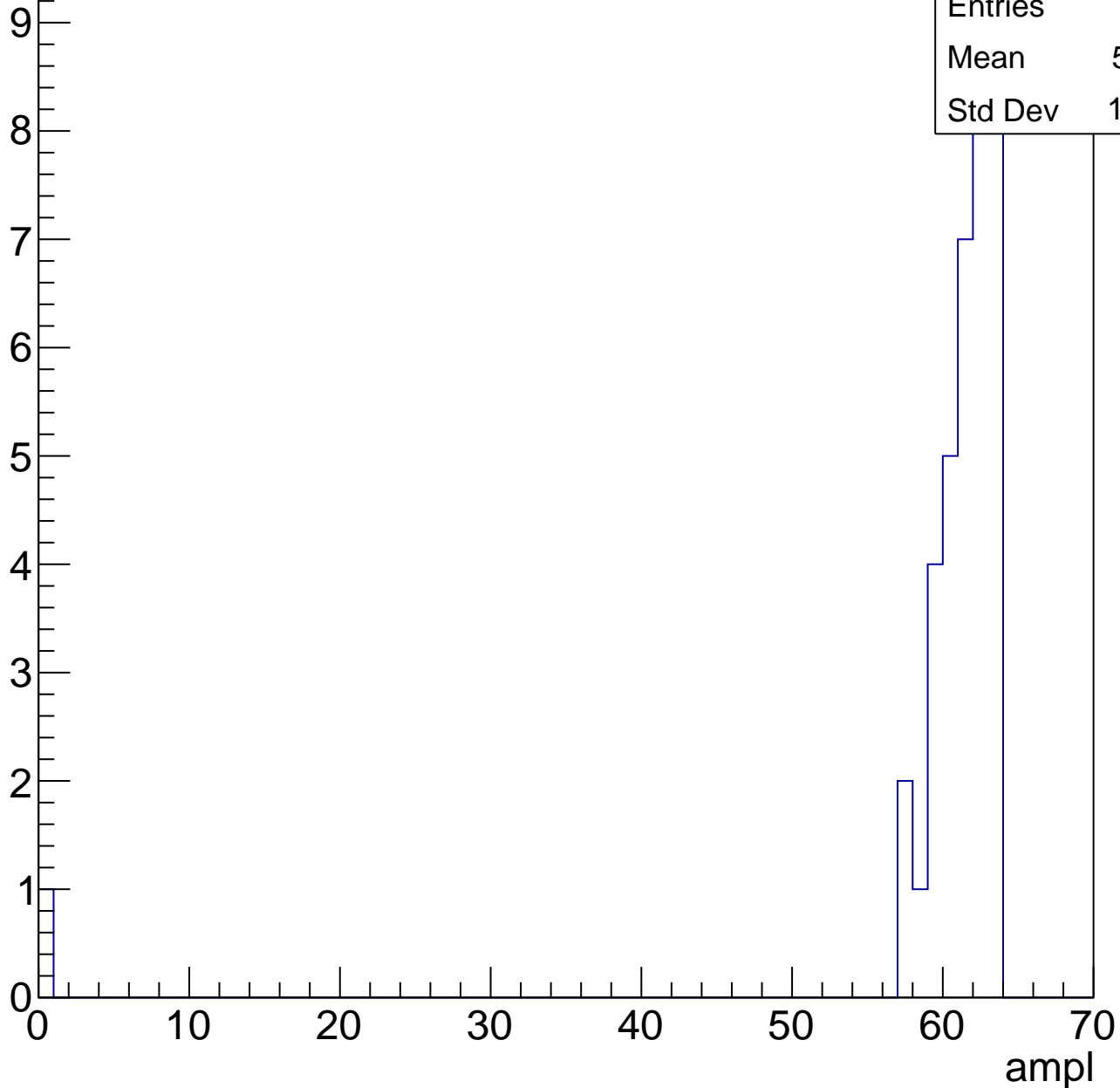
Entries	41
Mean	56.59
Std Dev	3.06



B1L101S, U21-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch64, adc0

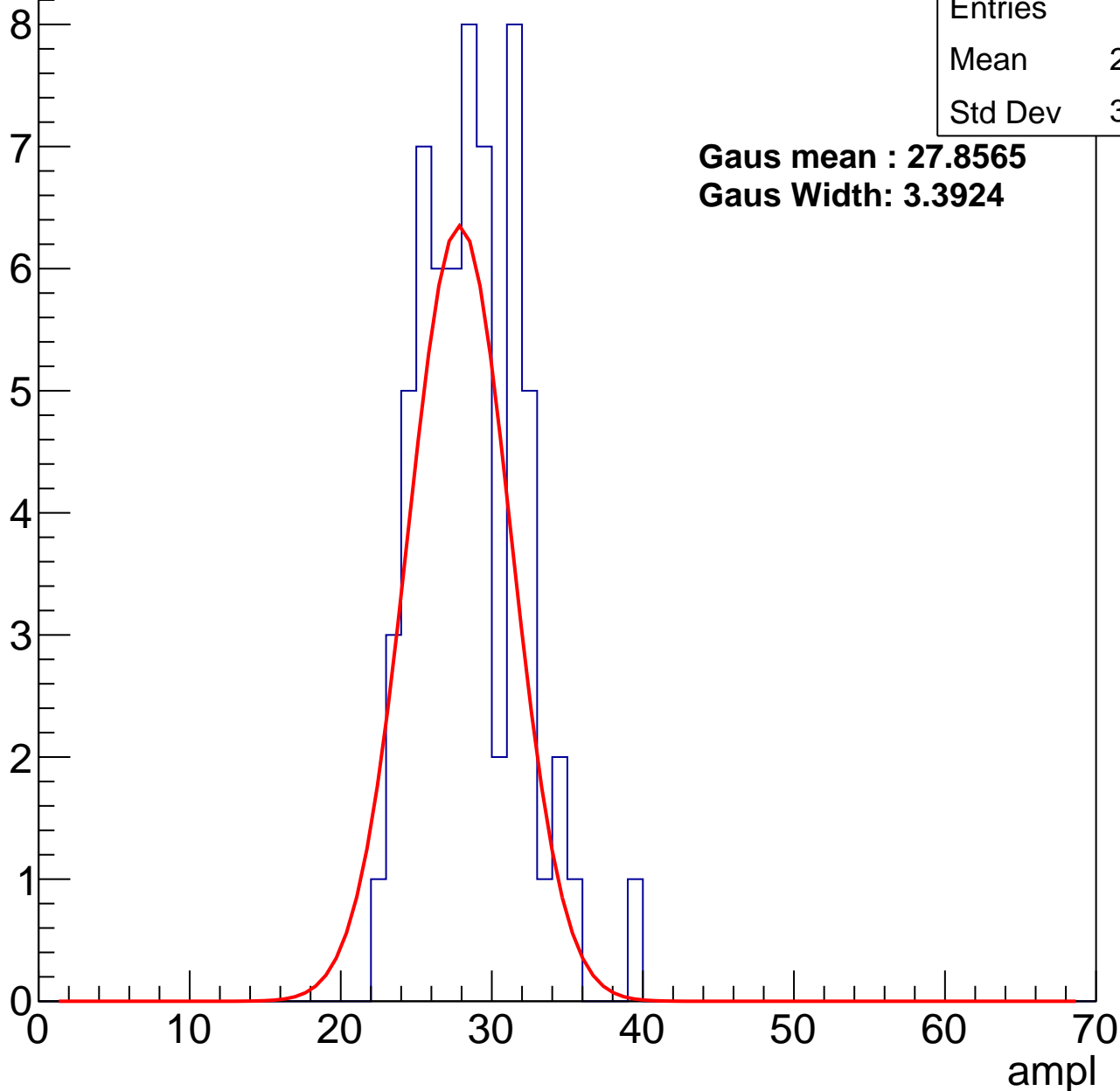
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.16
Std Dev	3.372

Gaus mean : 27.8565

Gaus Width: 3.3924



B1L101S, U21-ch64, adc1

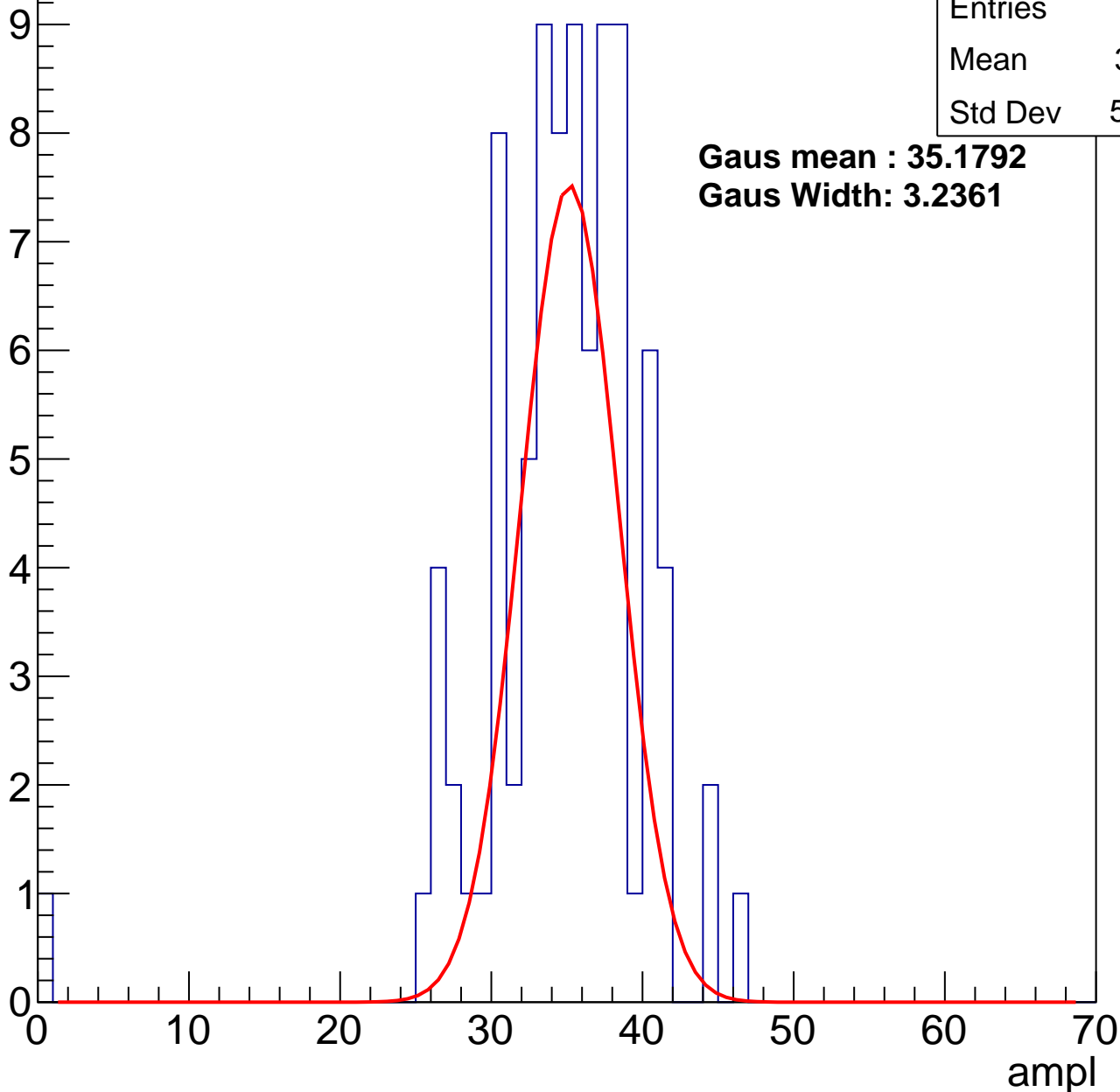
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	34.31
Std Dev	5.666

Gaus mean : 35.1792

Gaus Width: 3.2361



B1L101S, U21-ch64, adc2

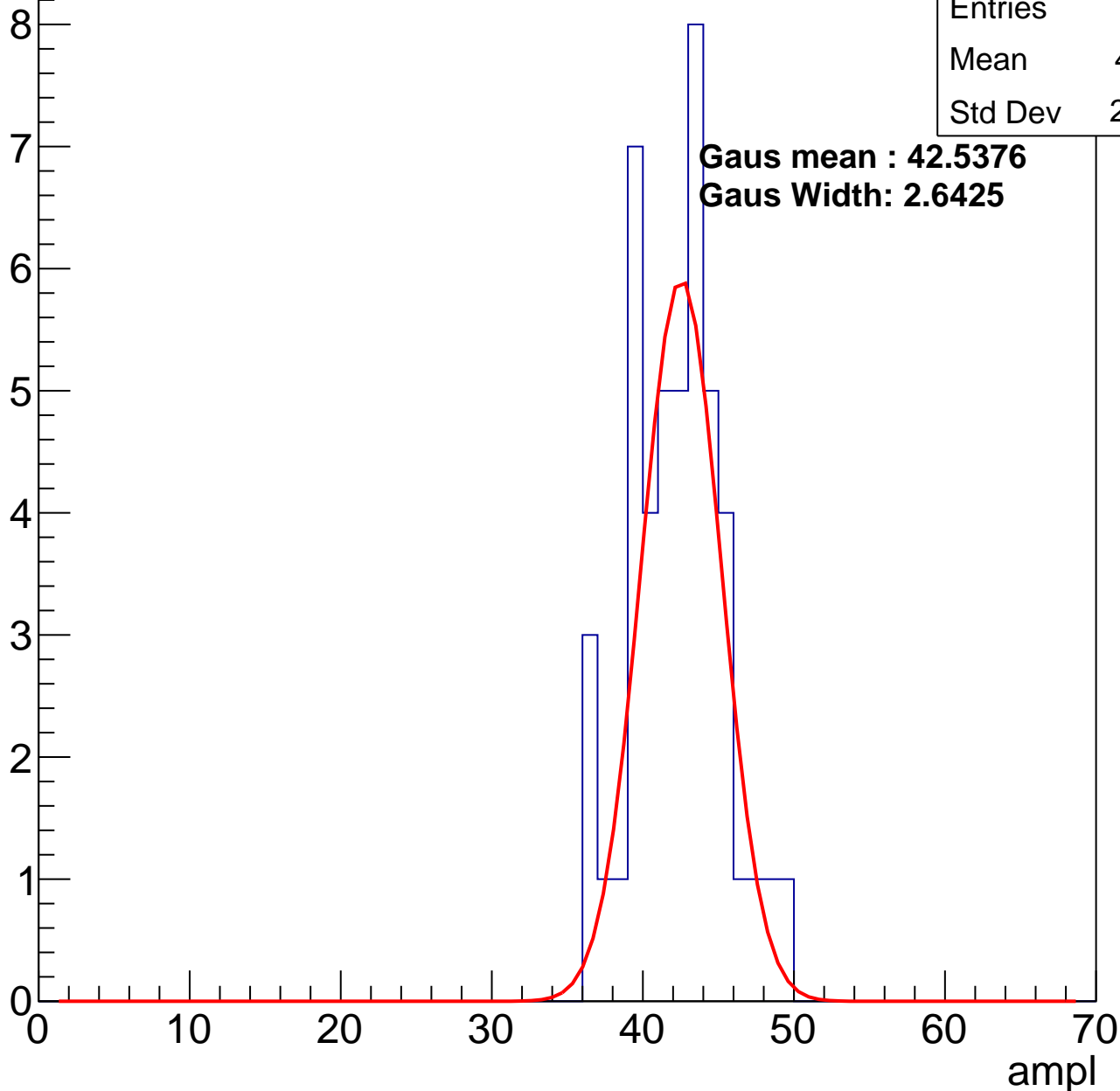
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	41.81
Std Dev	2.994

Gaus mean : 42.5376

Gaus Width: 2.6425

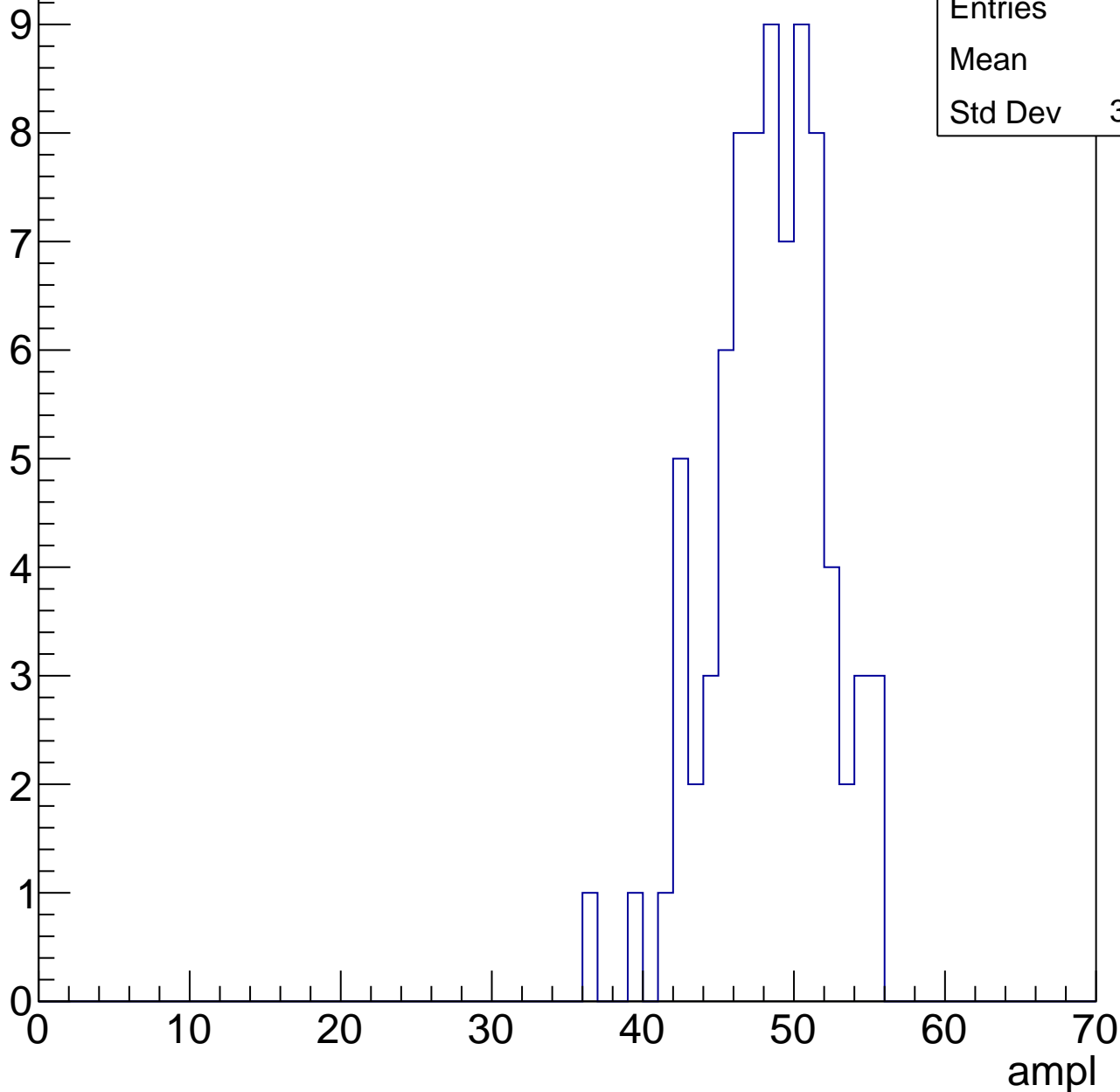


B1L101S, U21-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	47.9
Std Dev	3.757

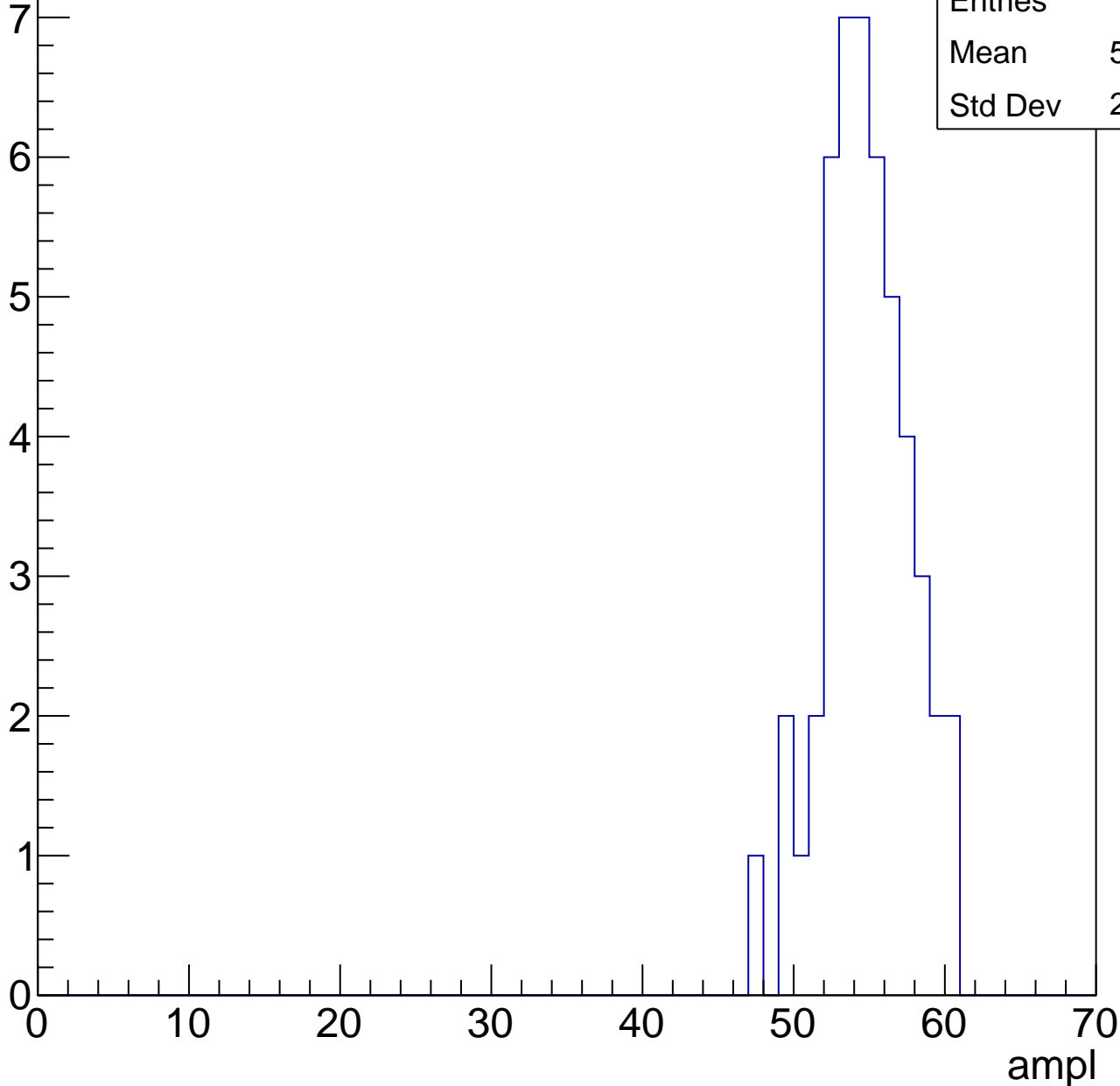


B1L101S, U21-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

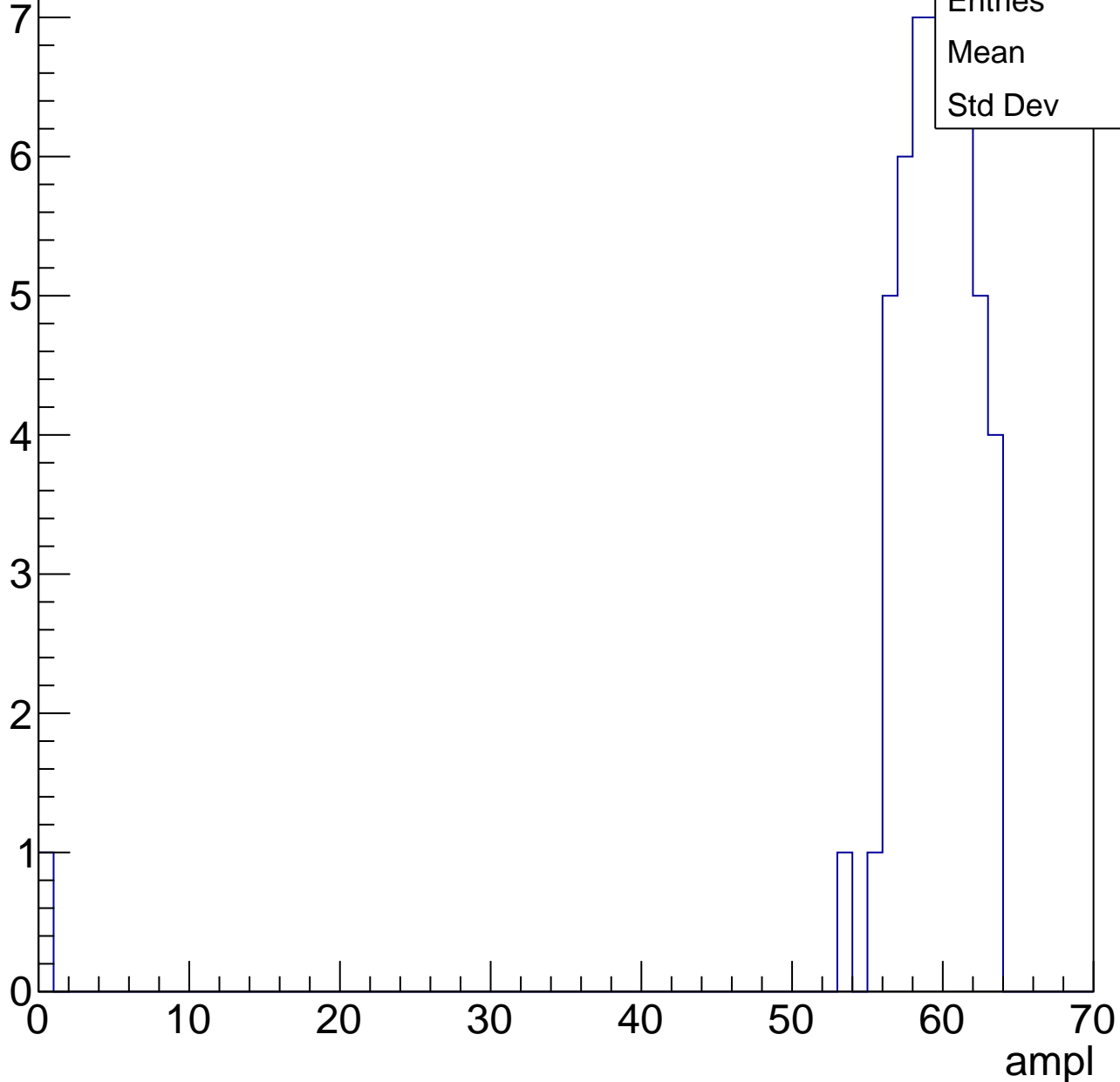
Entries	48
Mean	54.33
Std Dev	2.853



B1L101S, U21-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

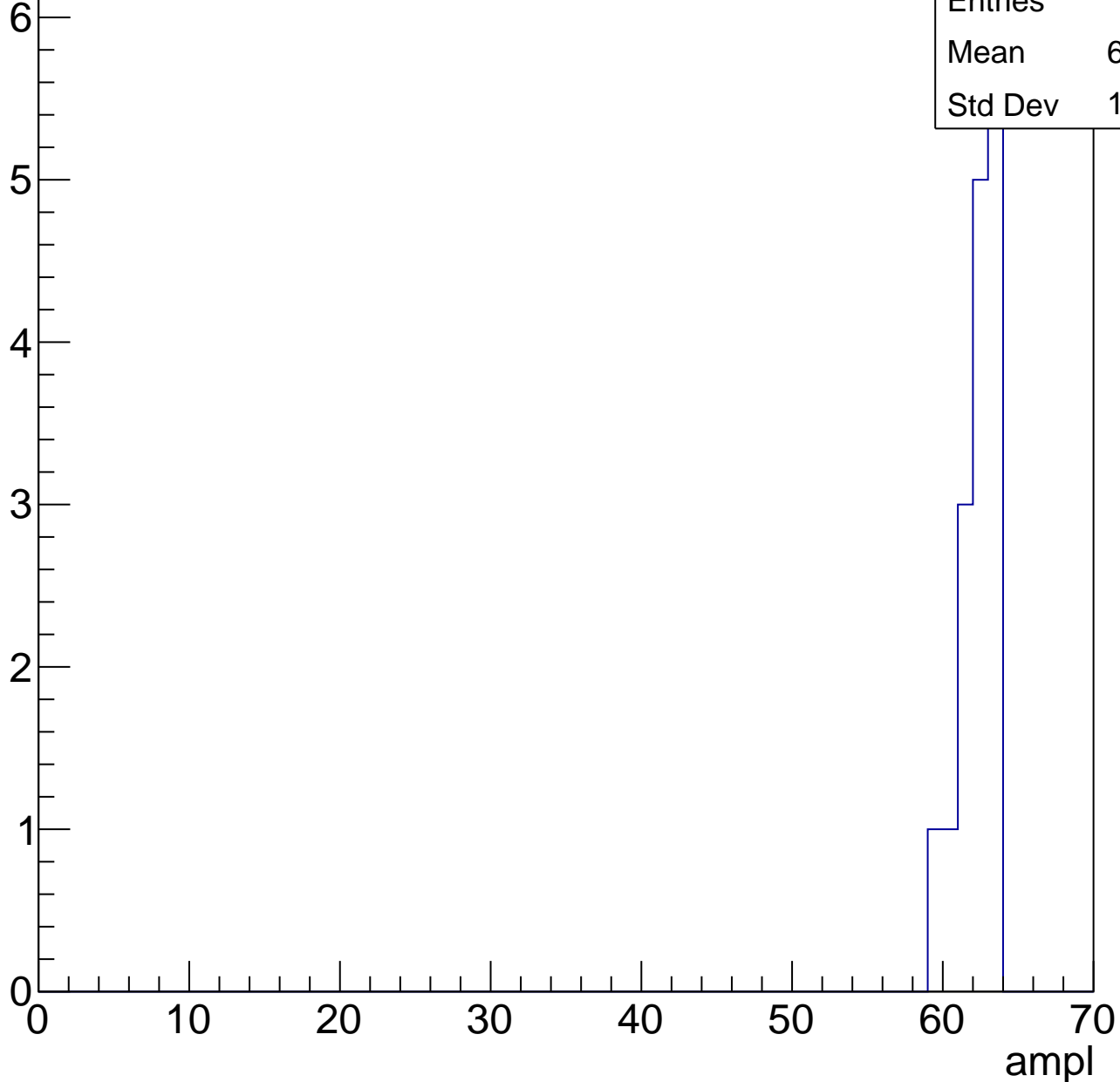
Entry



B1L101S, U21-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch65, adc0

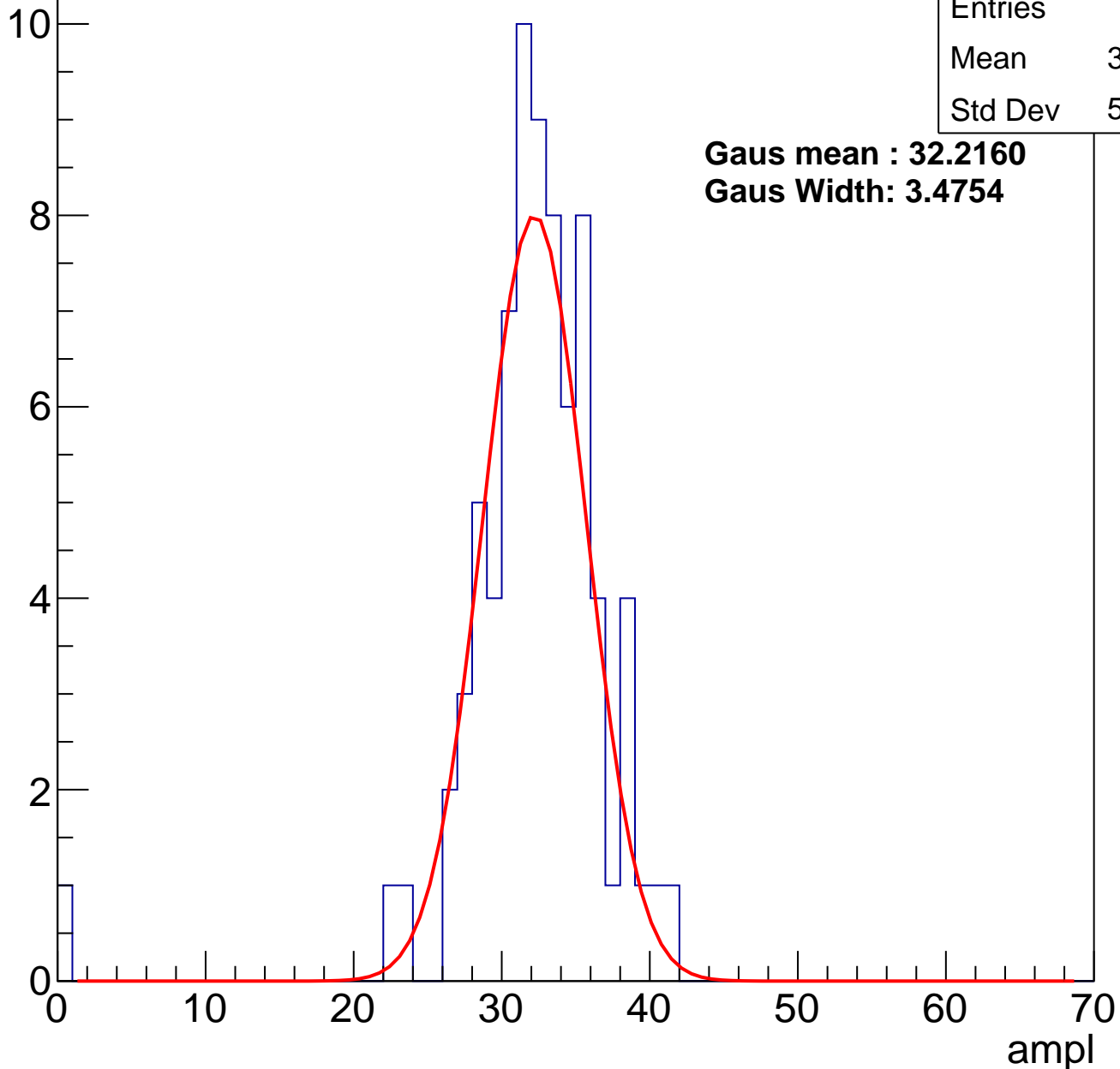
calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	31.73
Std Dev	5.136

Gaus mean : 32.2160

Gaus Width: 3.4754

Entry



B1L101S, U21-ch65, adc1

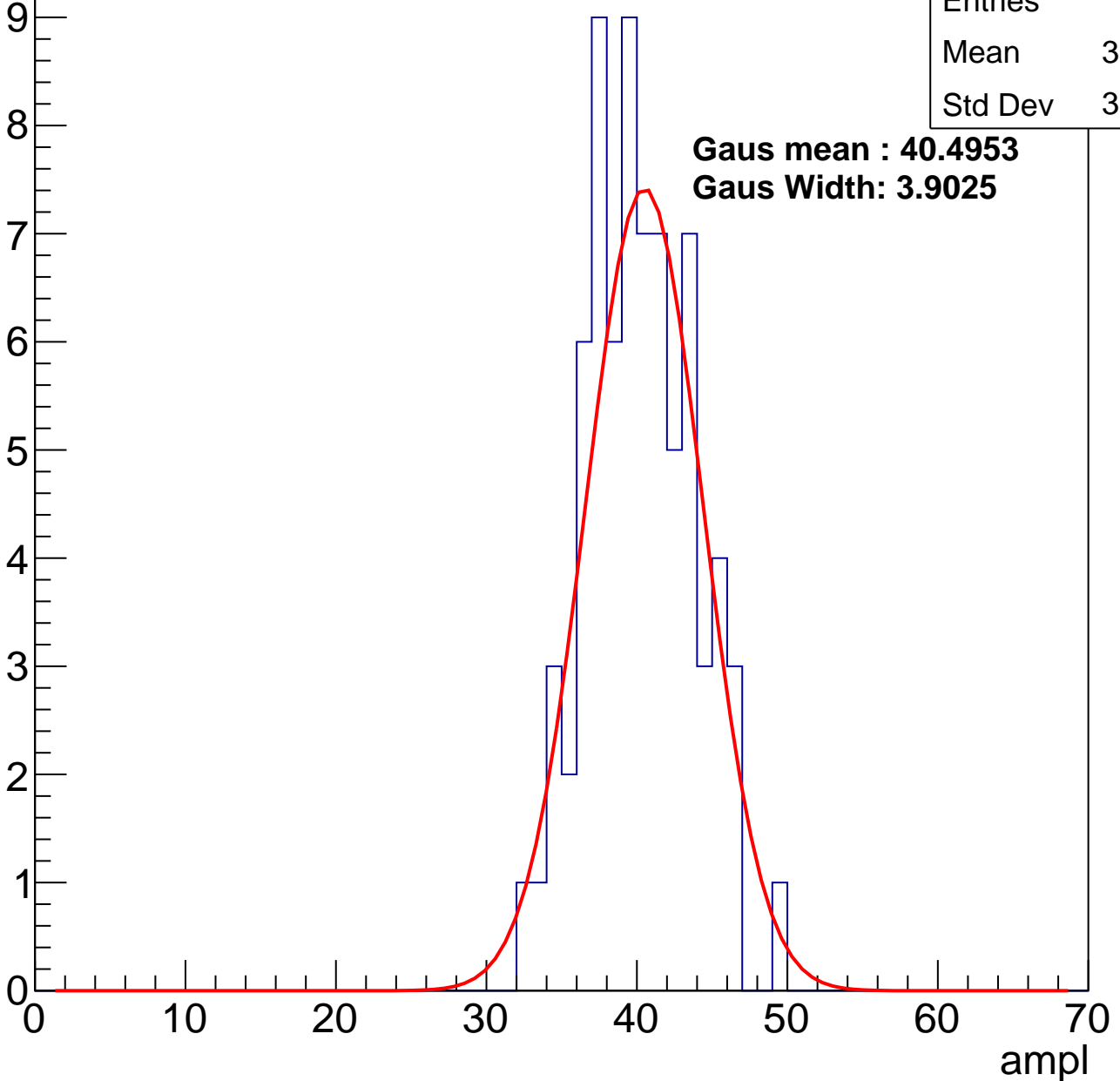
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	39.76
Std Dev	3.494

Gaus mean : 40.4953

Gaus Width: 3.9025



B1L101S, U21-ch65, adc2

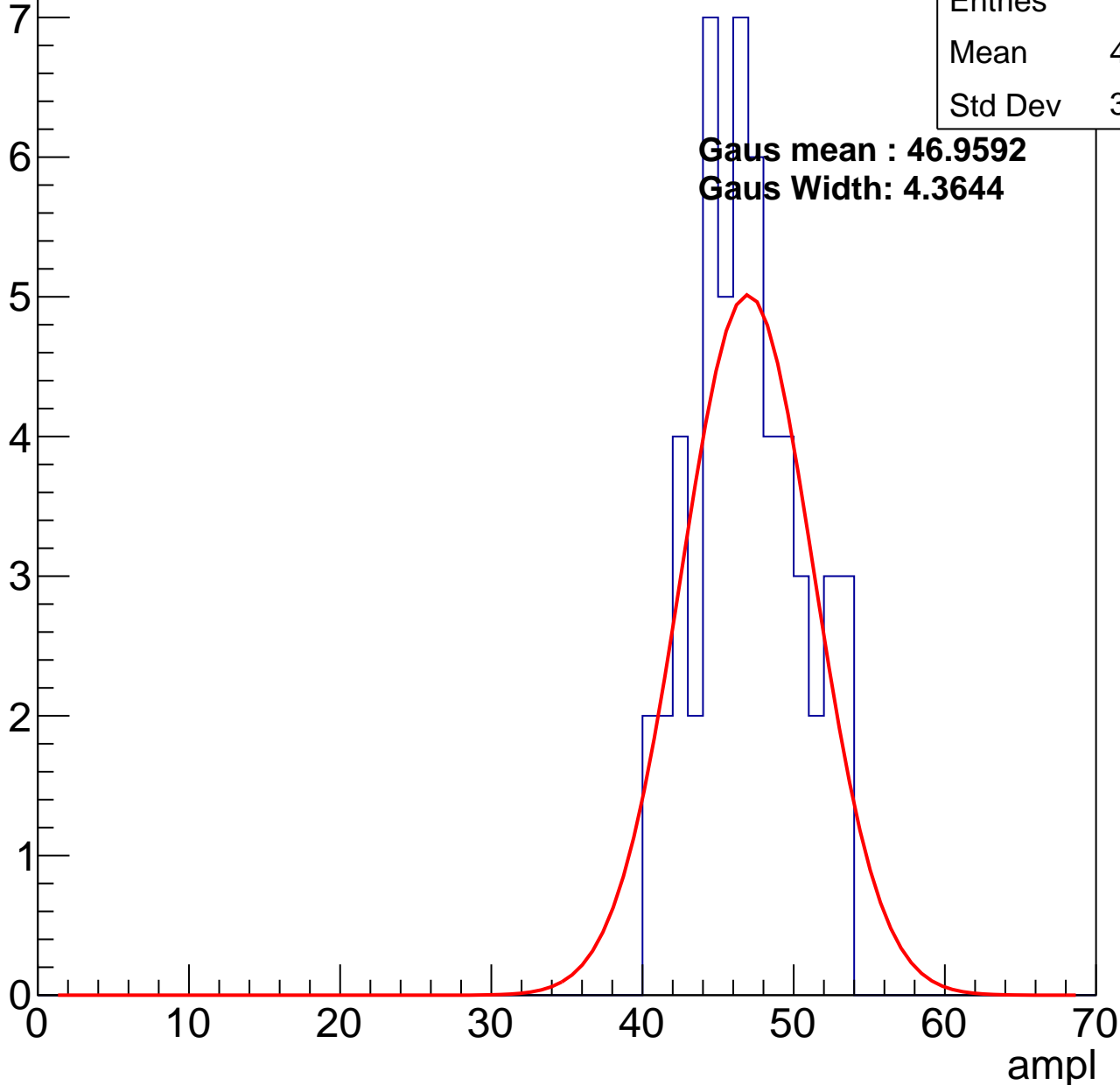
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	46.44
Std Dev	3.435

Gaus mean : 46.9592

Gaus Width: 4.3644

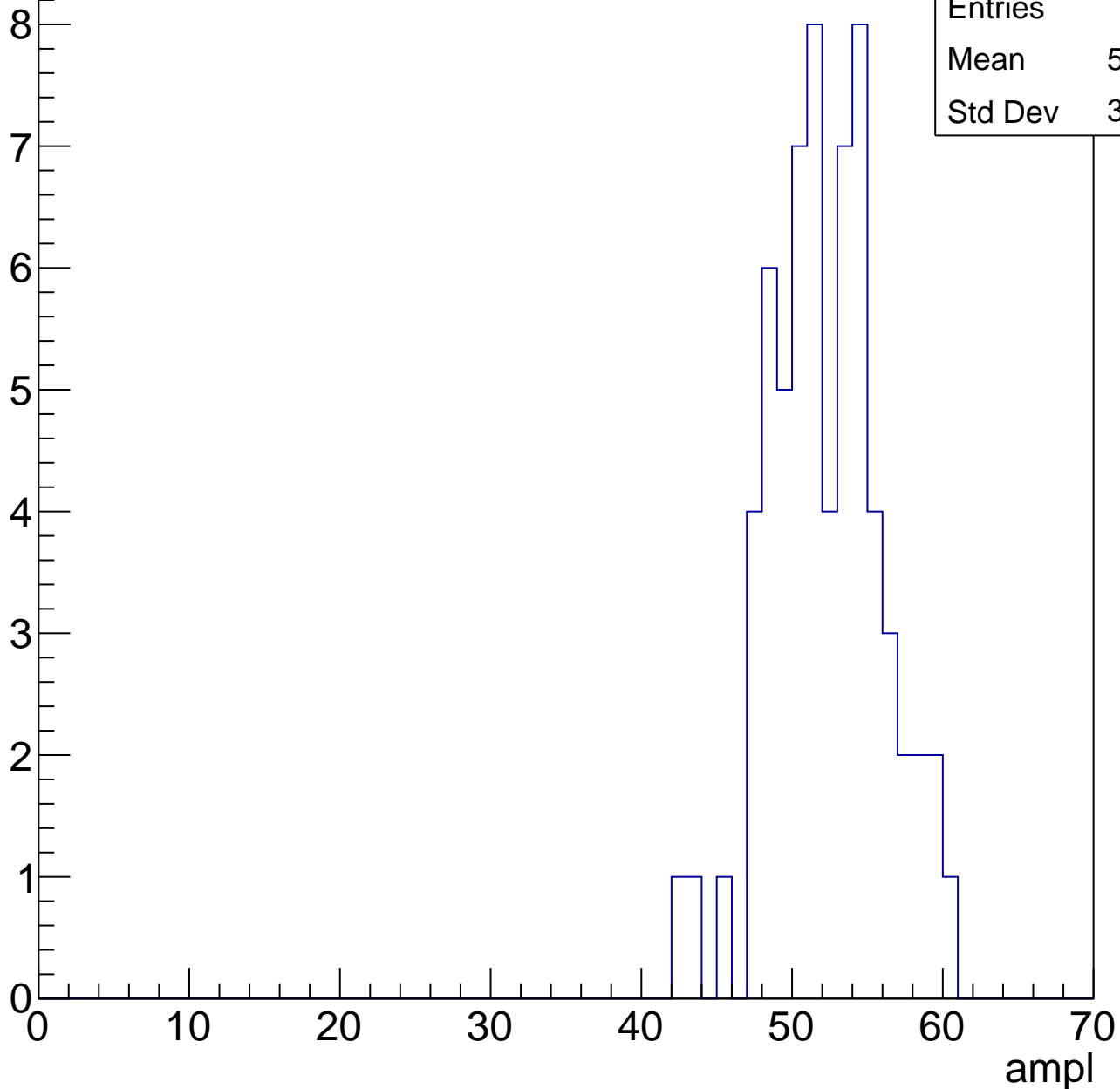


B1L101S, U21-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	51.76
Std Dev	3.713

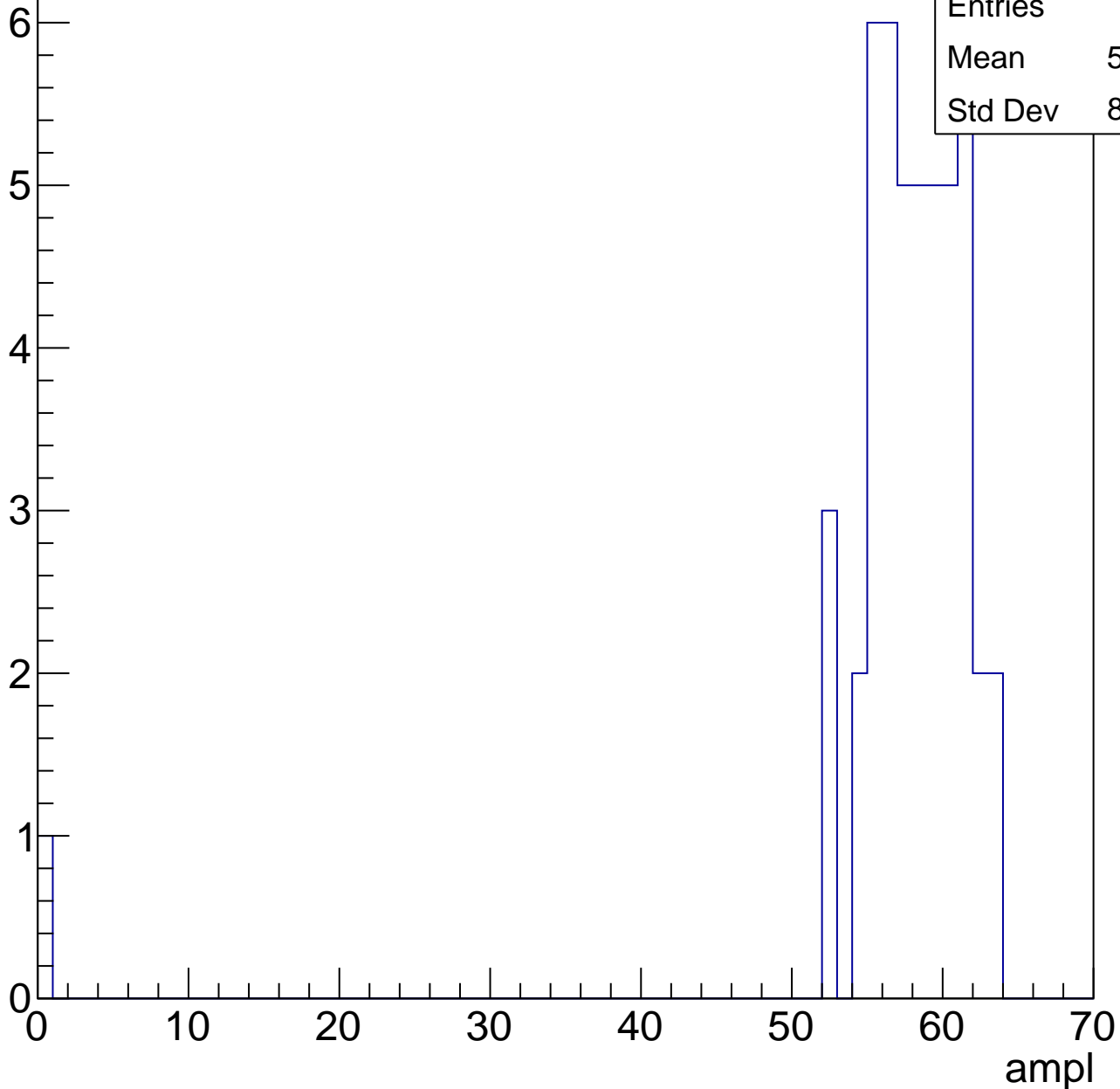


B1L101S, U21-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	56.58
Std Dev	8.722

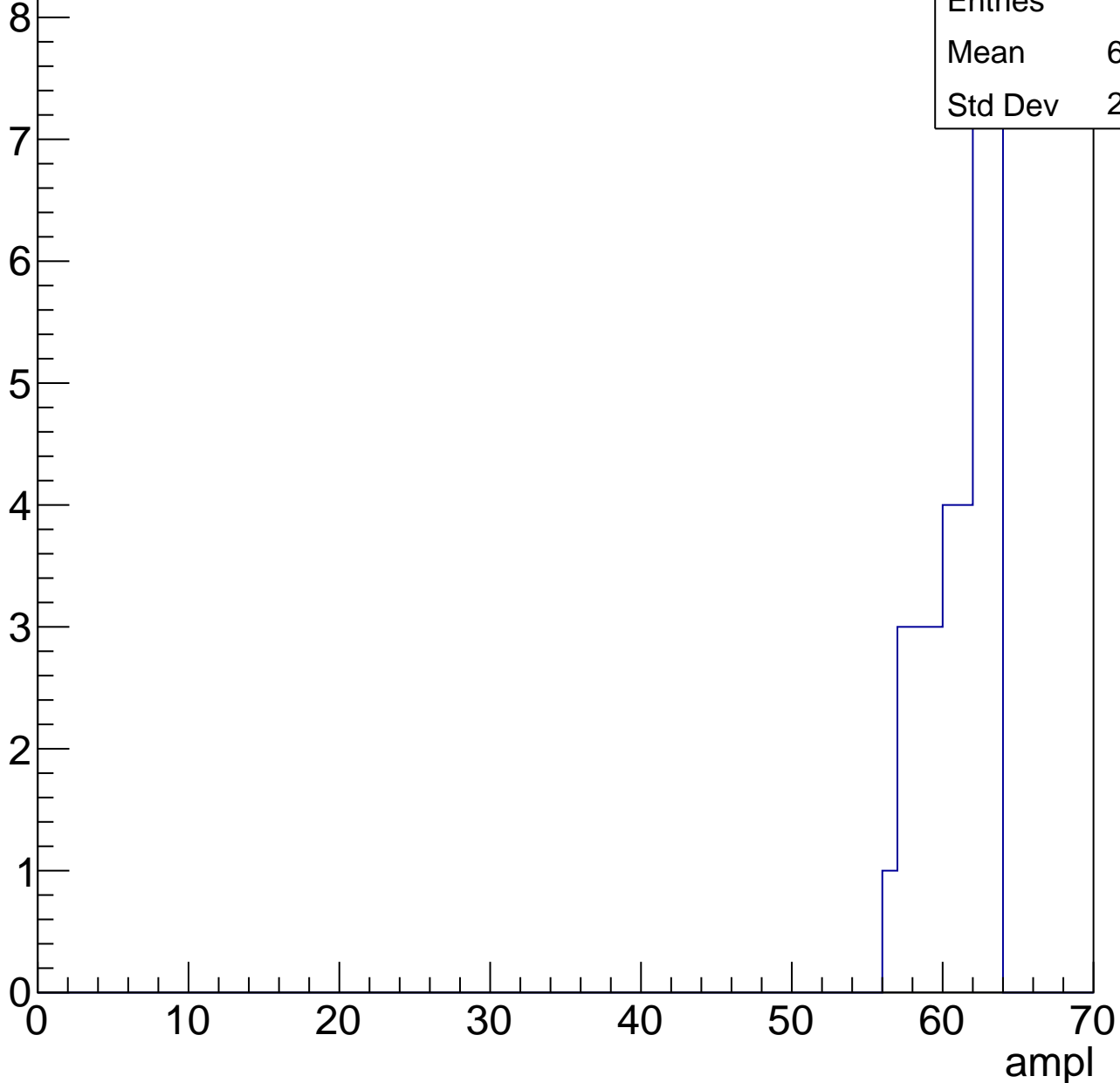


B1L101S, U21-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	60.65
Std Dev	2.113



B1L101S, U21-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch66, adc0

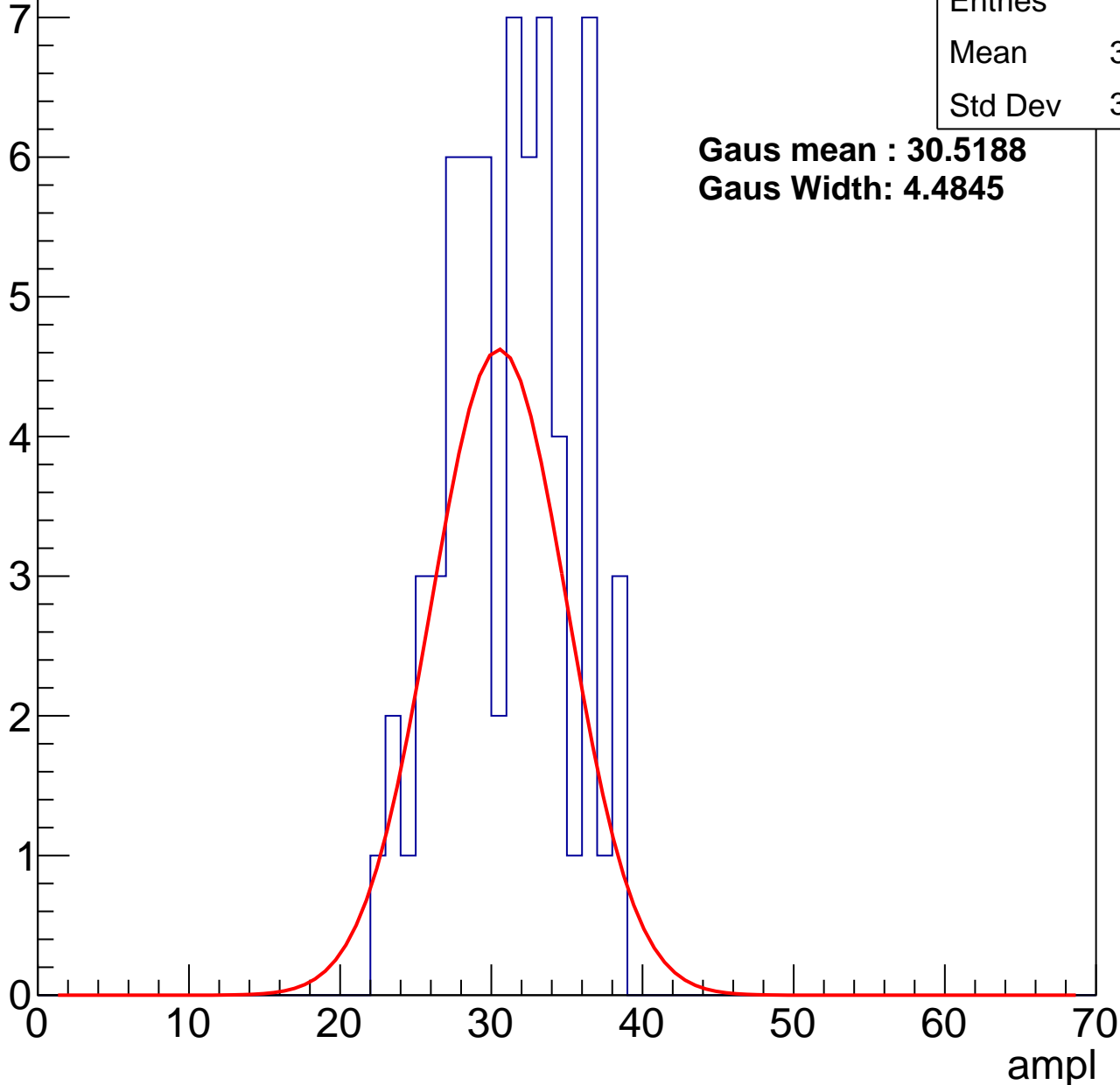
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.65
Std Dev	3.994

Gaus mean : 30.5188

Gaus Width: 4.4845



B1L101S, U21-ch66, adc1

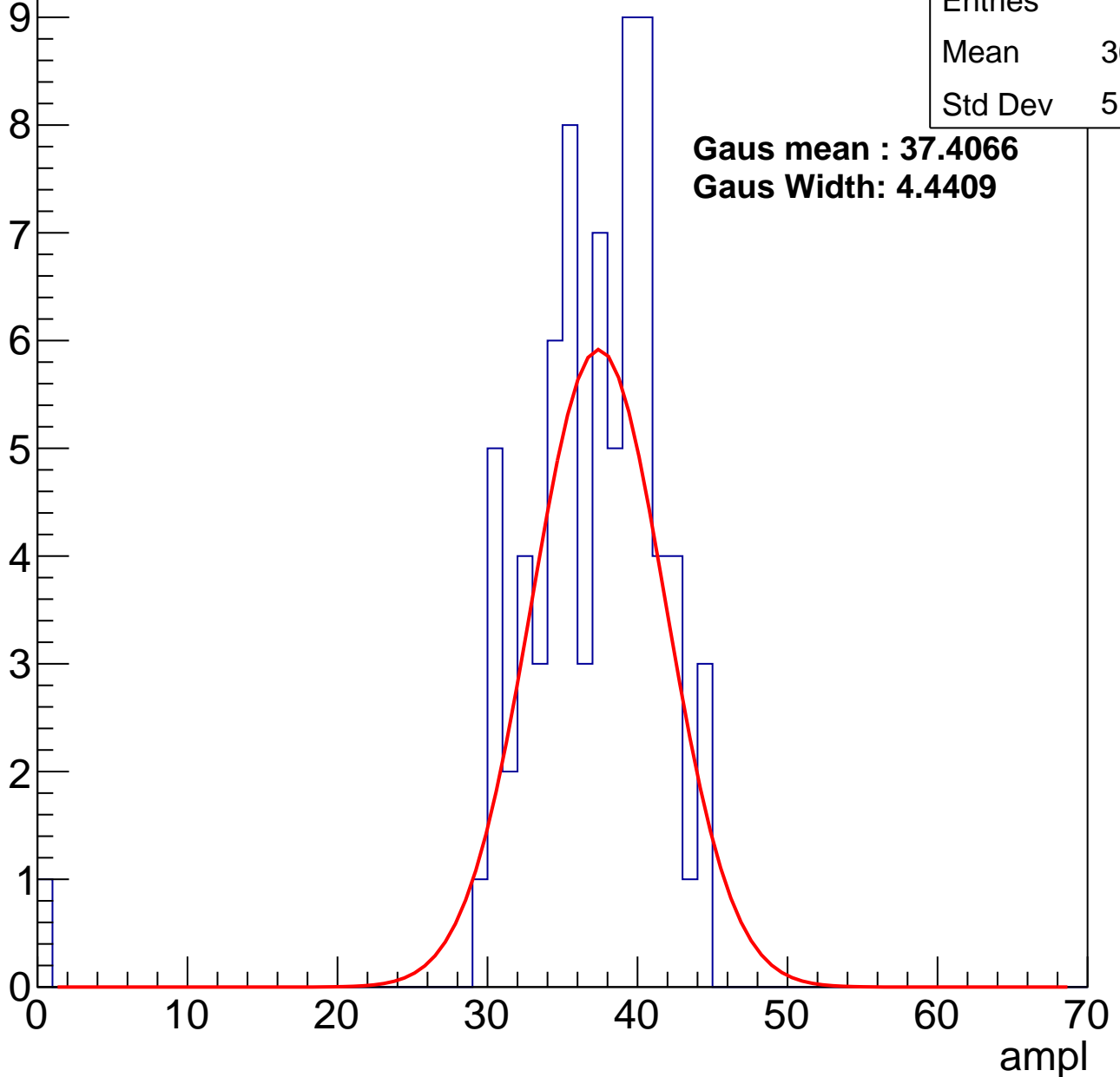
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.36
Std Dev	5.687

Gaus mean : 37.4066

Gaus Width: 4.4409



B1L101S, U21-ch66, adc2

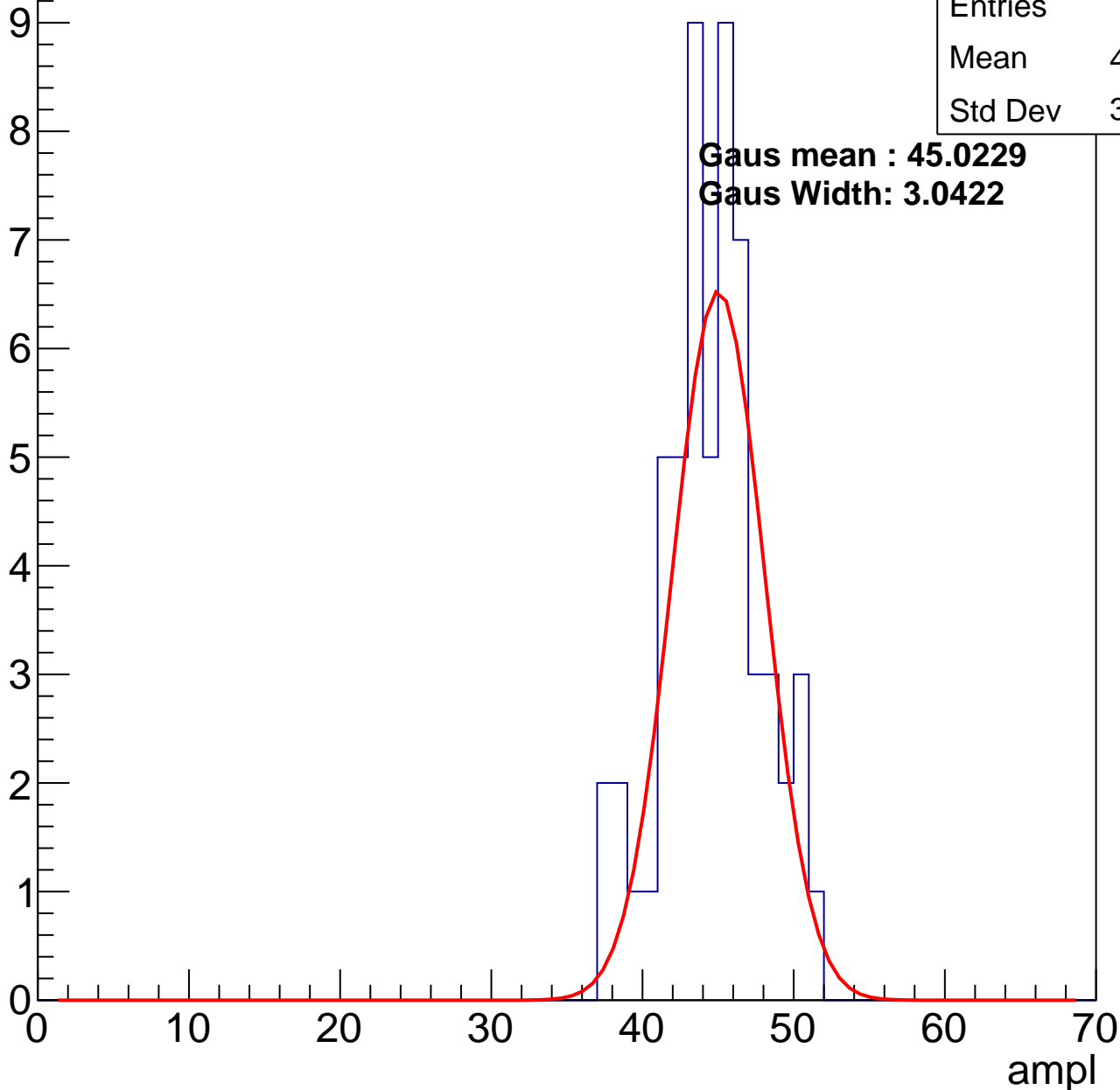
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.17
Std Dev	3.222

Gaus mean : 45.0229

Gaus Width: 3.0422

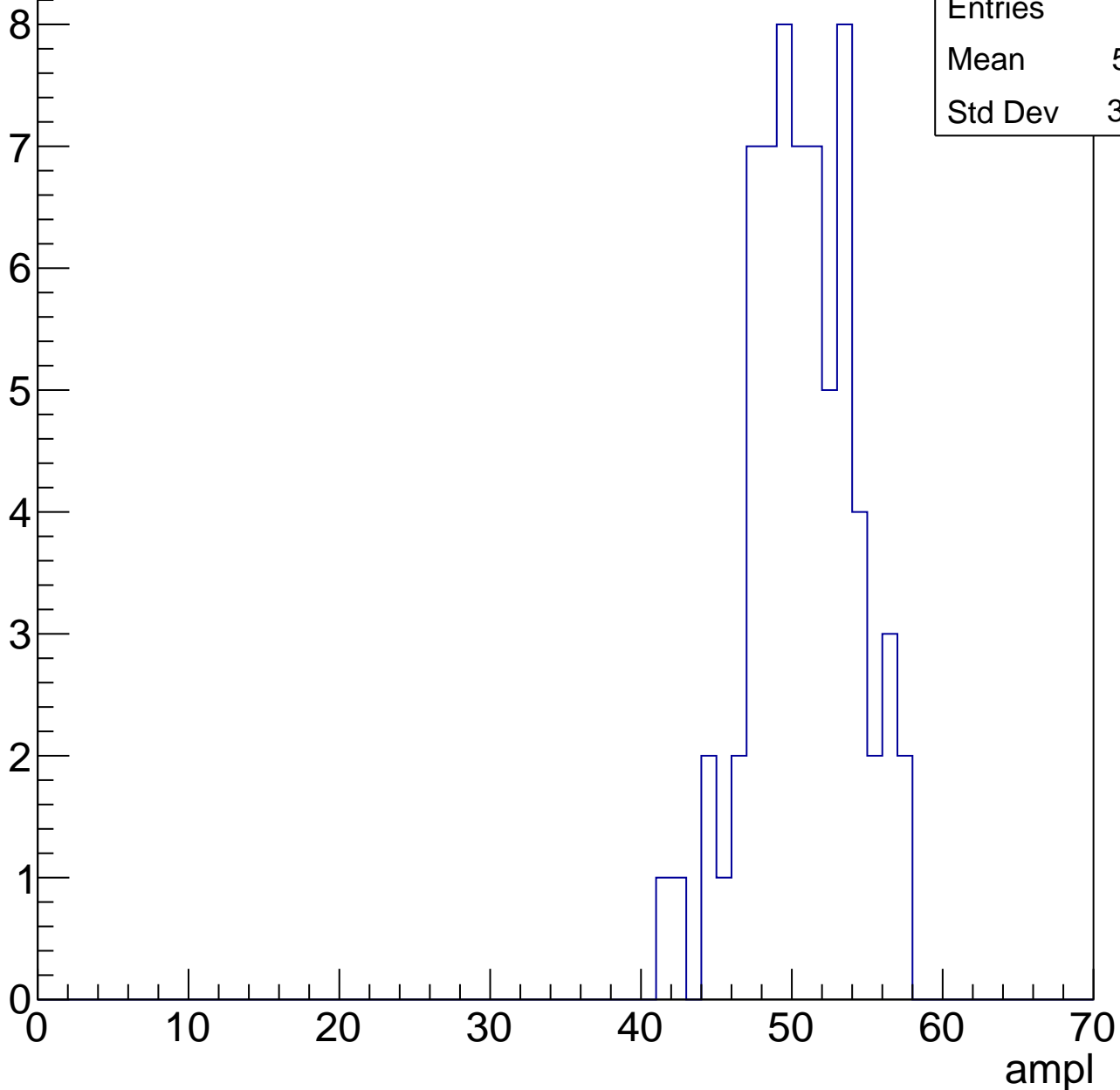


B1L101S, U21-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.21
Std Dev	3.432

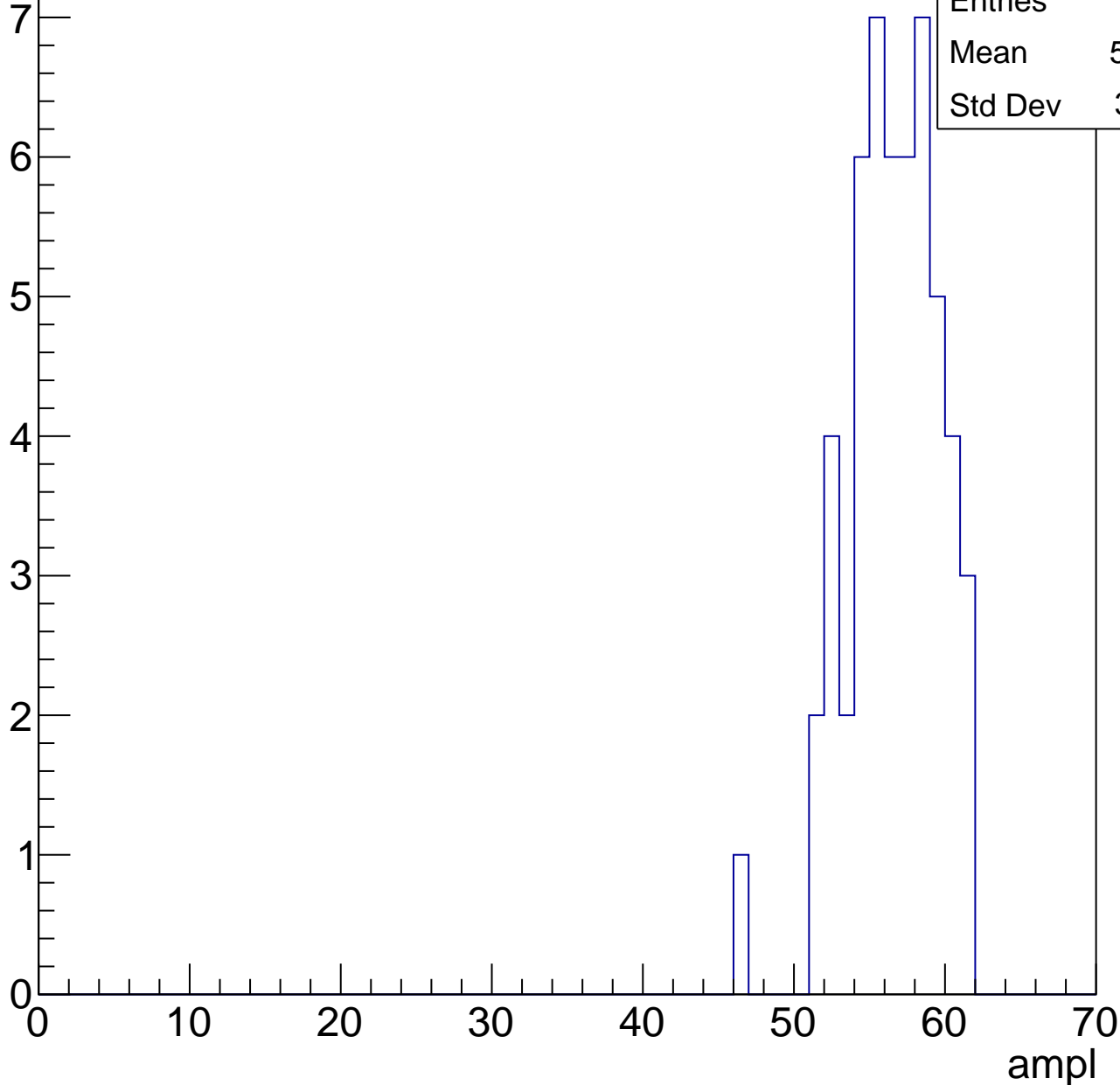


B1L101S, U21-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	56.09
Std Dev	3.011

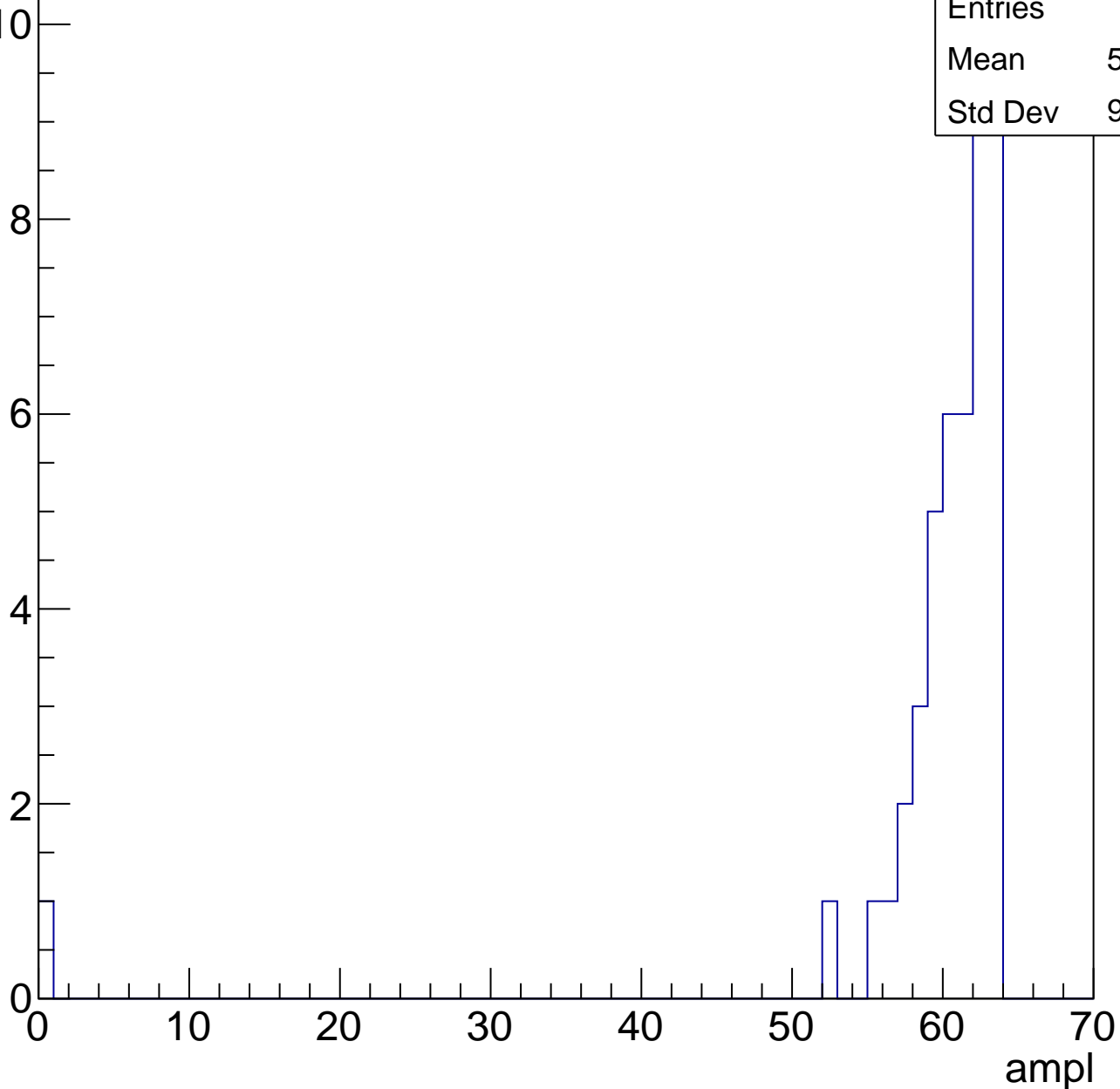


B1L101S, U21-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	59.09
Std Dev	9.225



B1L101S, U21-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U21-ch67, adc0

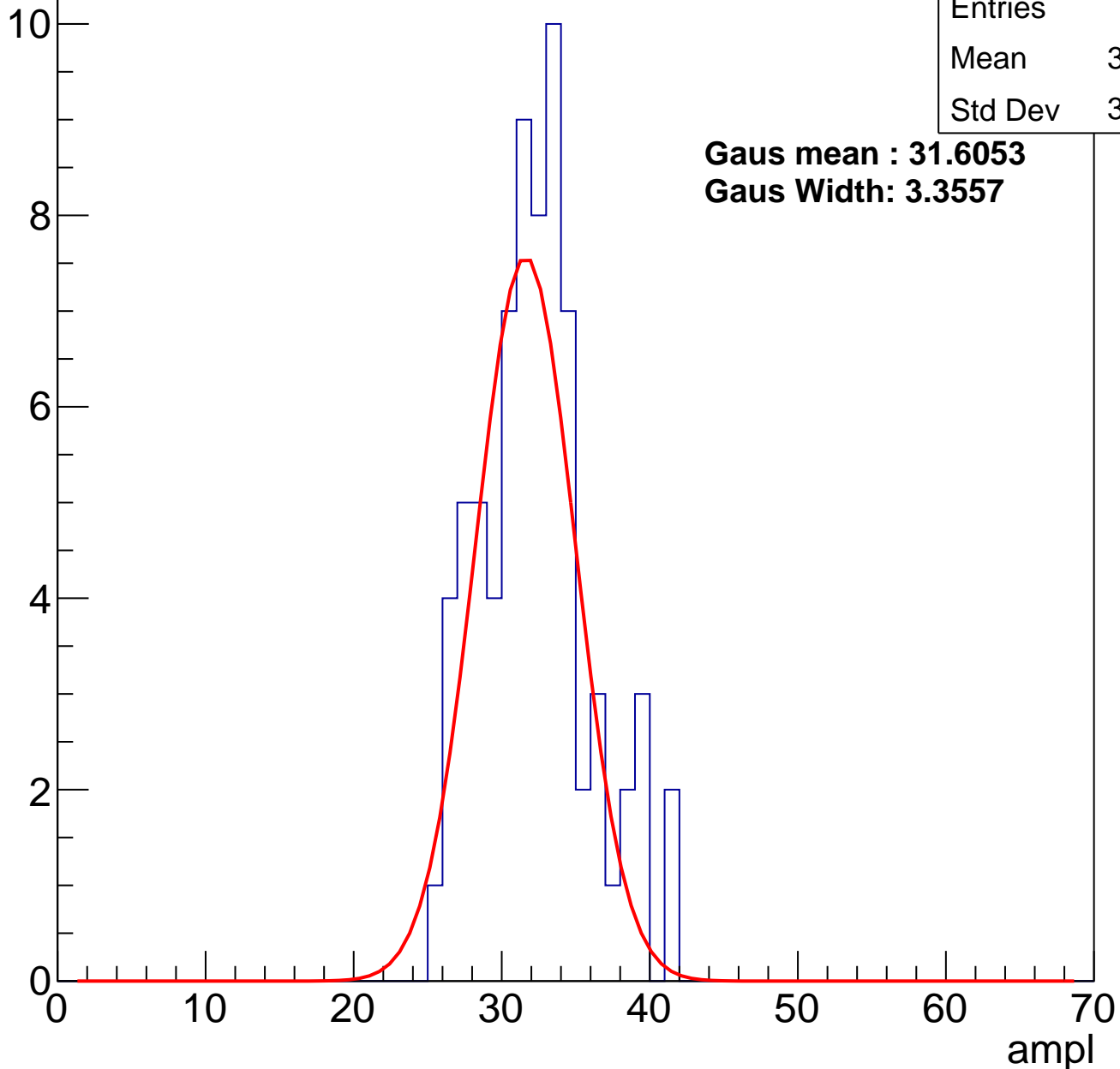
calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	31.82
Std Dev	3.662

Gaus mean : 31.6053

Gaus Width: 3.3557

Entry



B1L101S, U21-ch67, adc1

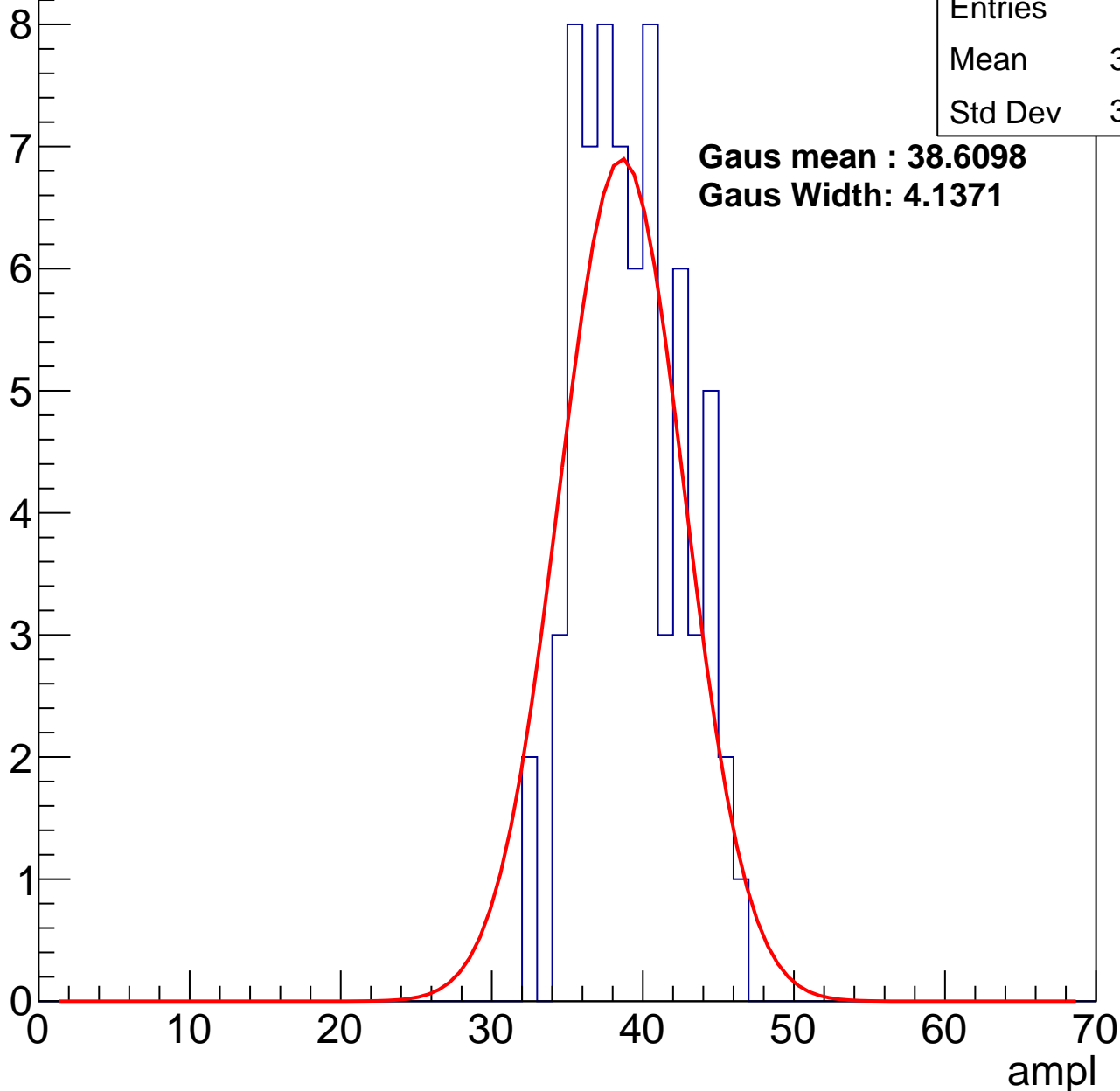
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	38.75
Std Dev	3.334

Gaus mean : 38.6098

Gaus Width: 4.1371



B1L101S, U21-ch67, adc2

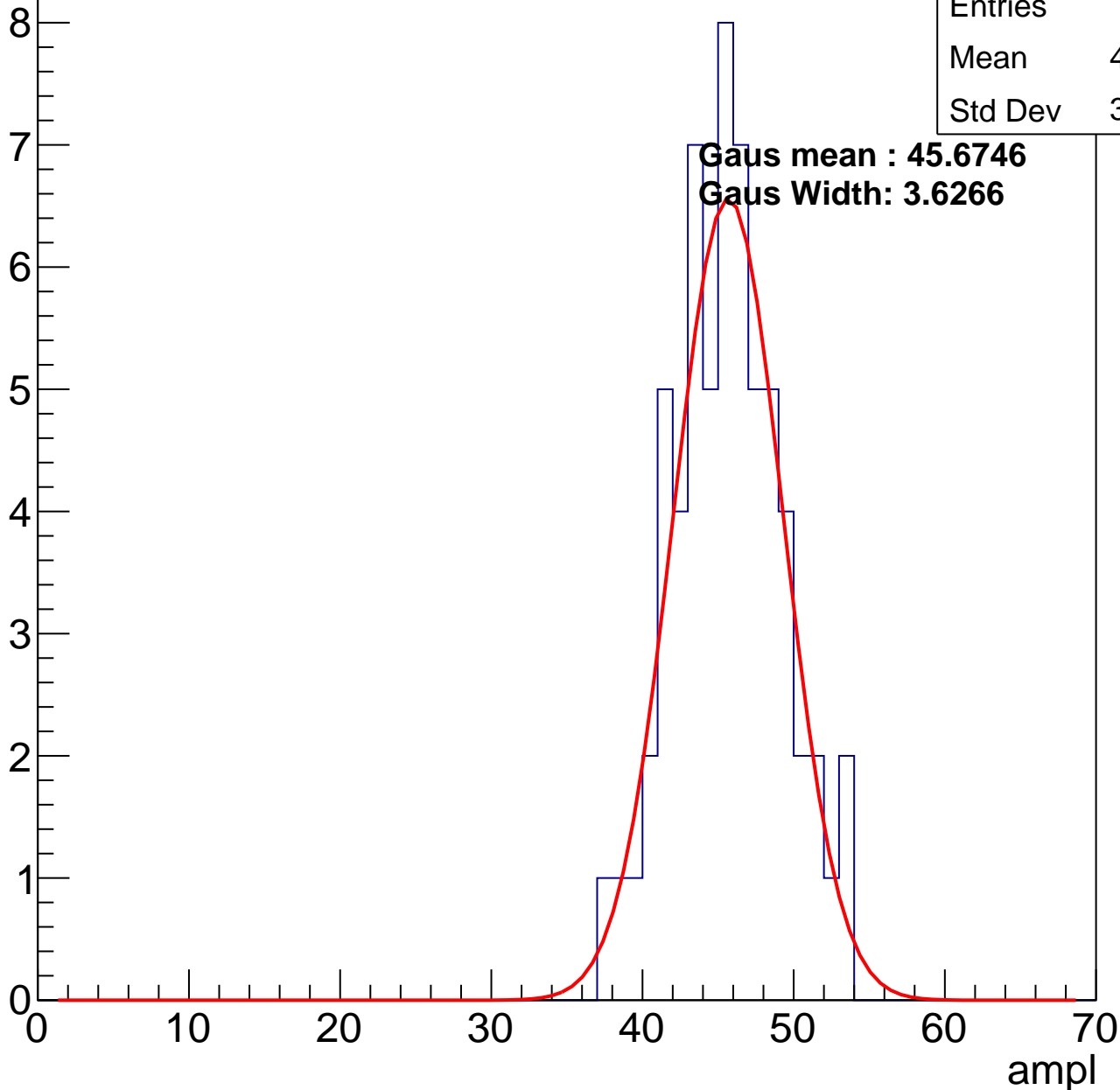
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	45.18
Std Dev	3.549

Gaus mean : 45.6746

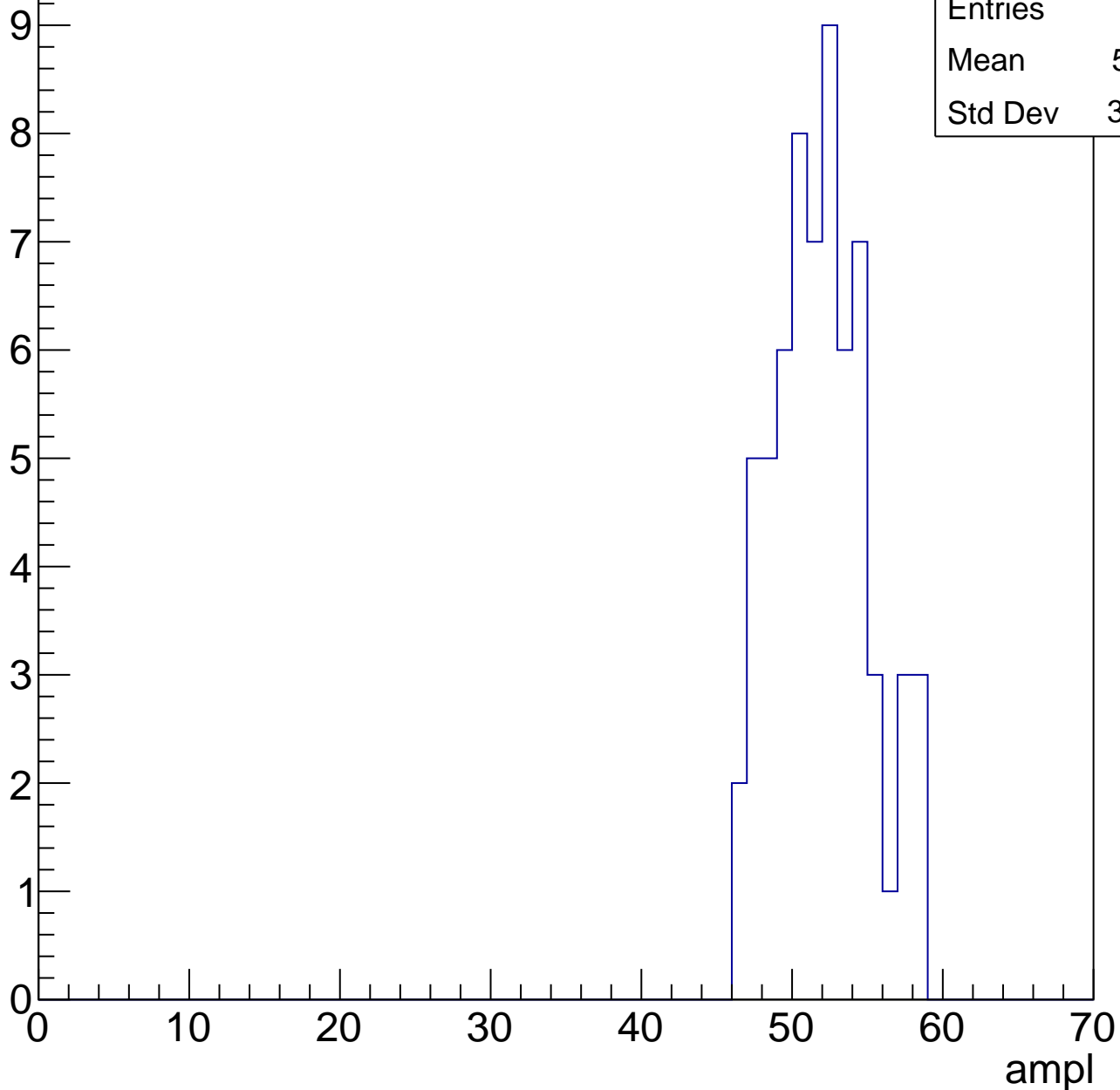
Gaus Width: 3.6266



B1L101S, U21-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

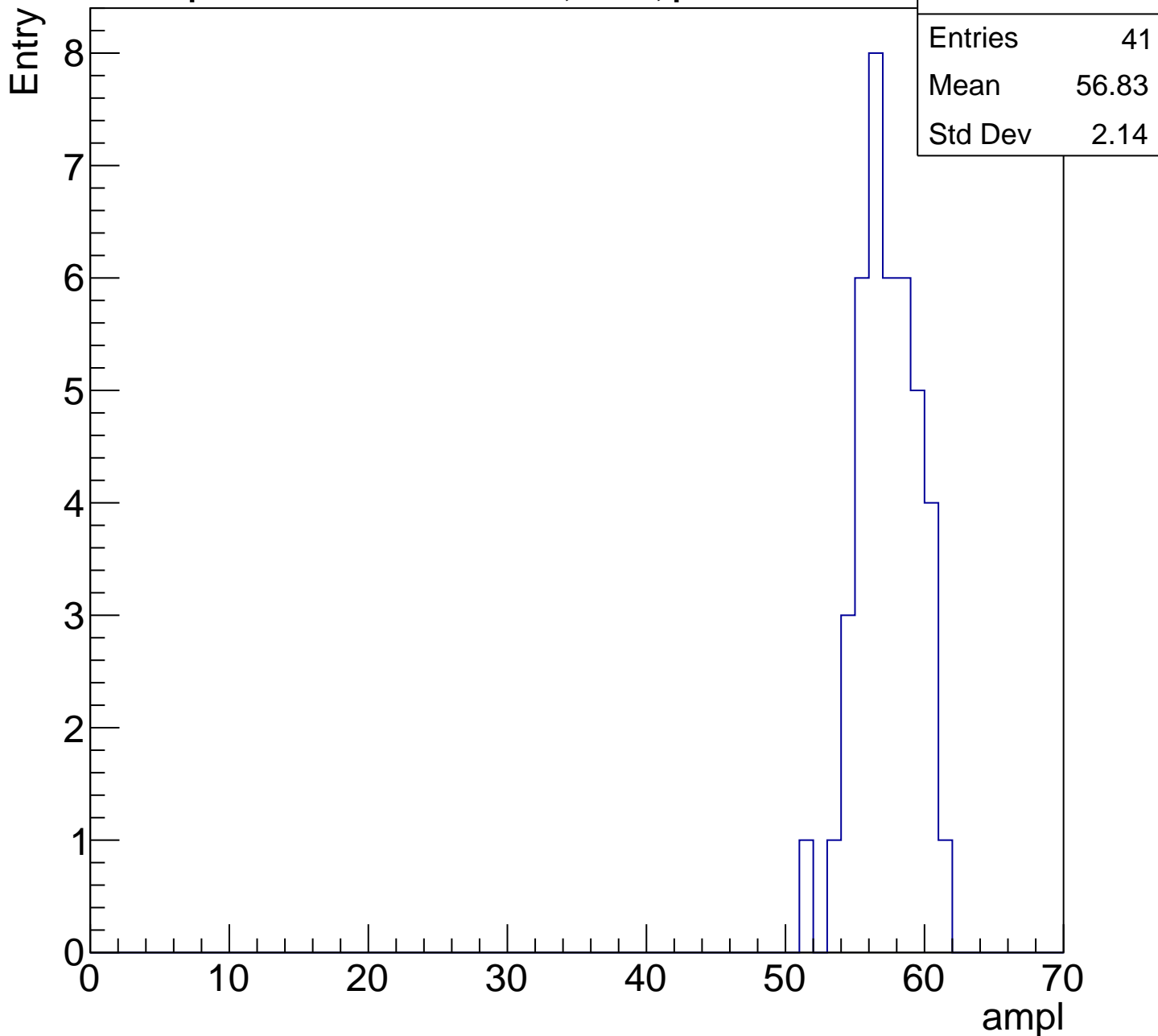
Entry



Entries	65
Mean	51.51
Std Dev	3.074

B1L101S, U21-ch67, adc4

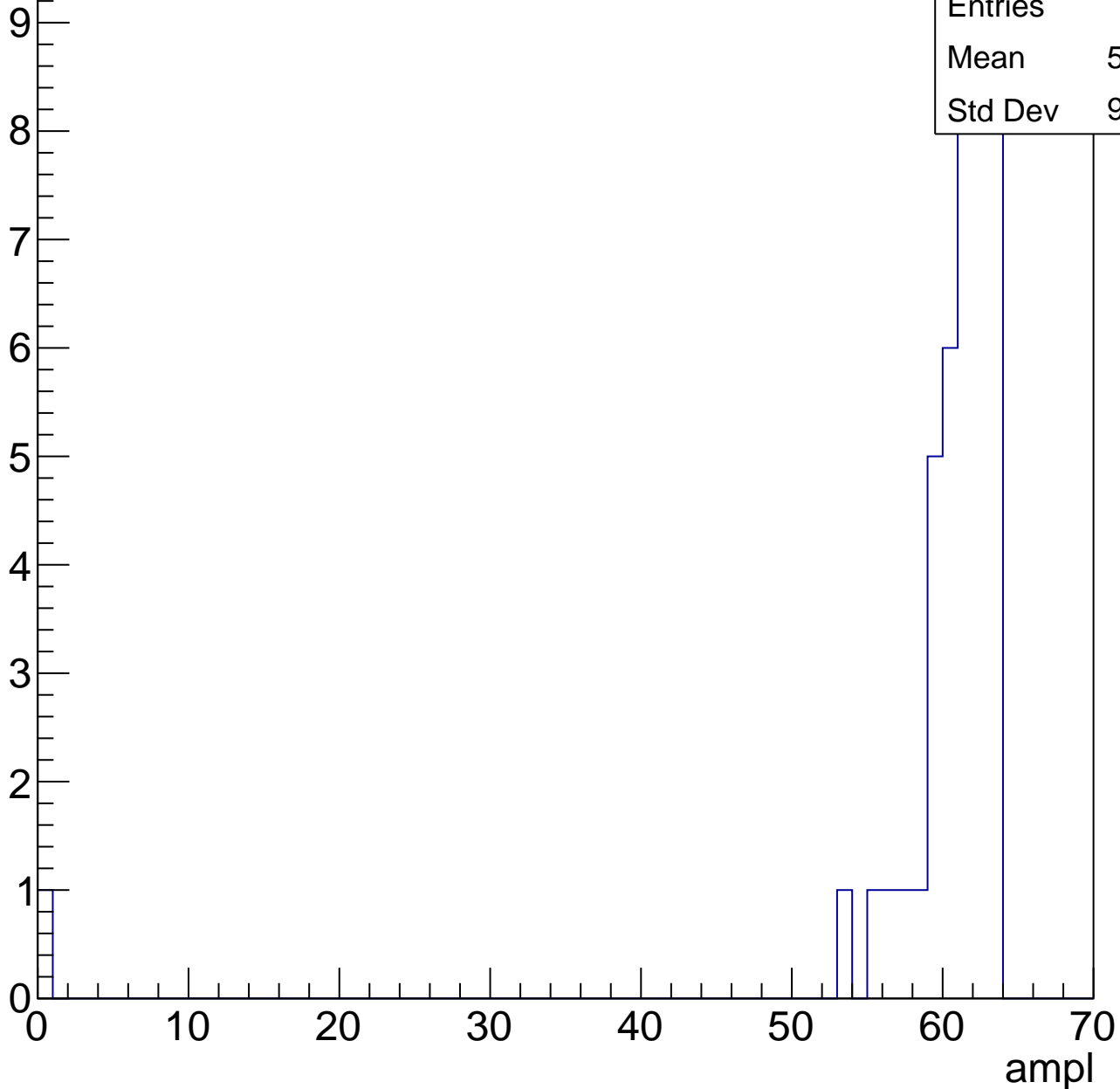
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch68, adc0

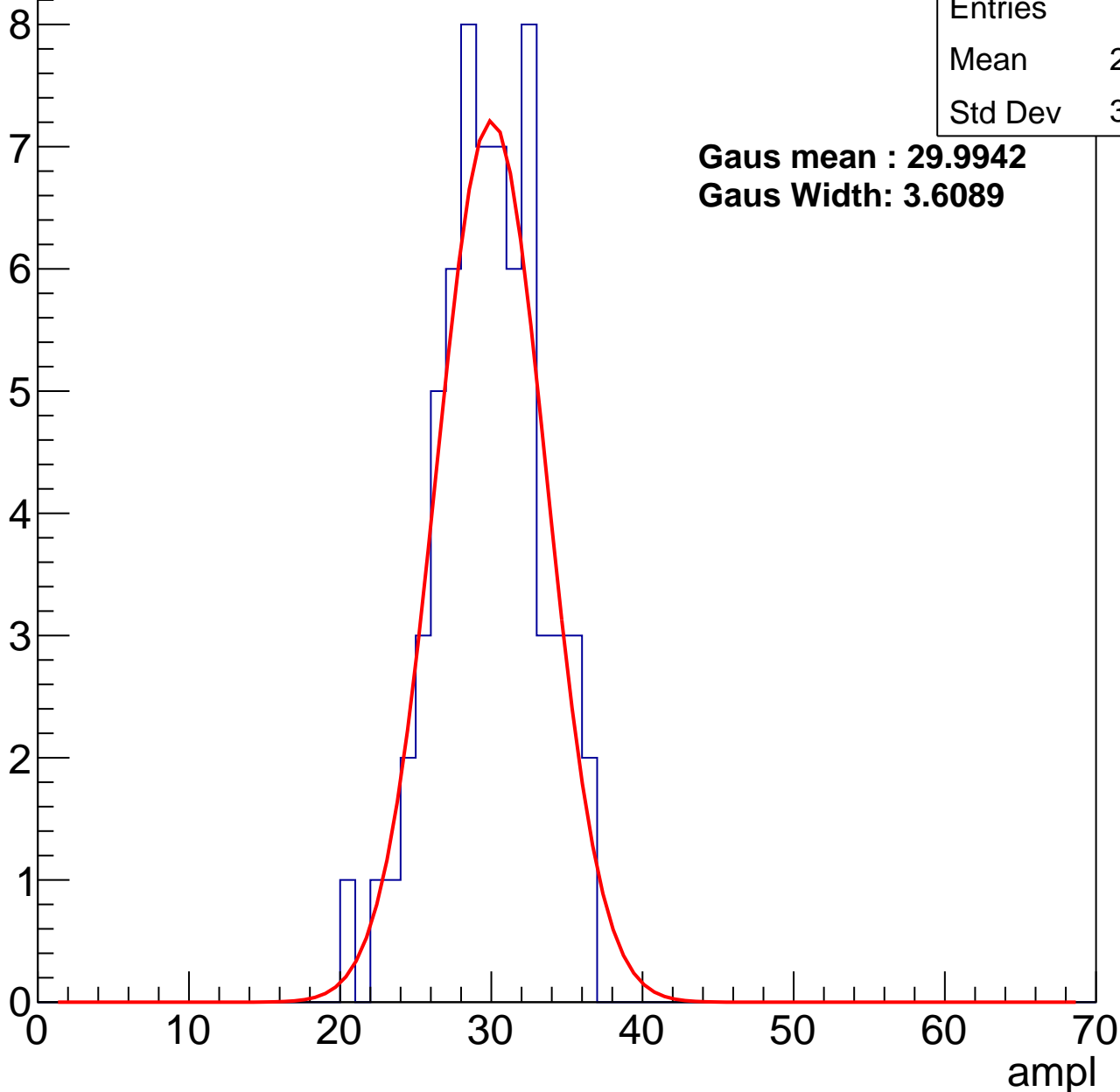
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.35
Std Dev	3.405

Gaus mean : 29.9942

Gaus Width: 3.6089



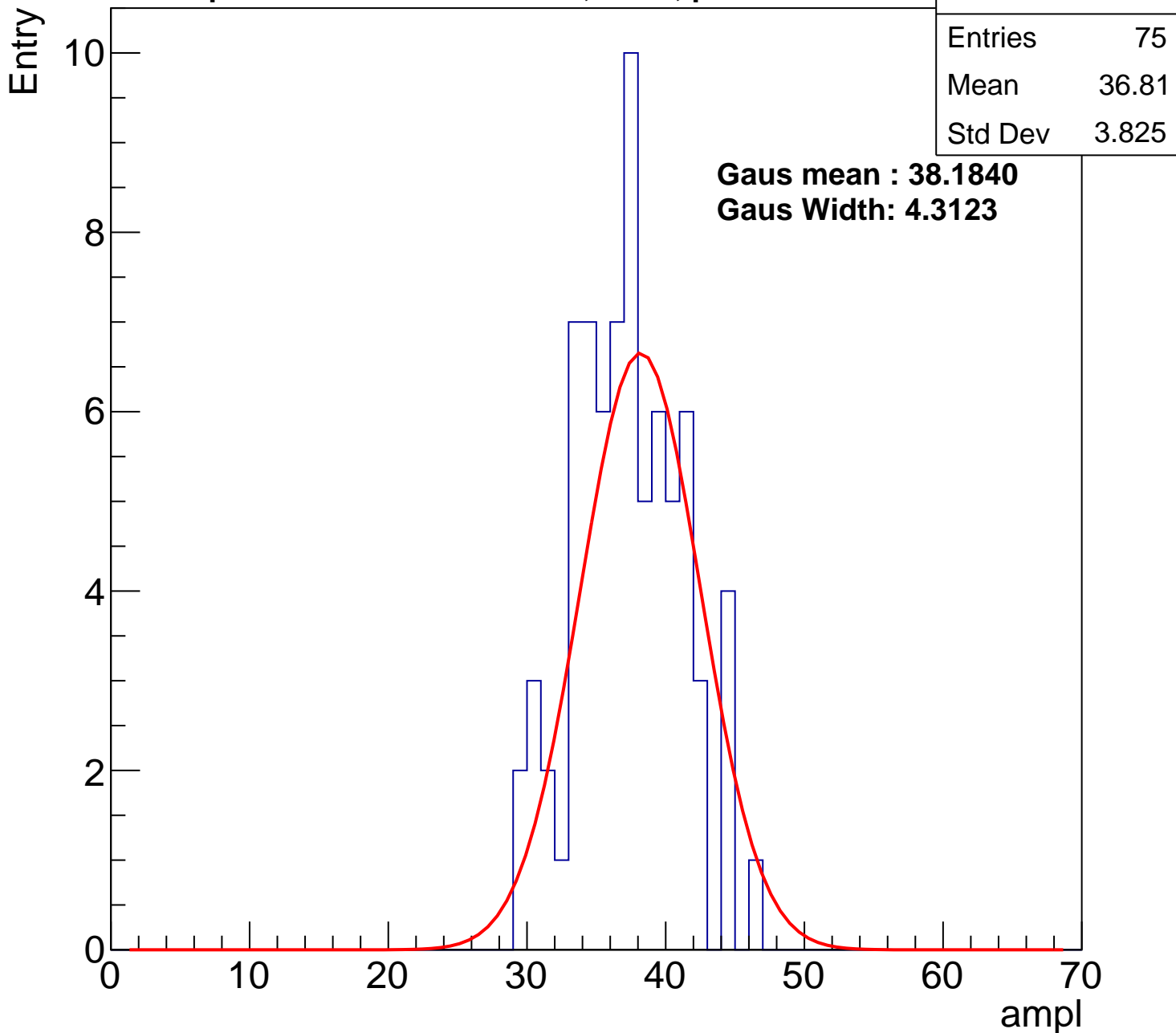
B1L101S, U21-ch68, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	36.81
Std Dev	3.825

Gaus mean : 38.1840

Gaus Width: 4.3123



B1L101S, U21-ch68, adc2

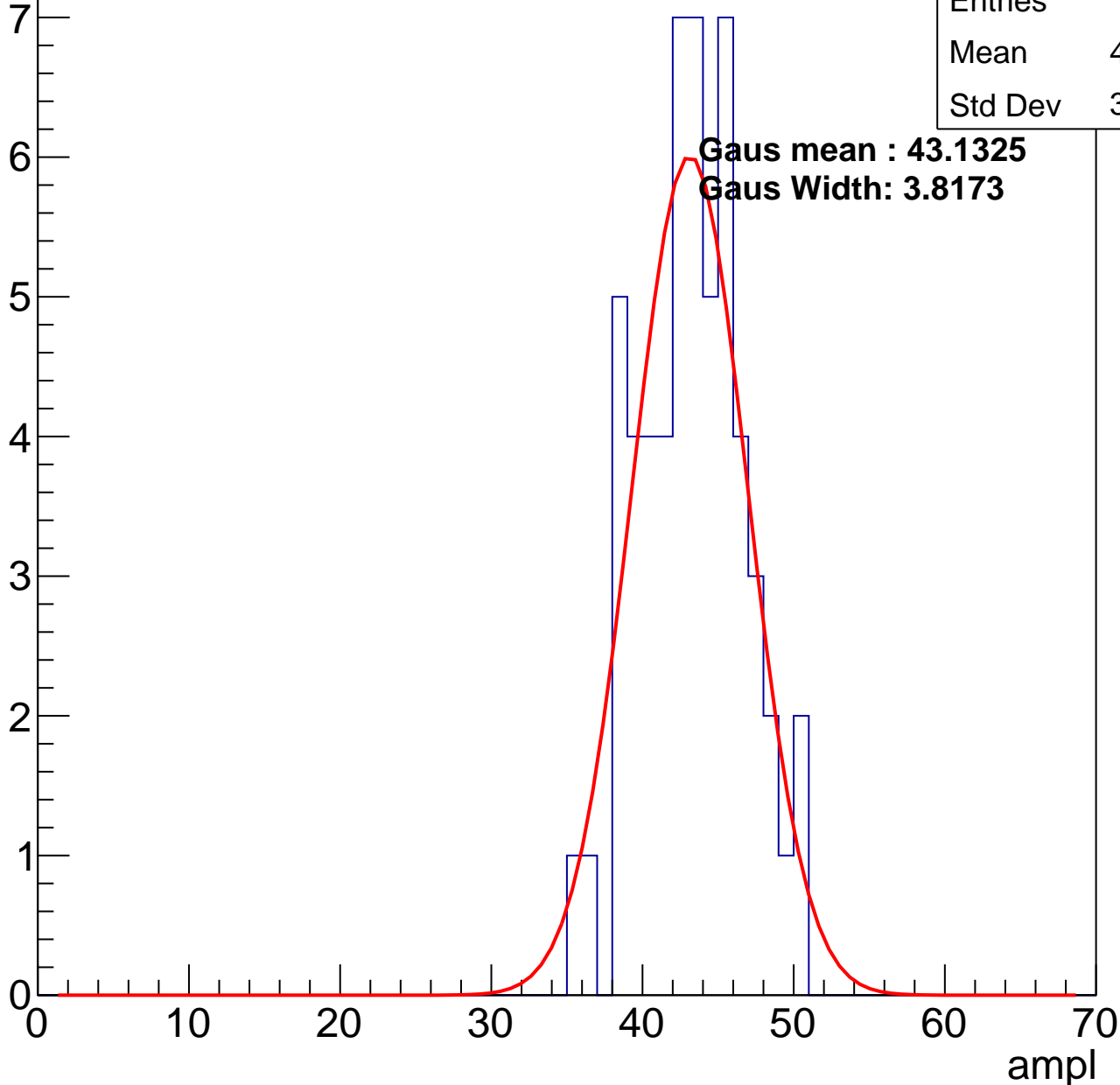
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.82
Std Dev	3.403

Gaus mean : 43.1325

Gaus Width: 3.8173

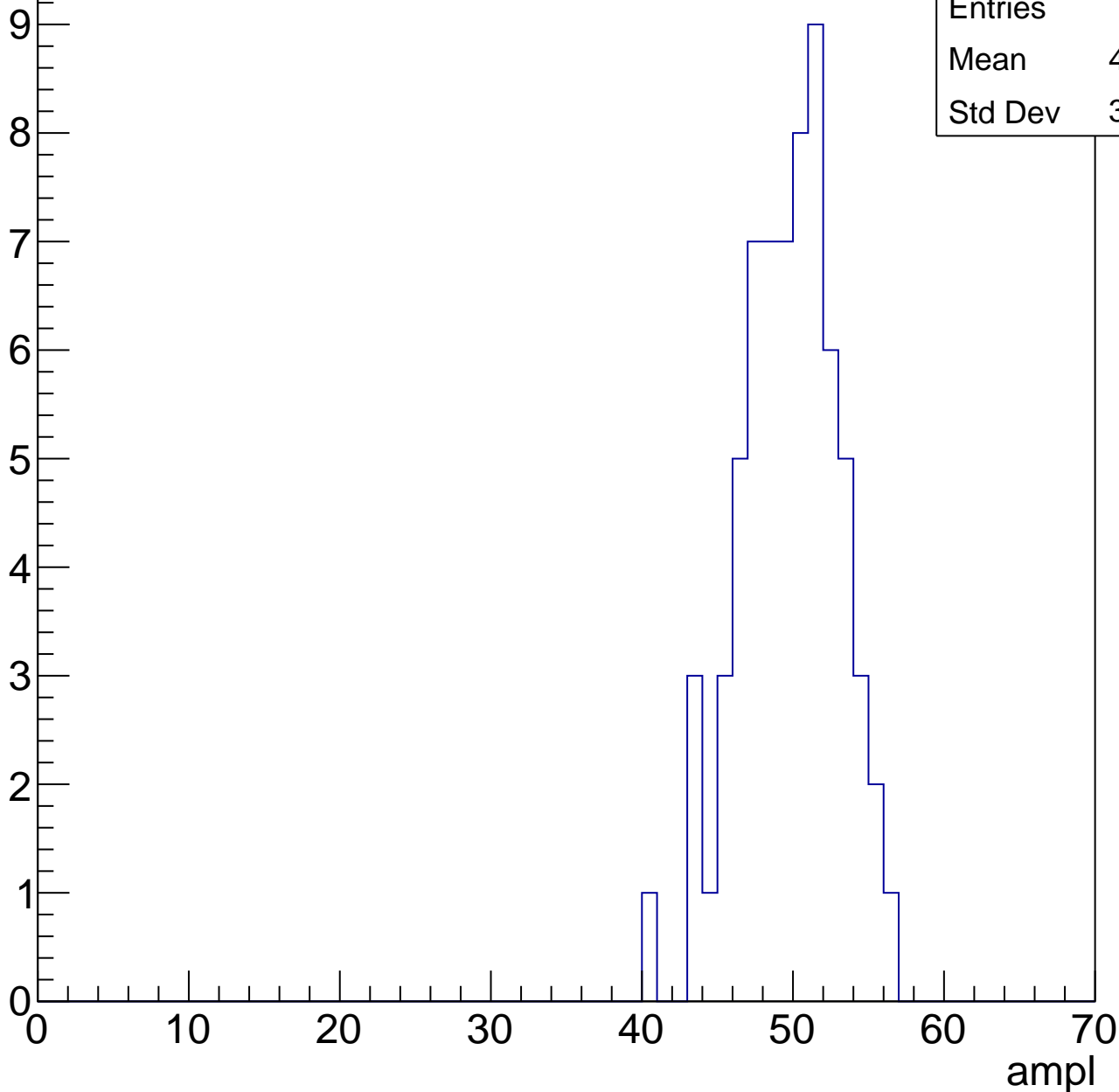


B1L101S, U21-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

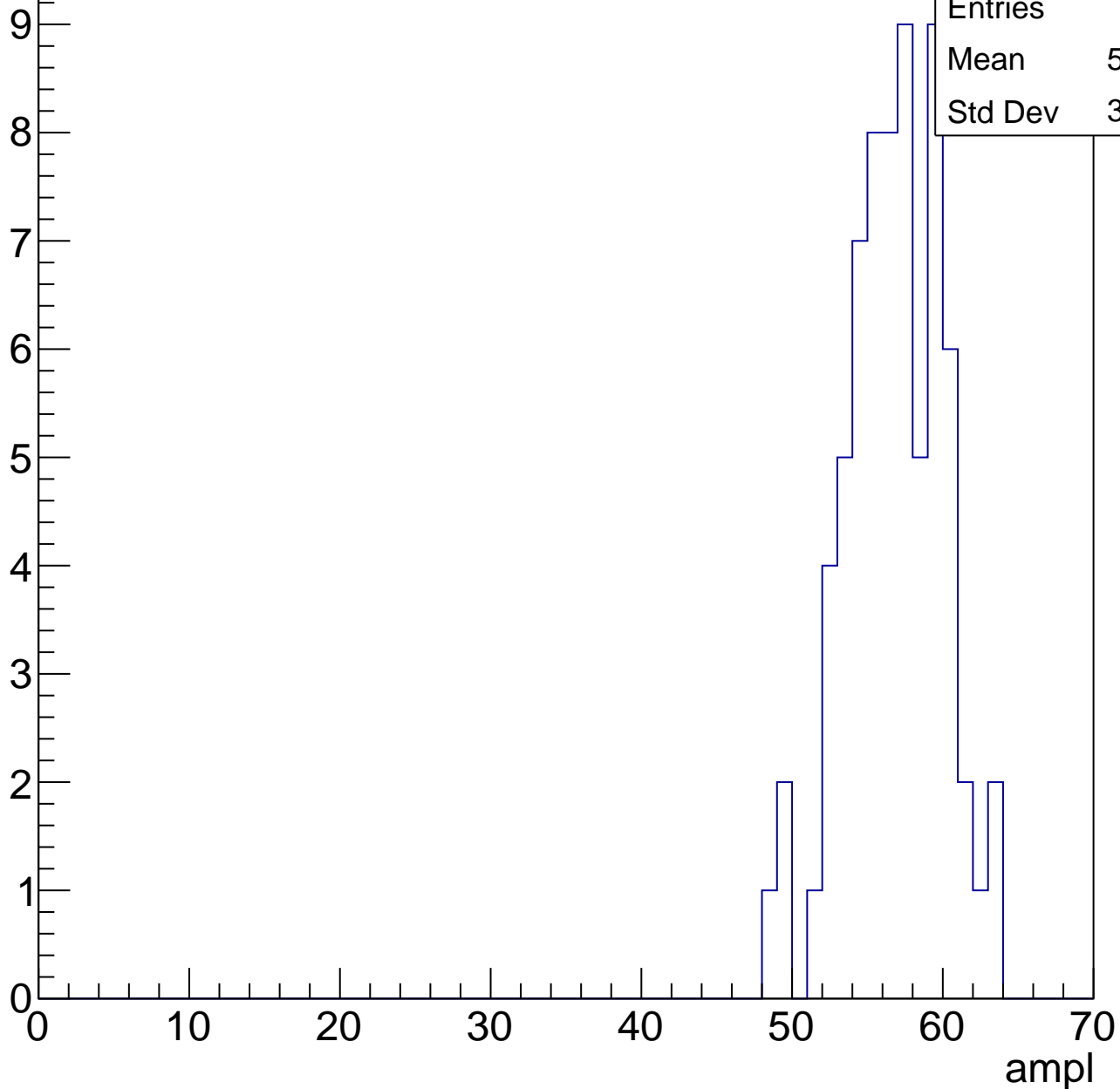
Entries	68
Mean	49.26
Std Dev	3.234



B1L101S, U21-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

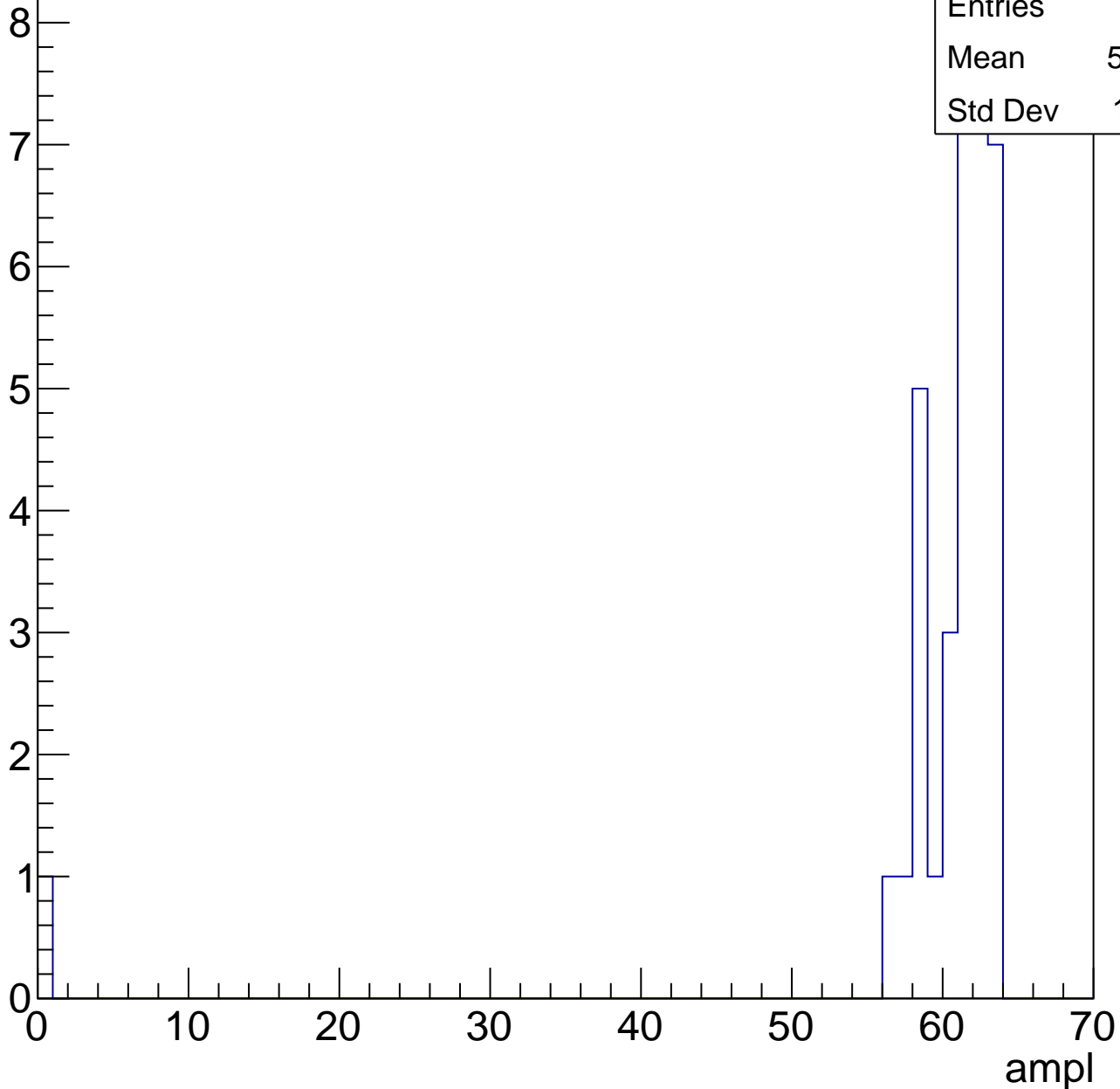


B1L101S, U21-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	59.06
Std Dev	10.31



B1L101S, U21-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch69, adc0

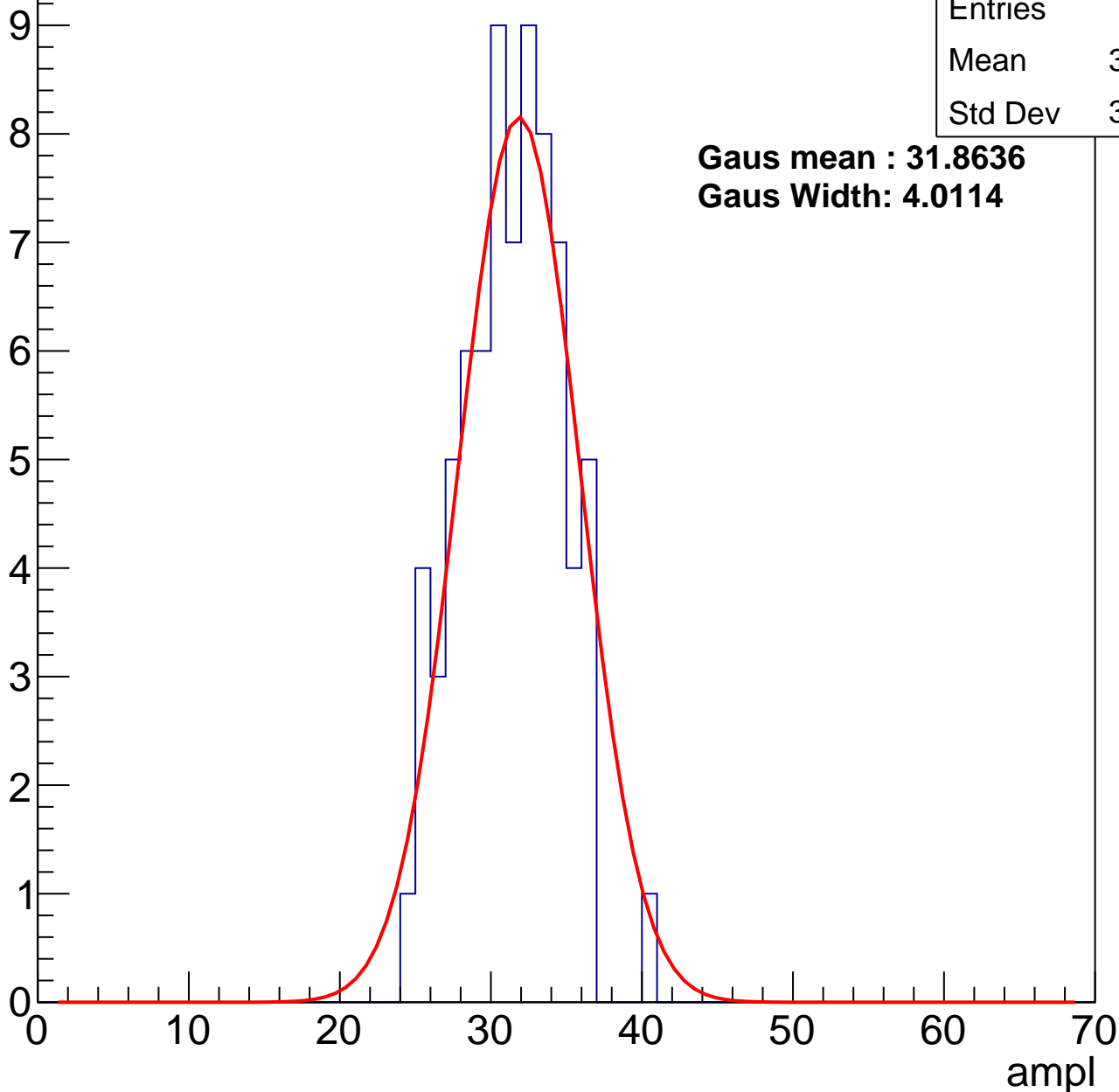
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.88
Std Dev	3.282

Gaus mean : 31.8636

Gaus Width: 4.0114



B1L101S, U21-ch69, adc1

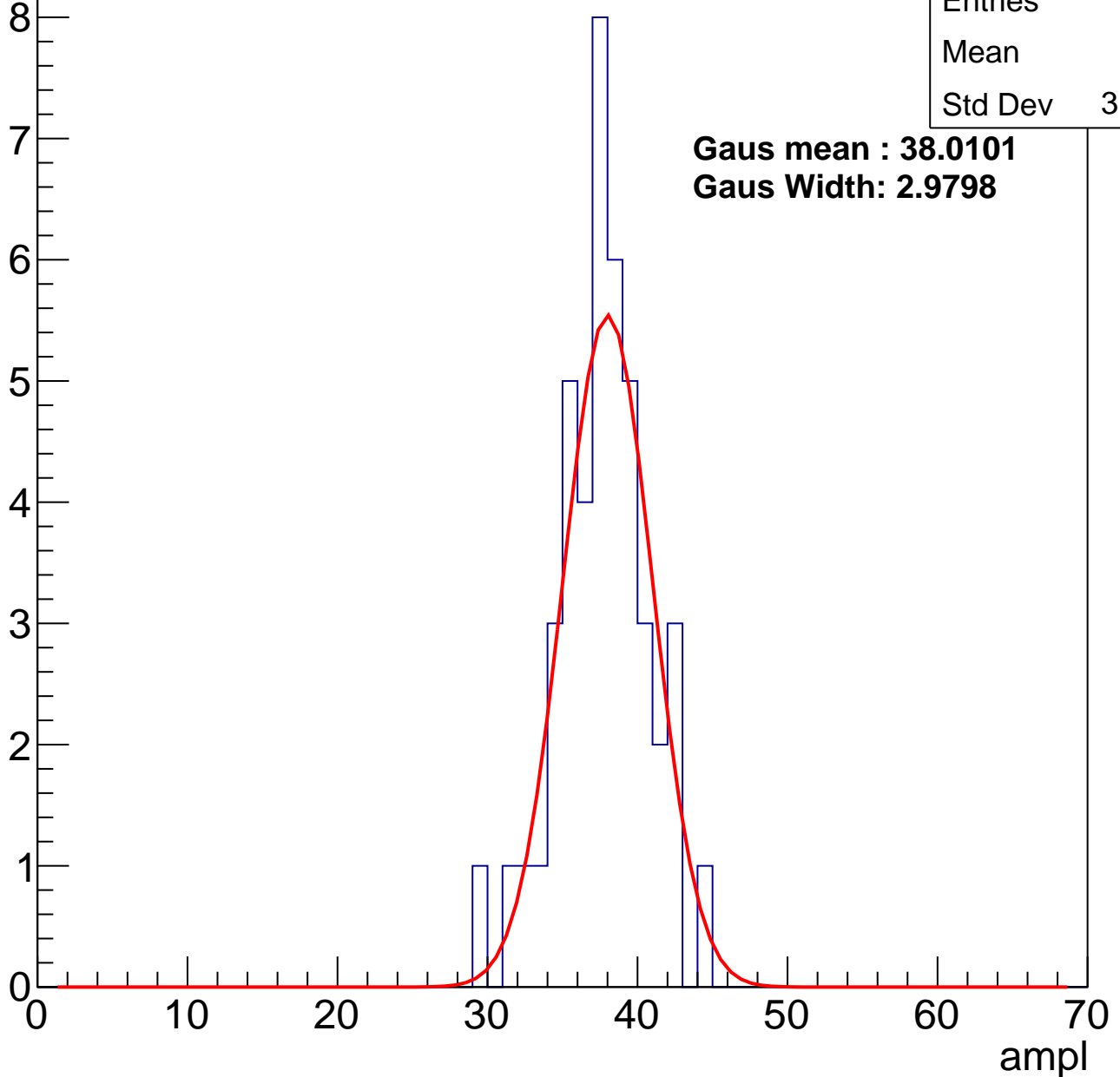
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	37.2
Std Dev	3.012

Gaus mean : 38.0101

Gaus Width: 2.9798



B1L101S, U21-ch69, adc2

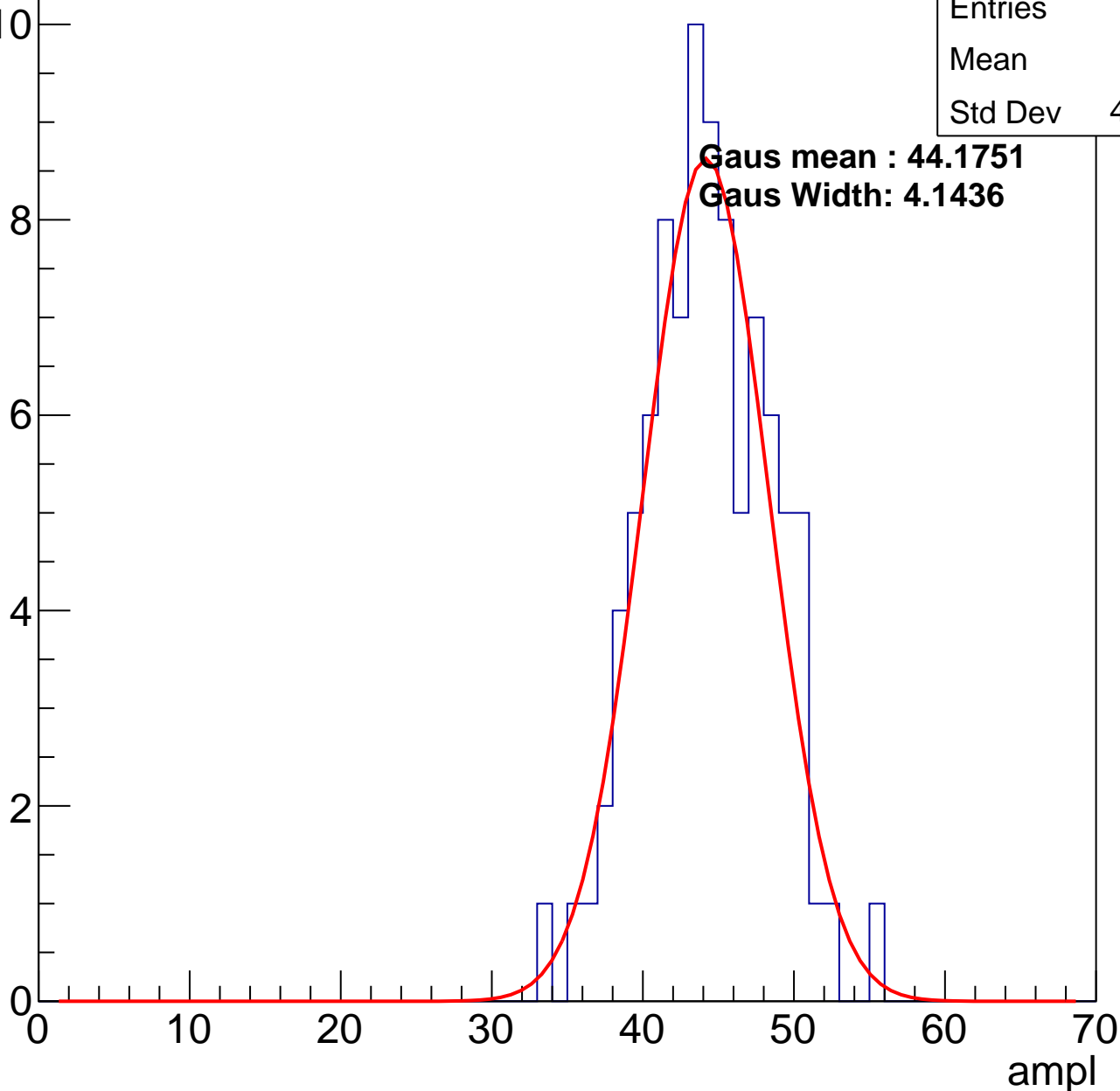
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	93
Mean	43.8
Std Dev	4.097

Gaus mean : 44.1751

Gaus Width: 4.1436

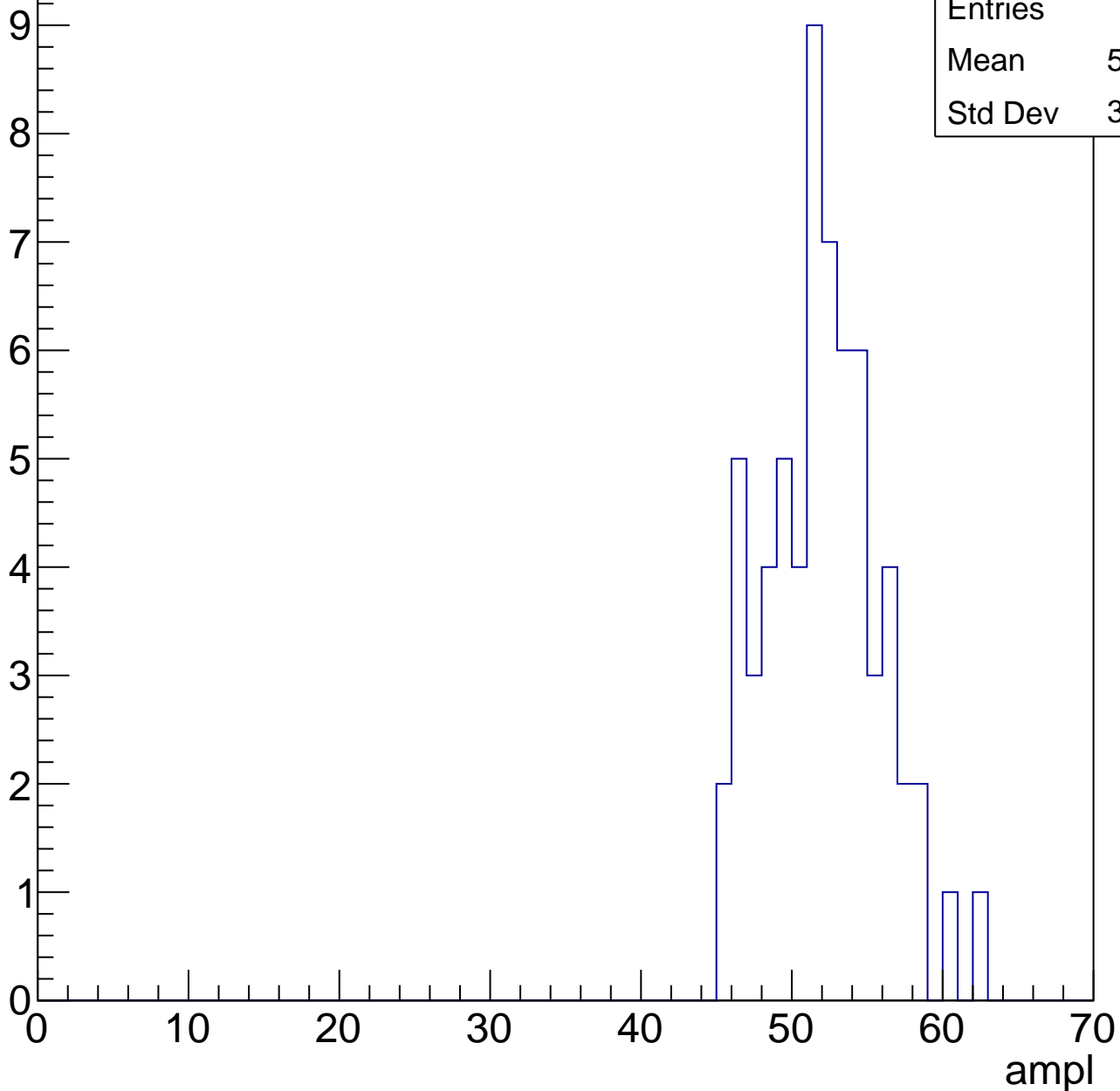


B1L101S, U21-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	51.62
Std Dev	3.719

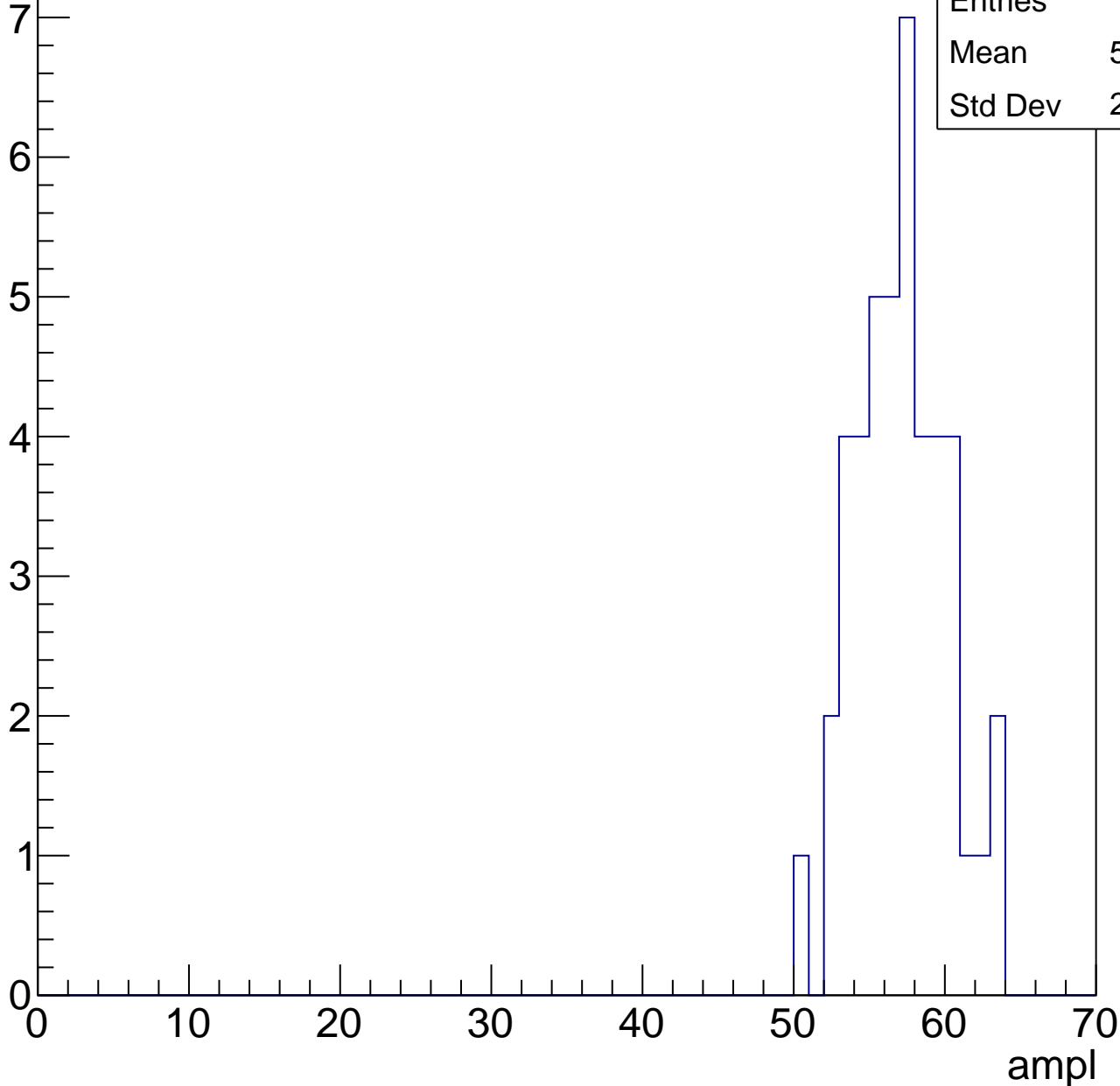


B1L101S, U21-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

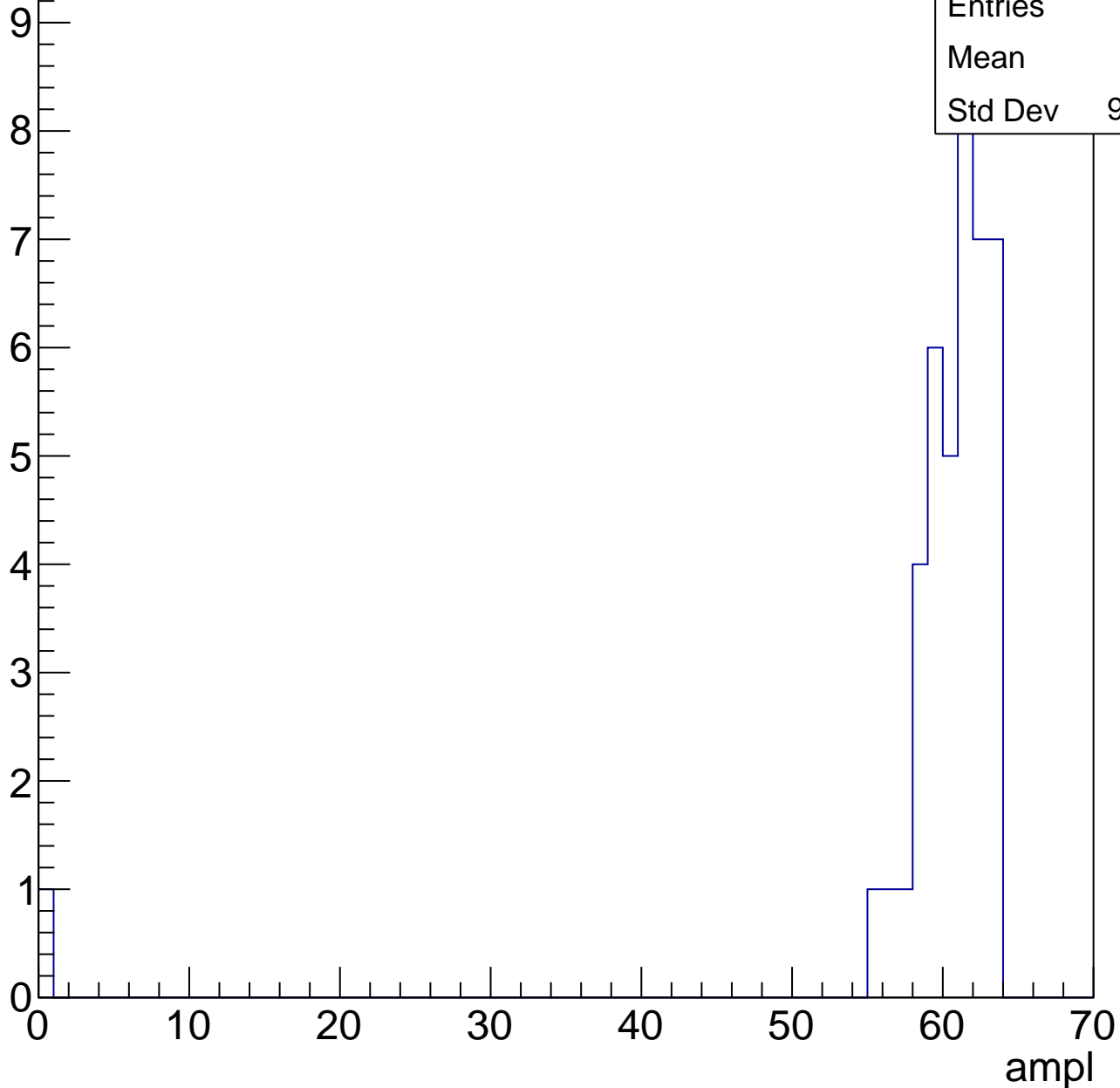
Entries	44
Mean	56.66
Std Dev	2.969



B1L101S, U21-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L101S, U21-ch70, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	30.56
Std Dev	3.477

Gaus mean : 30.9371

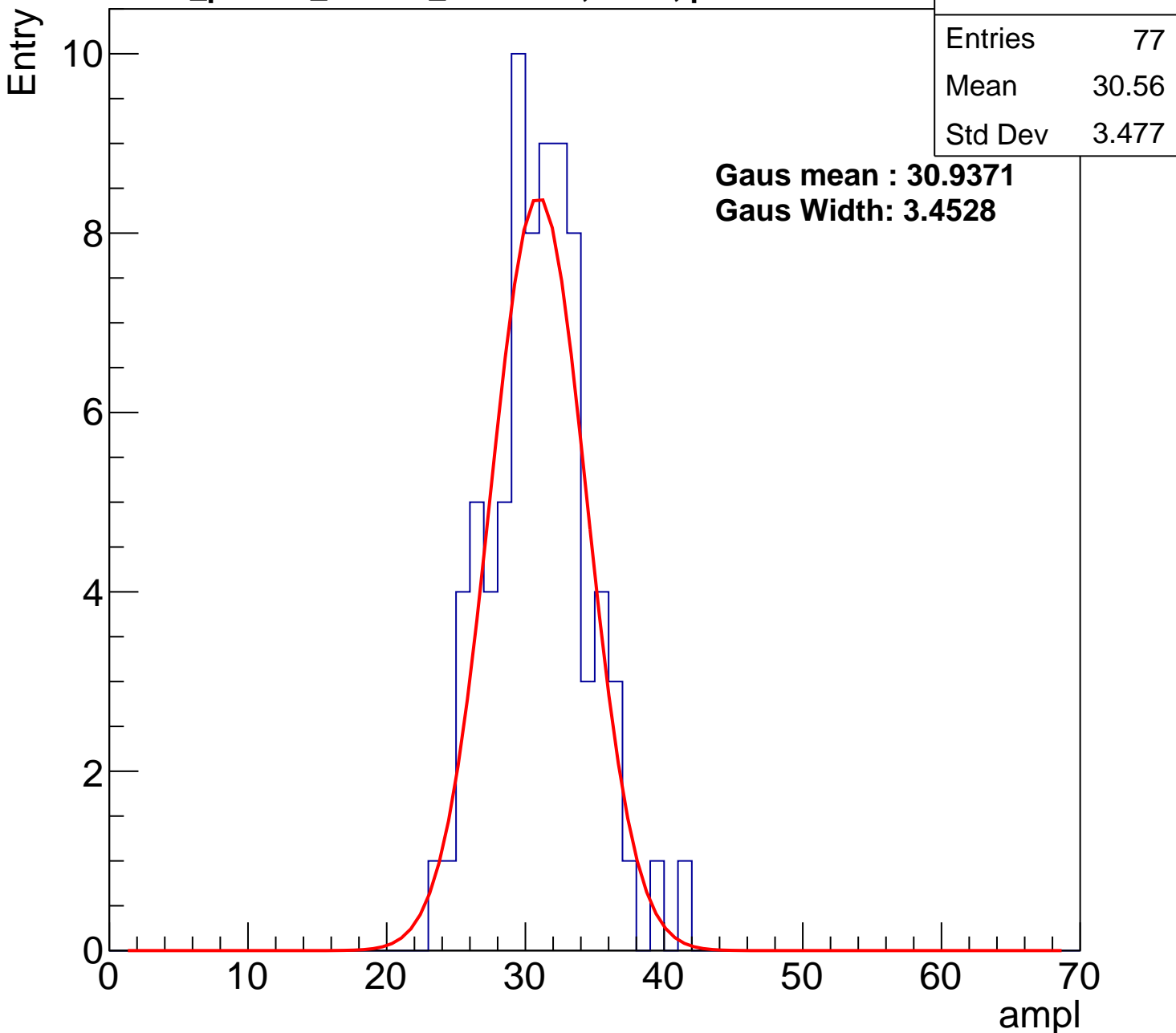
Gaus Width: 3.4528

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



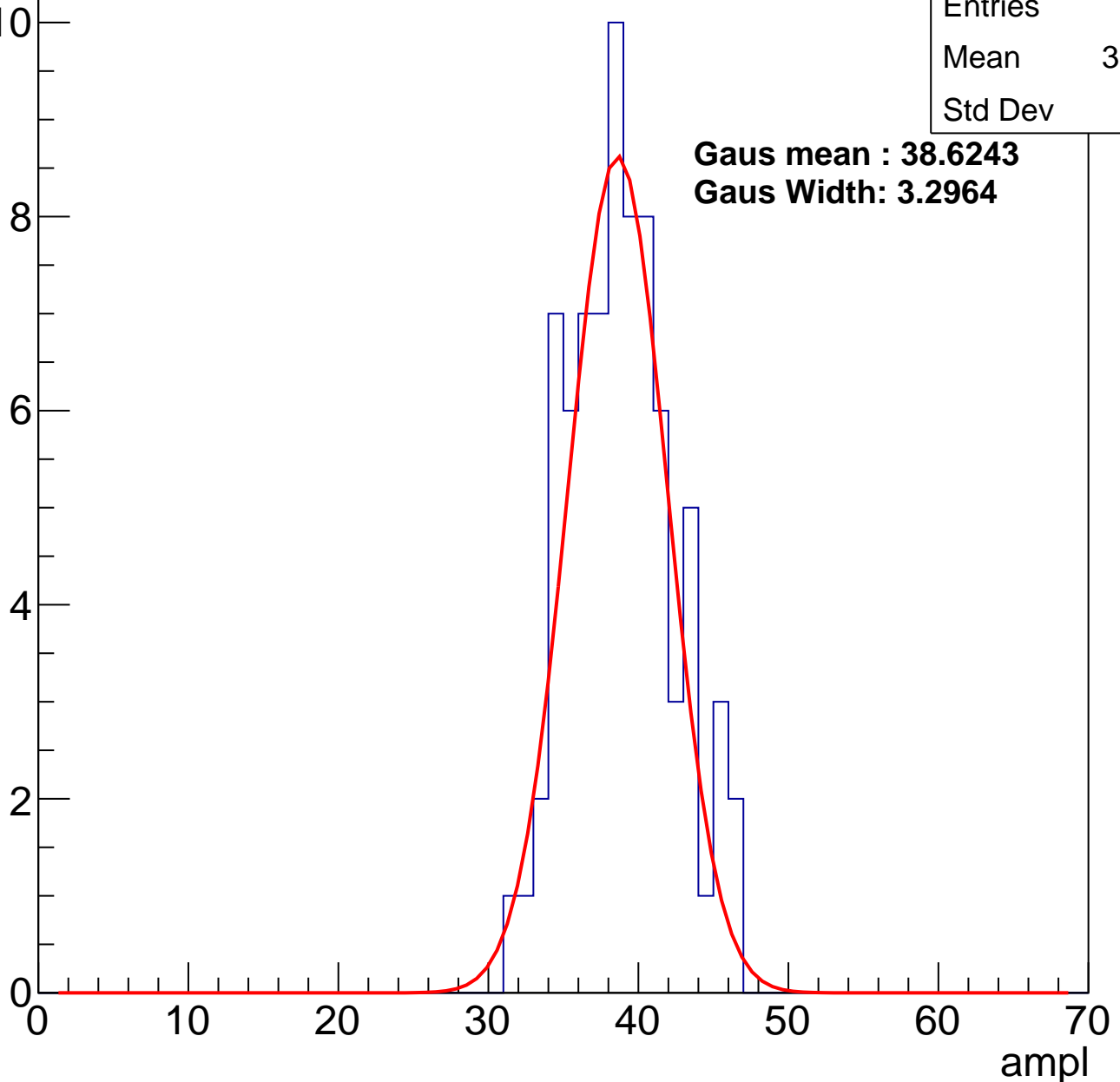
B1L101S, U21-ch70, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	38.42
Std Dev	3.42

Gaus mean : 38.6243
Gaus Width: 3.2964



B1L101S, U21-ch70, adc2

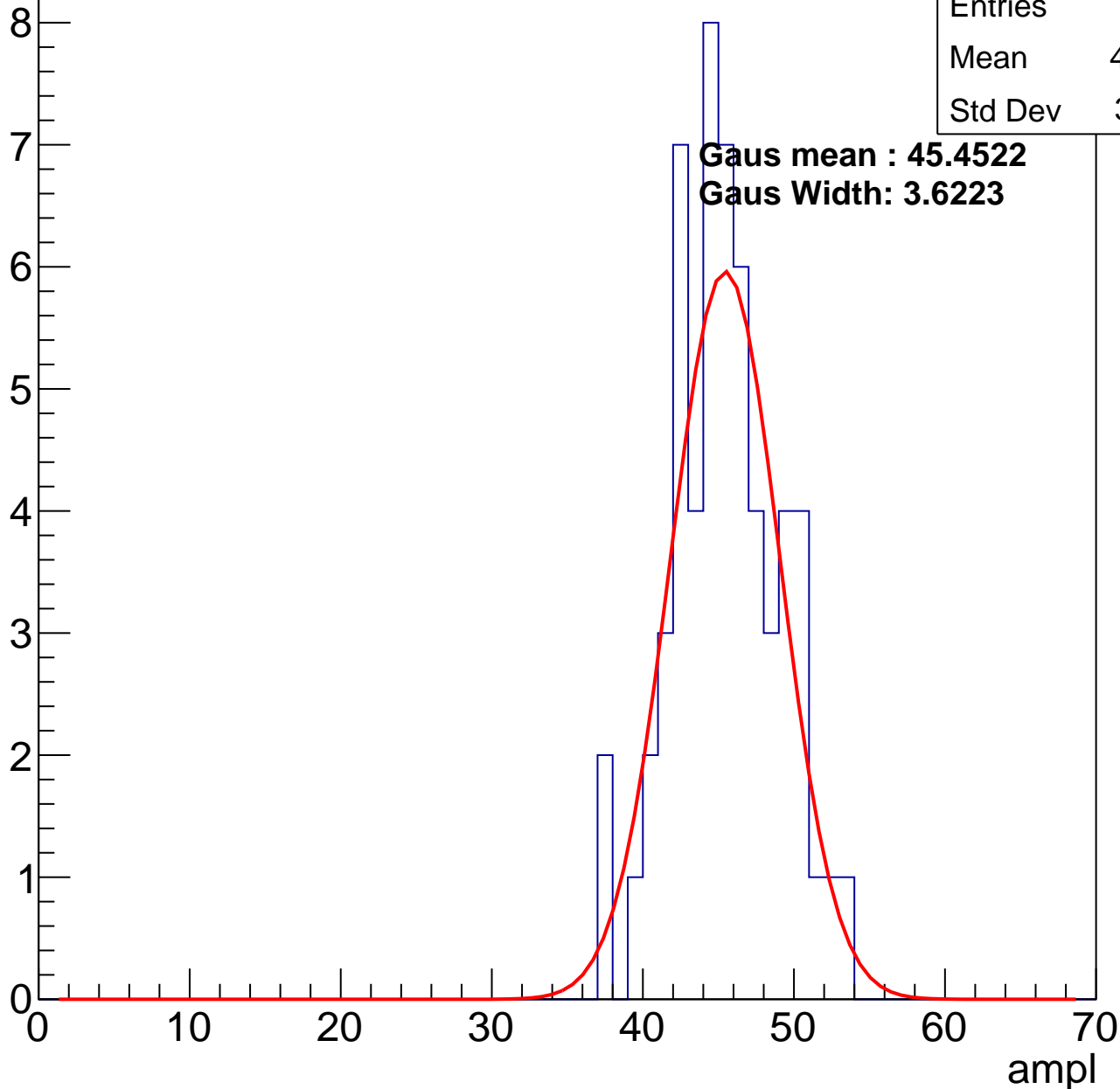
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.98
Std Dev	3.501

Gaus mean : 45.4522

Gaus Width: 3.6223

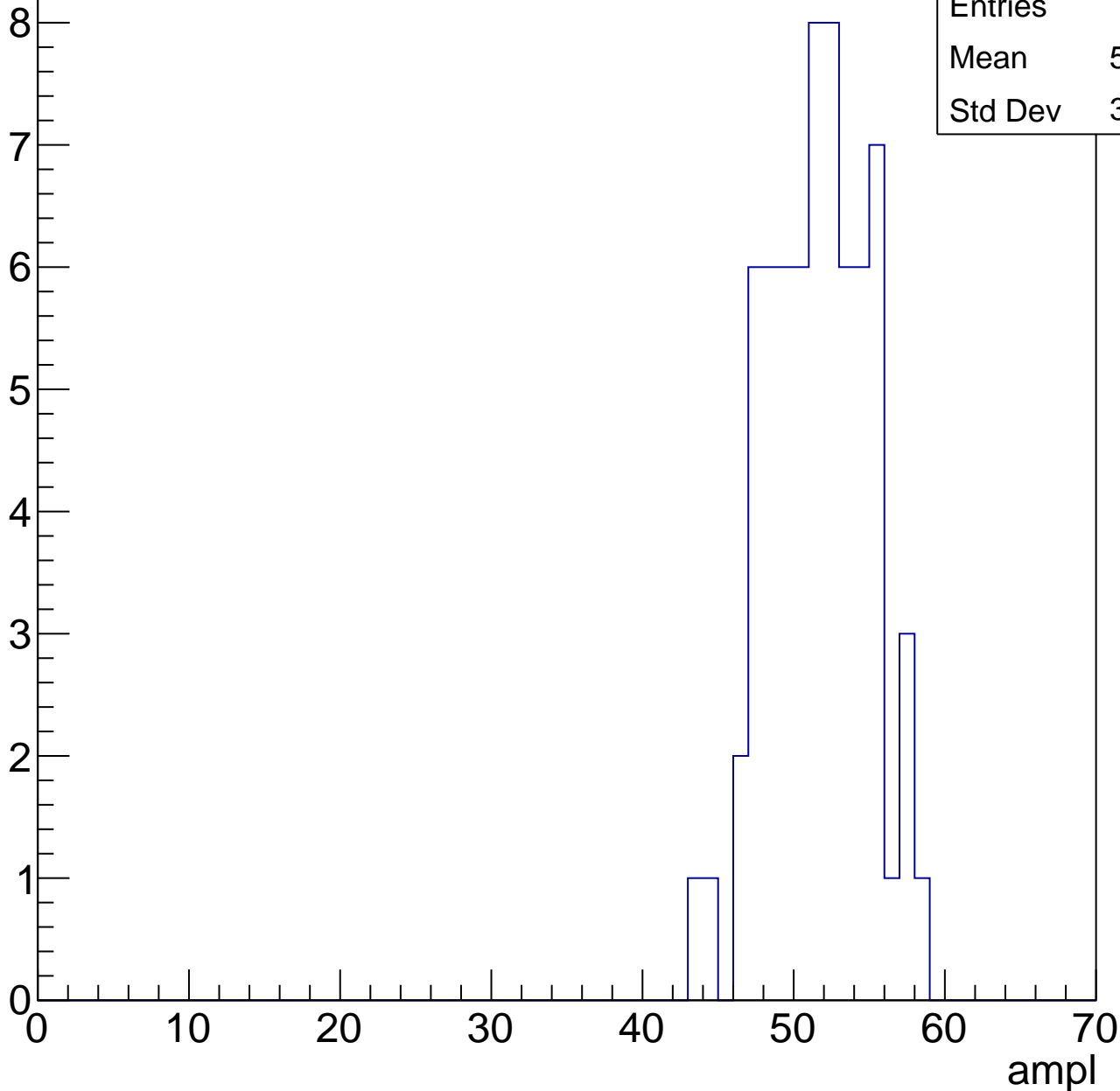


B1L101S, U21-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	51.16
Std Dev	3.257

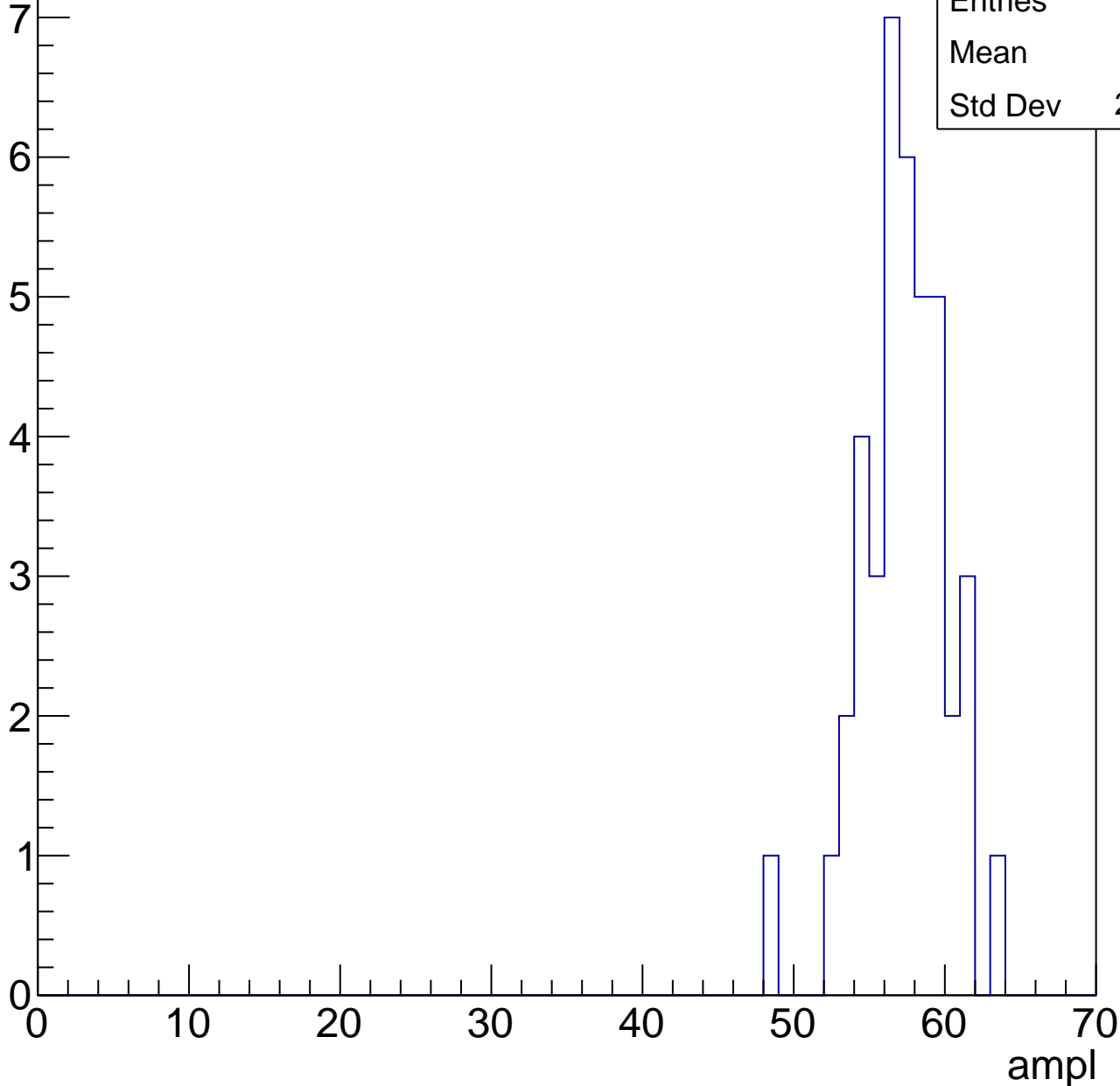


B1L101S, U21-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	56.8
Std Dev	2.821

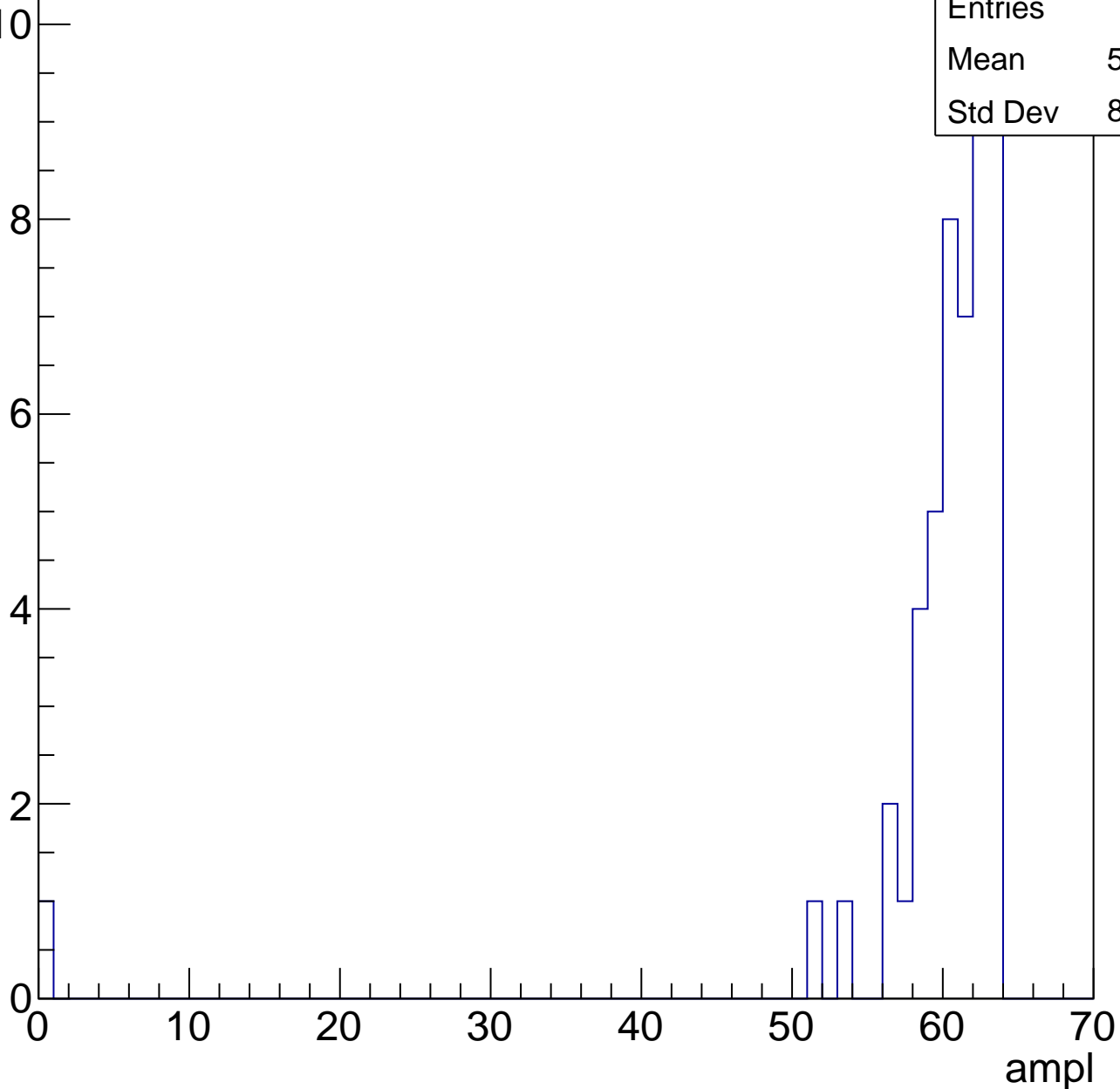


B1L101S, U21-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.06
Std Dev	8.895



B1L101S, U21-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch71, adc0

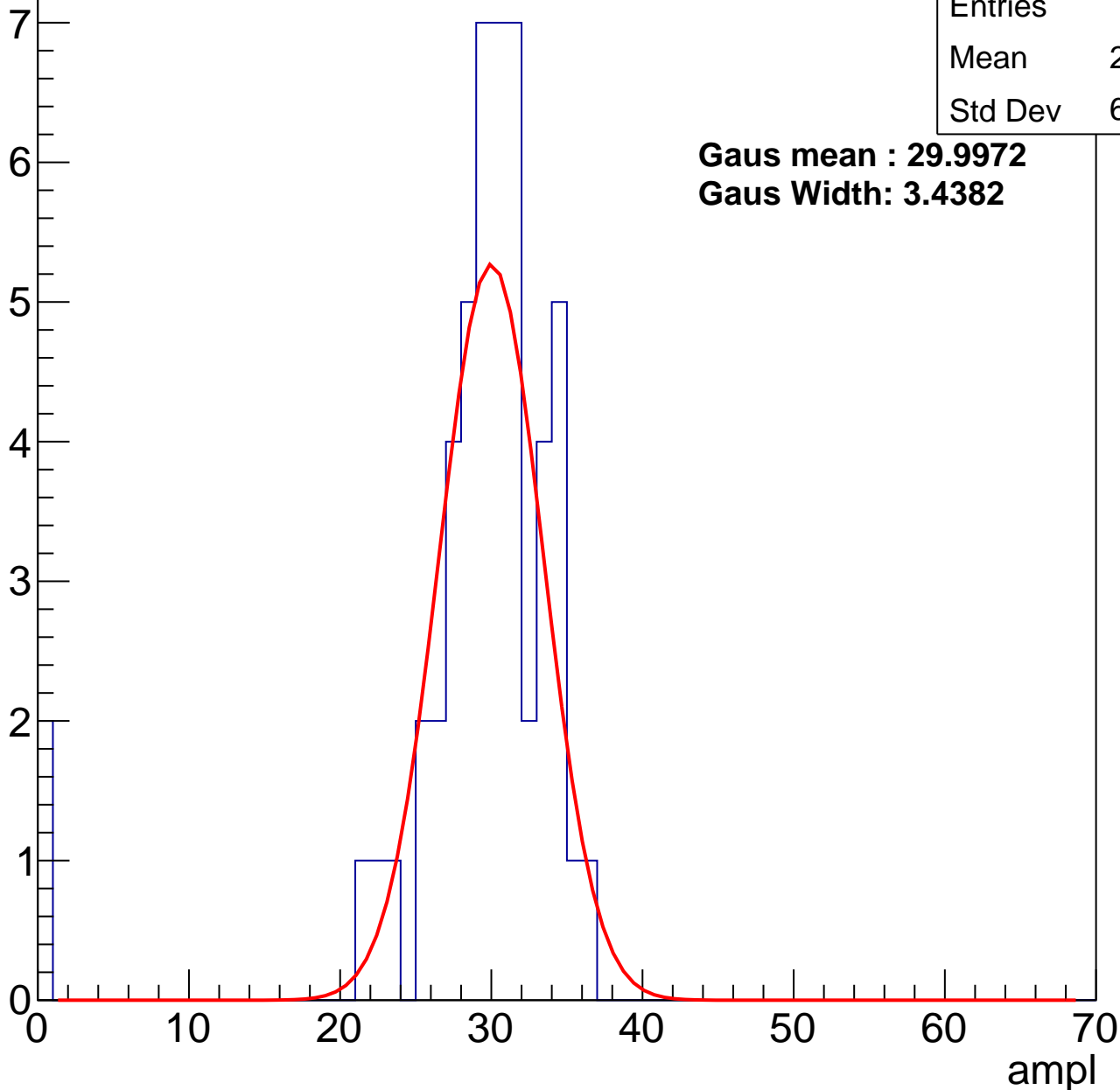
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	28.52
Std Dev	6.529

Gaus mean : 29.9972

Gaus Width: 3.4382



B1L101S, U21-ch71, adc1

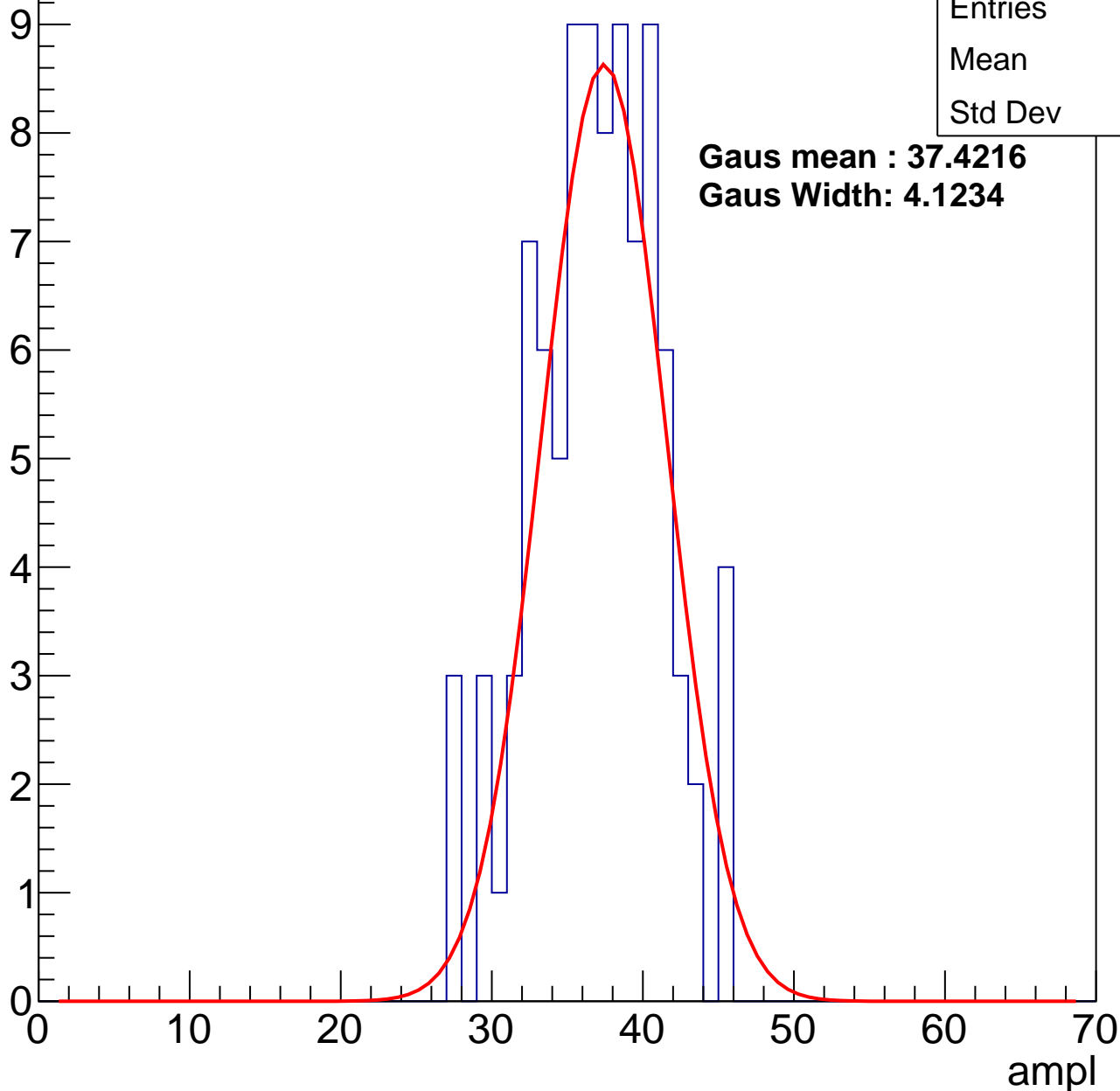
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	94
Mean	36.5
Std Dev	4.13

Gaus mean : 37.4216

Gaus Width: 4.1234



B1L101S, U21-ch71, adc2

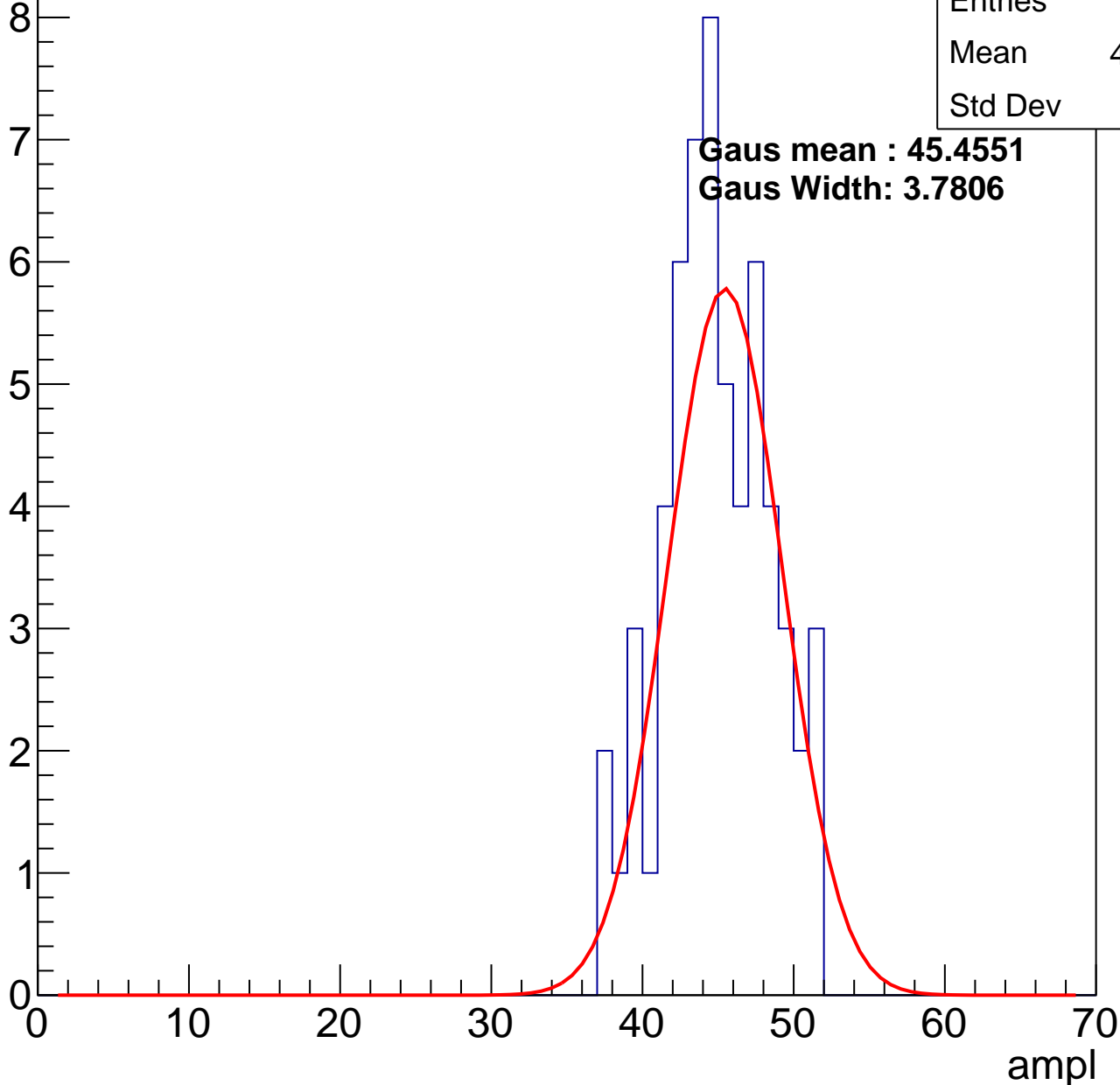
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.42
Std Dev	3.48

Gaus mean : 45.4551

Gaus Width: 3.7806

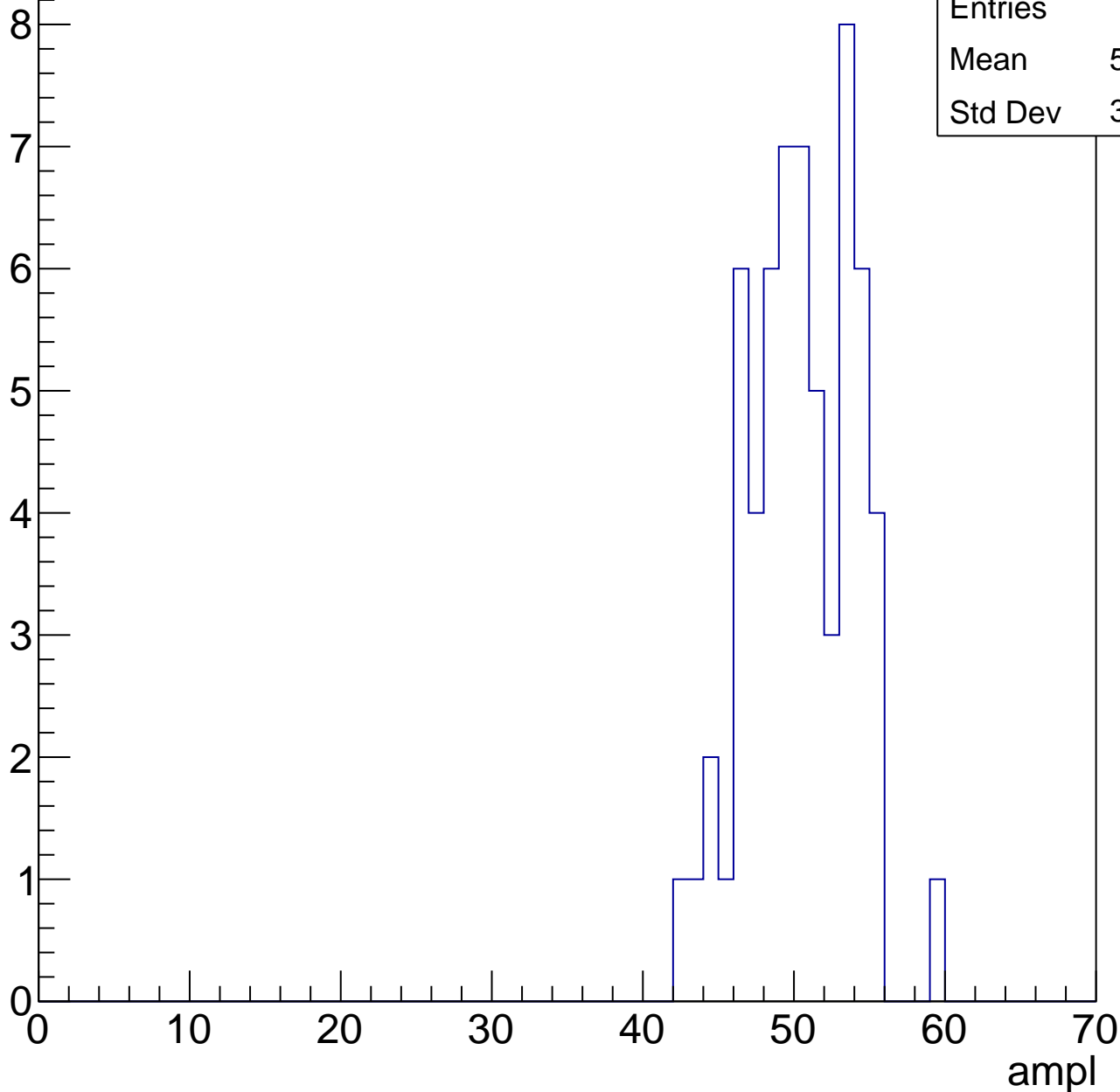


B1L101S, U21-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	50.02
Std Dev	3.452

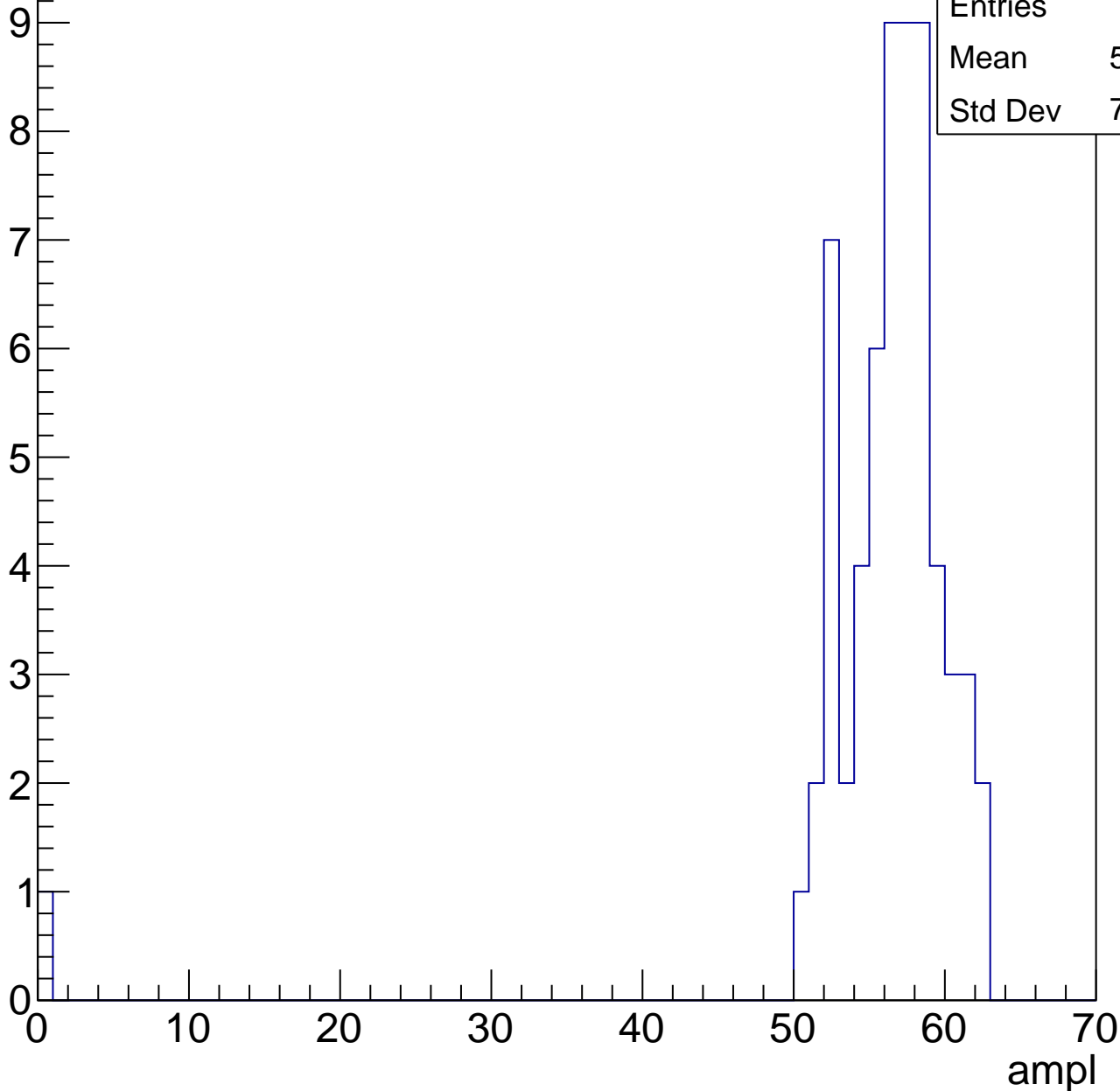


B1L101S, U21-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	55.32
Std Dev	7.643

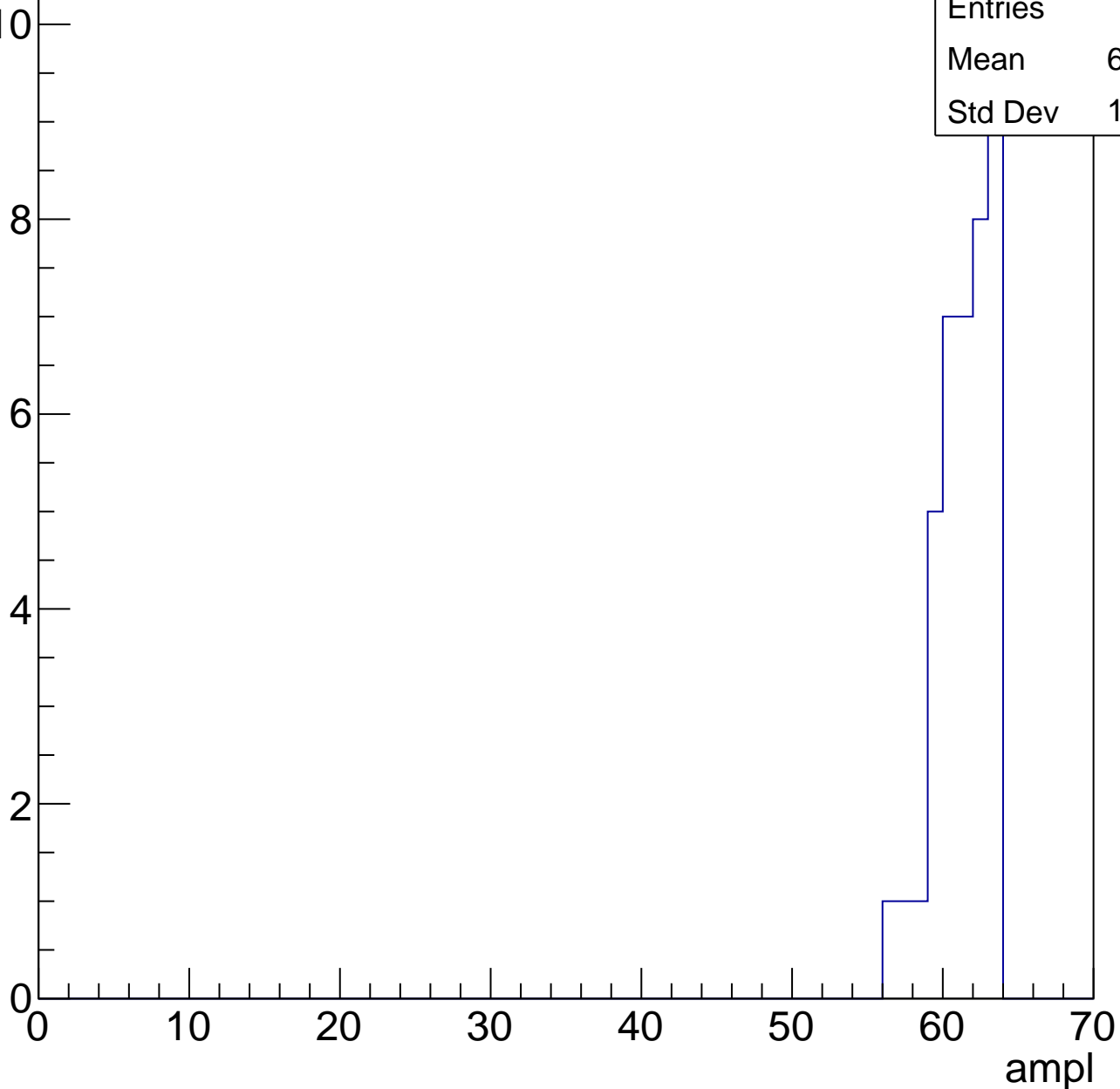


B1L101S, U21-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	60.98
Std Dev	1.768



B1L101S, U21-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch72, adc0

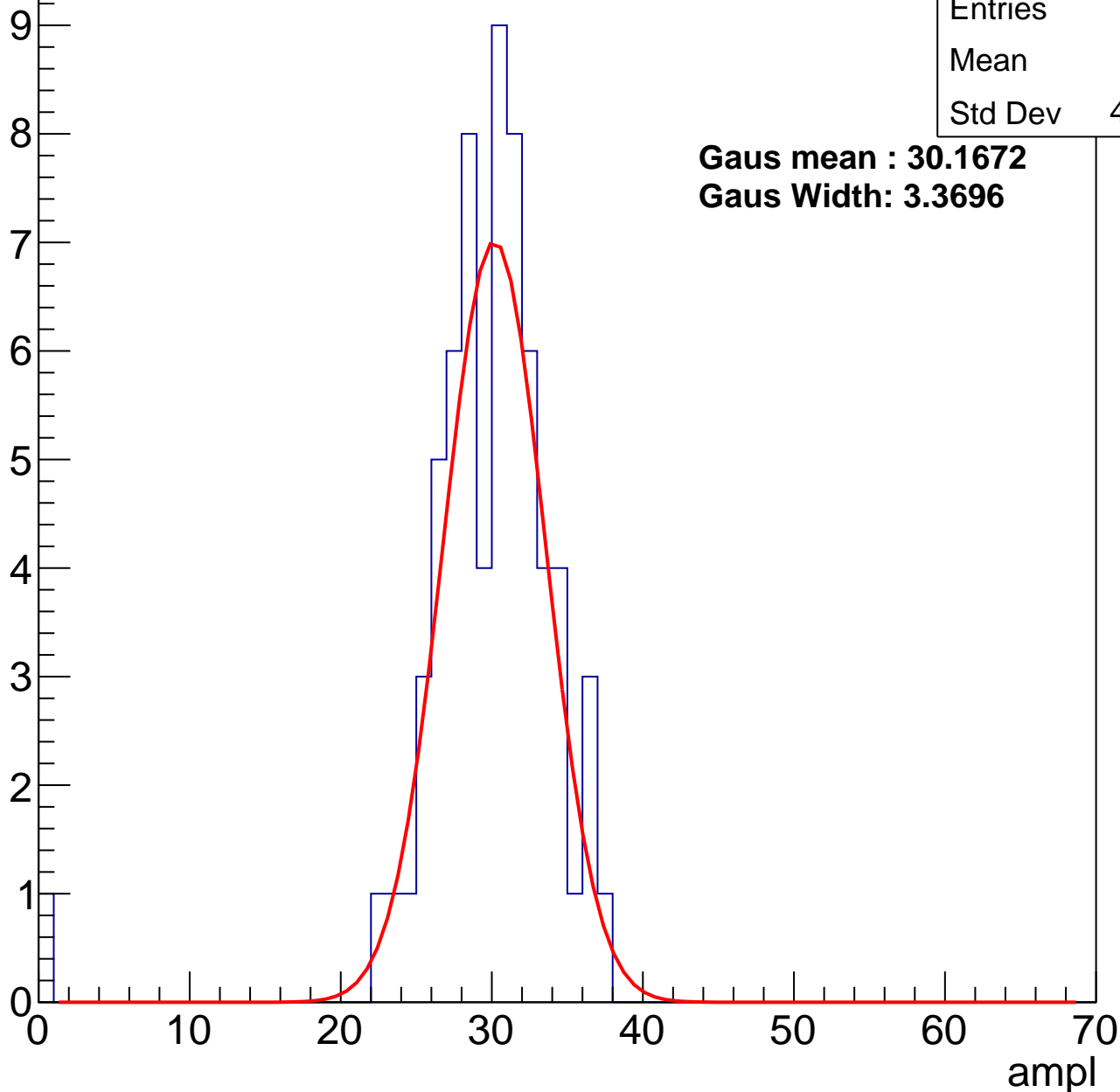
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.3
Std Dev	4.886

Gaus mean : 30.1672

Gaus Width: 3.3696



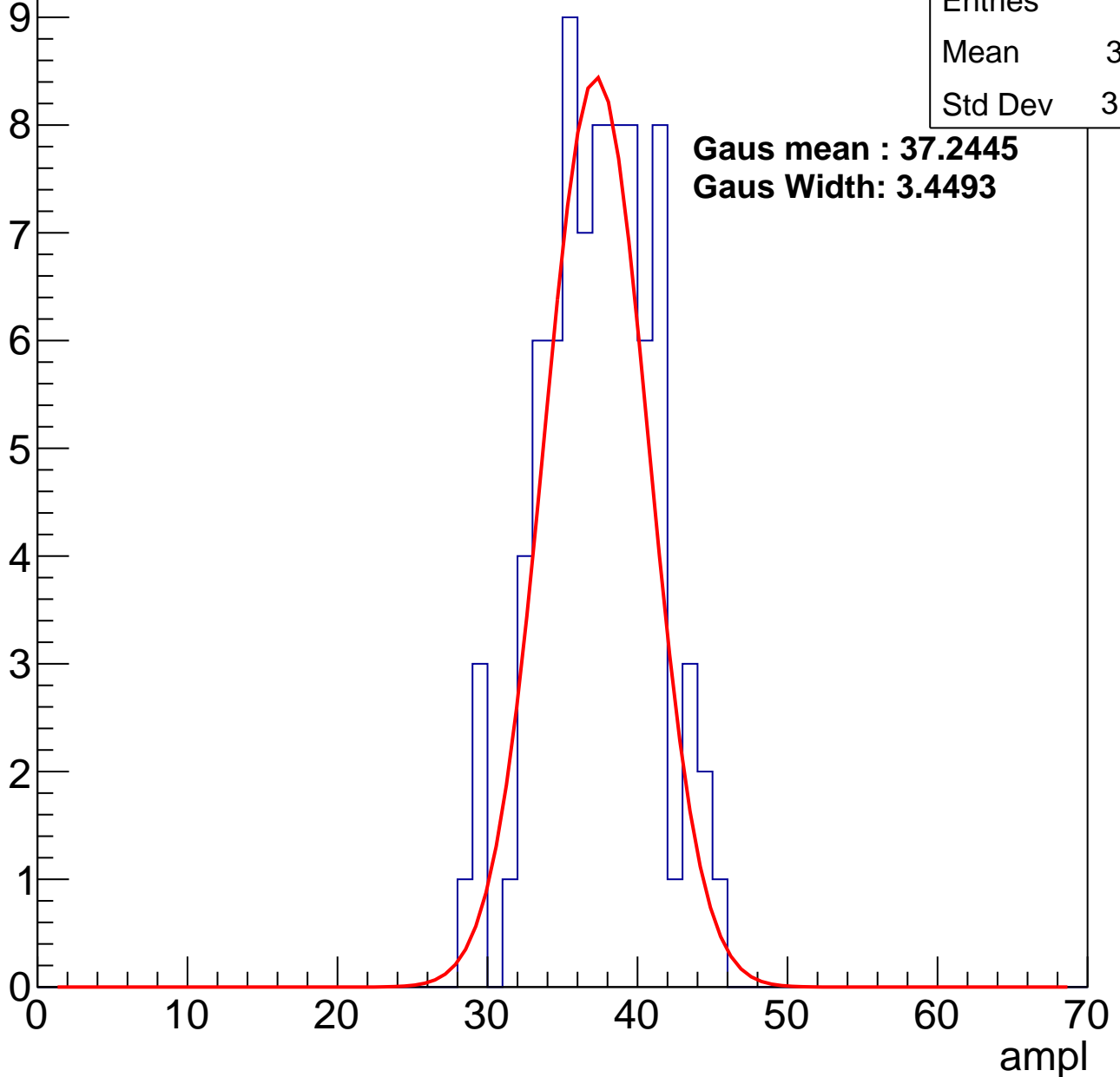
B1L101S, U21-ch72, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.91
Std Dev	3.693

Gaus mean : 37.2445
Gaus Width: 3.4493

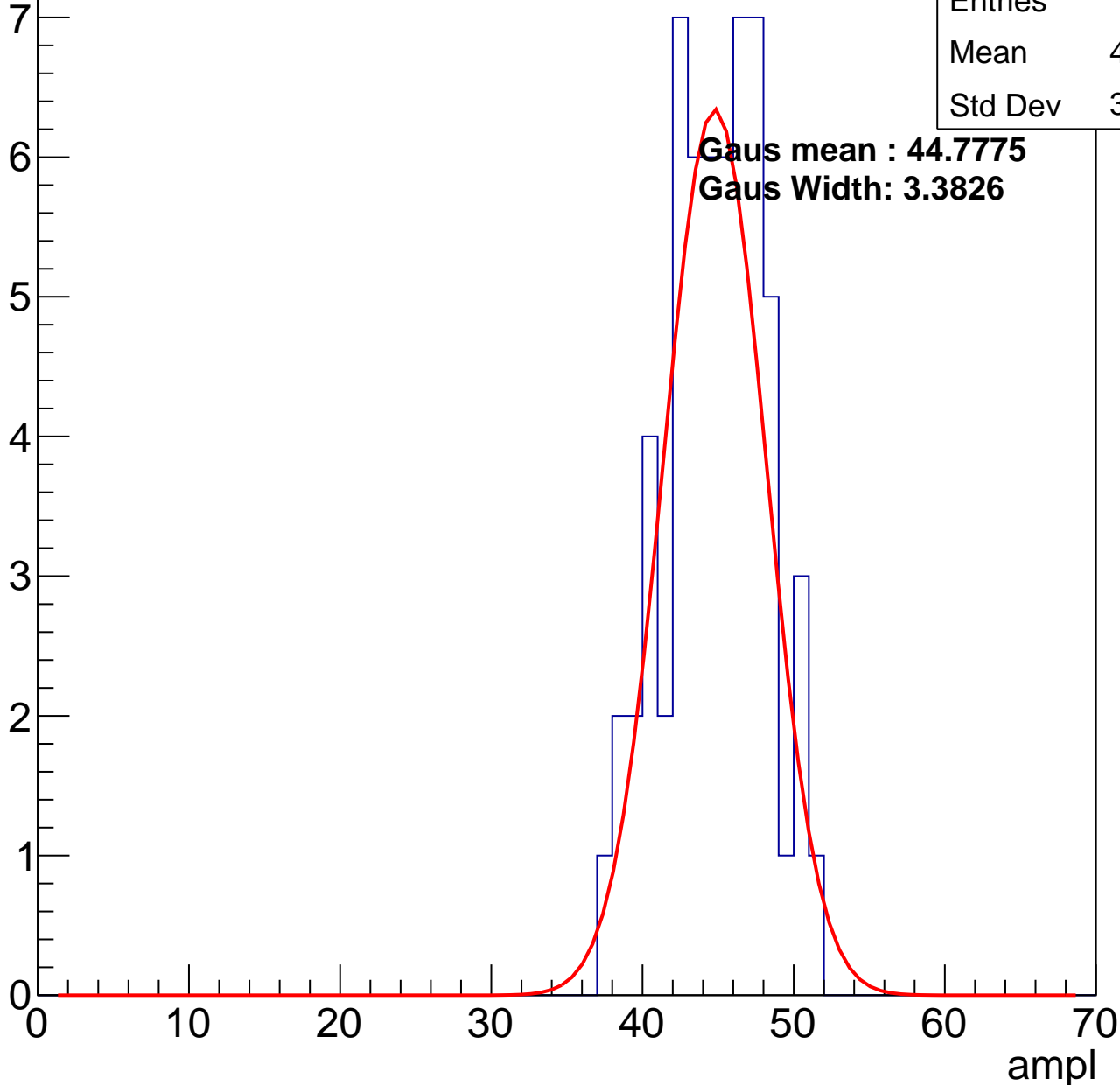


B1L101S, U21-ch72, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.33
Std Dev	3.264

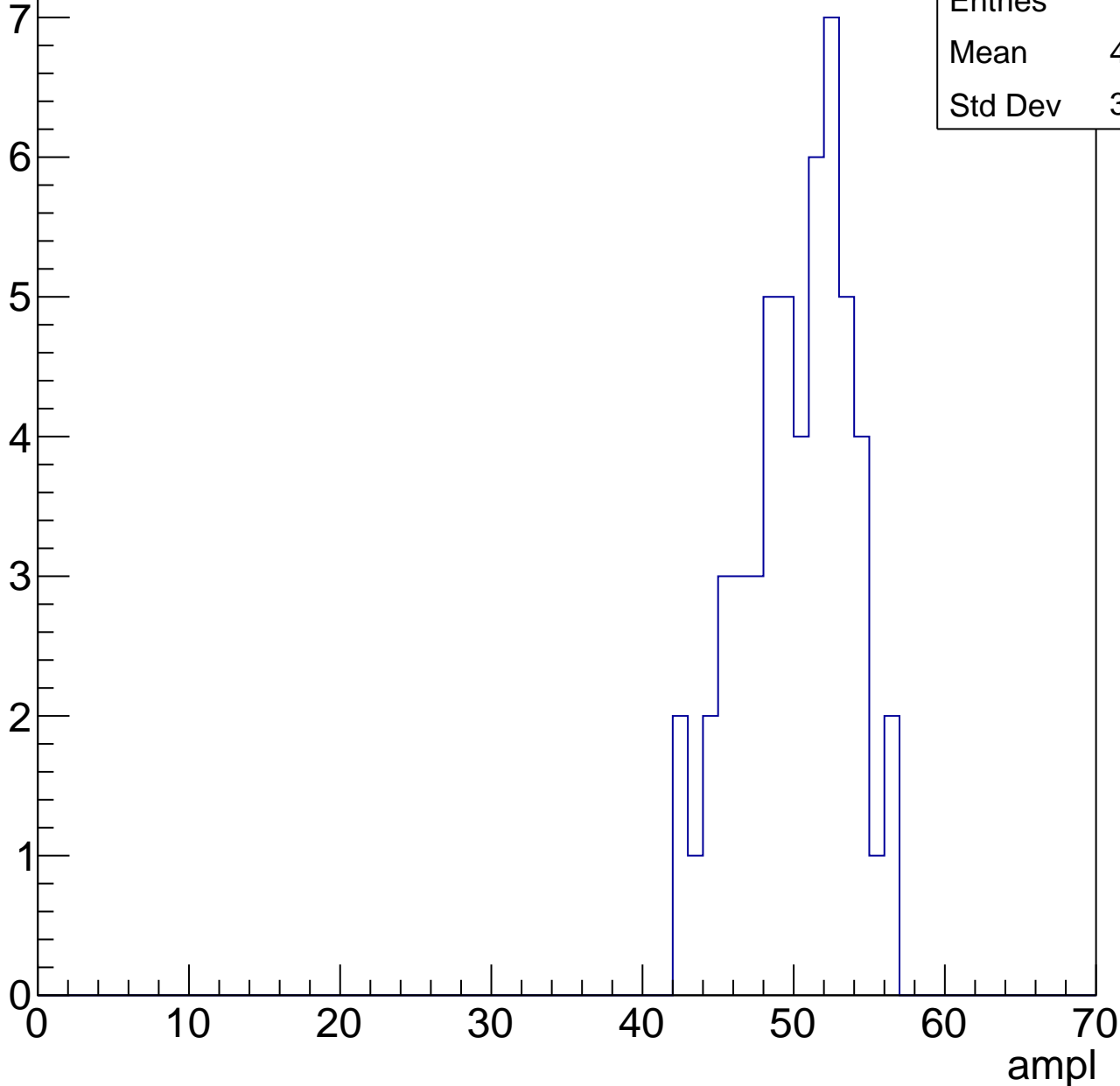


B1L101S, U21-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	49.66
Std Dev	3.523

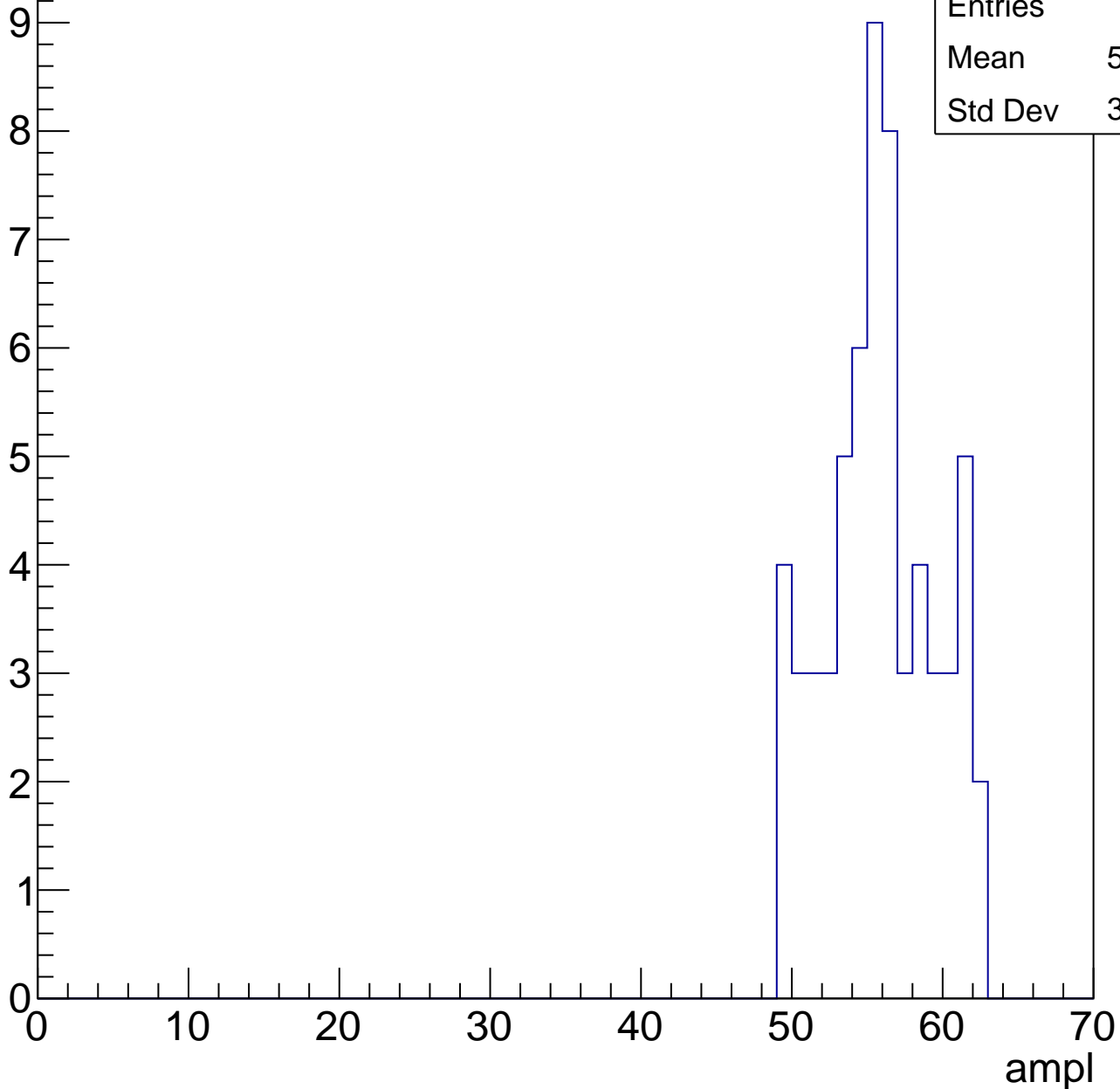


B1L101S, U21-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.34
Std Dev	3.552

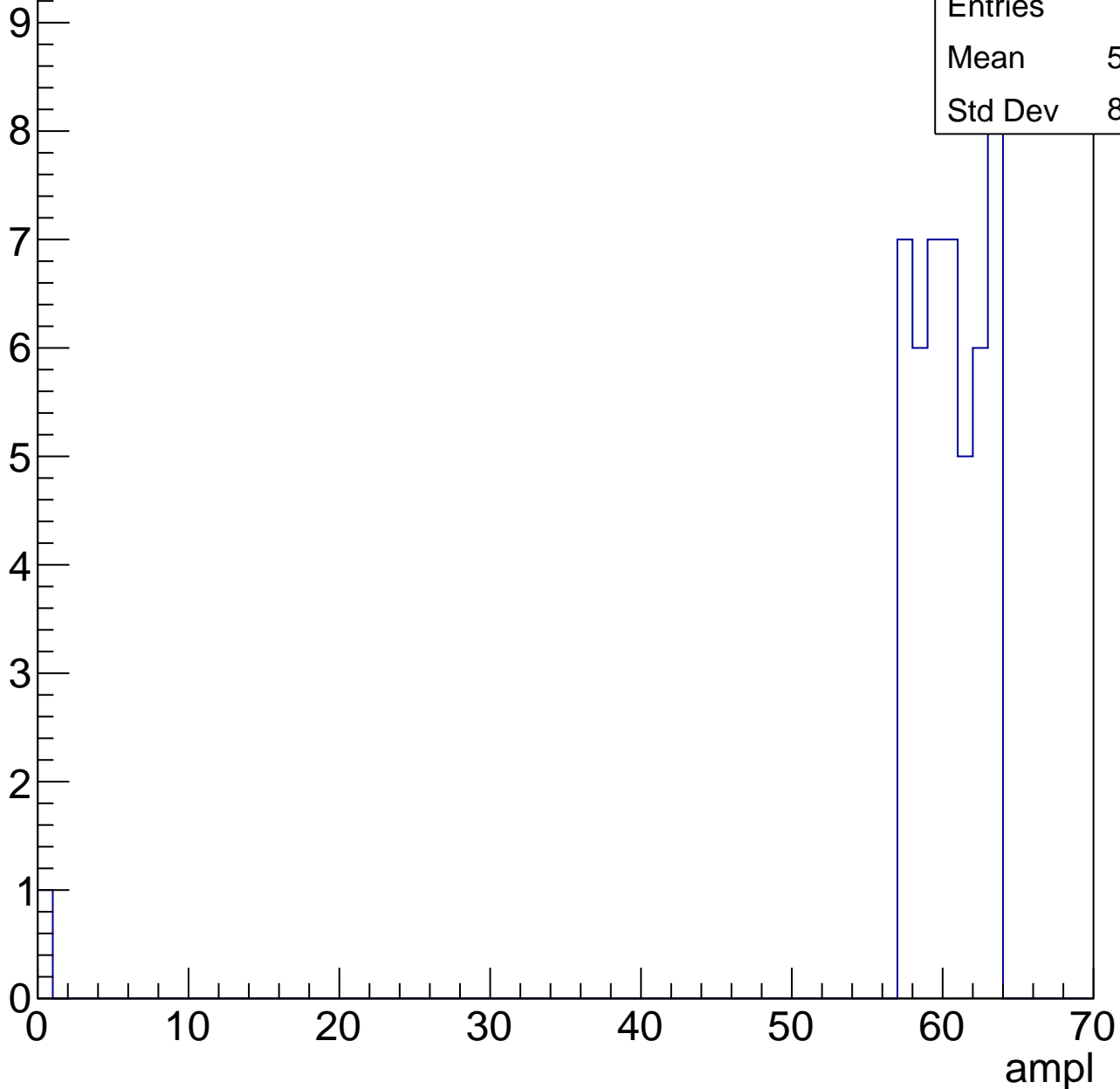


B1L101S, U21-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.83
Std Dev	8.825



B1L101S, U21-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L101S, U21-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch73, adc0

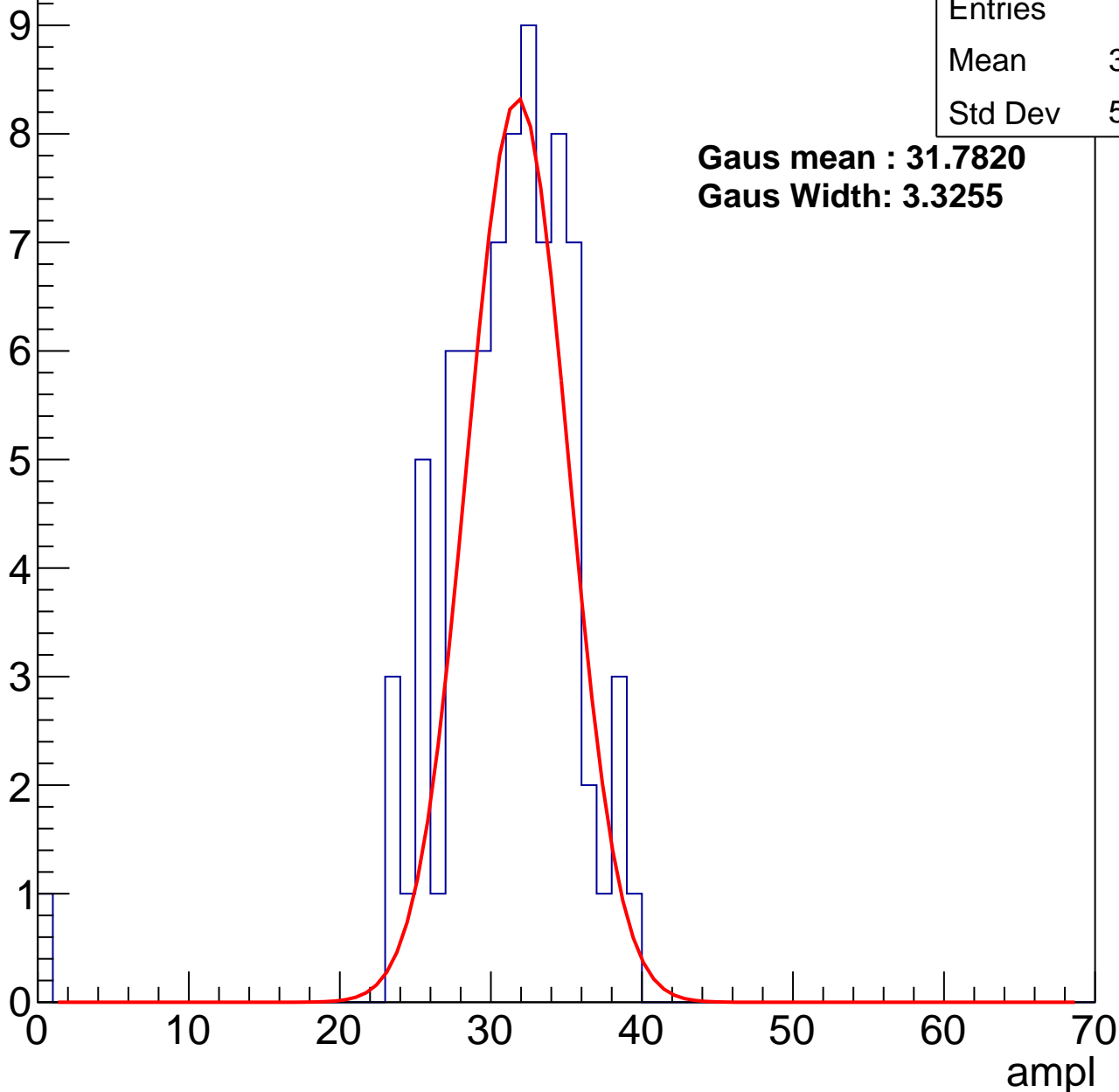
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	30.54
Std Dev	5.047

Gaus mean : 31.7820

Gaus Width: 3.3255



B1L101S, U21-ch73, adc1

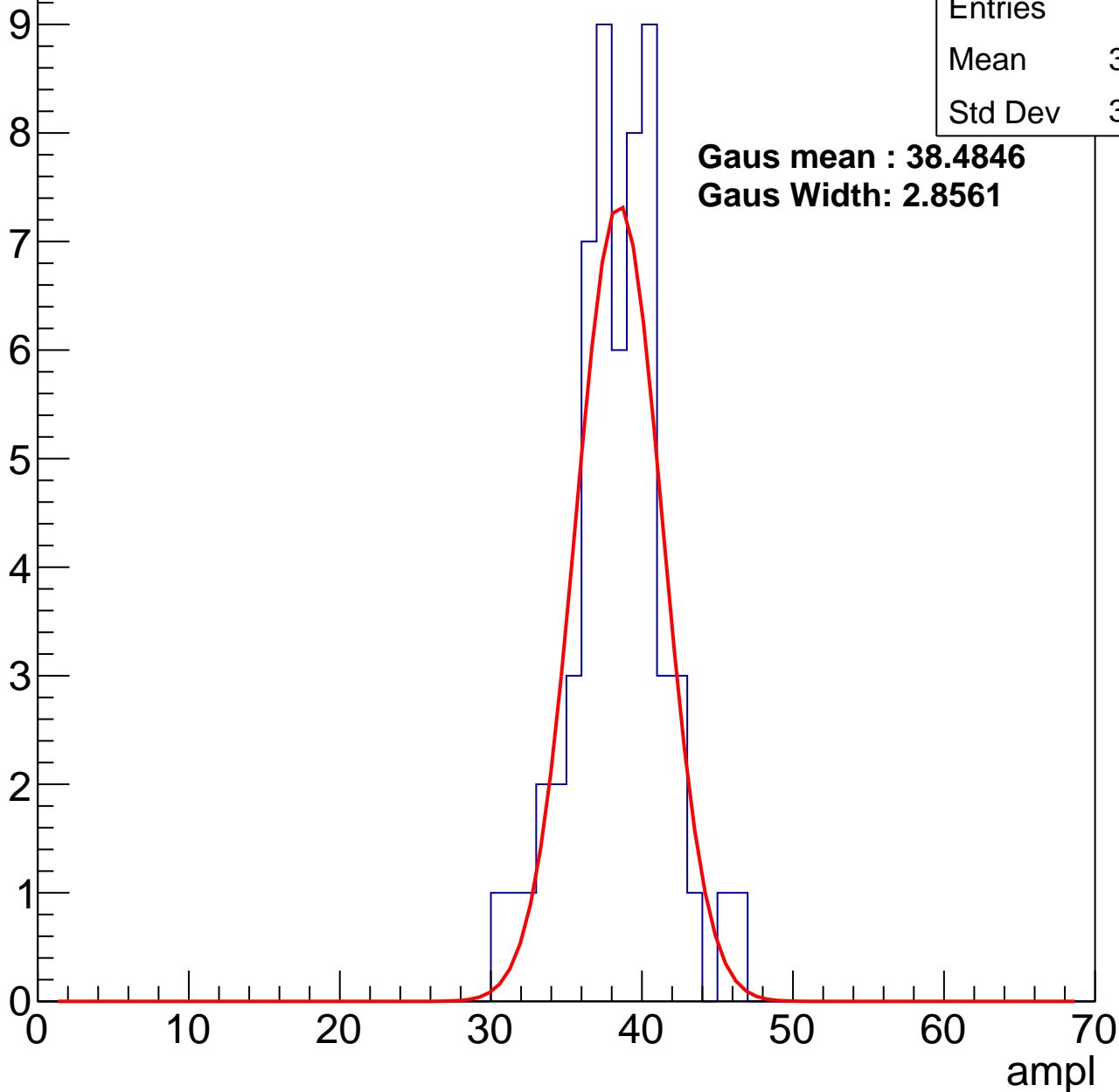
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	37.93
Std Dev	3.084

Gaus mean : 38.4846

Gaus Width: 2.8561



B1L101S, U21-ch73, adc2

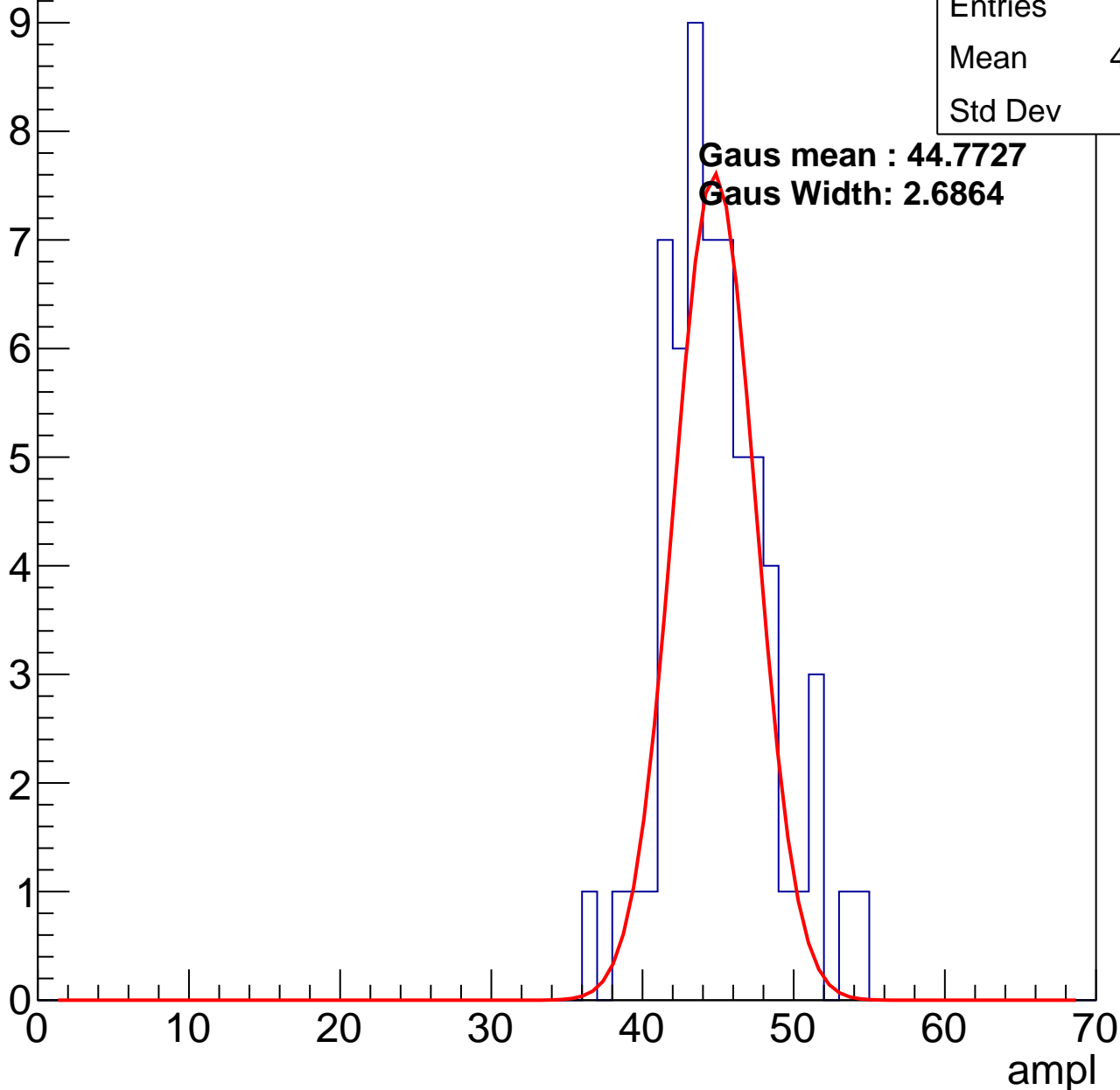
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	44.56
Std Dev	3.49

Gaus mean : 44.7727

Gaus Width: 2.6864

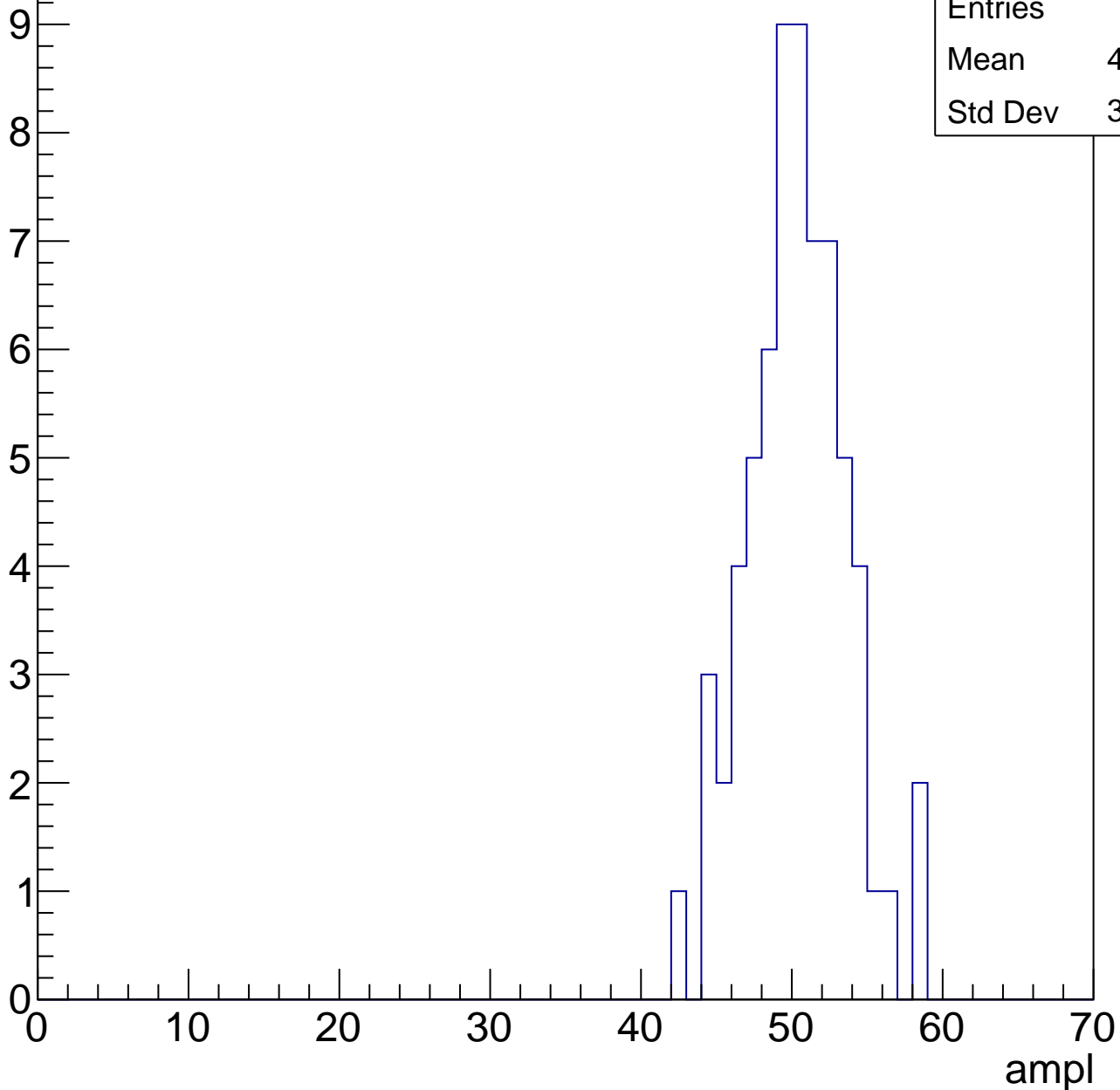


B1L101S, U21-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

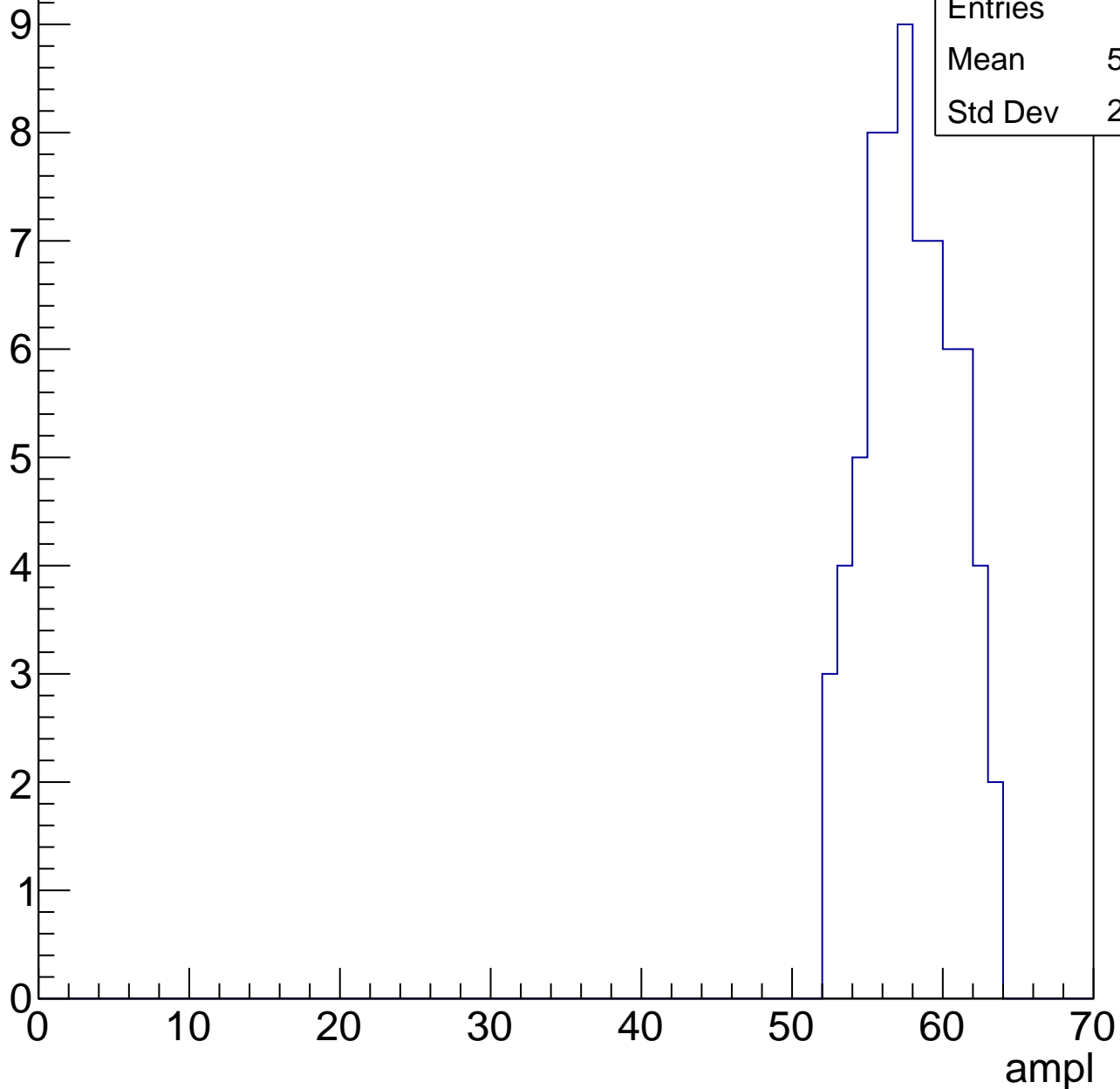
Entries	66
Mean	49.86
Std Dev	3.247



B1L101S, U21-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

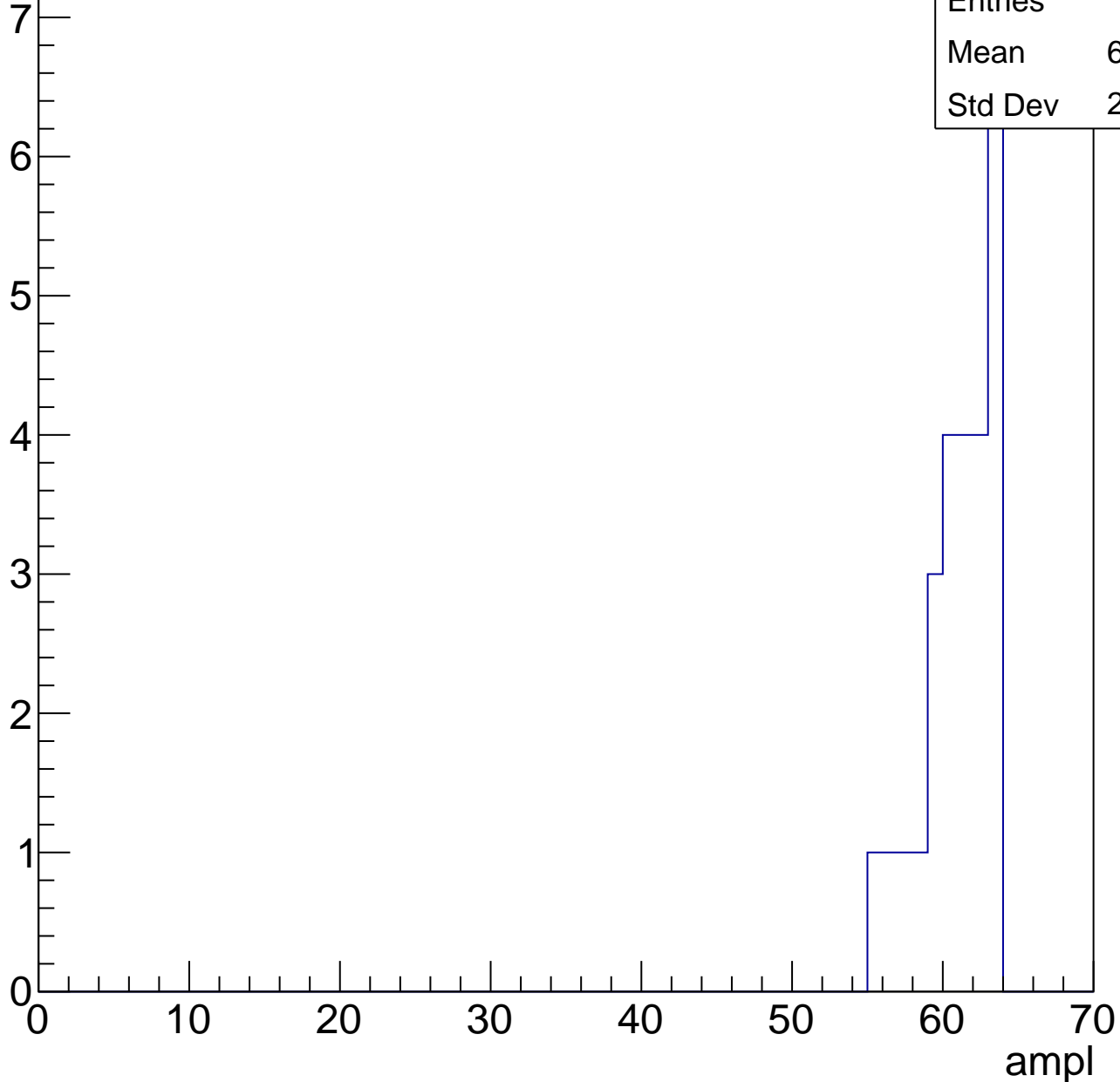


Entries	69
Mean	57.36
Std Dev	2.879

B1L101S, U21-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

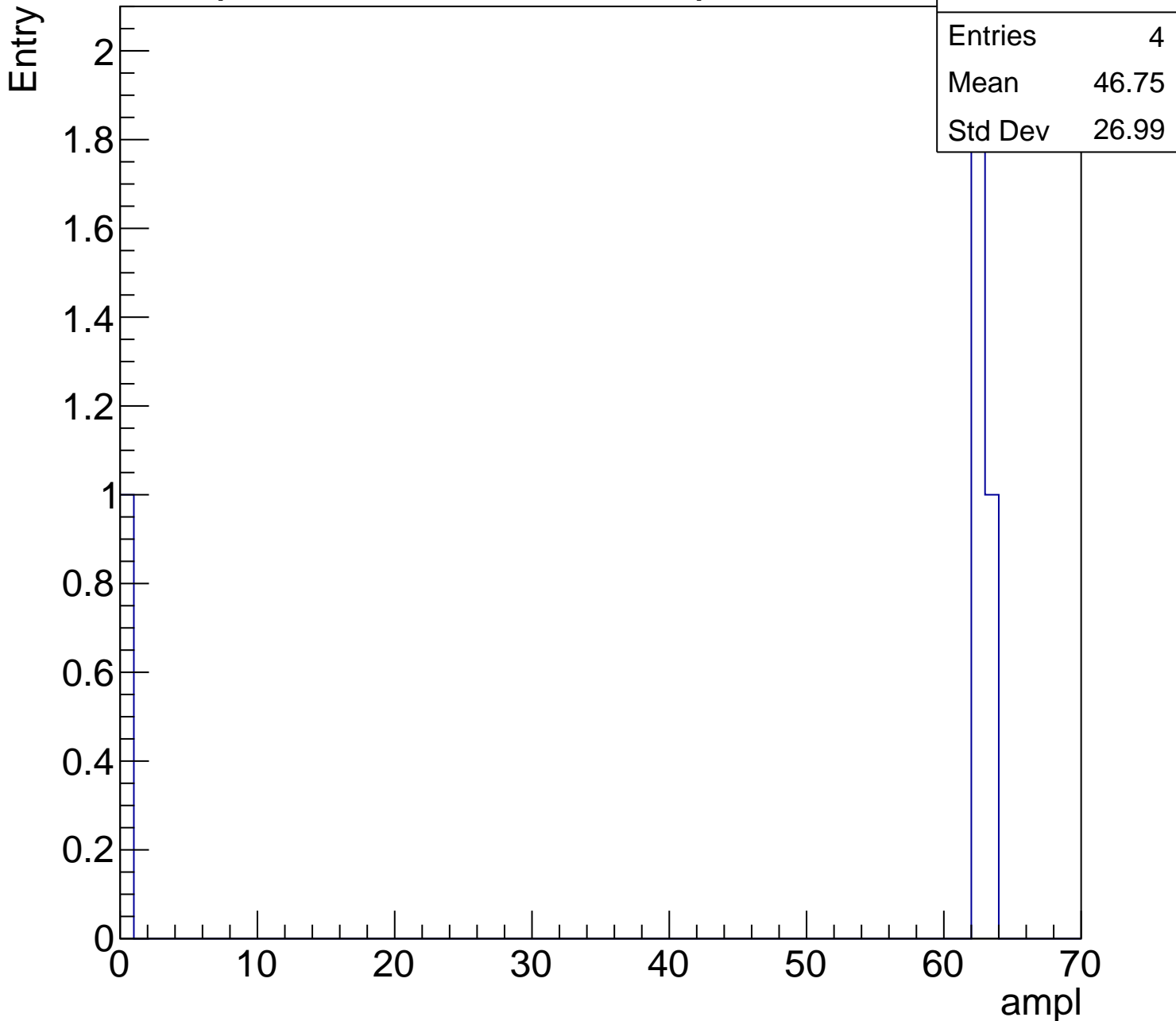
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	26.99

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch74, adc0

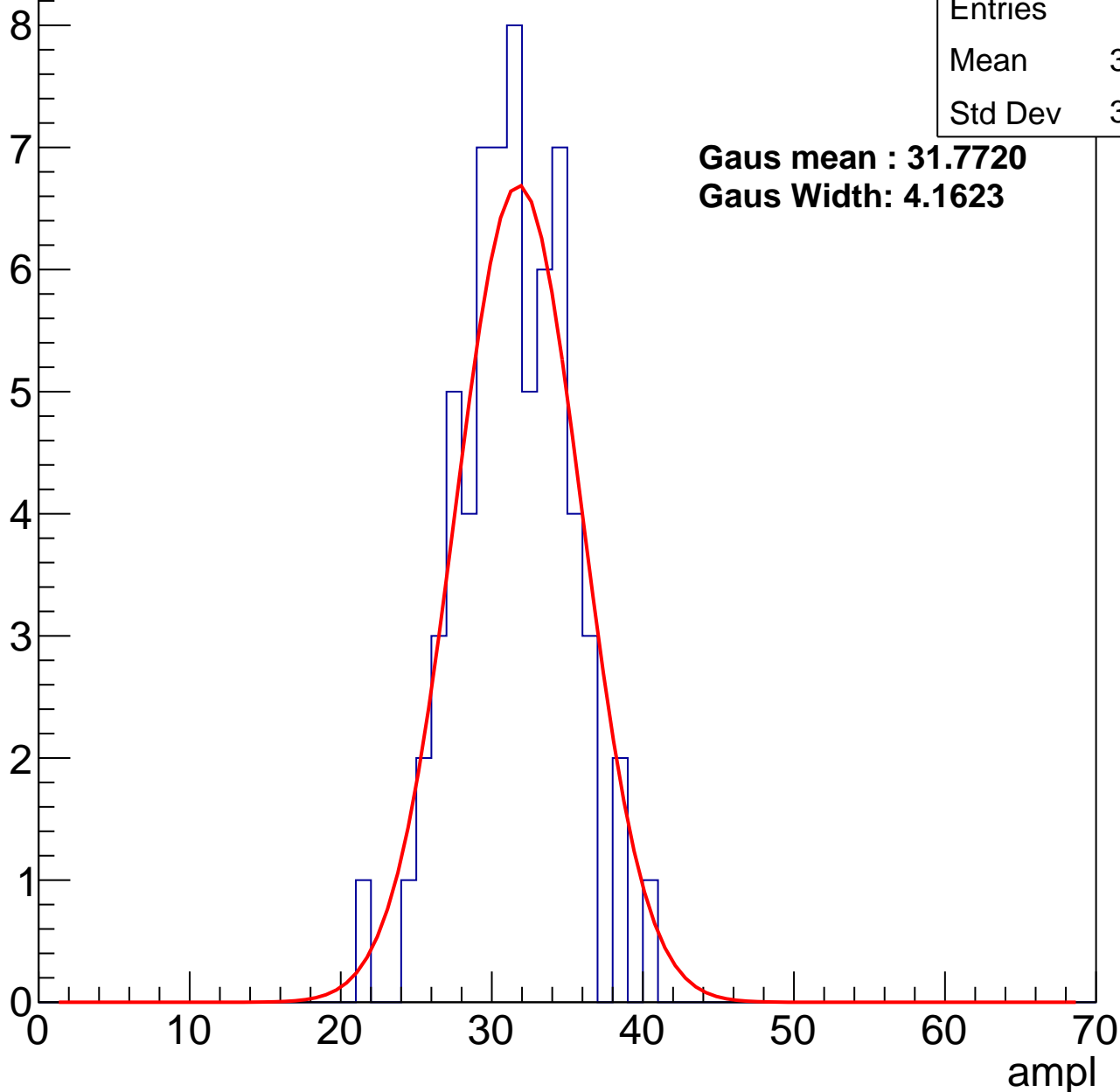
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	30.92
Std Dev	3.603

Gaus mean : 31.7720

Gaus Width: 4.1623



B1L101S, U21-ch74, adc1

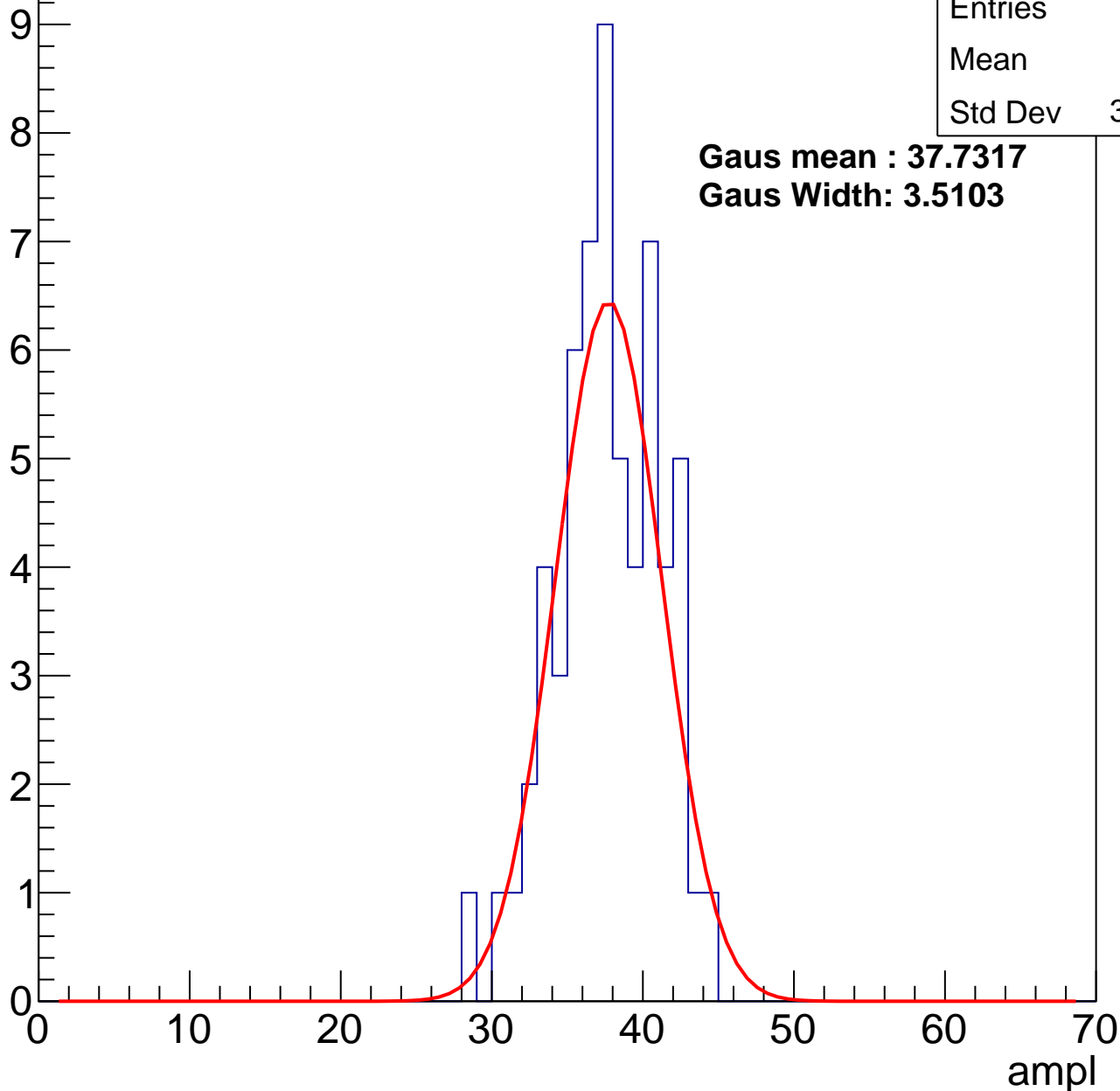
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.2
Std Dev	3.372

Gaus mean : 37.7317

Gaus Width: 3.5103



B1L101S, U21-ch74, adc2

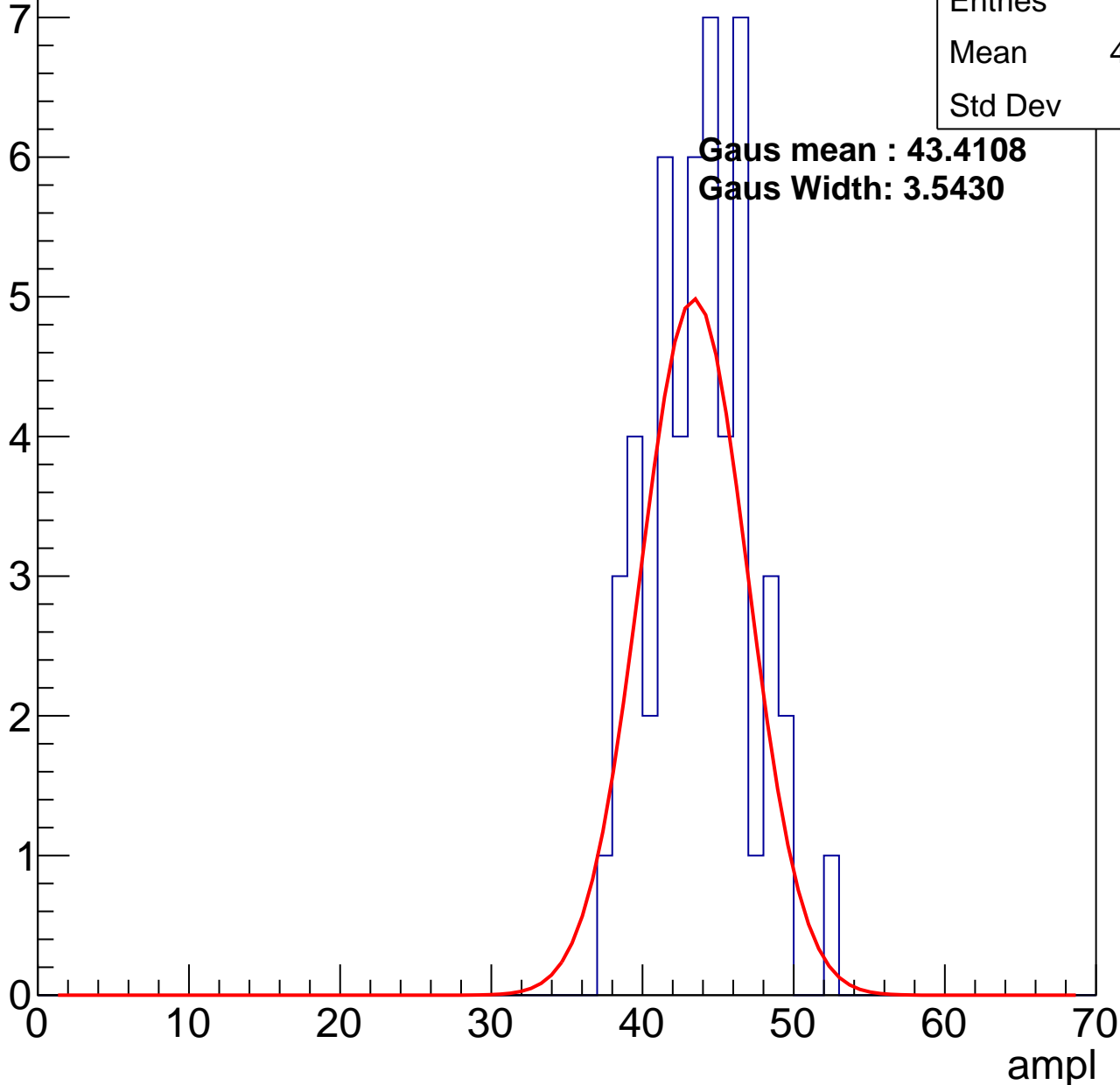
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	43.33
Std Dev	3.27

Gaus mean : 43.4108

Gaus Width: 3.5430

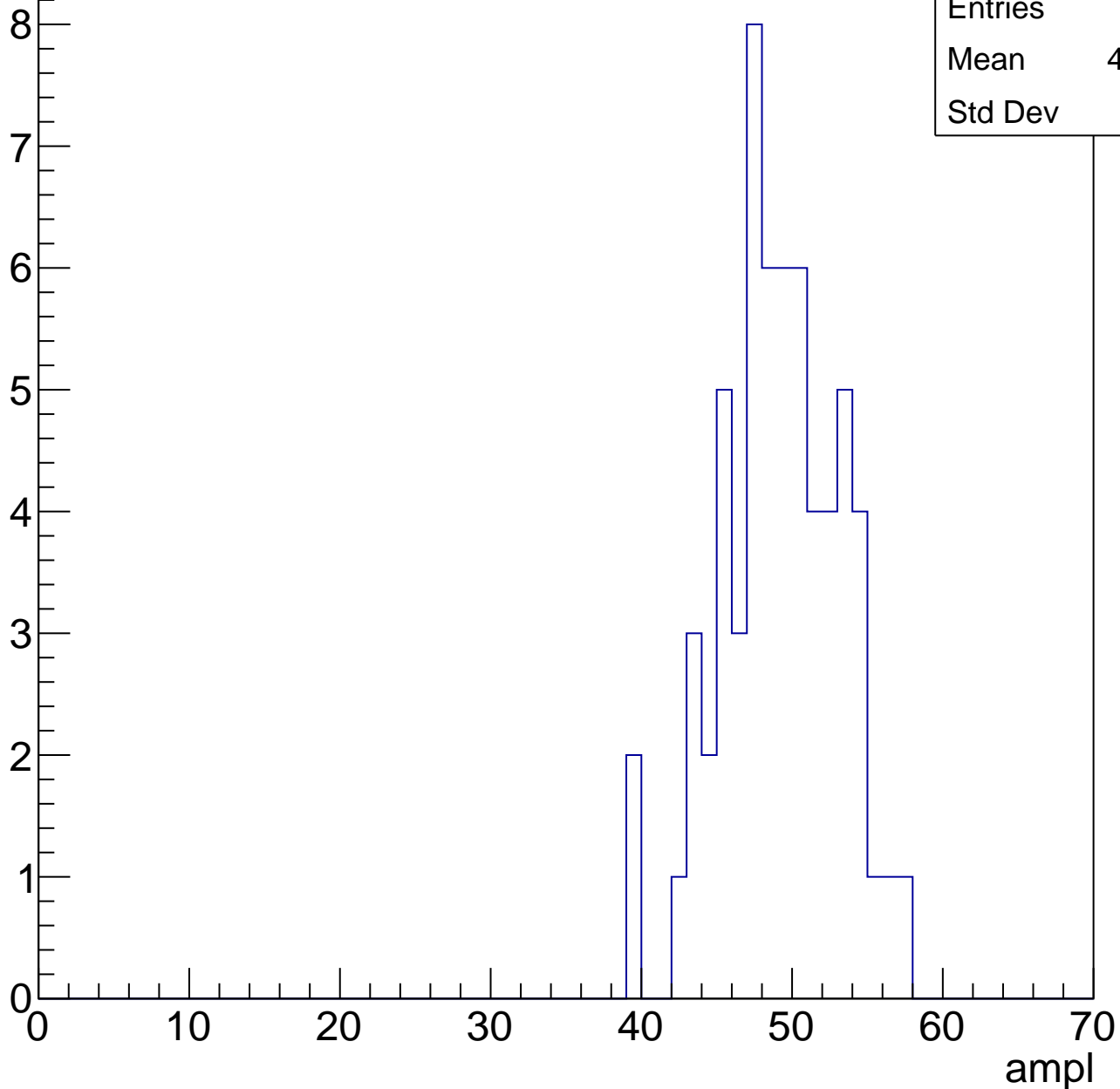


B1L101S, U21-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

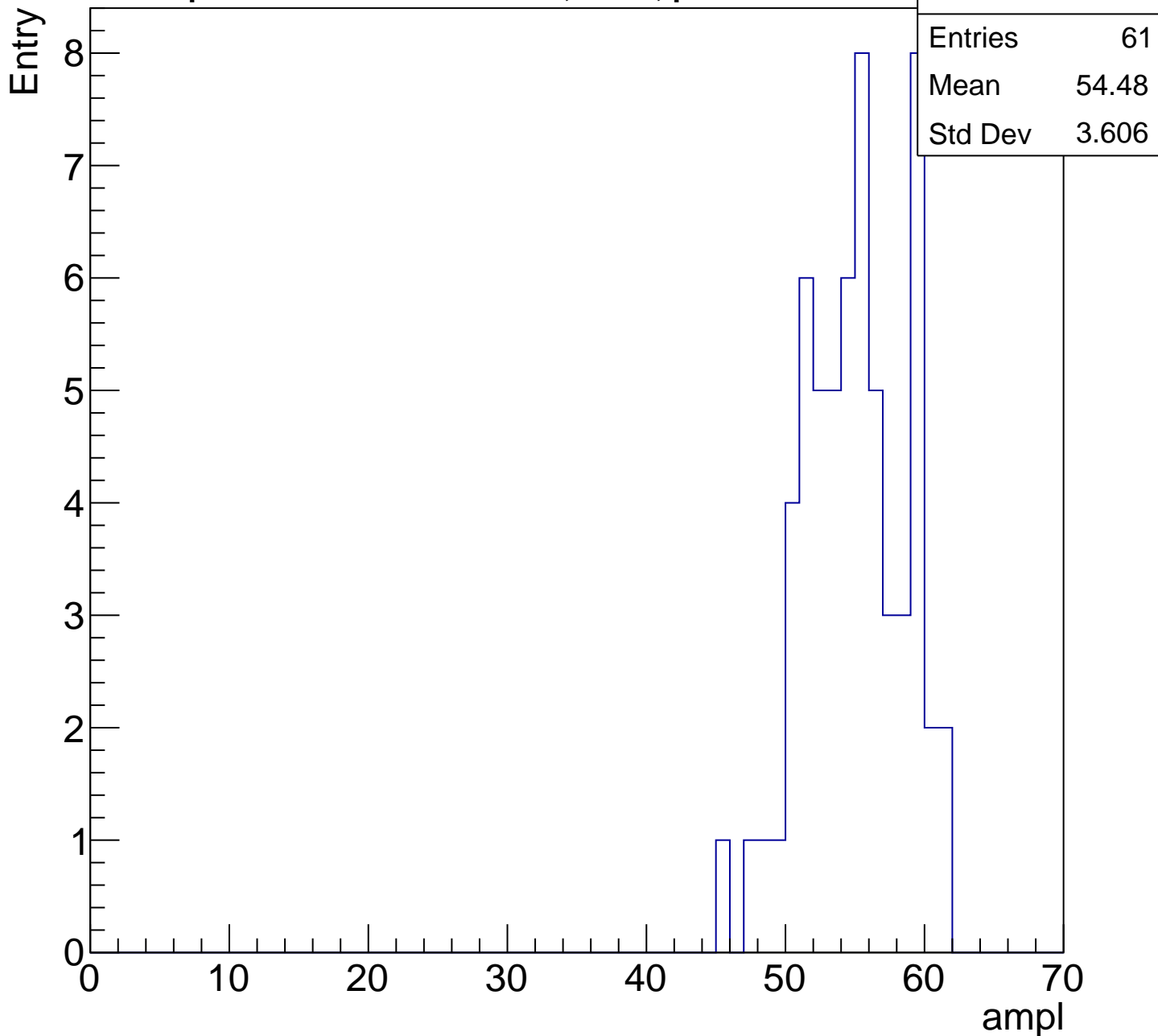
Entry

Entries	62
Mean	48.69
Std Dev	3.88



B1L101S, U21-ch74, adc4

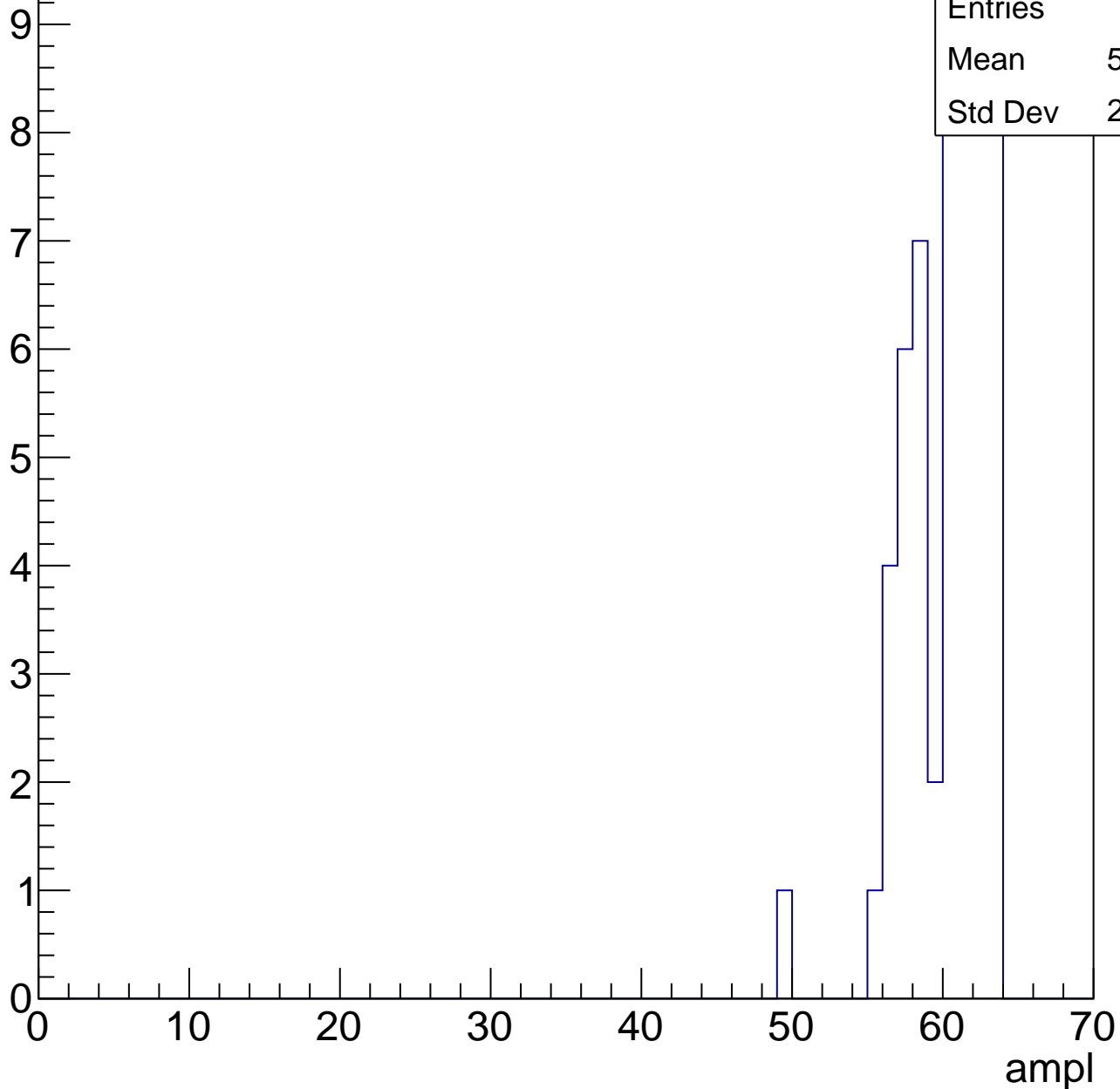
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

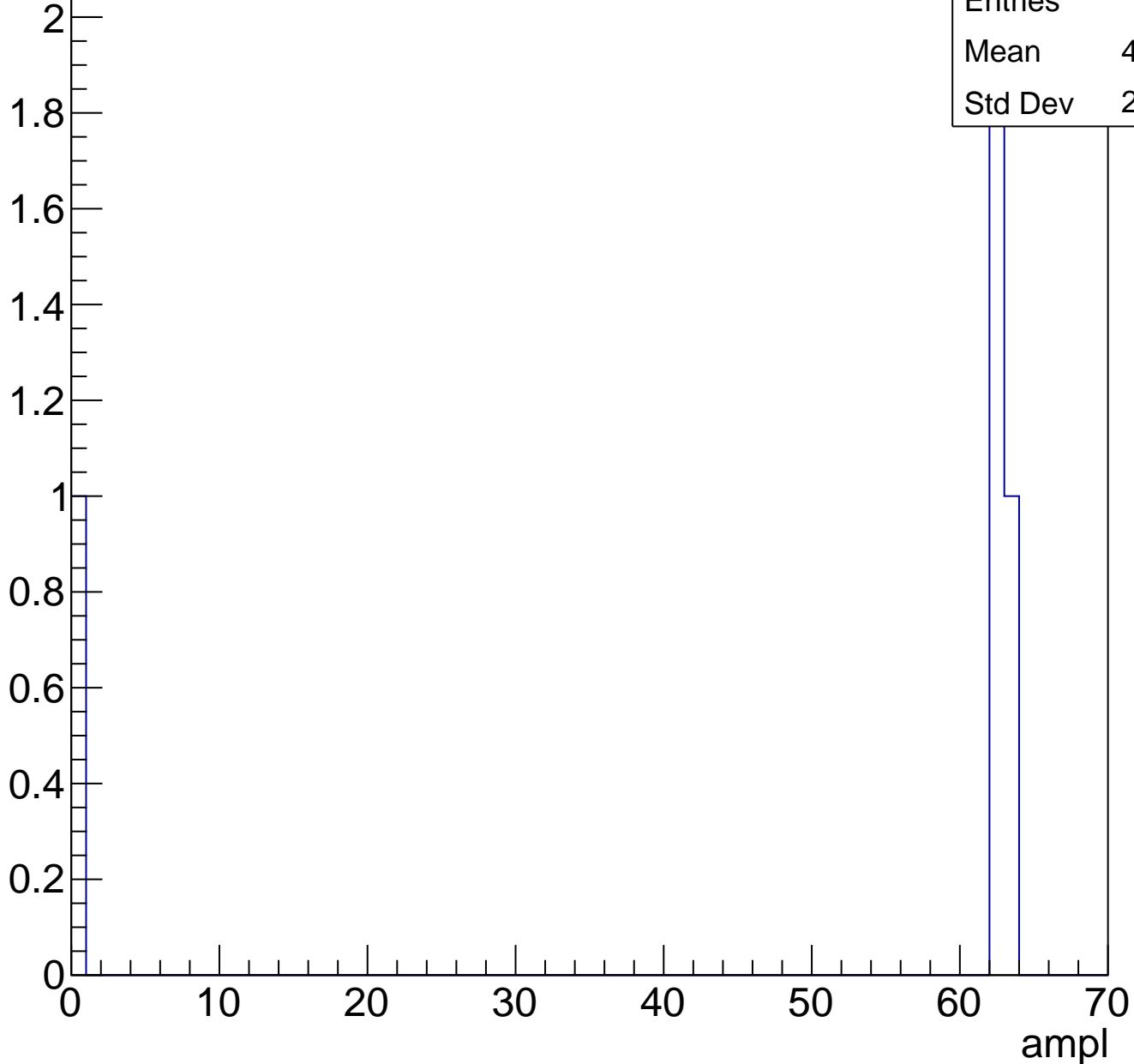
Entry



B1L101S, U21-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch75, adc0

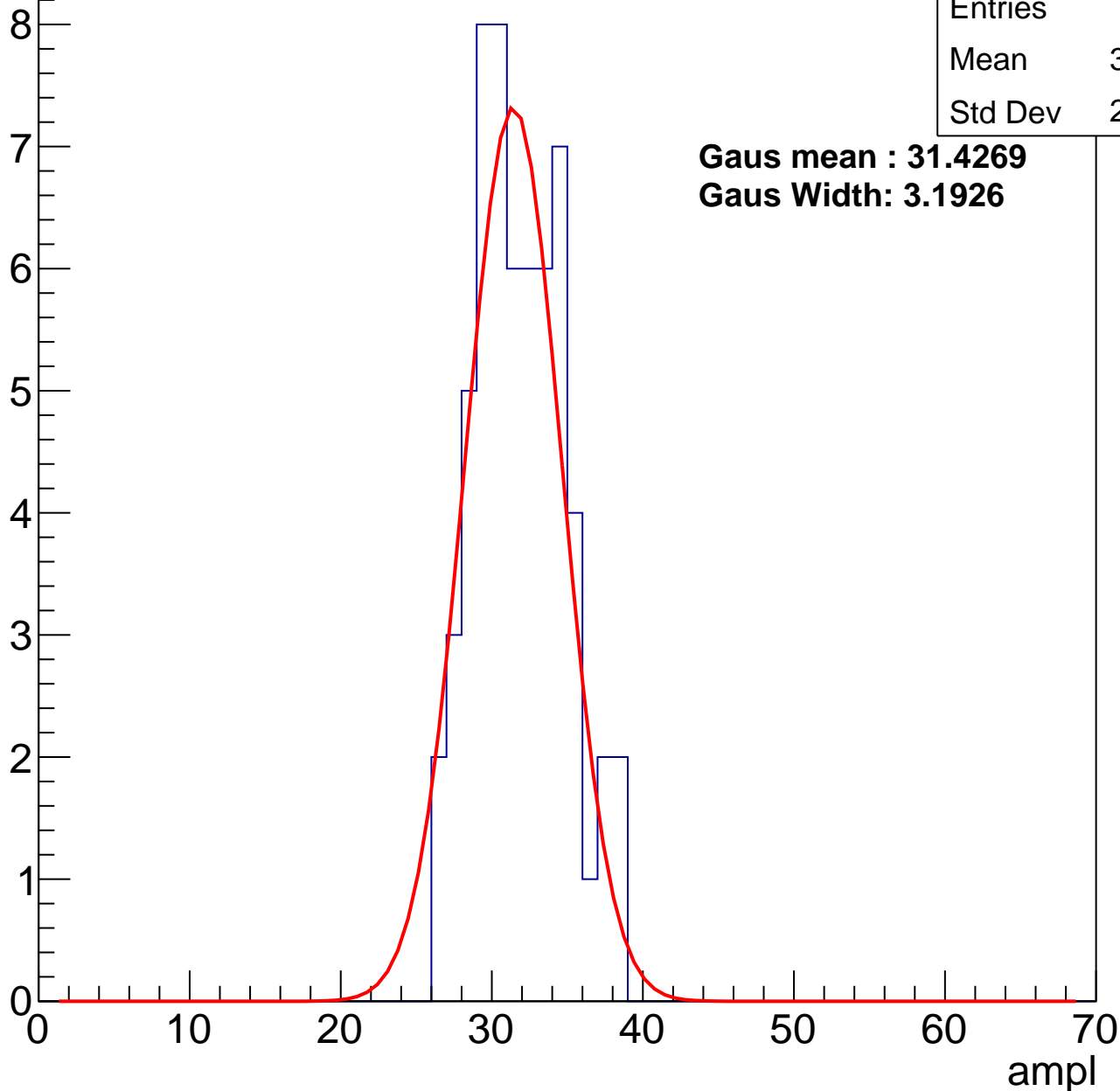
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	31.42
Std Dev	2.957

Gaus mean : 31.4269

Gaus Width: 3.1926



B1L101S, U21-ch75, adc1

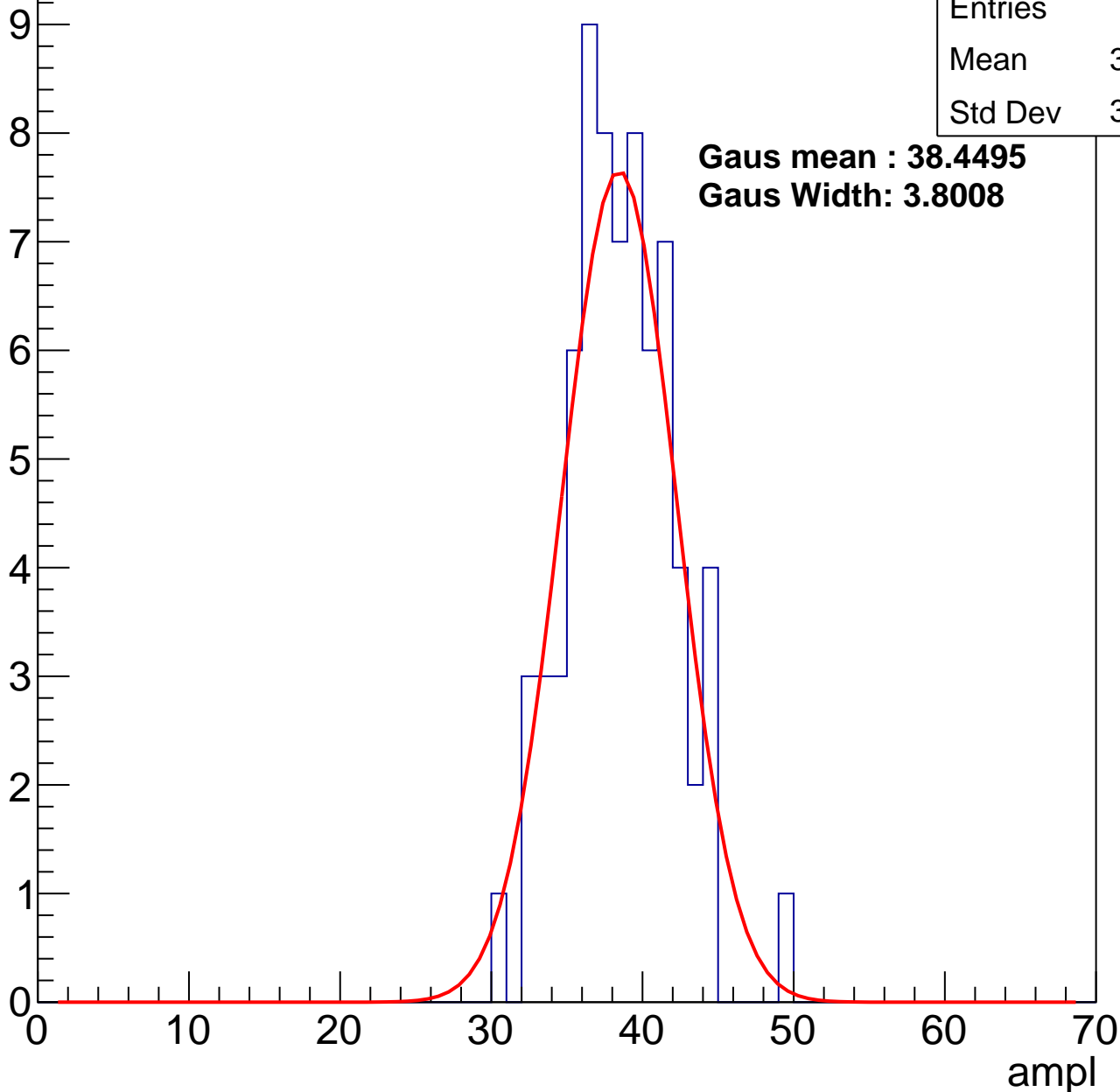
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	38.07
Std Dev	3.469

Gaus mean : 38.4495

Gaus Width: 3.8008



B1L101S, U21-ch75, adc2

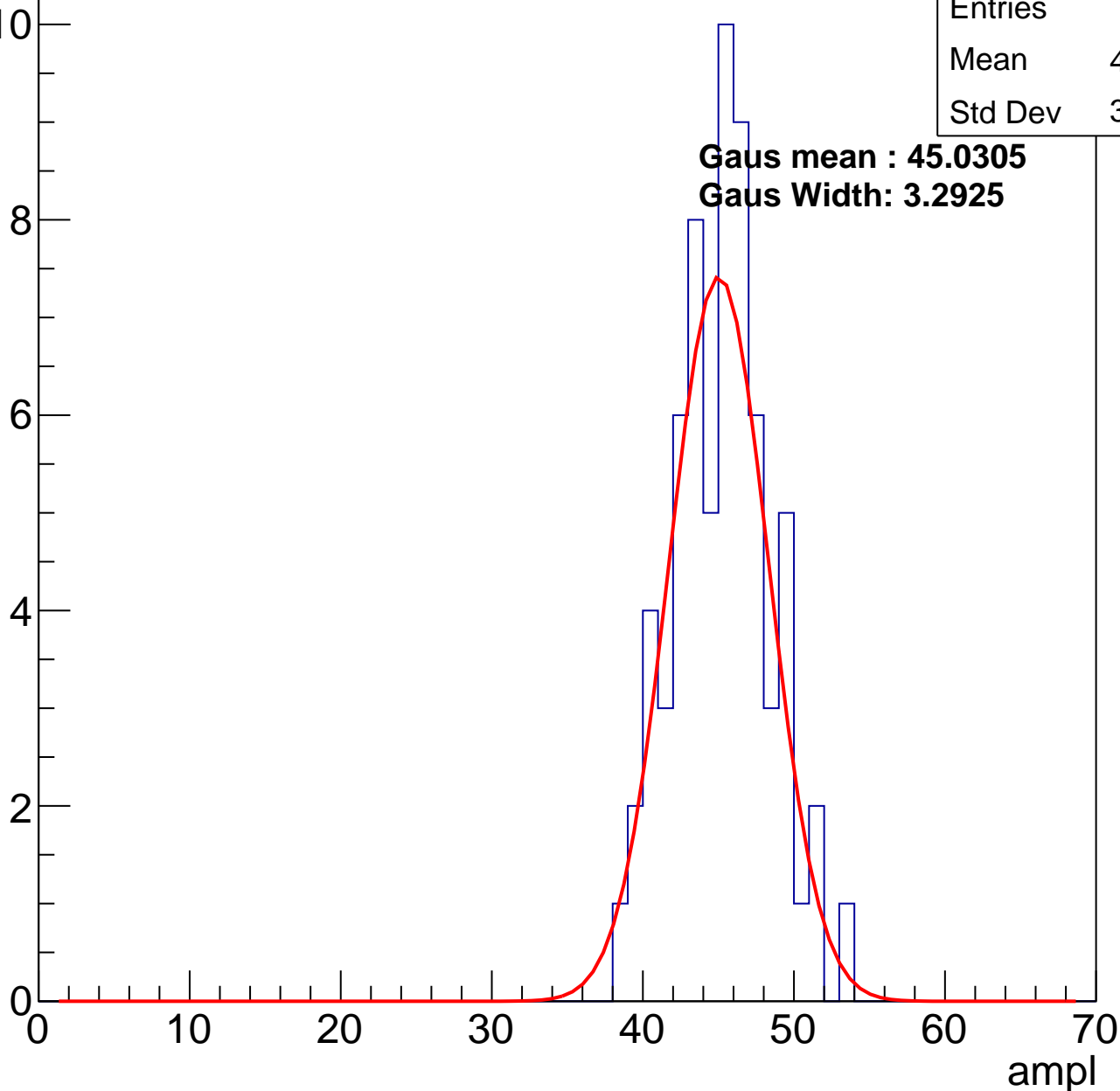
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.77
Std Dev	3.157

Gaus mean : 45.0305

Gaus Width: 3.2925

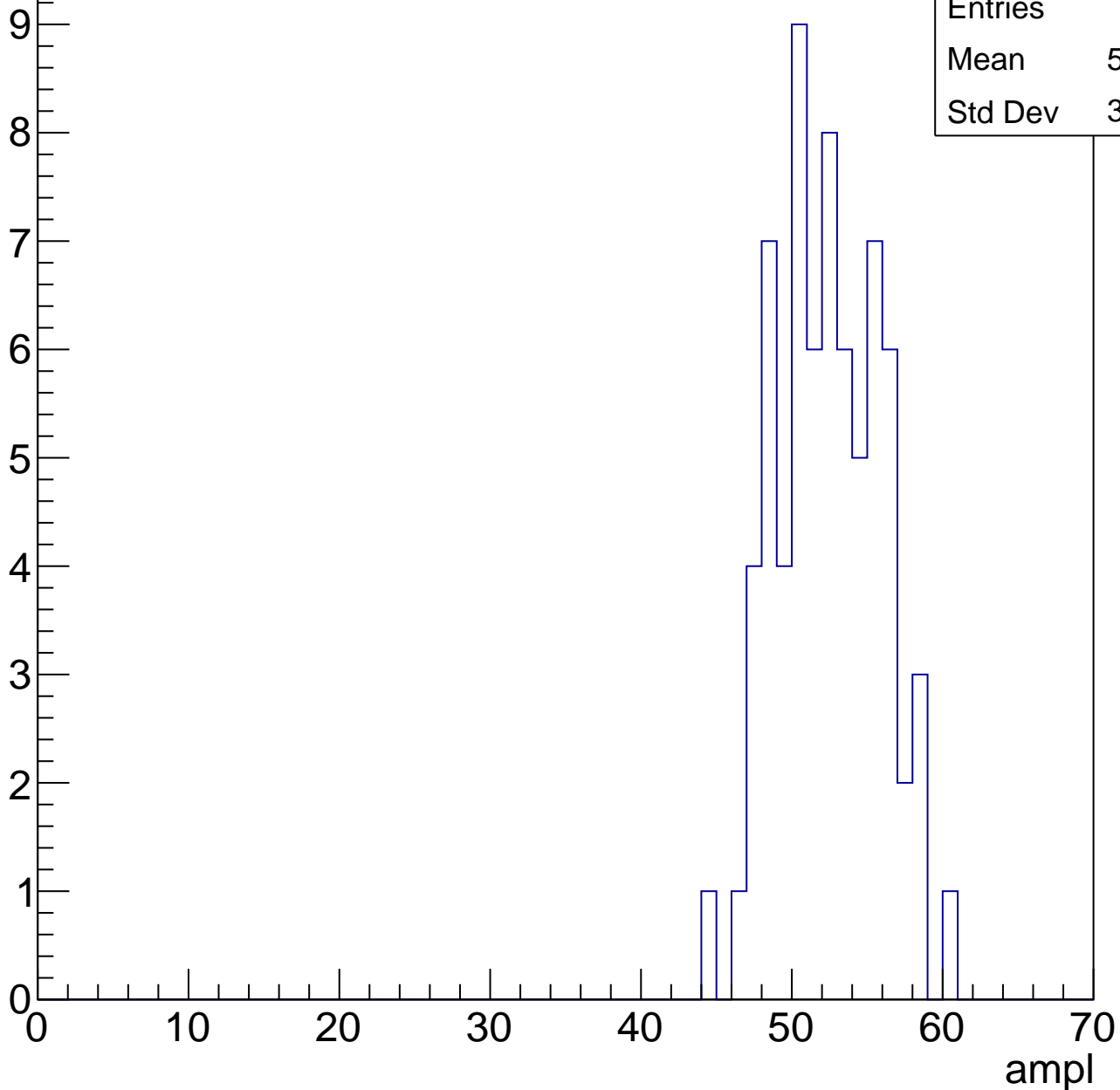


B1L101S, U21-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	51.99
Std Dev	3.374

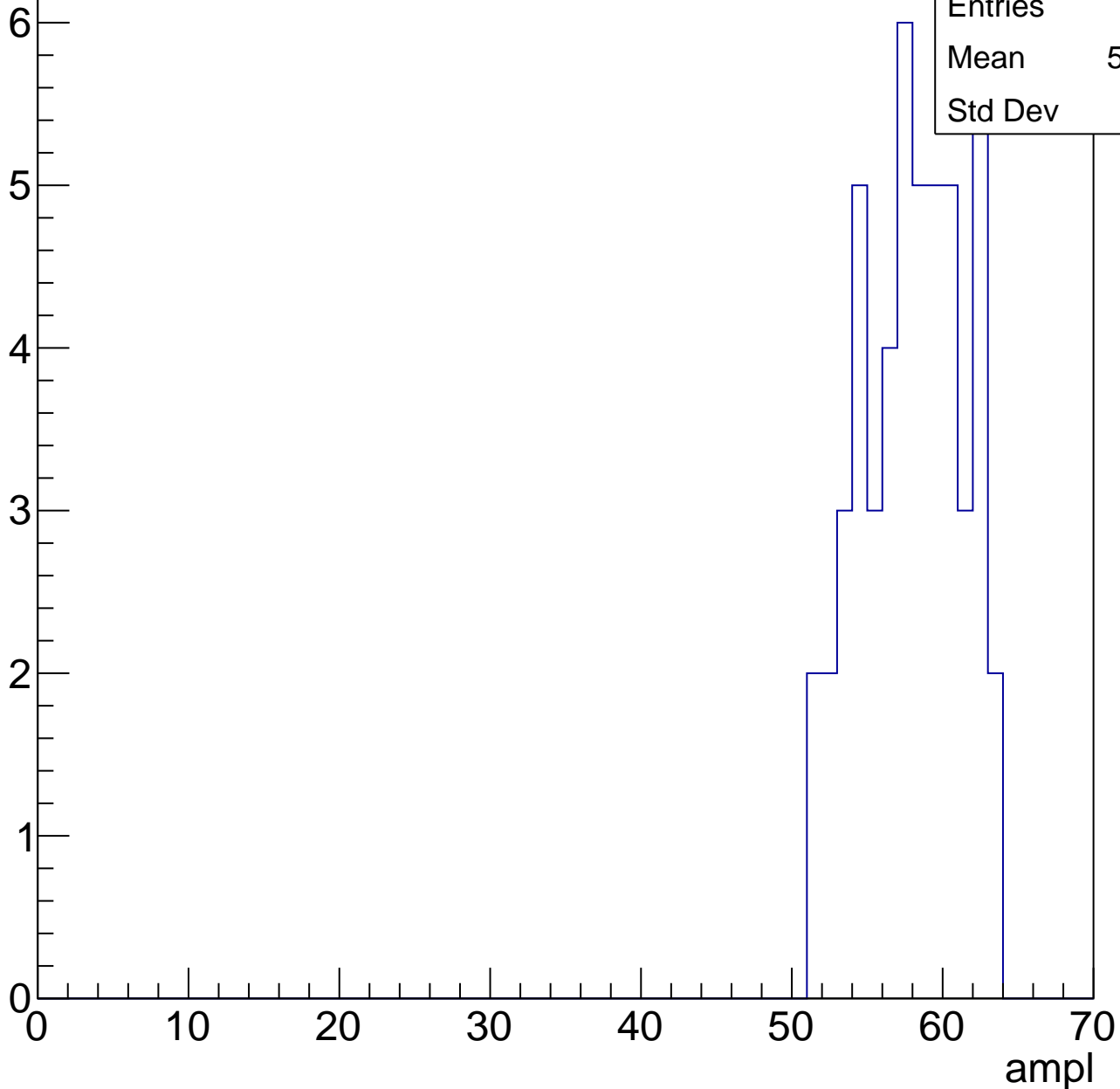


B1L101S, U21-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	57.49
Std Dev	3.31

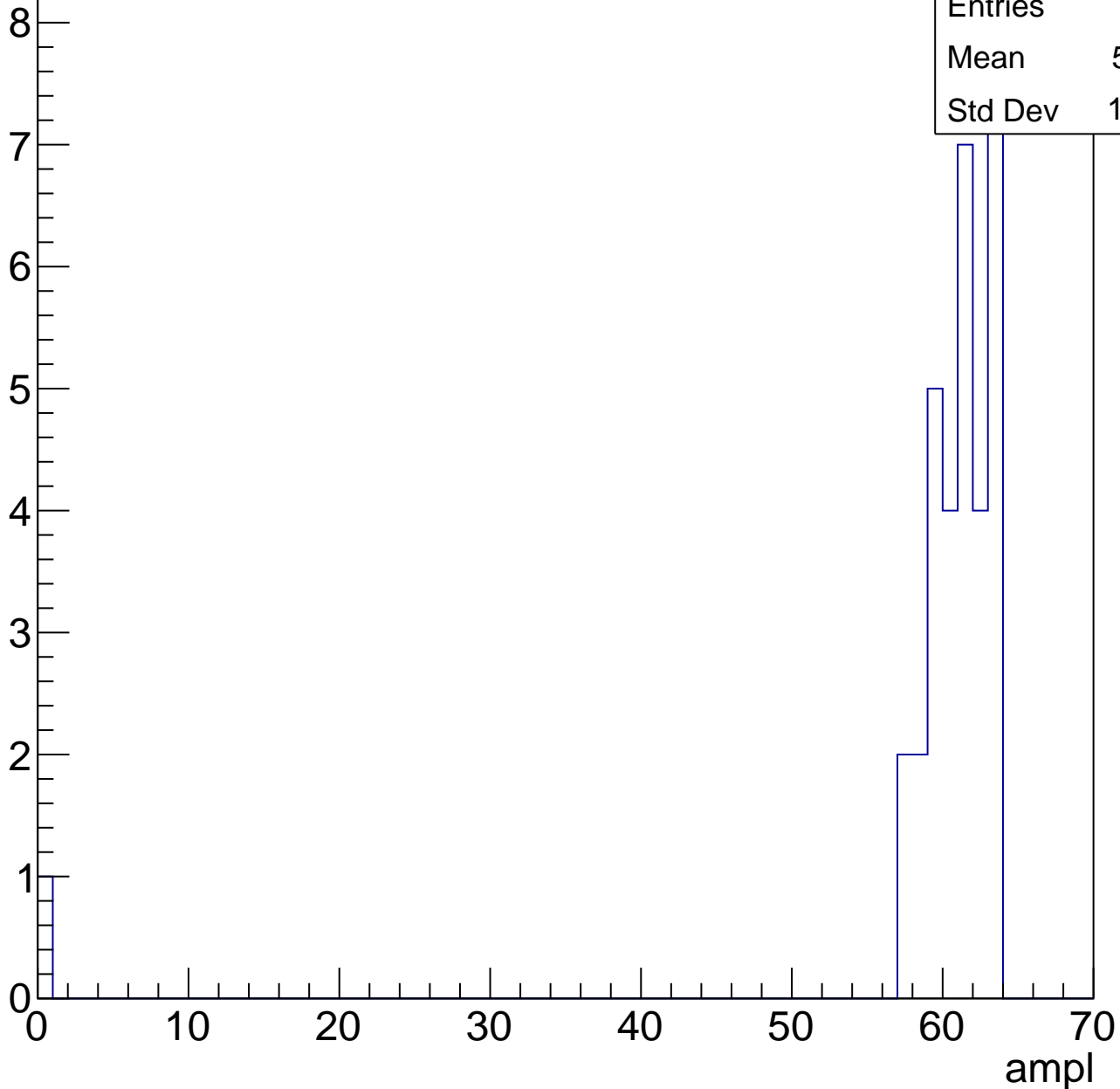


B1L101S, U21-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.91
Std Dev	10.57



B1L101S, U21-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L101S, U21-ch76, adc0

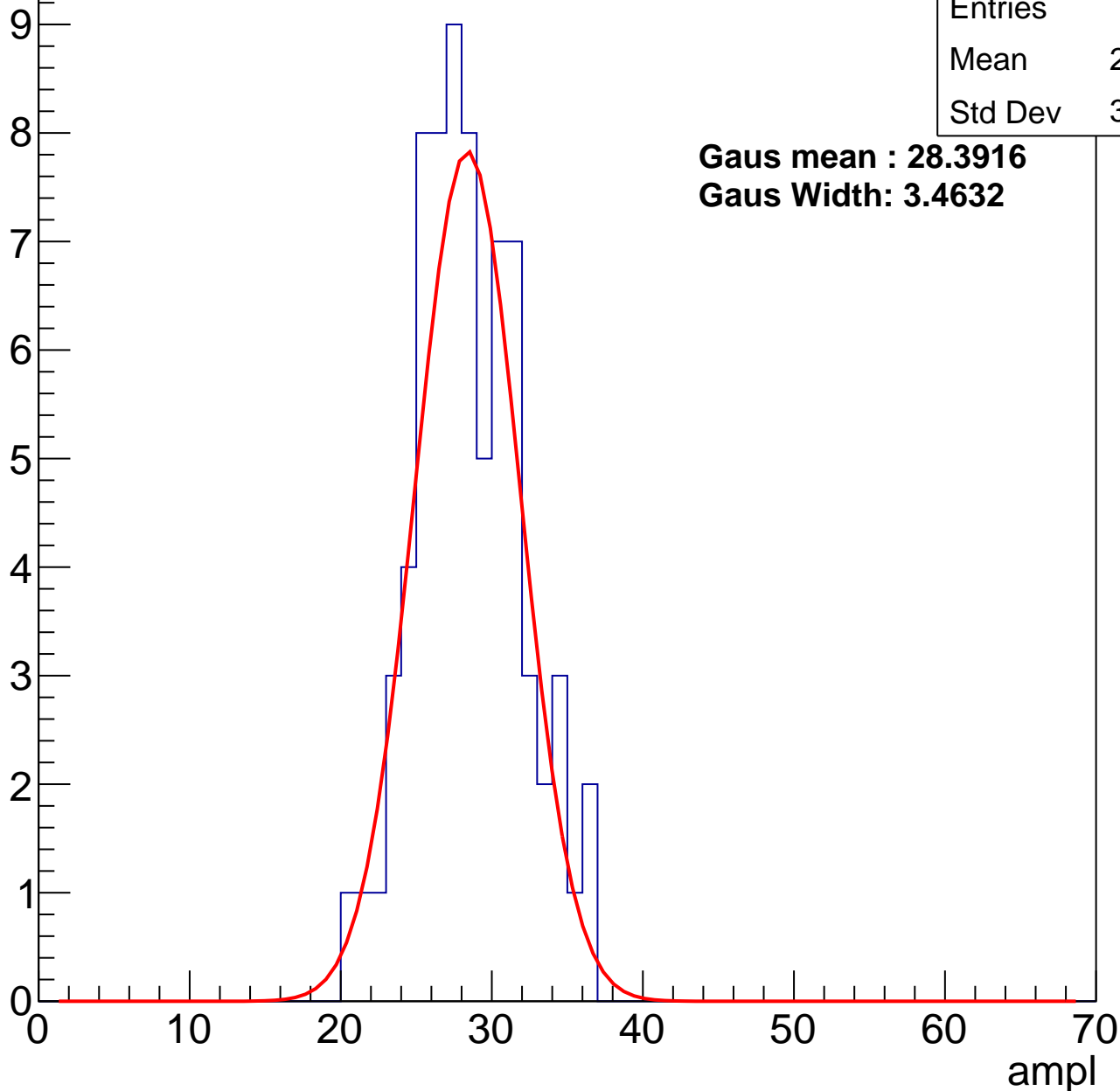
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	28.03
Std Dev	3.472

Gaus mean : 28.3916

Gaus Width: 3.4632



B1L101S, U21-ch76, adc1

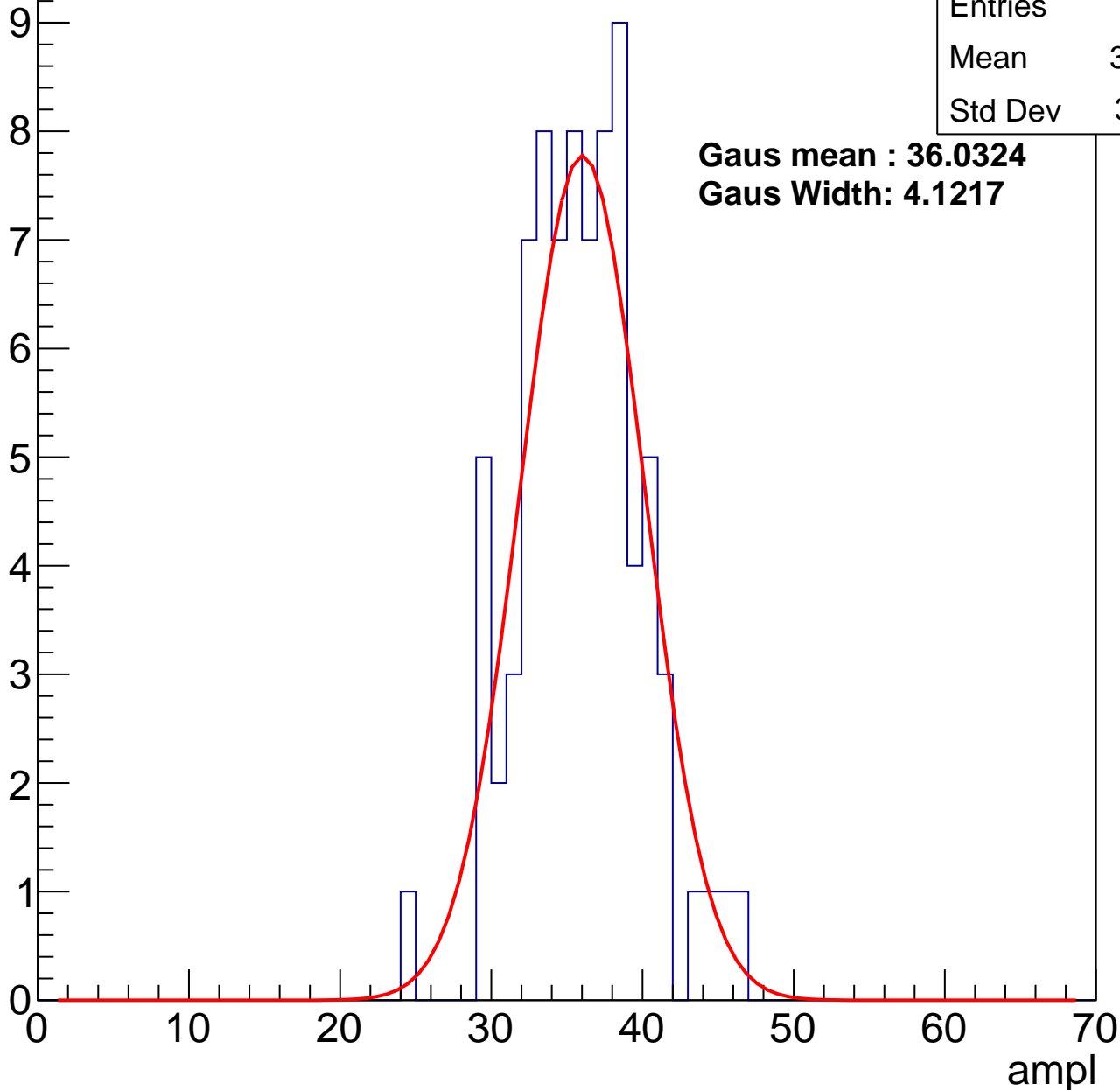
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	35.49
Std Dev	3.951

Gaus mean : 36.0324

Gaus Width: 4.1217



B1L101S, U21-ch76, adc2

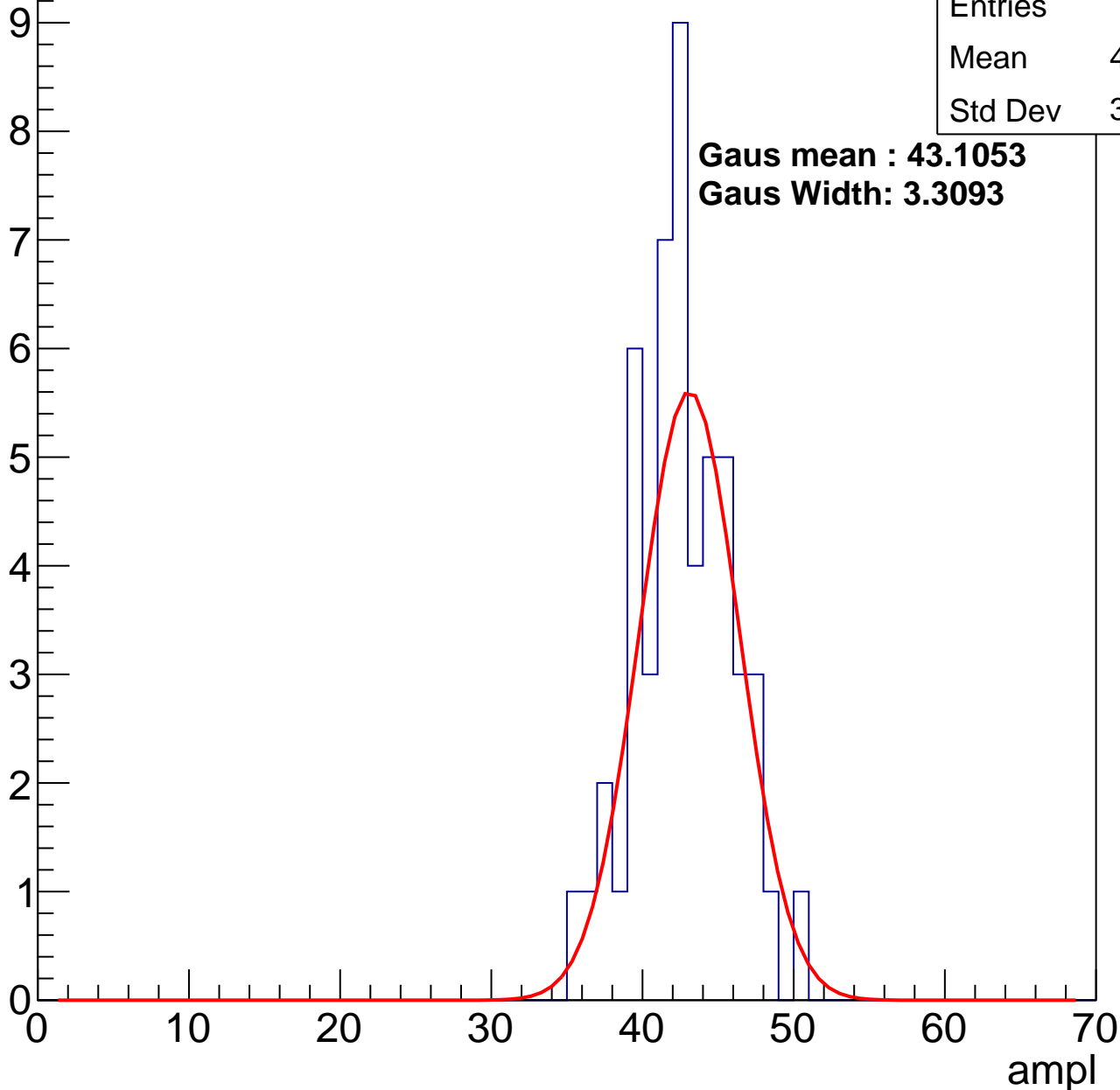
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	42.23
Std Dev	3.142

Gaus mean : 43.1053

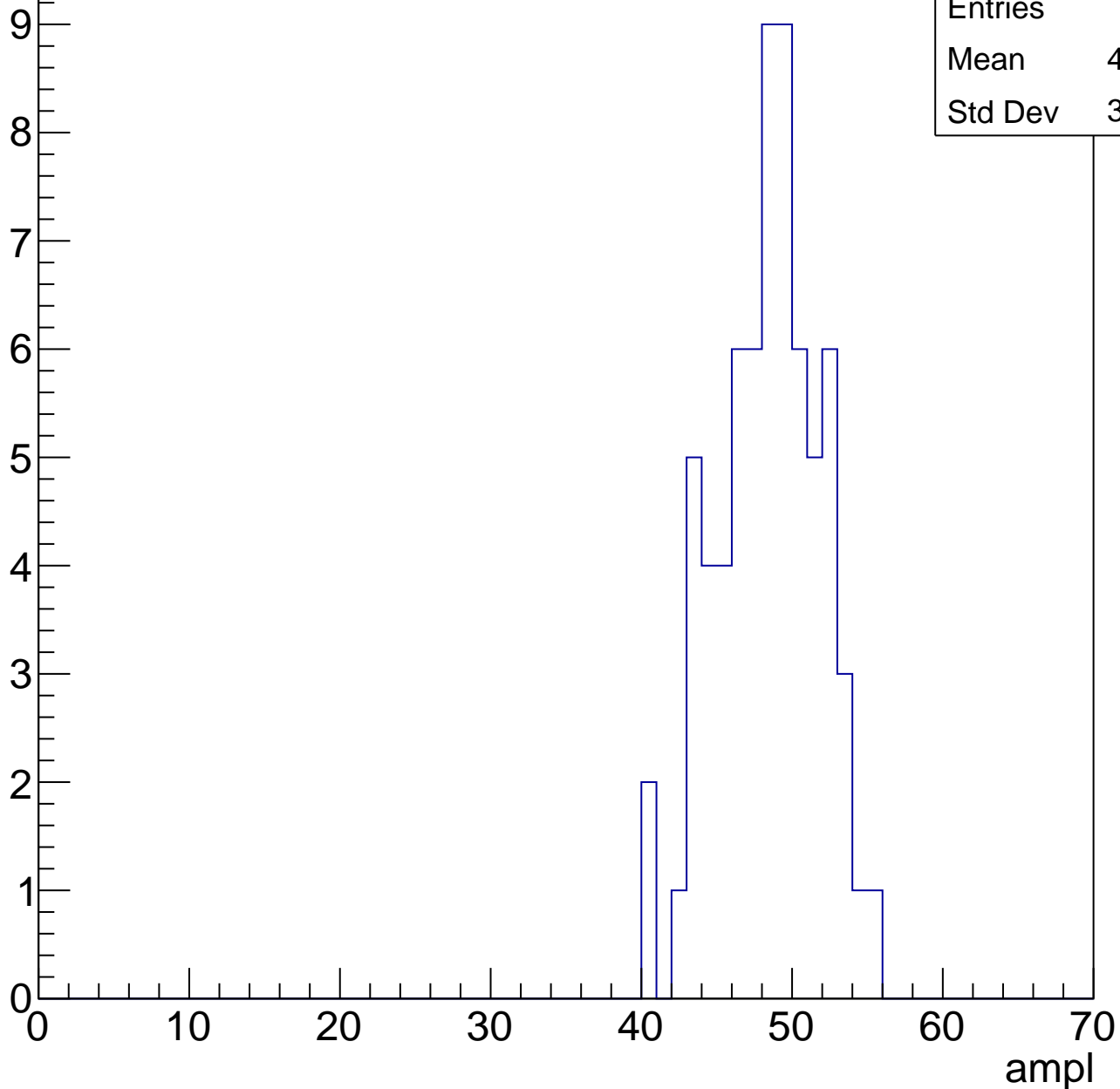
Gaus Width: 3.3093



B1L101S, U21-ch76, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



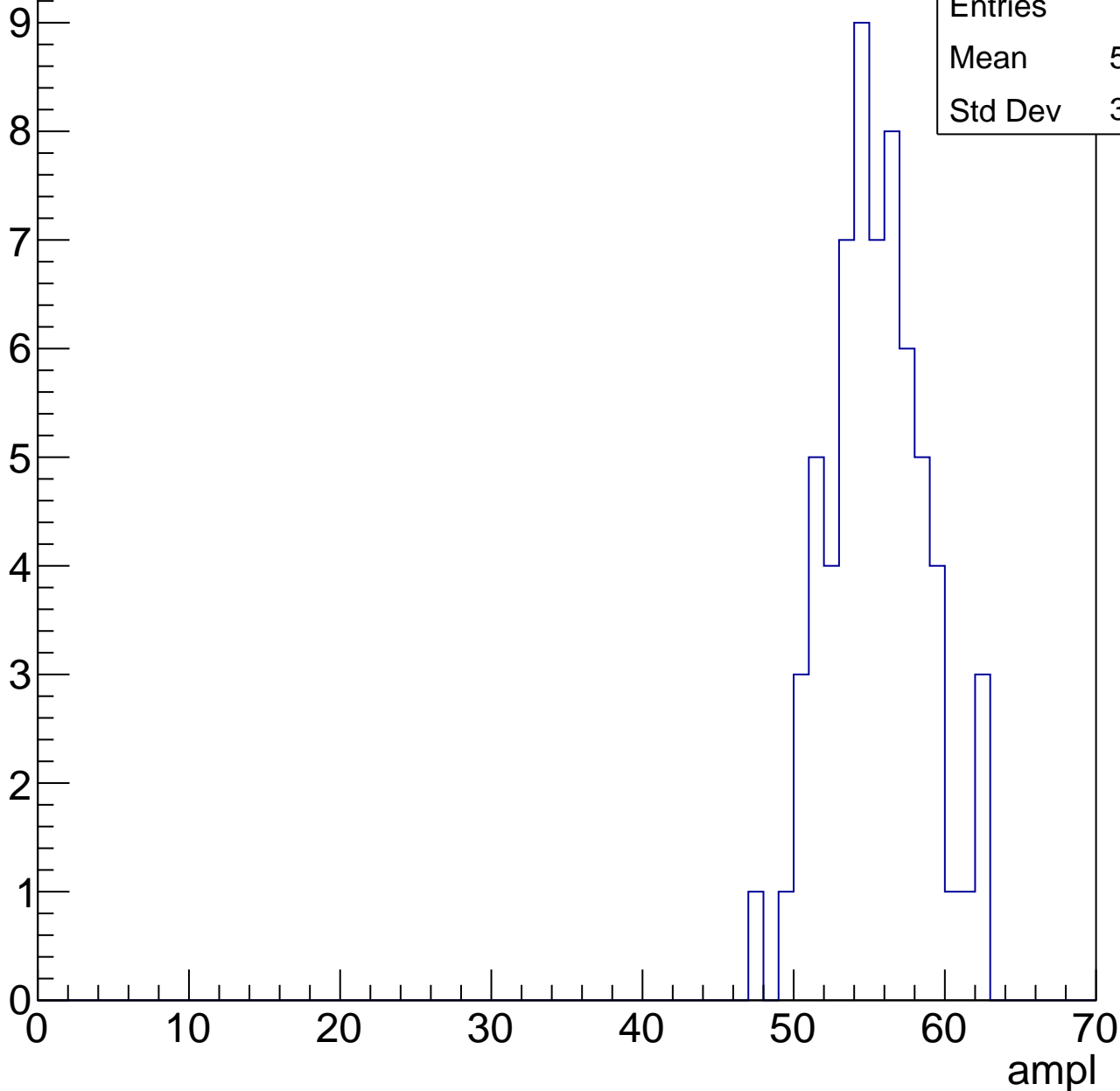
Entries	68
Mean	47.93
Std Dev	3.327

B1L101S, U21-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.98
Std Dev	3.227



B1L101S, U21-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

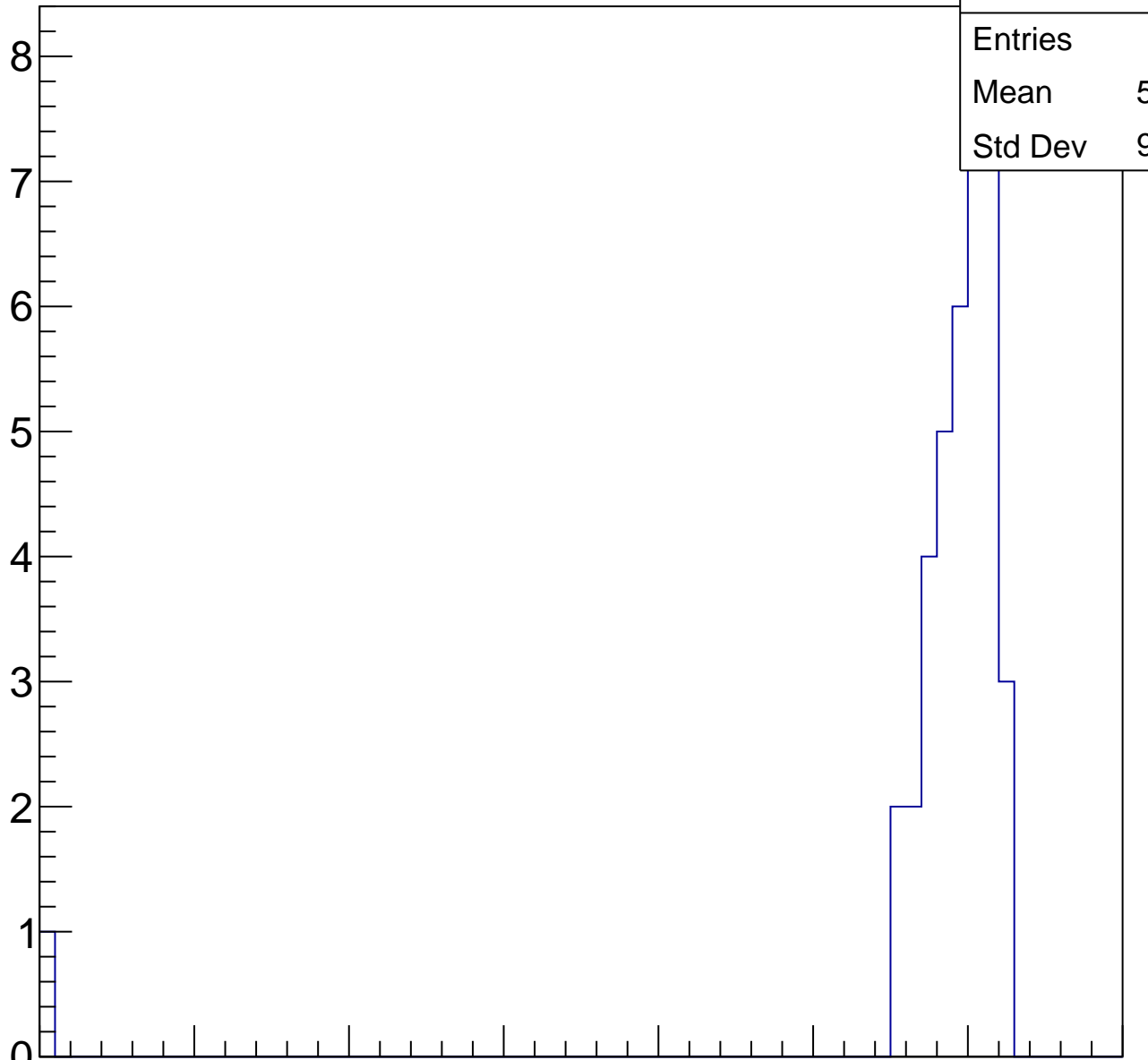
Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	57.64
Std Dev	9.537

ampl

0 10 20 30 40 50 60 70

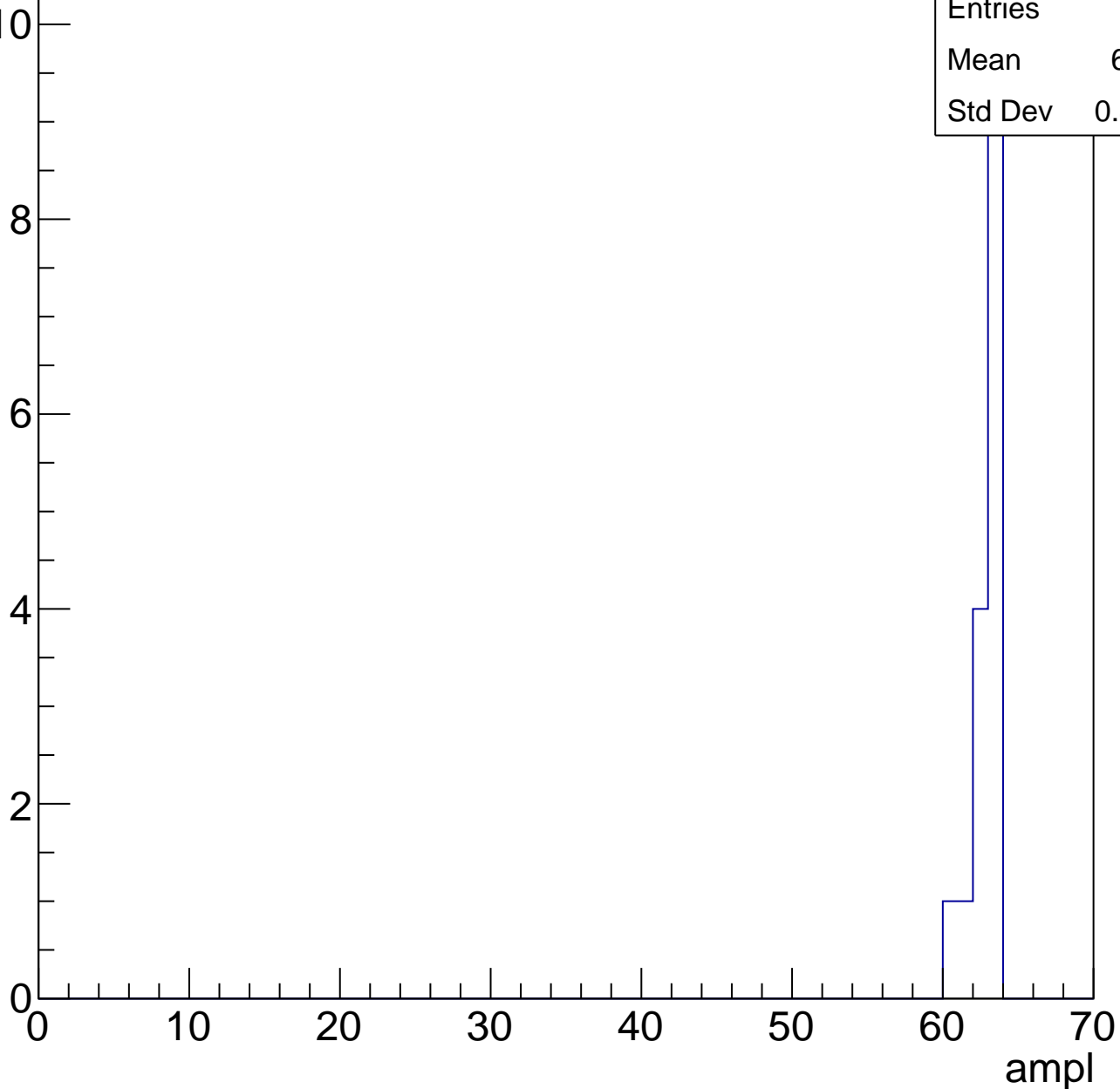


B1L101S, U21-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	16
Mean	62.44
Std Dev	0.8638



B1L101S, U21-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch77, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	29.64
Std Dev	3.482

Gaus mean : 30.3920

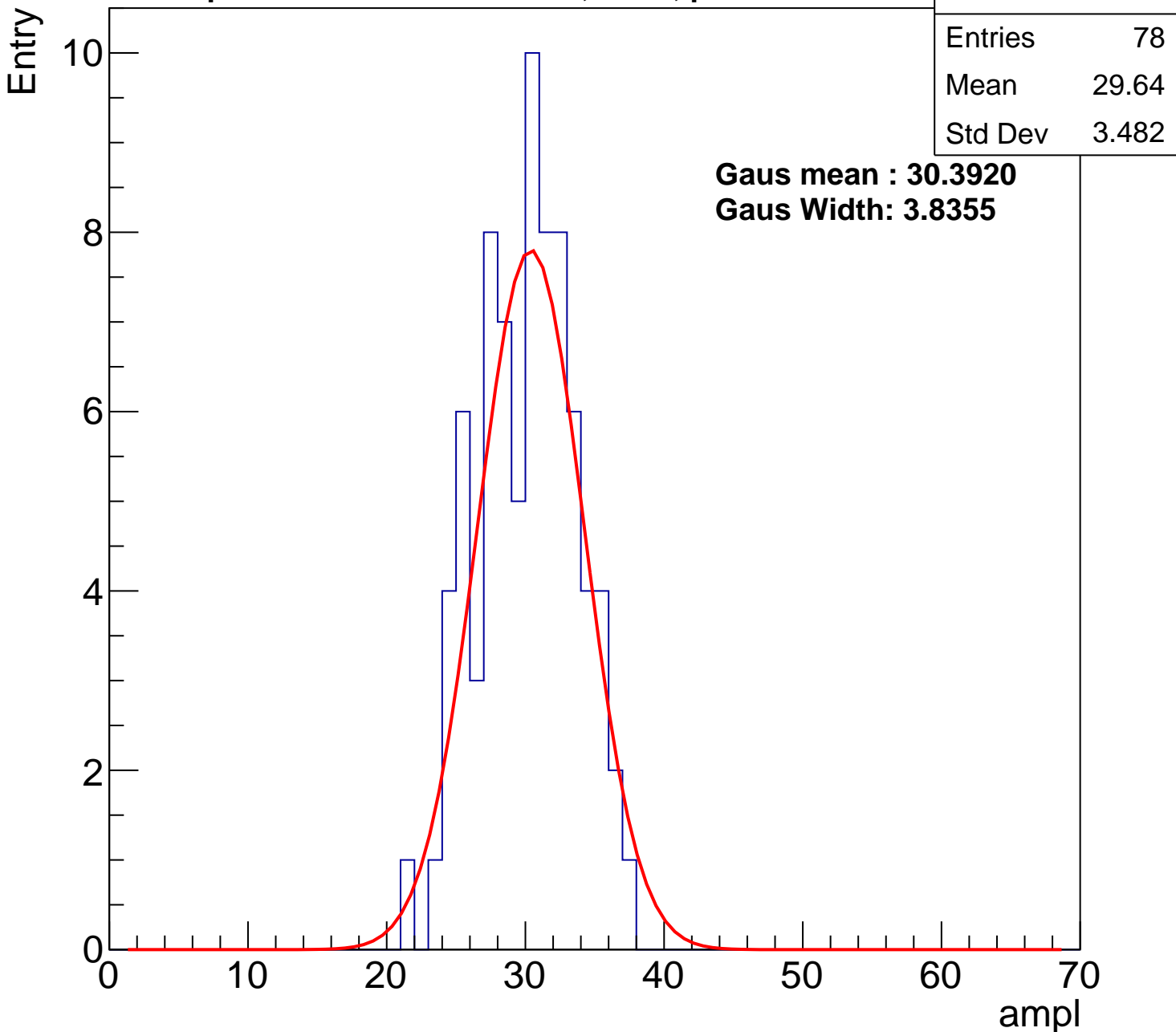
Gaus Width: 3.8355

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch77, adc1

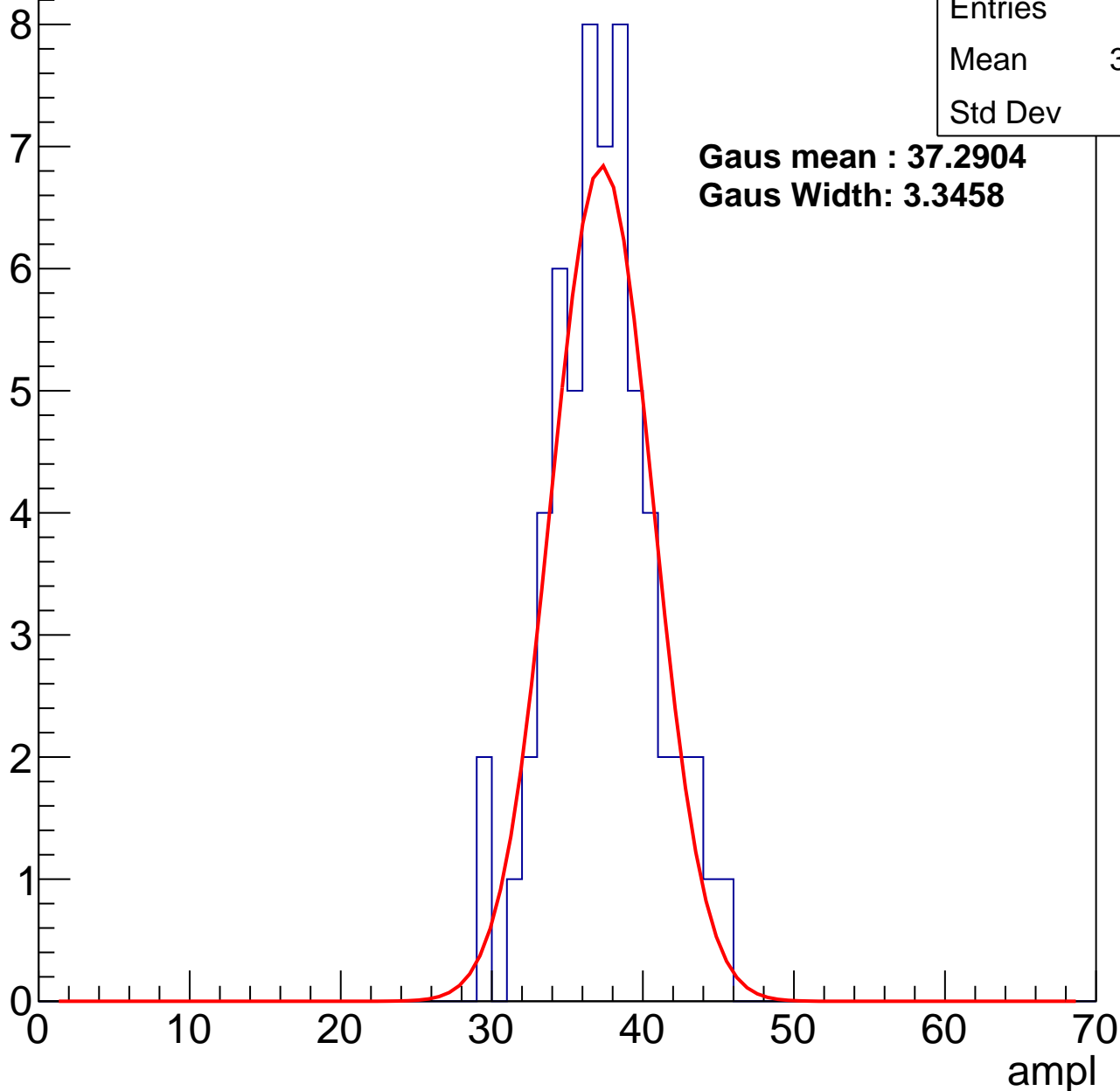
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.85
Std Dev	3.39

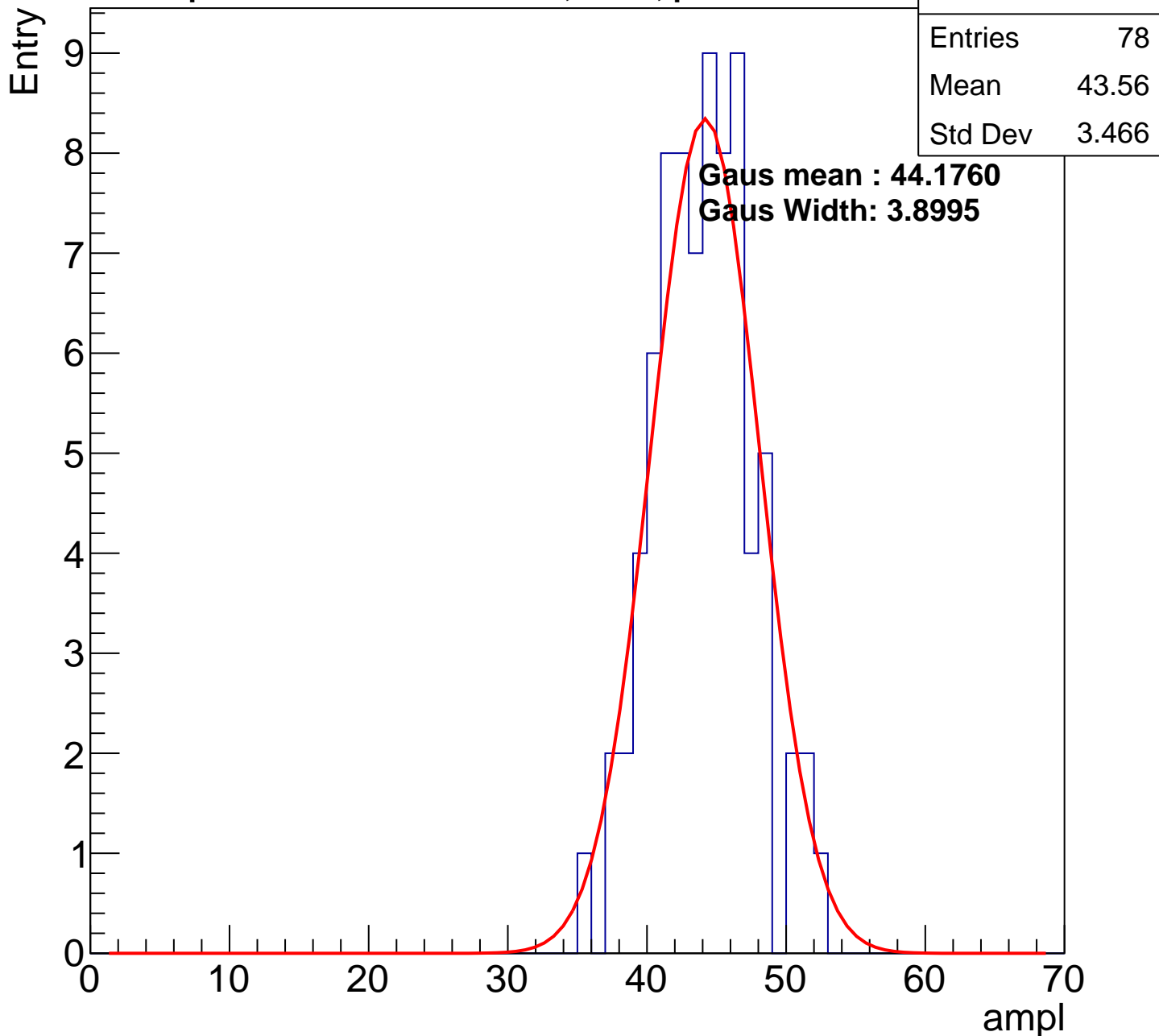
Gaus mean : 37.2904

Gaus Width: 3.3458



B1L101S, U21-ch77, adc2

calib_packv5_042523_0143.root, FC#0, port D2

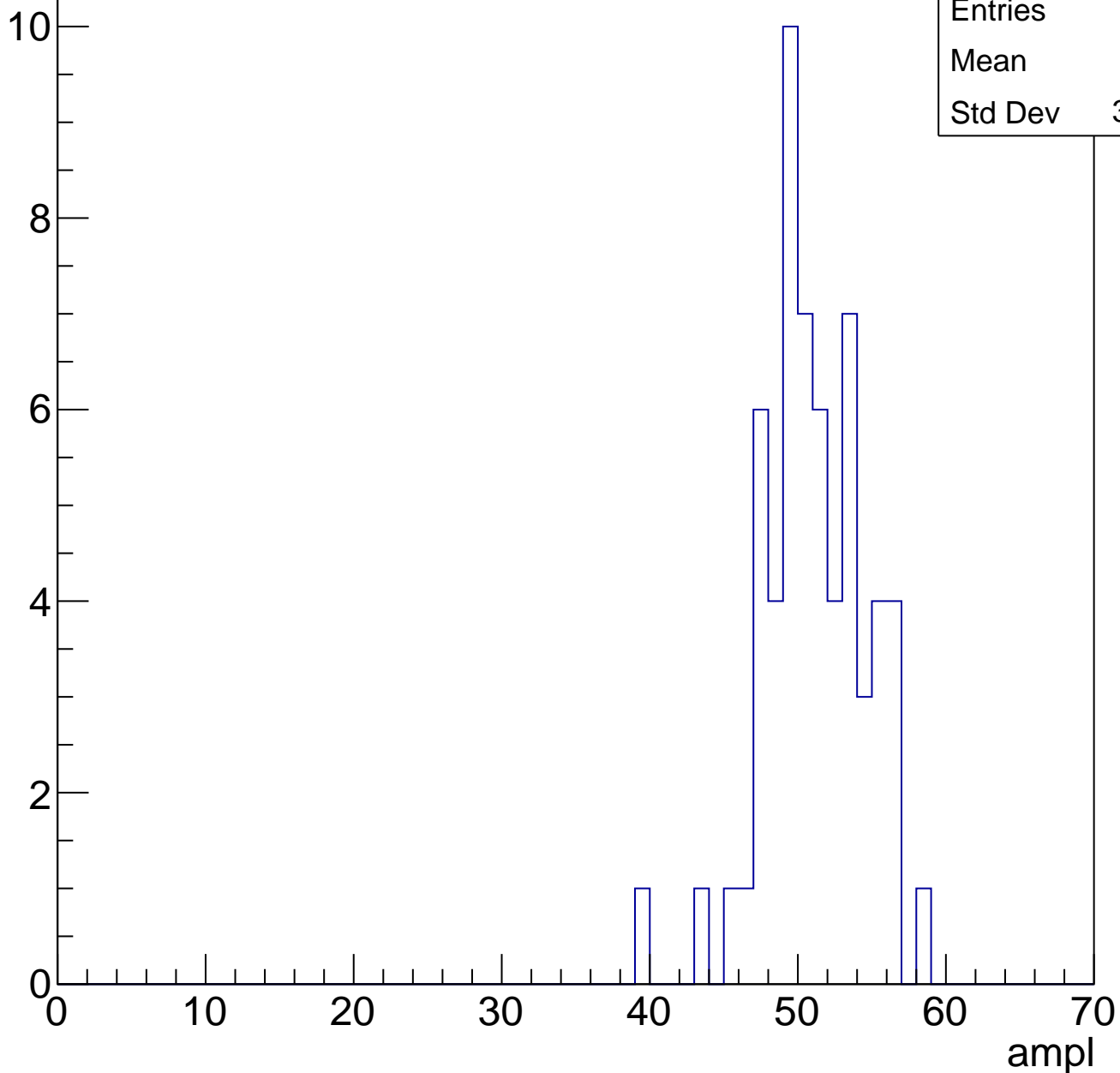


B1L101S, U21-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	50.6
Std Dev	3.441

Entry

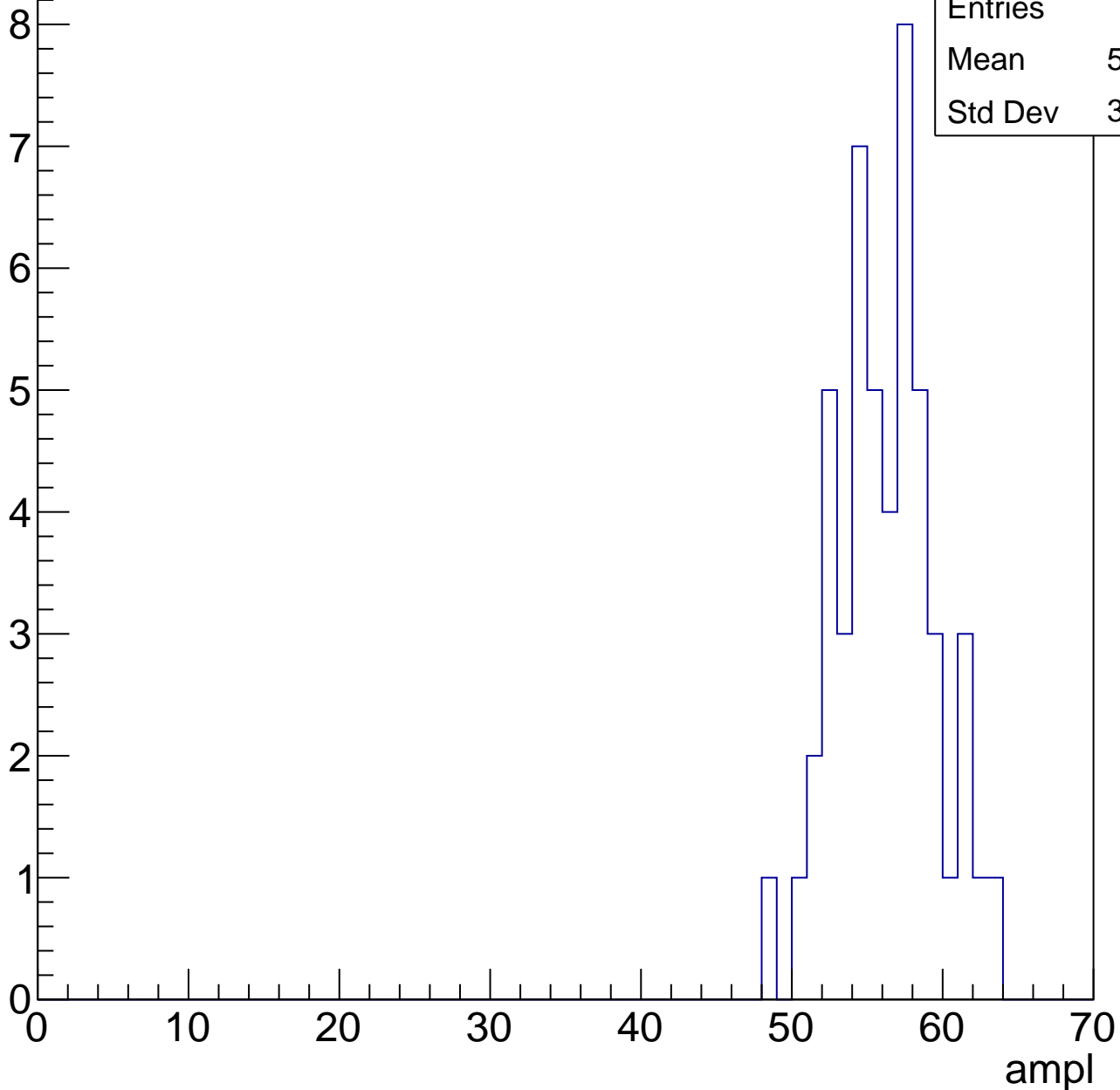


B1L101S, U21-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

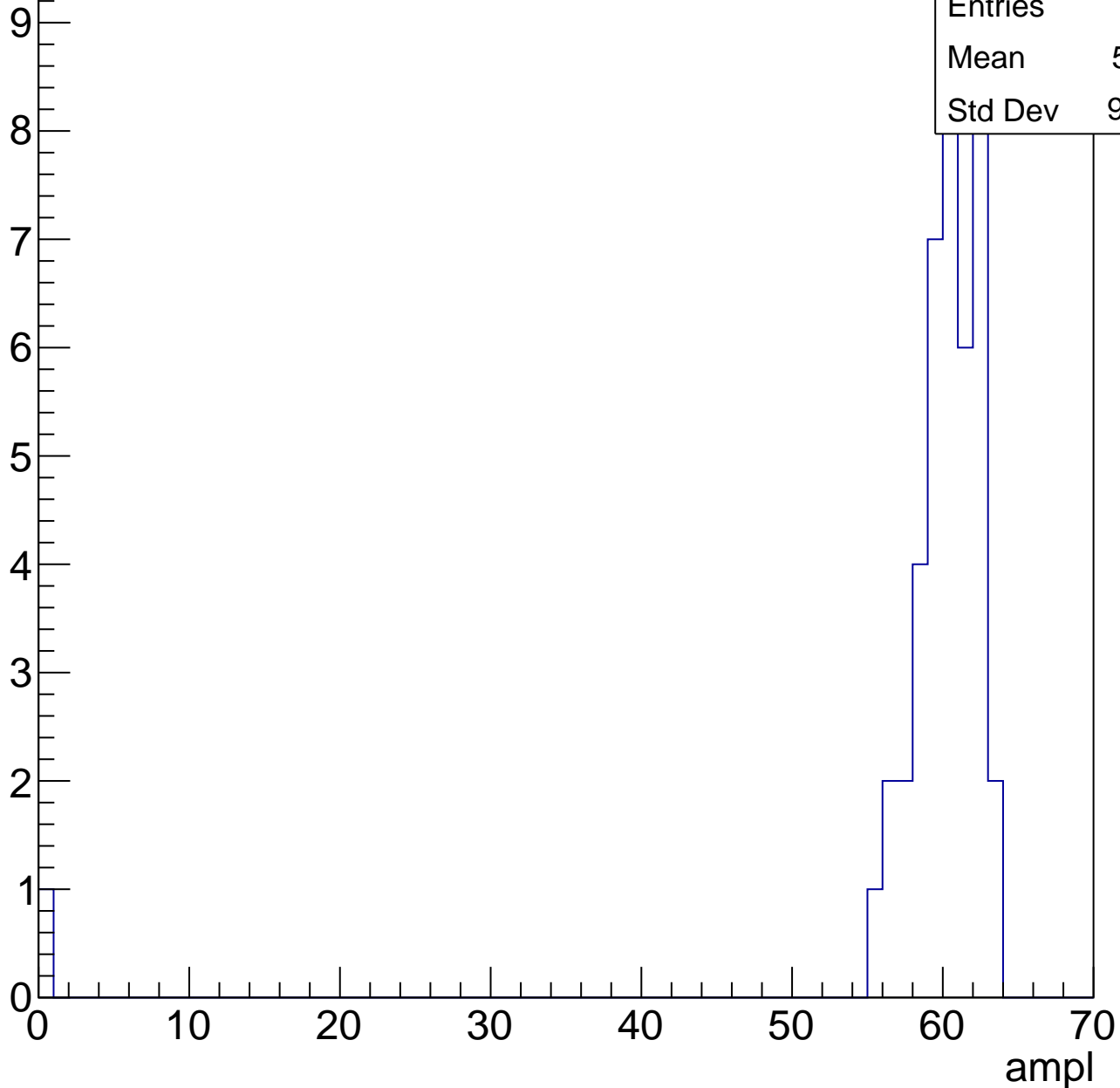
Entries	50
Mean	55.74
Std Dev	3.218



B1L101S, U21-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

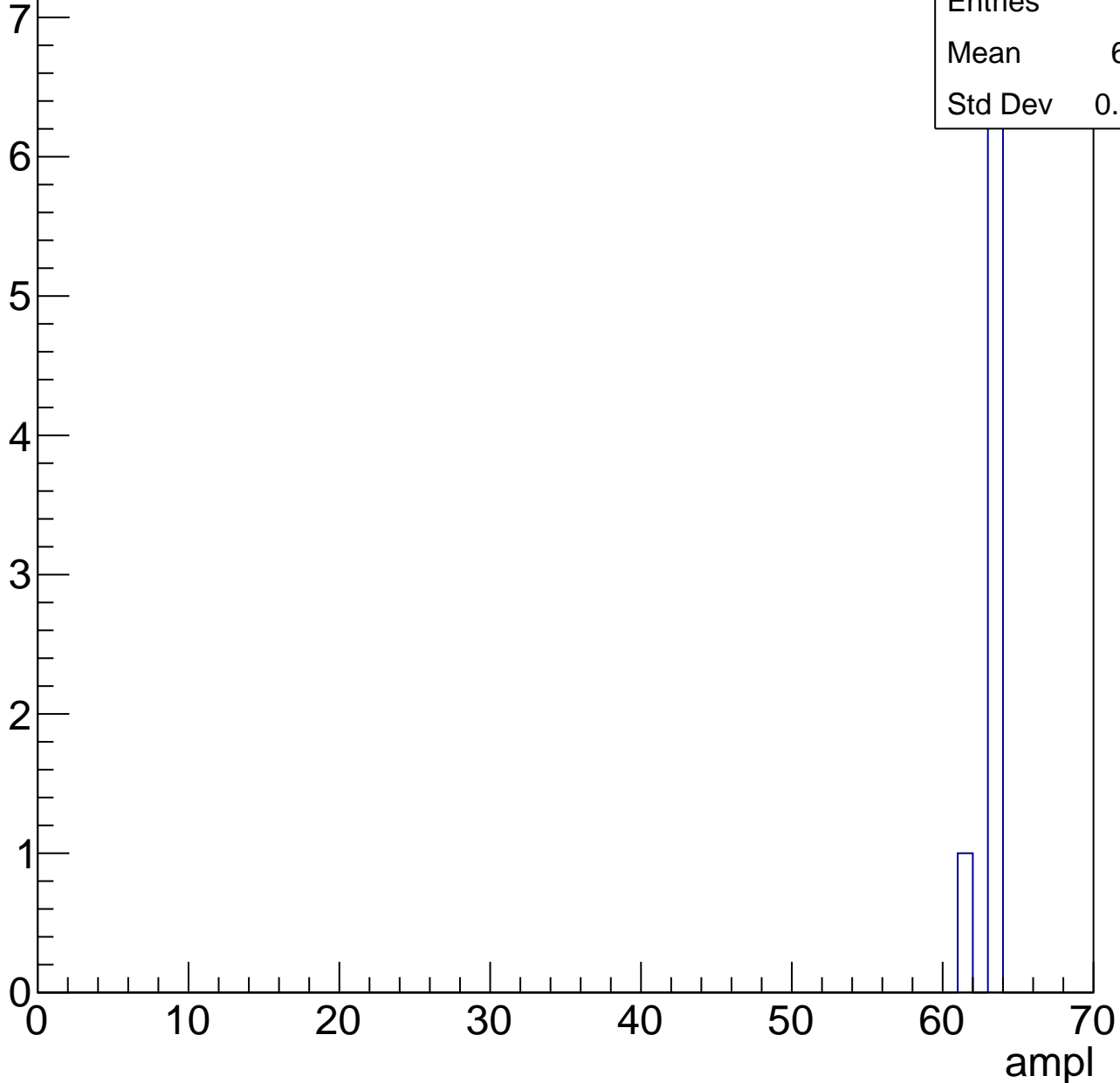


B1L101S, U21-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.75
Std Dev	0.6614



B1L101S, U21-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch78, adc0

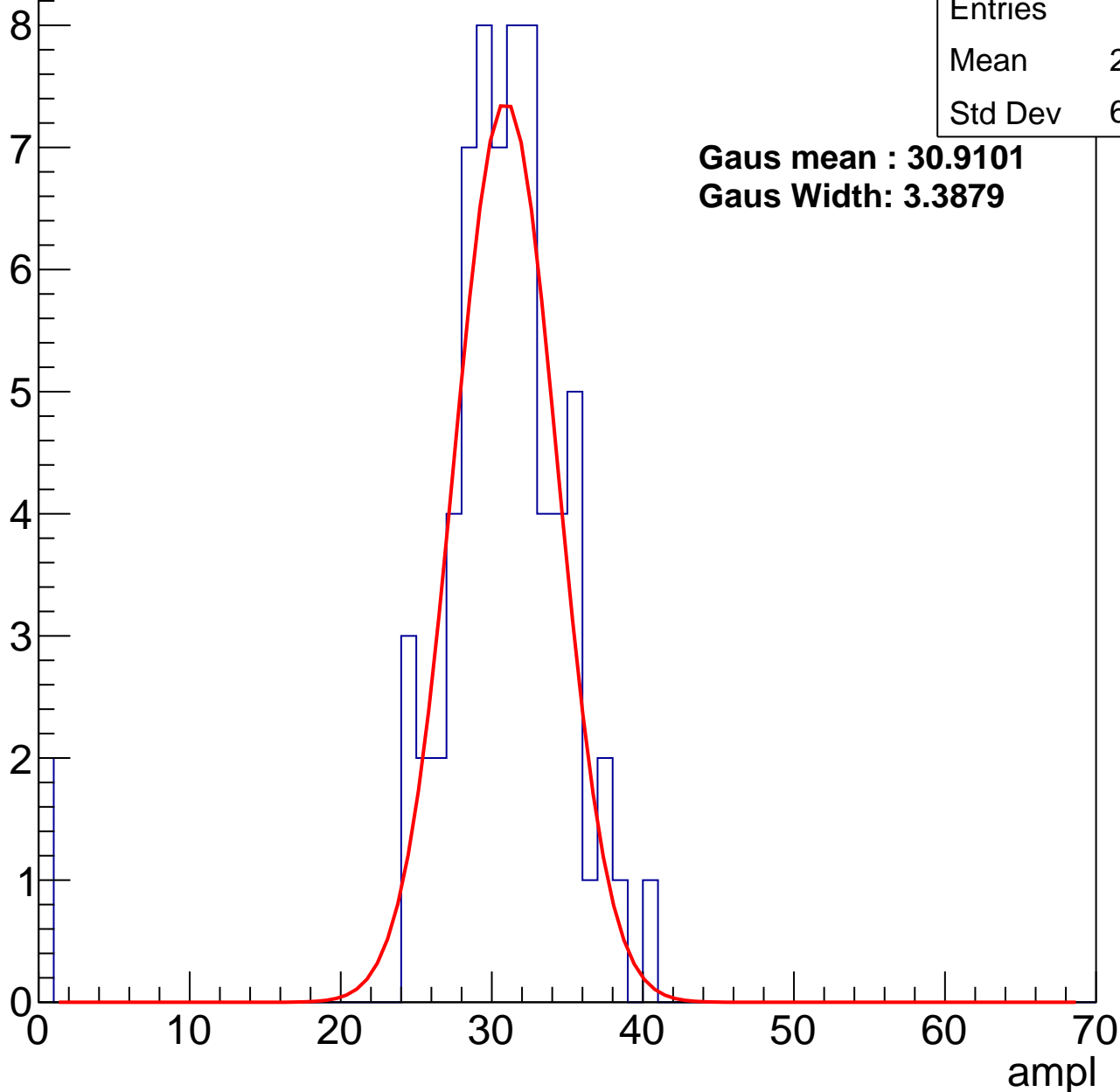
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.78
Std Dev	6.164

Gaus mean : 30.9101

Gaus Width: 3.3879



B1L101S, U21-ch78, adc1

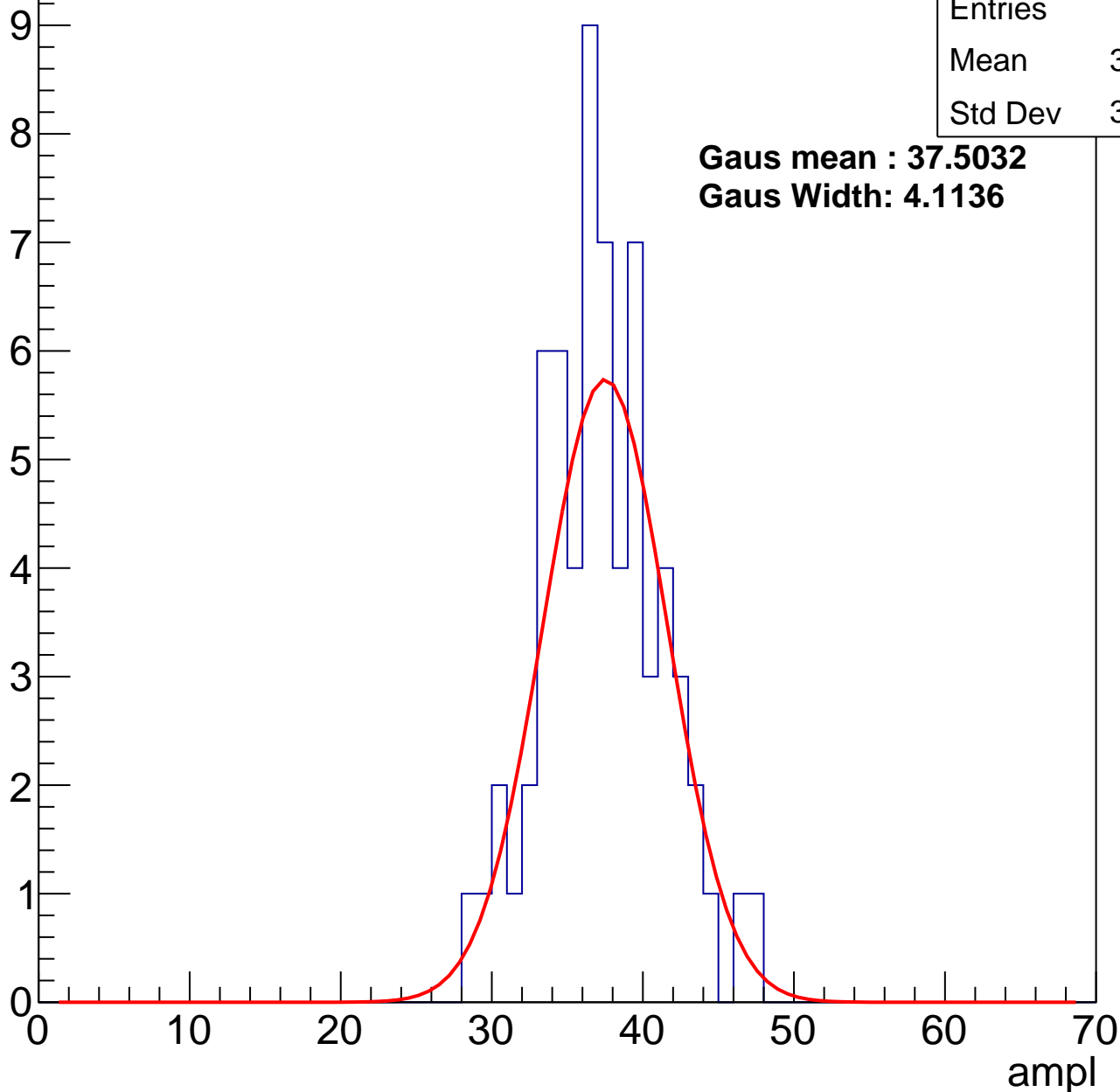
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	36.85
Std Dev	3.927

Gaus mean : 37.5032

Gaus Width: 4.1136



B1L101S, U21-ch78, adc2

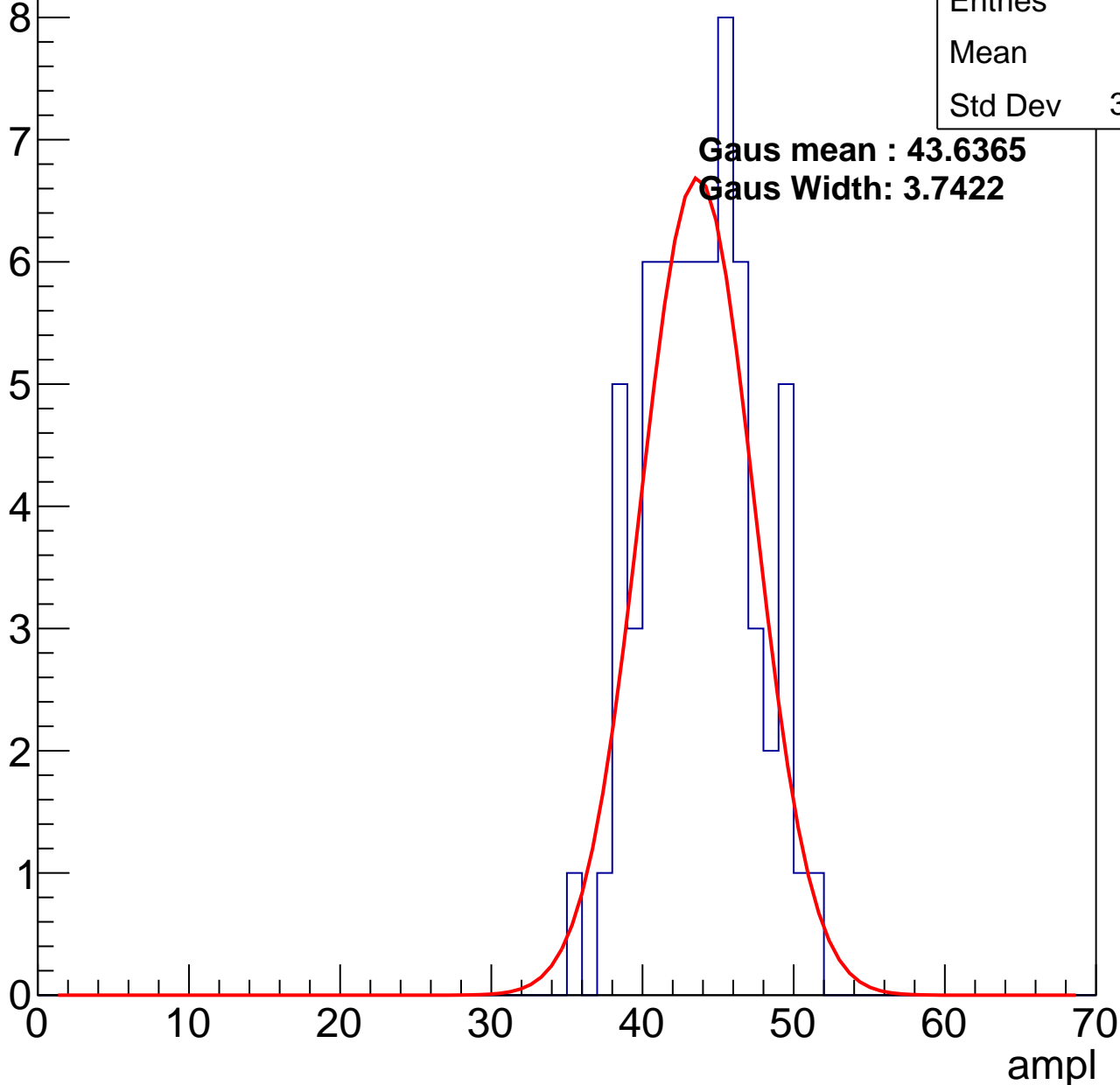
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	43.3
Std Dev	3.555

Gaus mean : 43.6365

Gaus Width: 3.7422

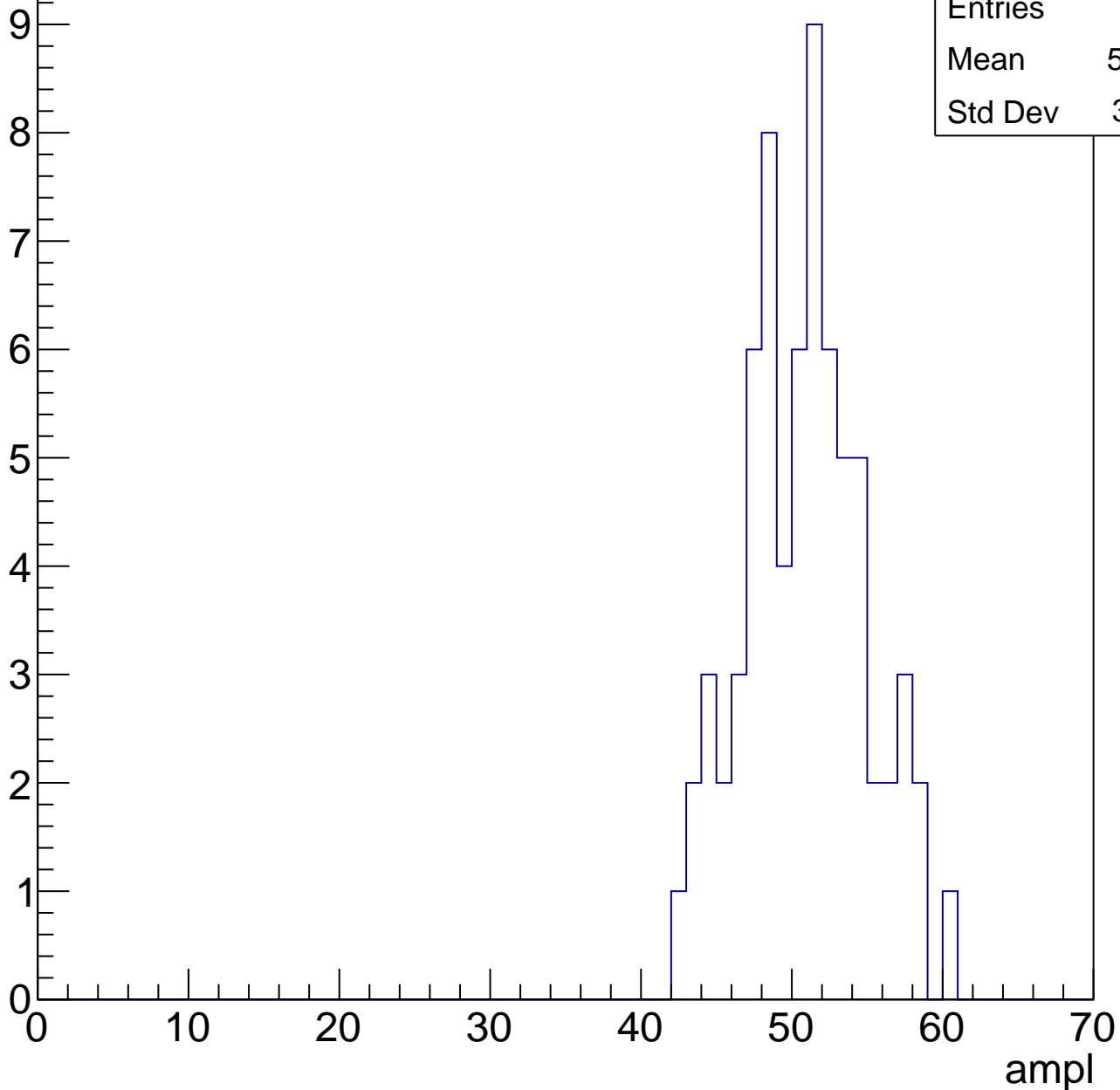


B1L101S, U21-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.36
Std Dev	3.971

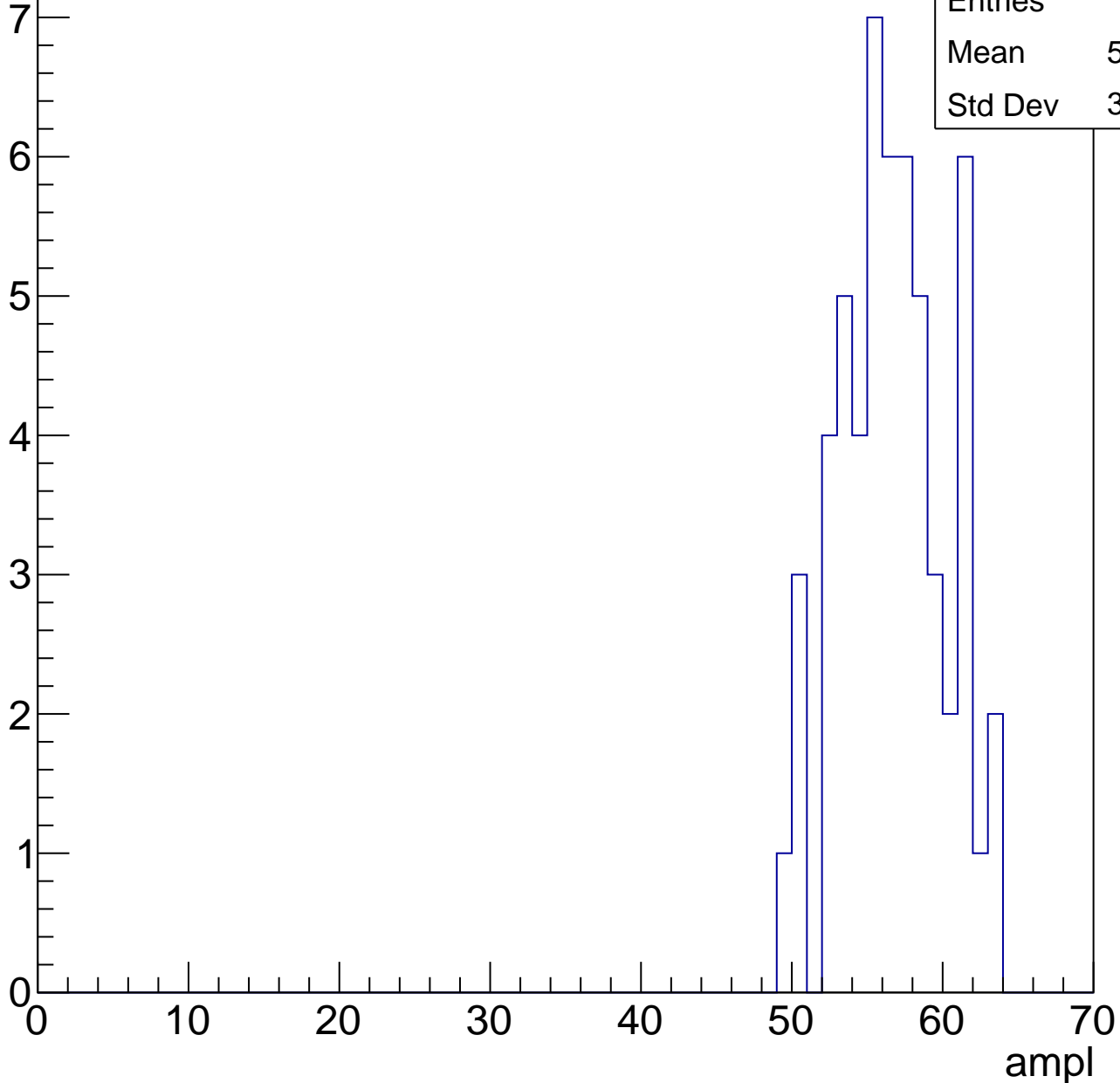


B1L101S, U21-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.22
Std Dev	3.452



B1L101S, U21-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	36
Mean	58.11
Std Dev	10.07

1

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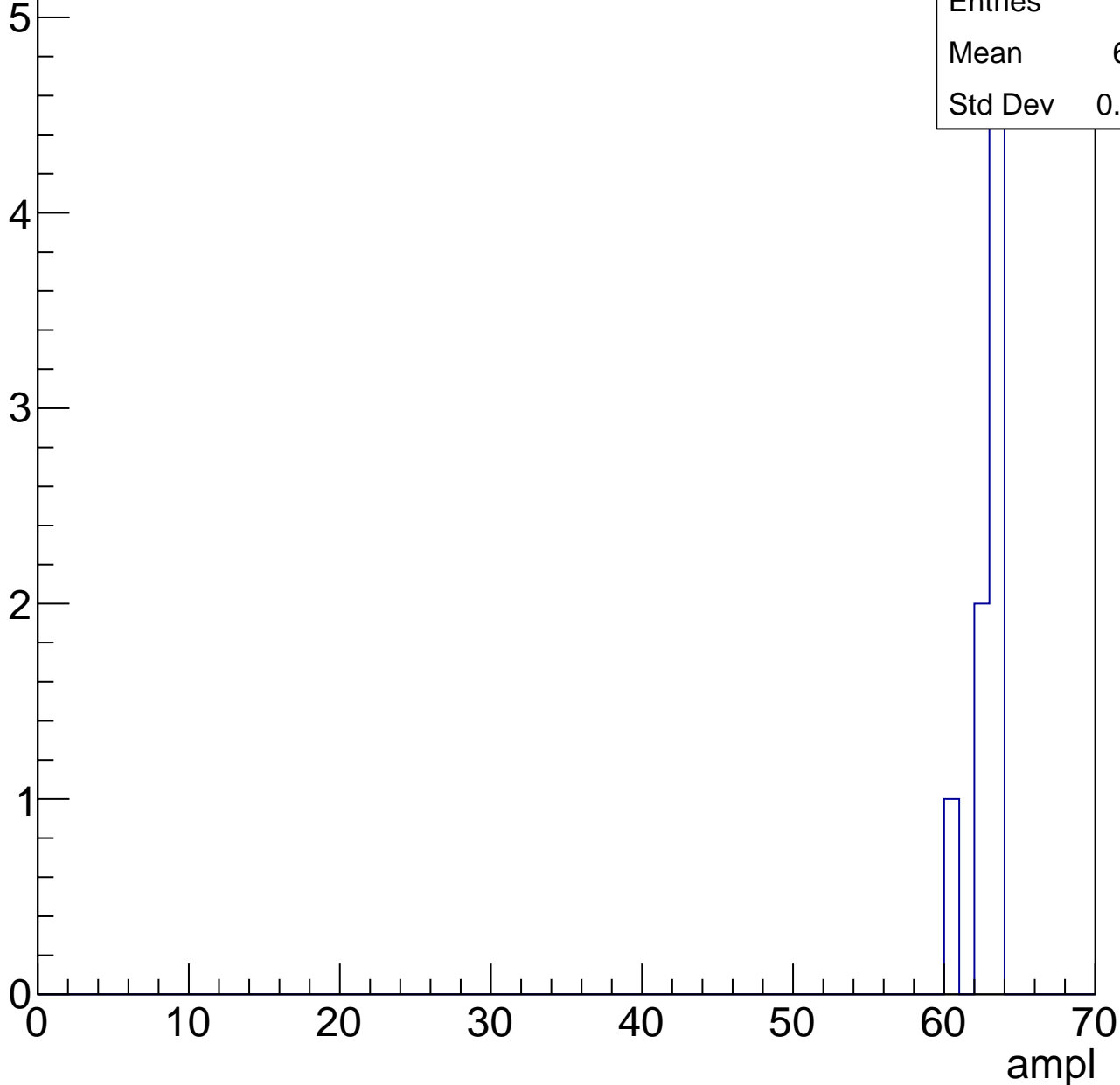
362

B1L101S, U21-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.38
Std Dev	0.9922



B1L101S, U21-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch79, adc0

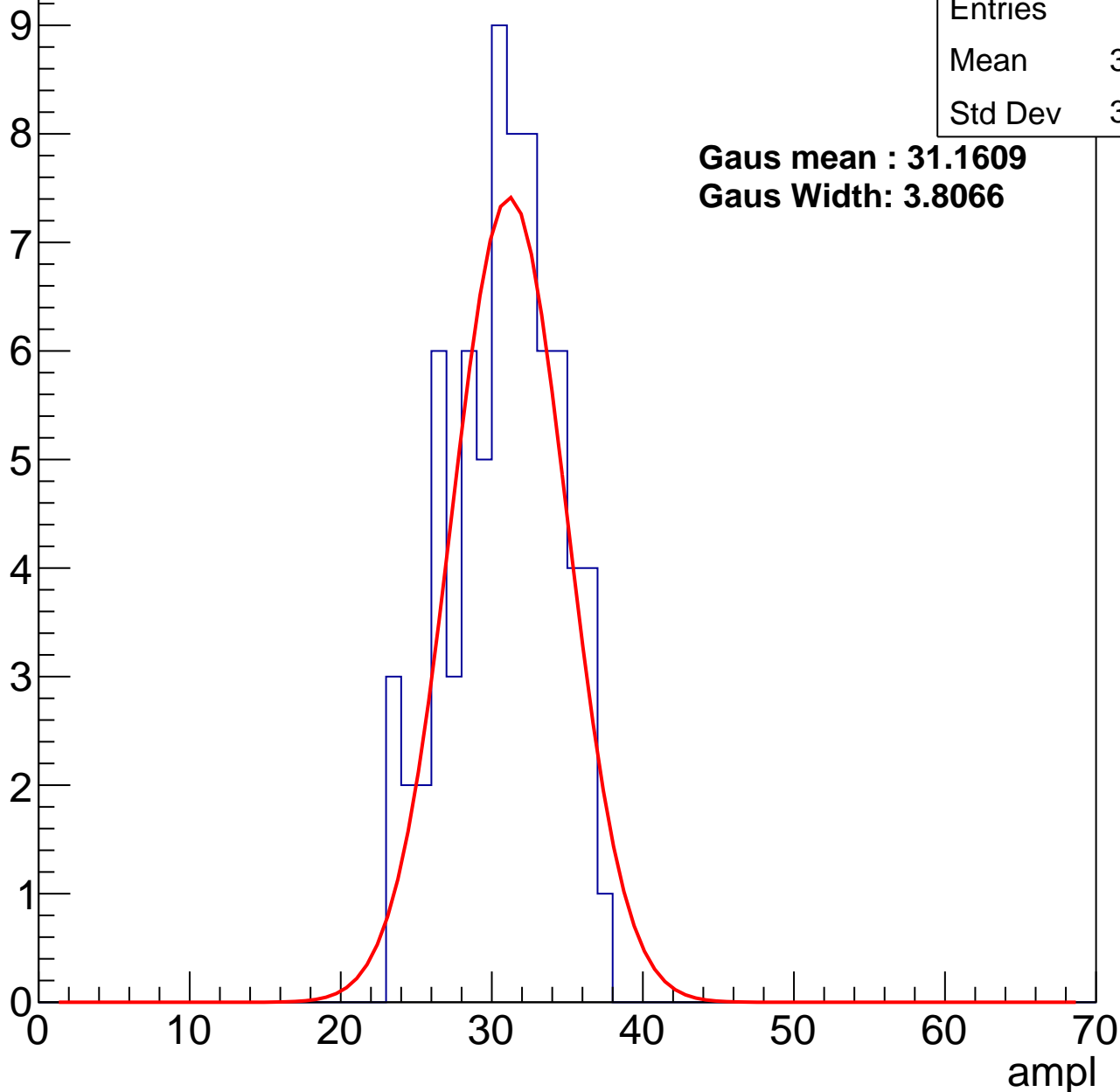
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.33
Std Dev	3.504

Gaus mean : 31.1609

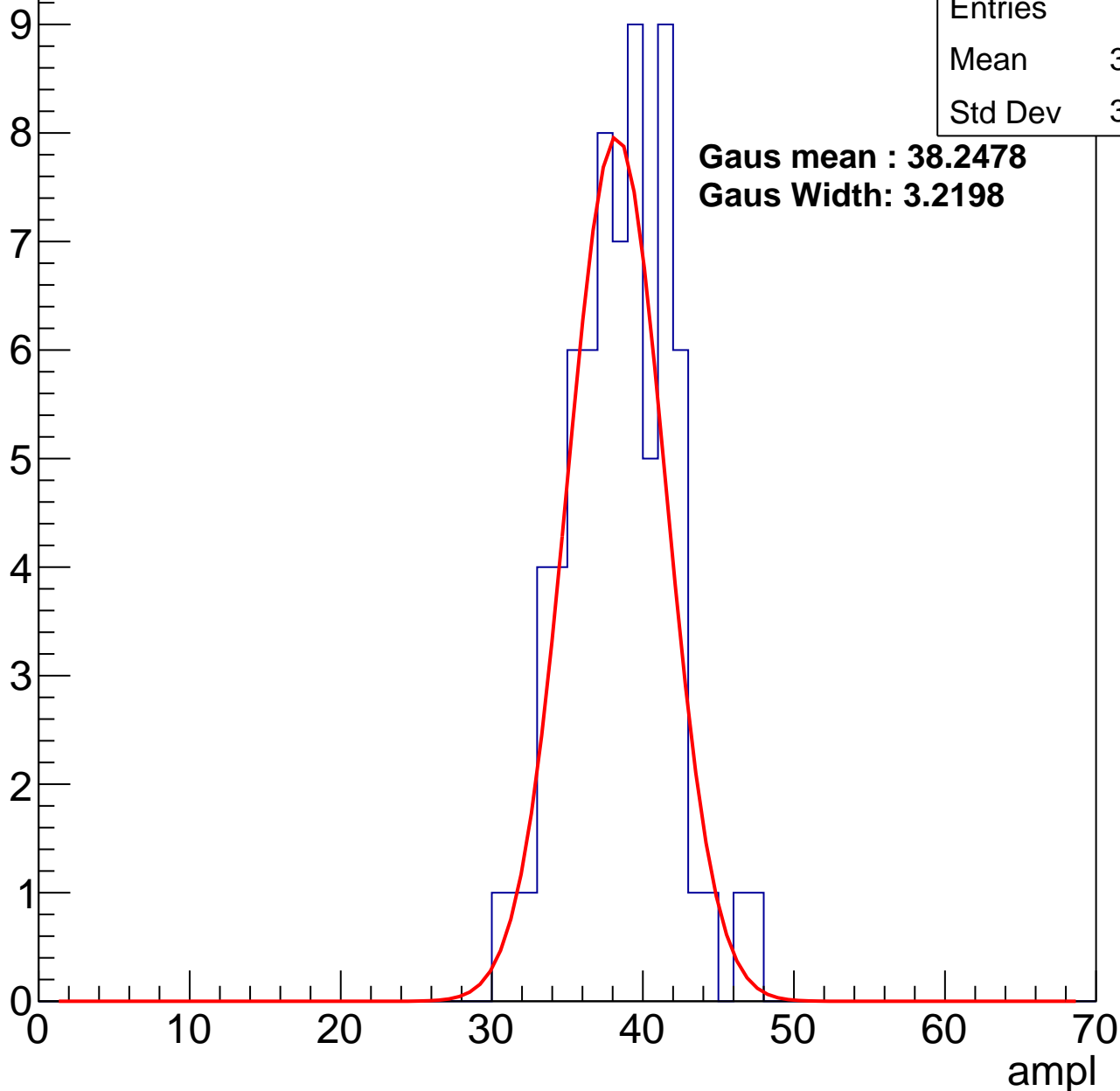
Gaus Width: 3.8066



B1L101S, U21-ch79, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch79, adc2

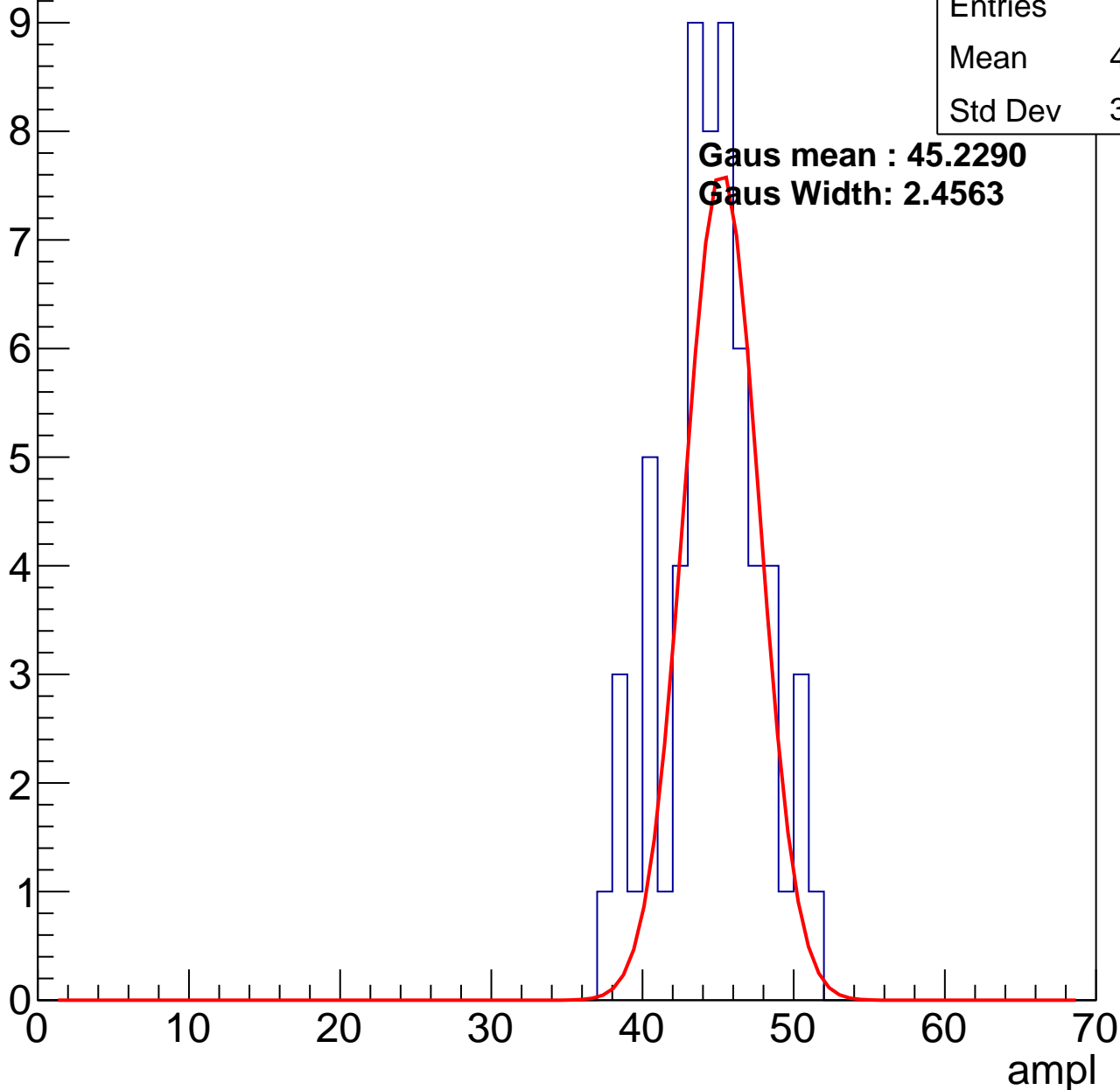
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.15
Std Dev	3.188

Gaus mean : 45.2290

Gaus Width: 2.4563

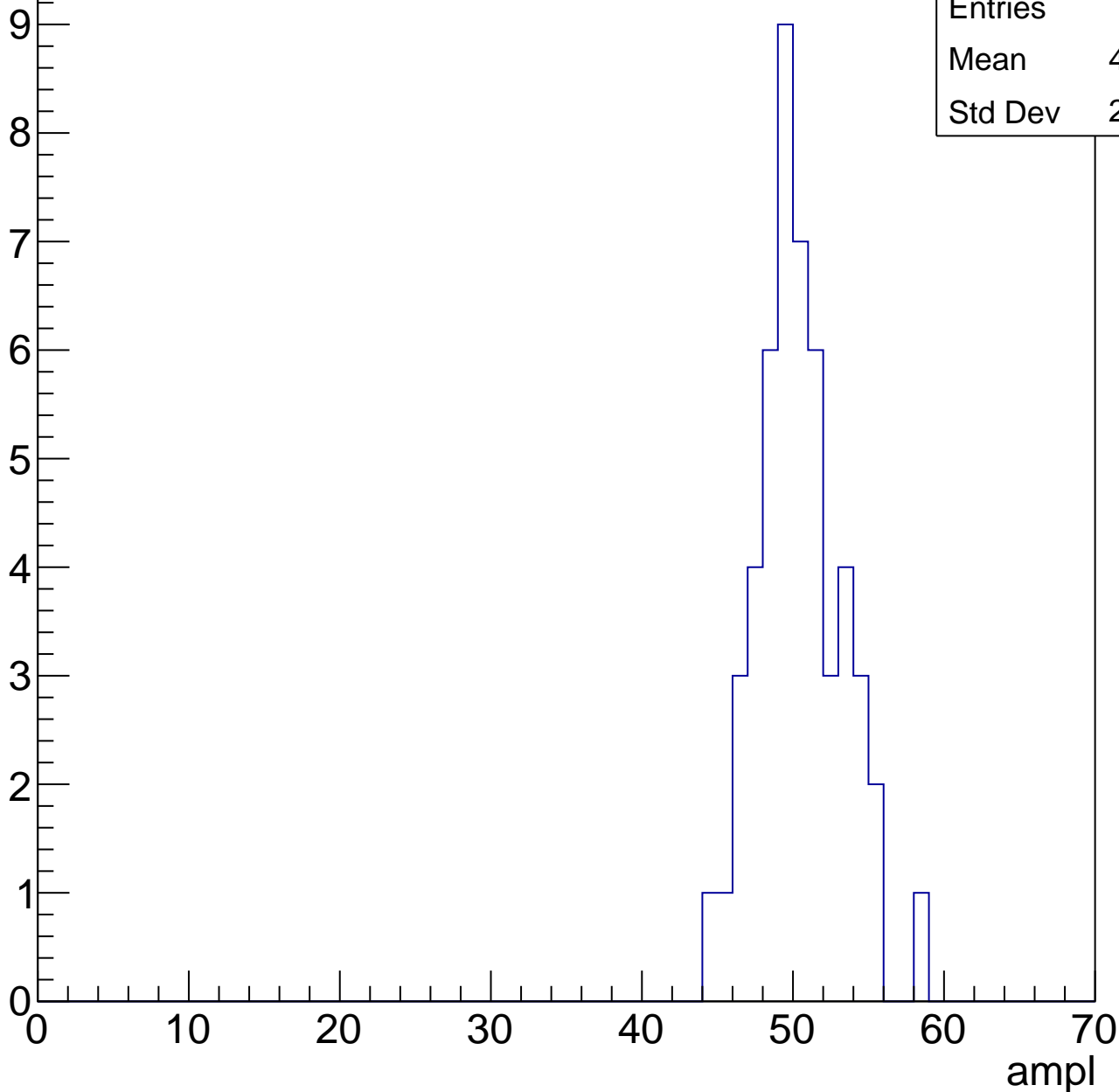


B1L101S, U21-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	49.96
Std Dev	2.807



B1L101S, U21-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	55.44
Std Dev	7.141

Entry

10

8

6

4

2

0

0

10

20

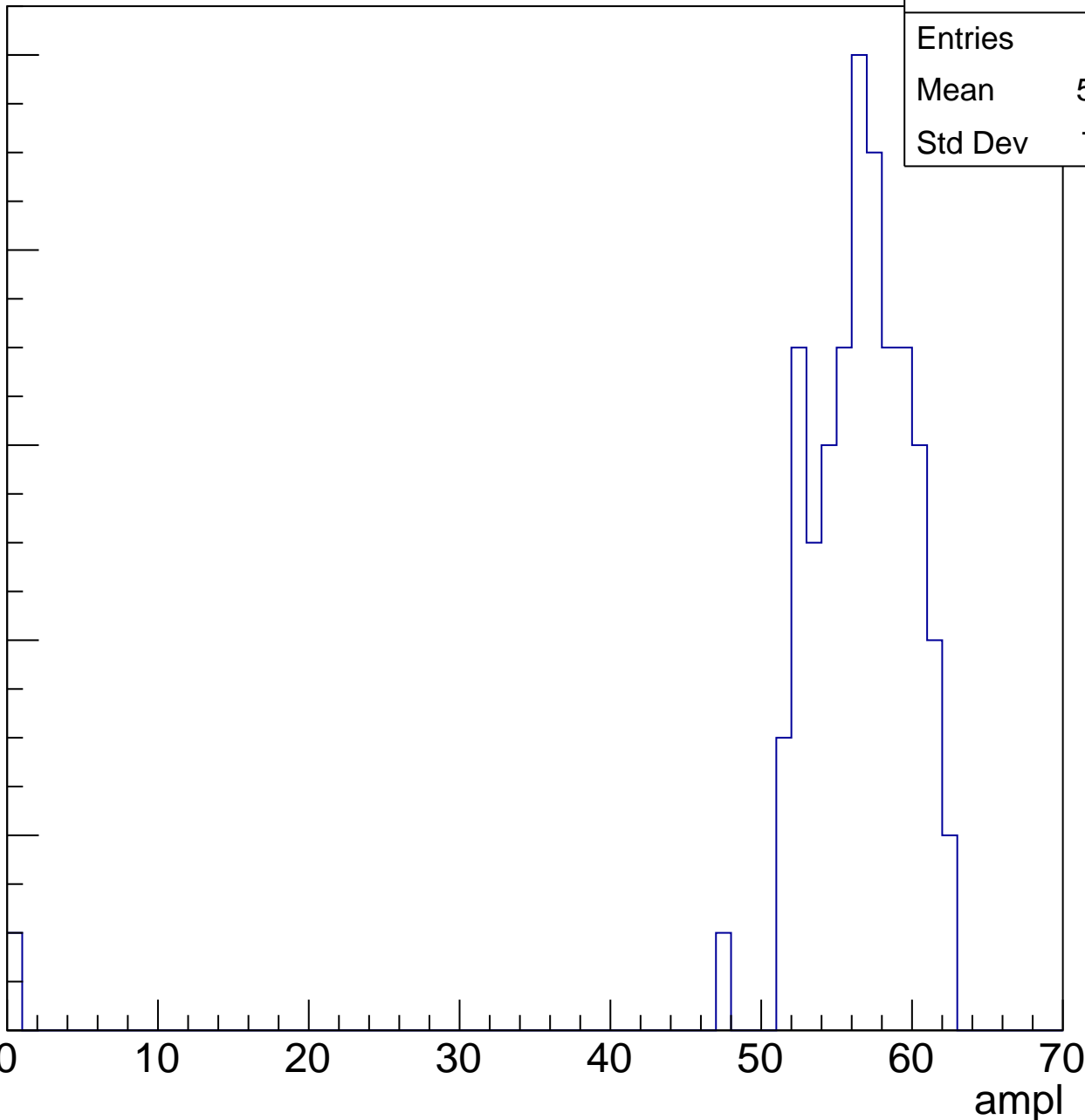
30

40

50

60

ampl

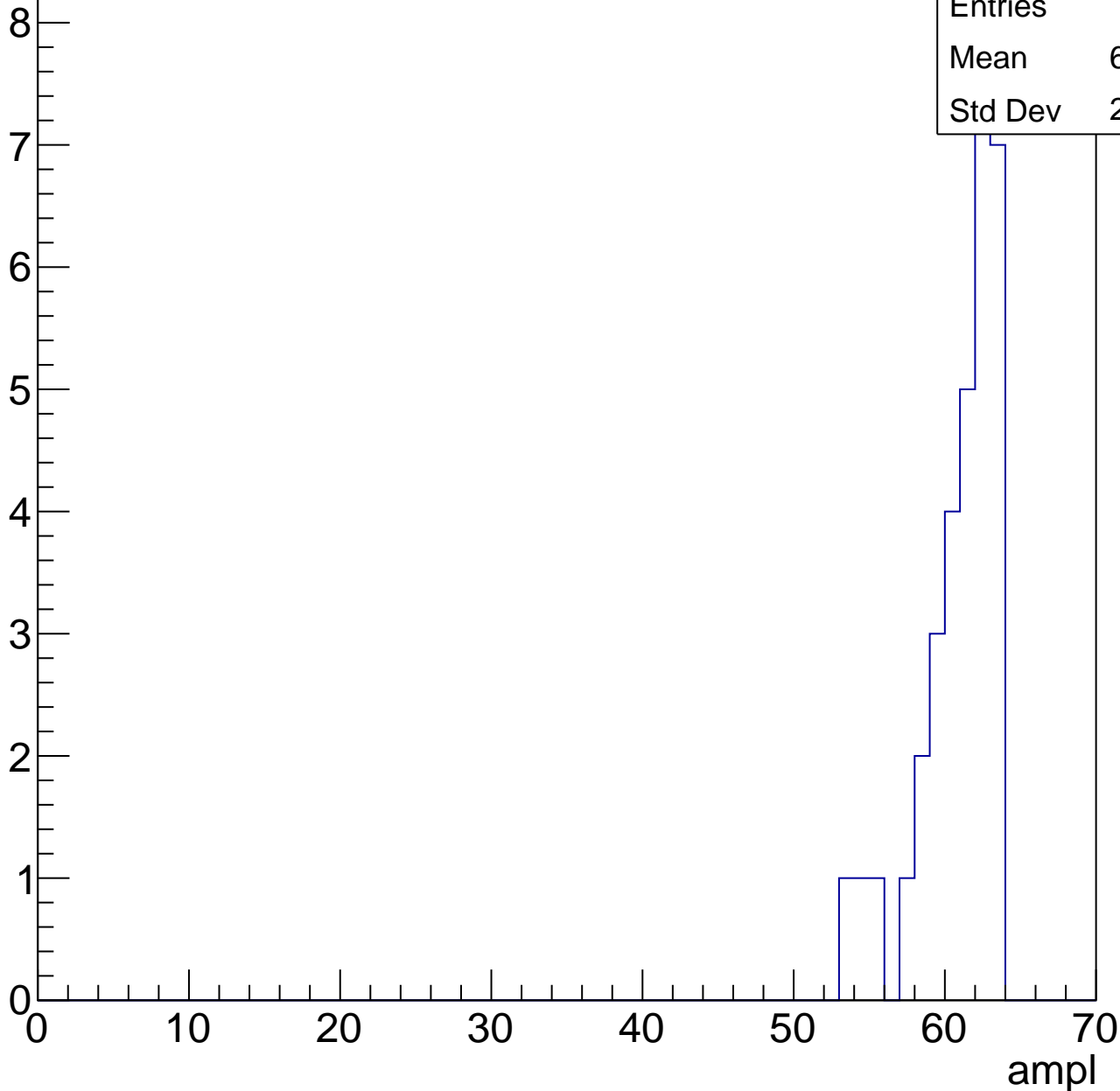


B1L101S, U21-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	60.42
Std Dev	2.606



B1L101S, U21-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch80, adc0

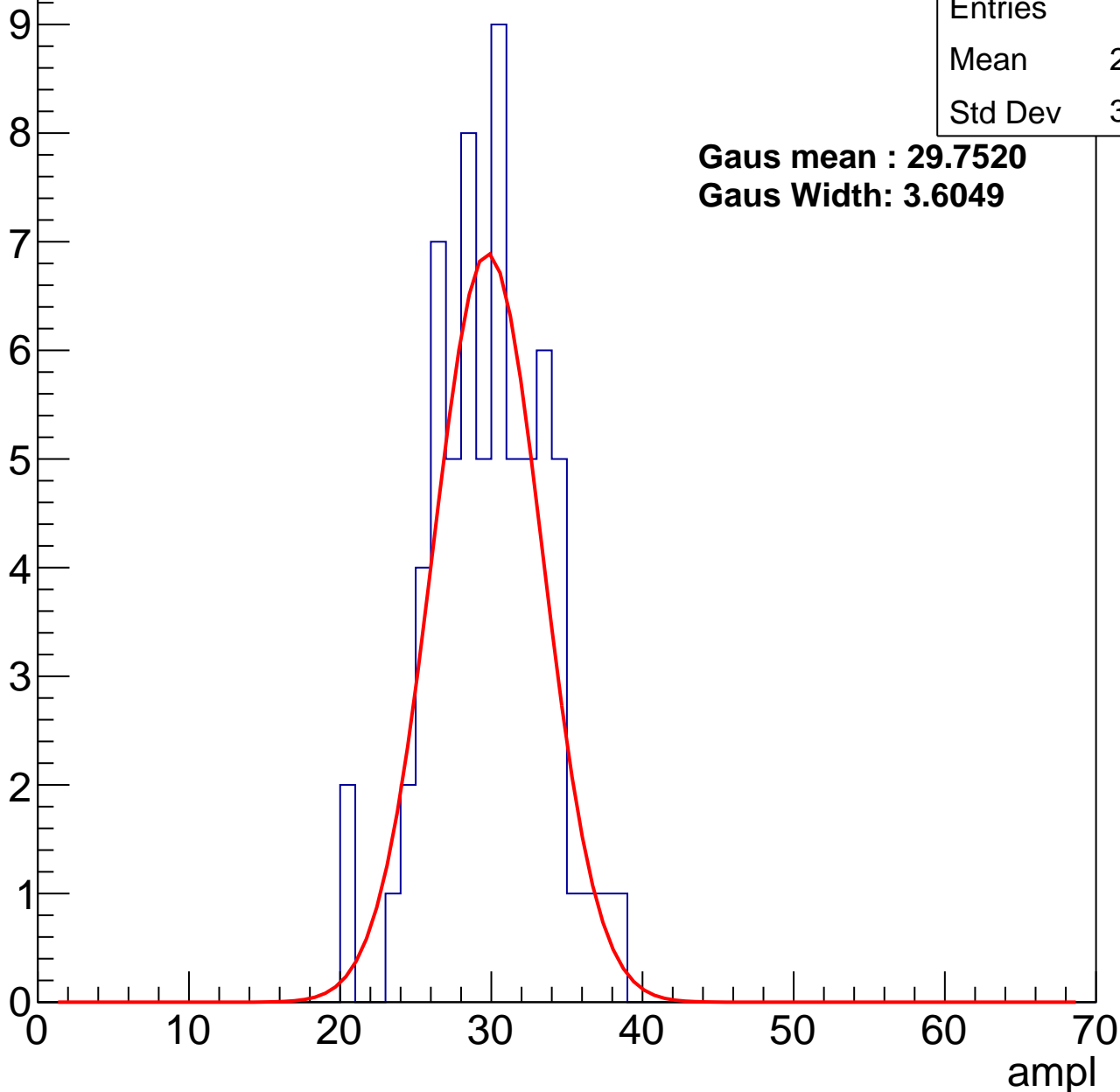
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	29.35
Std Dev	3.677

Gaus mean : 29.7520

Gaus Width: 3.6049



B1L101S, U21-ch80, adc1

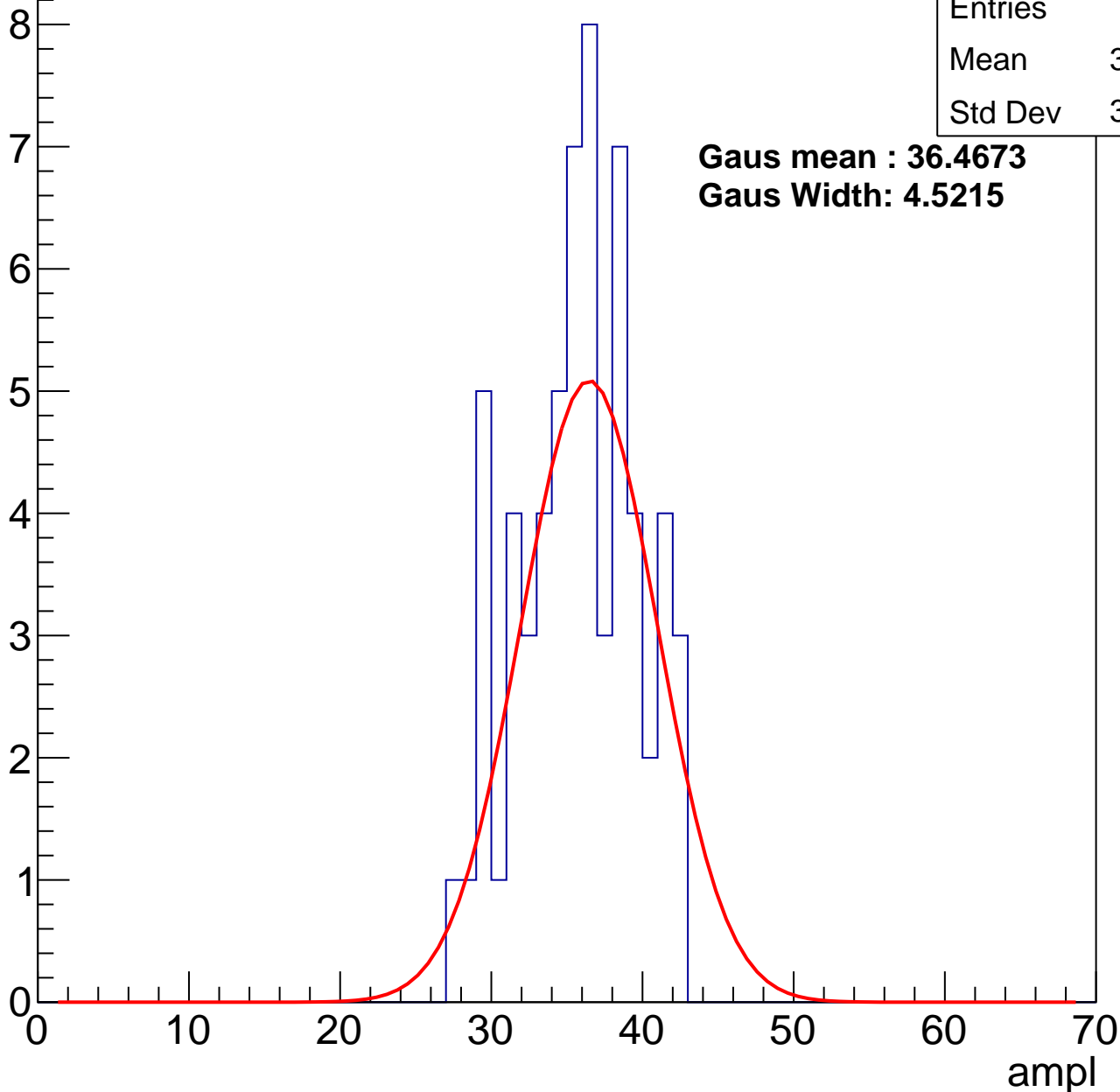
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	35.29
Std Dev	3.837

Gaus mean : 36.4673

Gaus Width: 4.5215



B1L101S, U21-ch80, adc2

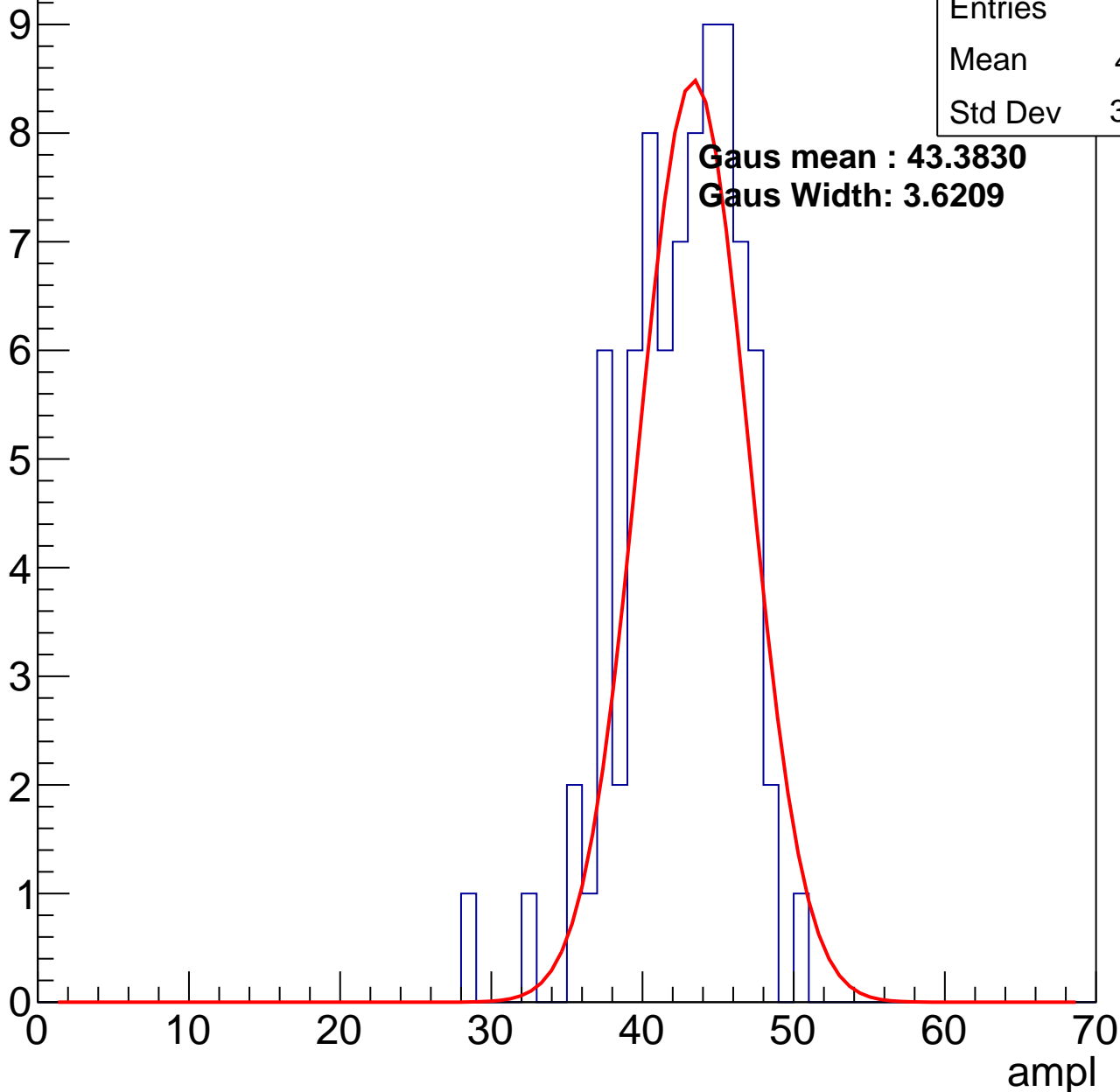
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	42.11
Std Dev	3.867

Gaus mean : 43.3830

Gaus Width: 3.6209

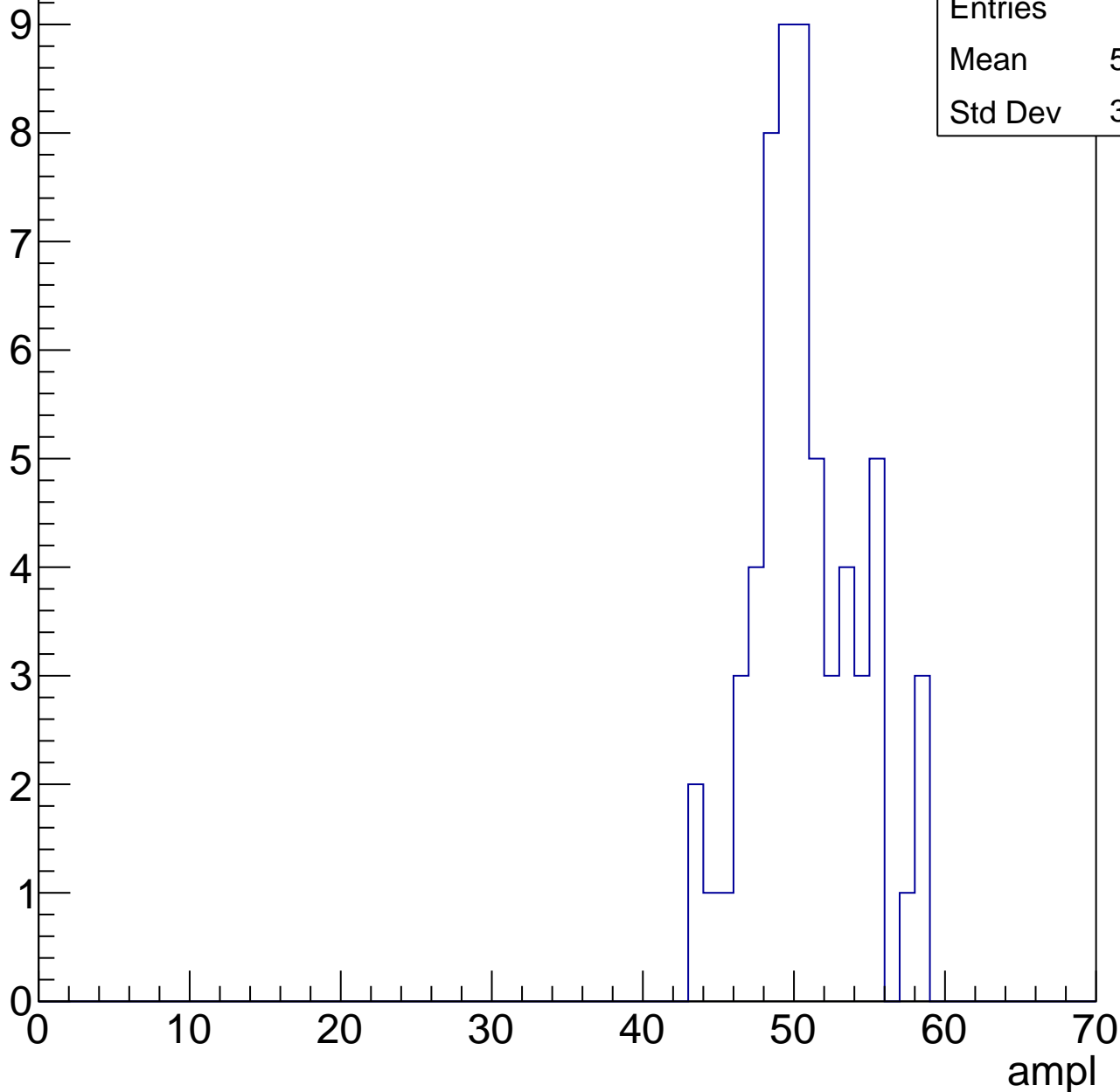


B1L101S, U21-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	50.28
Std Dev	3.498

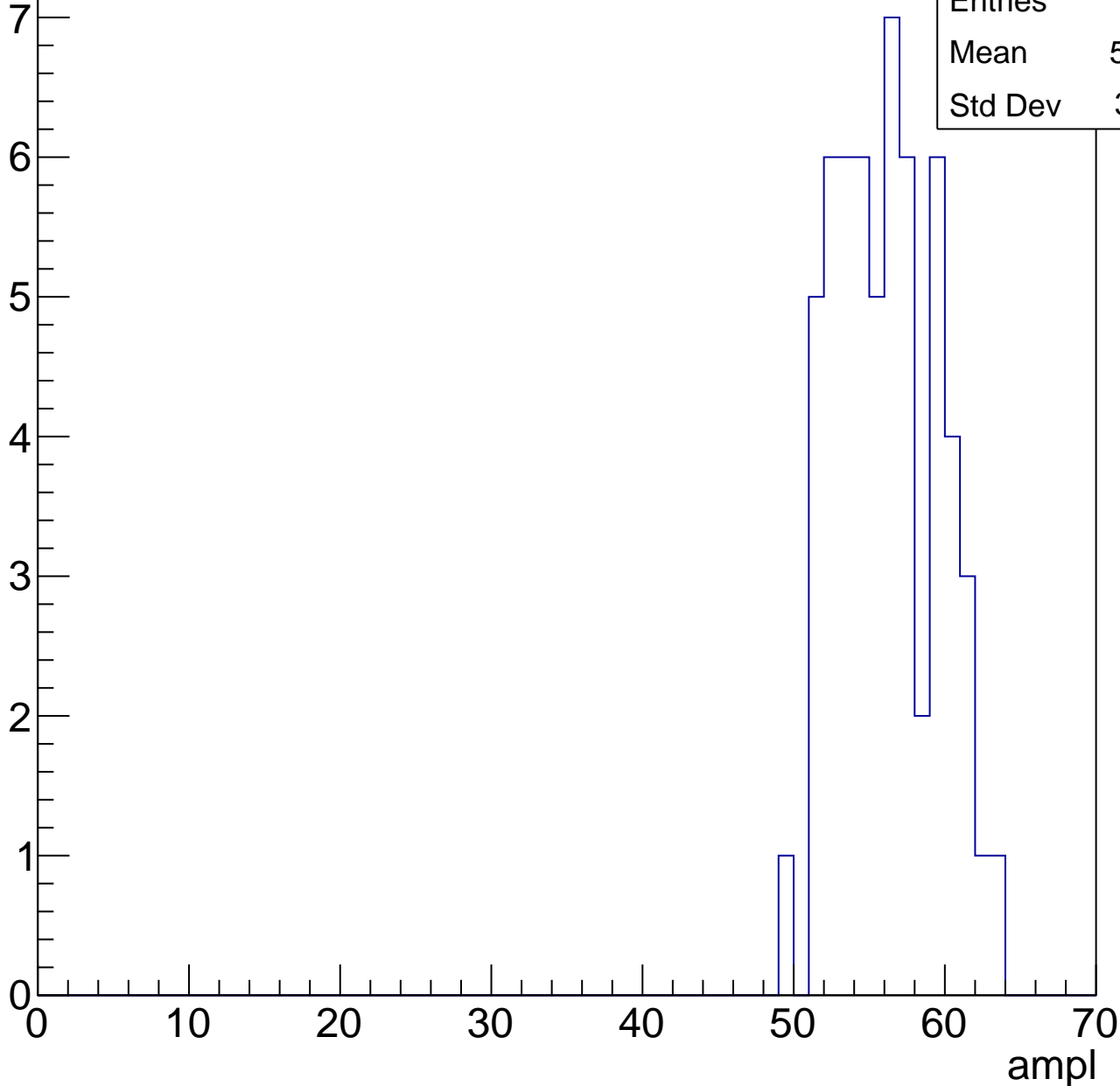


B1L101S, U21-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	55.68
Std Dev	3.291

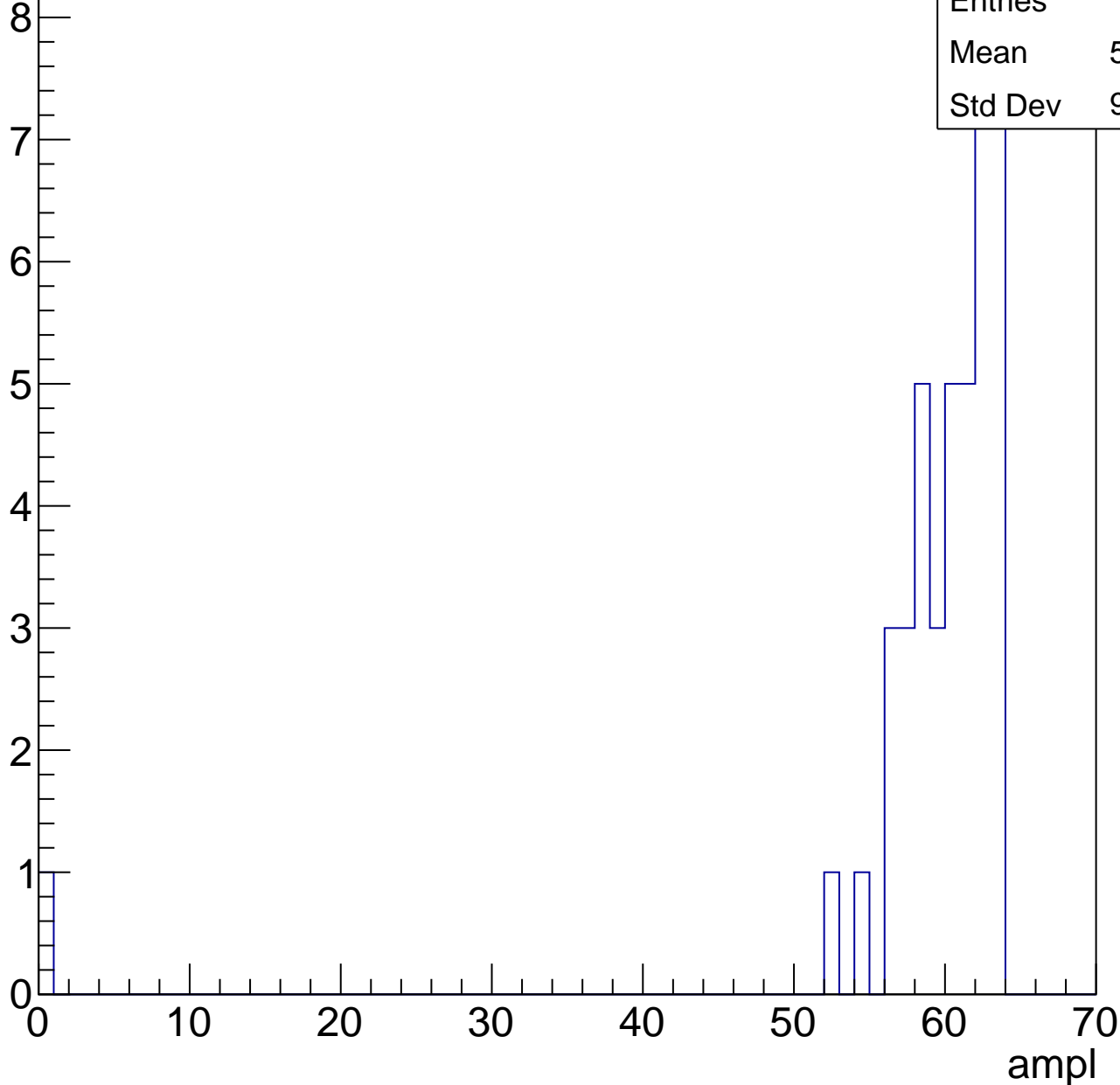


B1L101S, U21-ch80, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

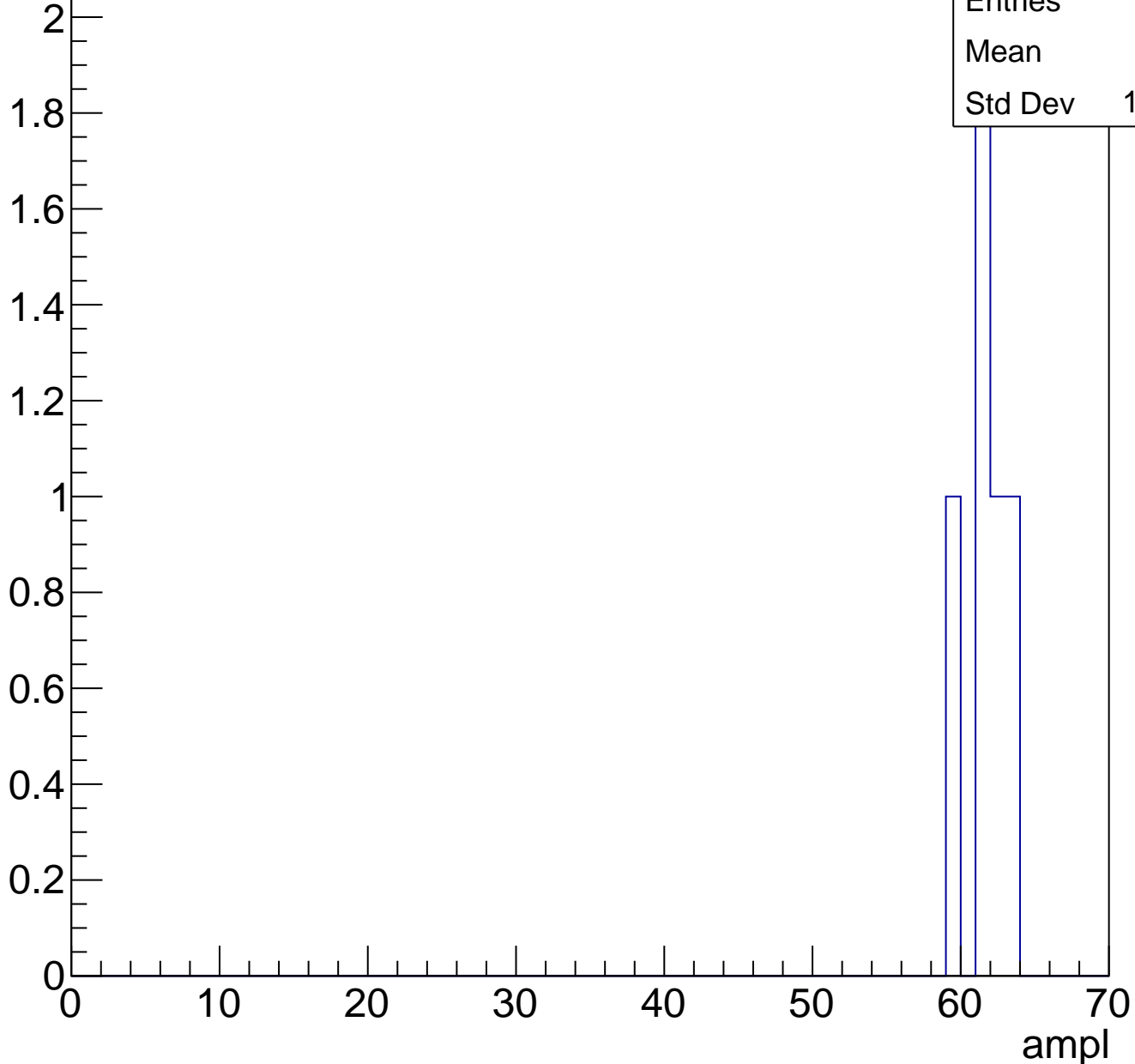
Entries	43
Mean	58.53
Std Dev	9.419



B1L101S, U21-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0 10 20 30 40 50 60 70

ampl

Entries	1
Mean	60
Std Dev	0

B1L101S, U21-ch81, adc0

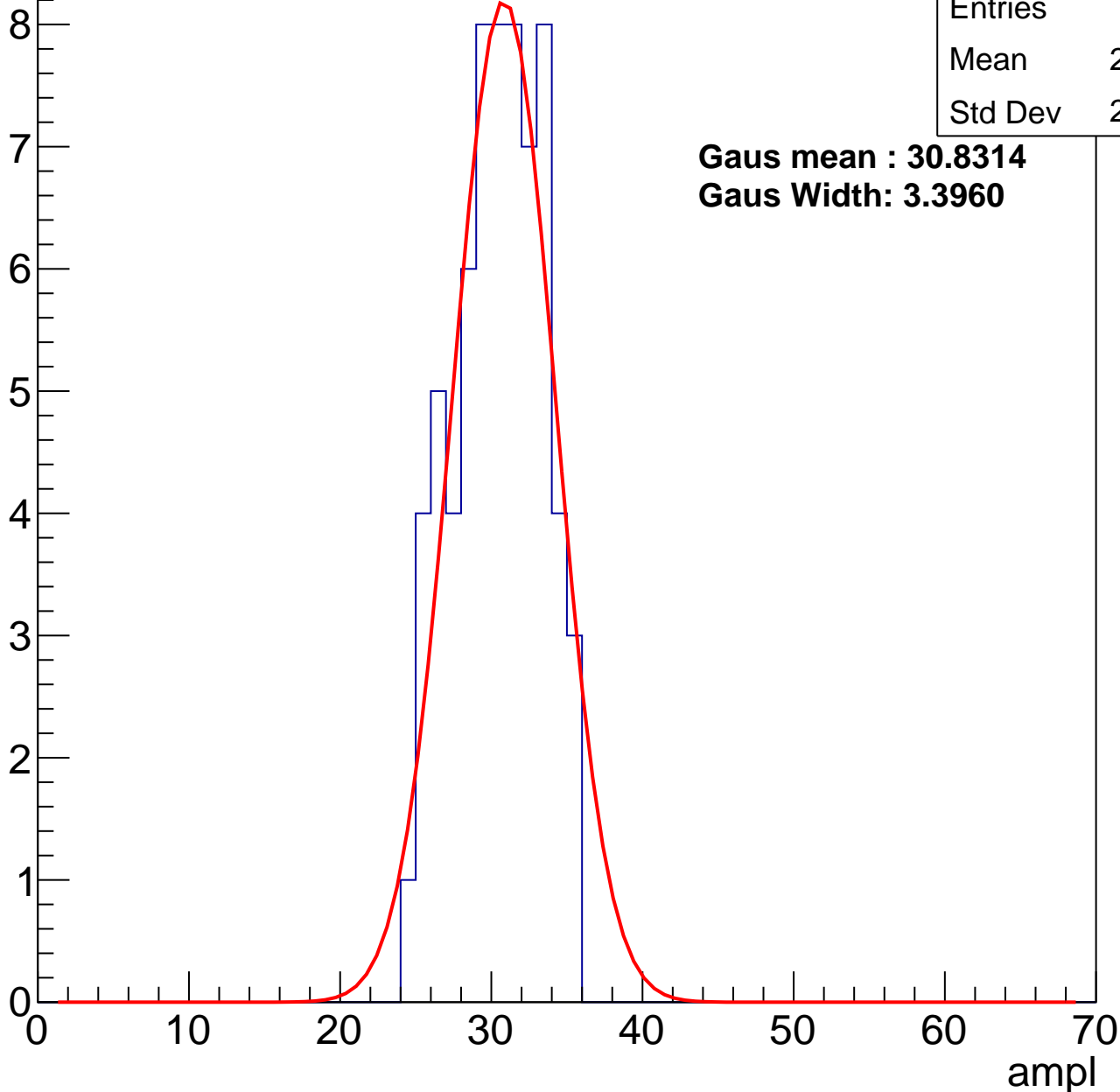
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.98
Std Dev	2.836

Gaus mean : 30.8314

Gaus Width: 3.3960

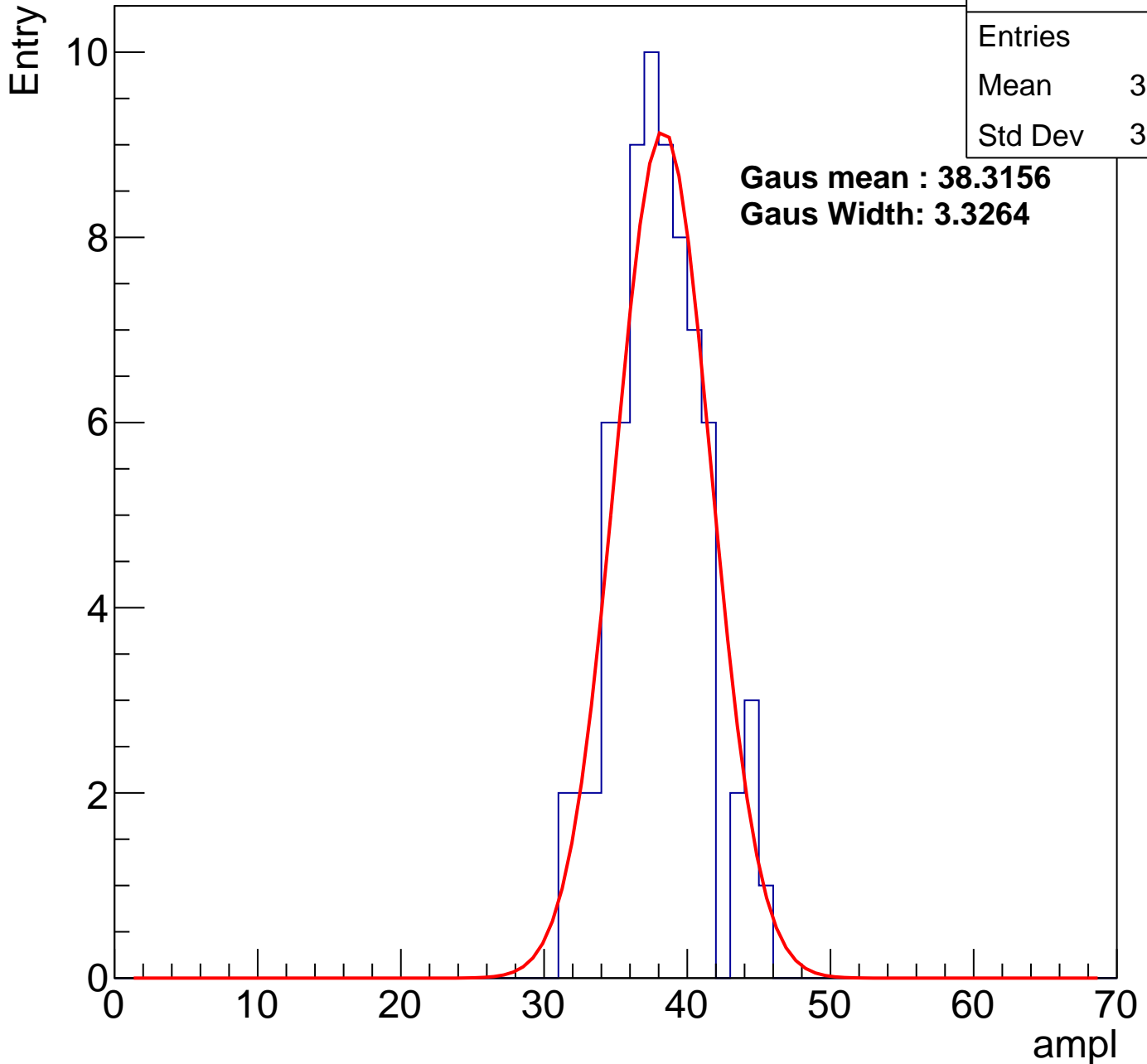


B1L101S, U21-ch81, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	37.58
Std Dev	3.096

Gaus mean : 38.3156
Gaus Width: 3.3264



B1L101S, U21-ch81, adc2

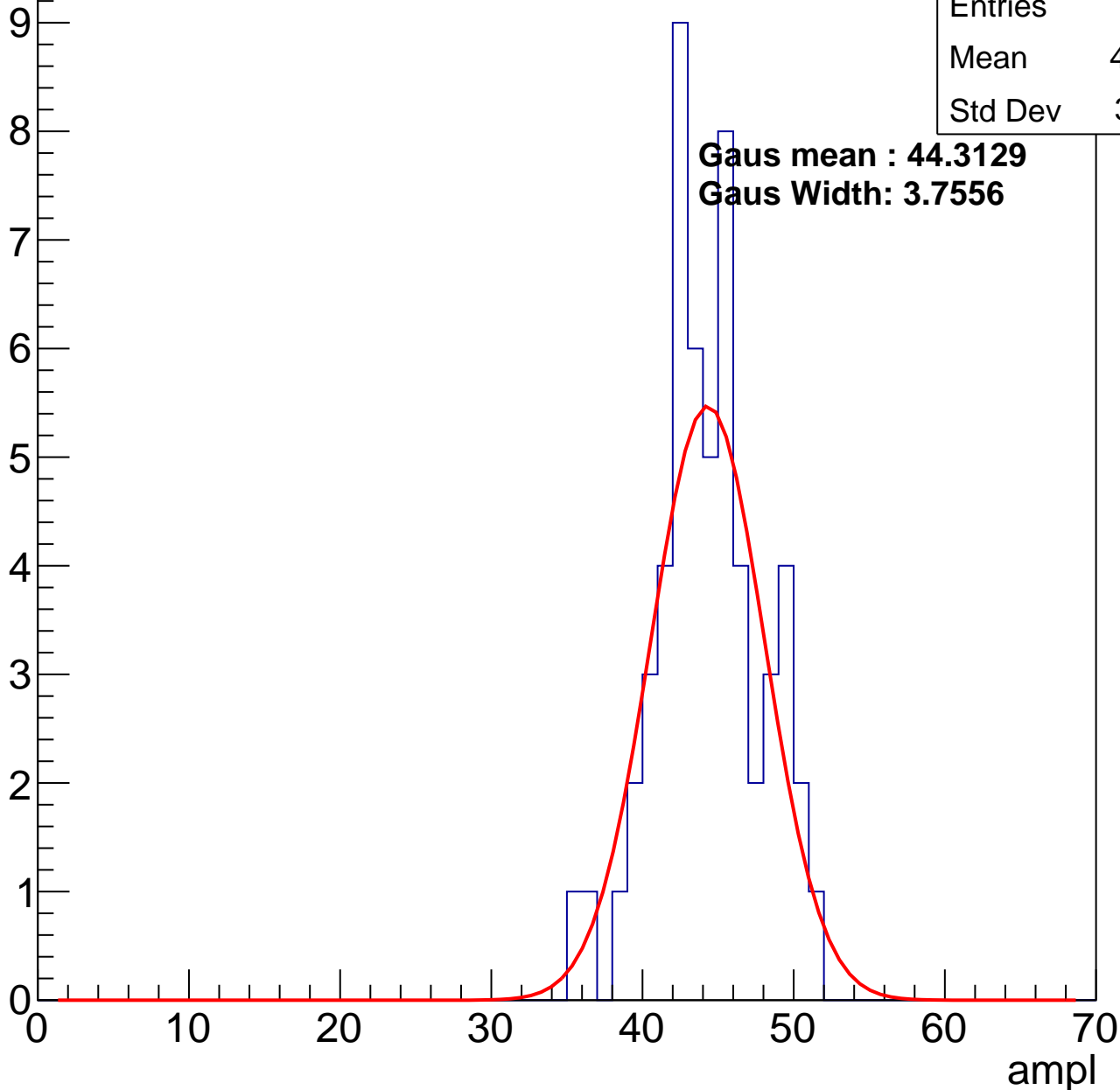
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.86
Std Dev	3.451

Gaus mean : 44.3129

Gaus Width: 3.7556

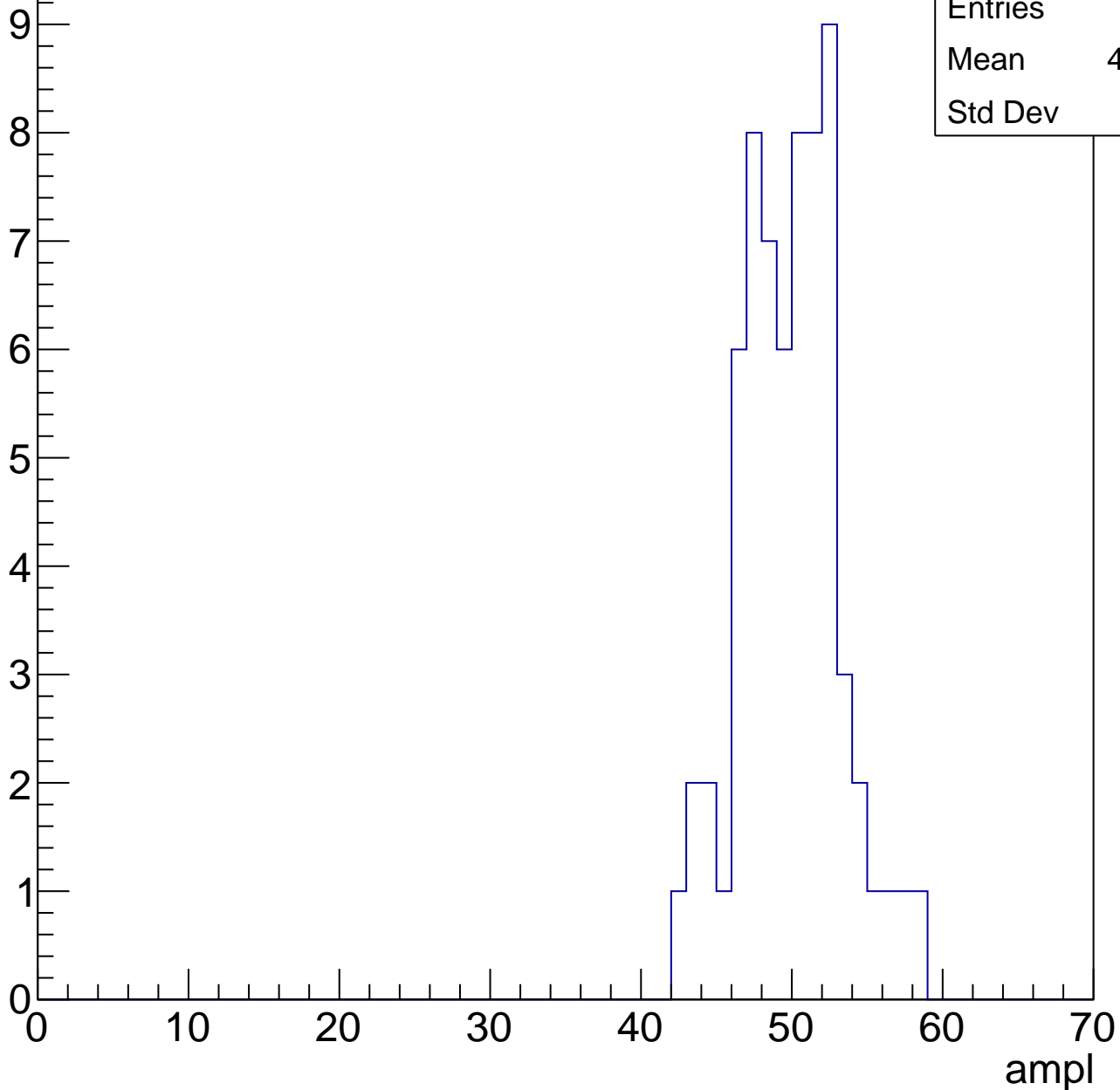


B1L101S, U21-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

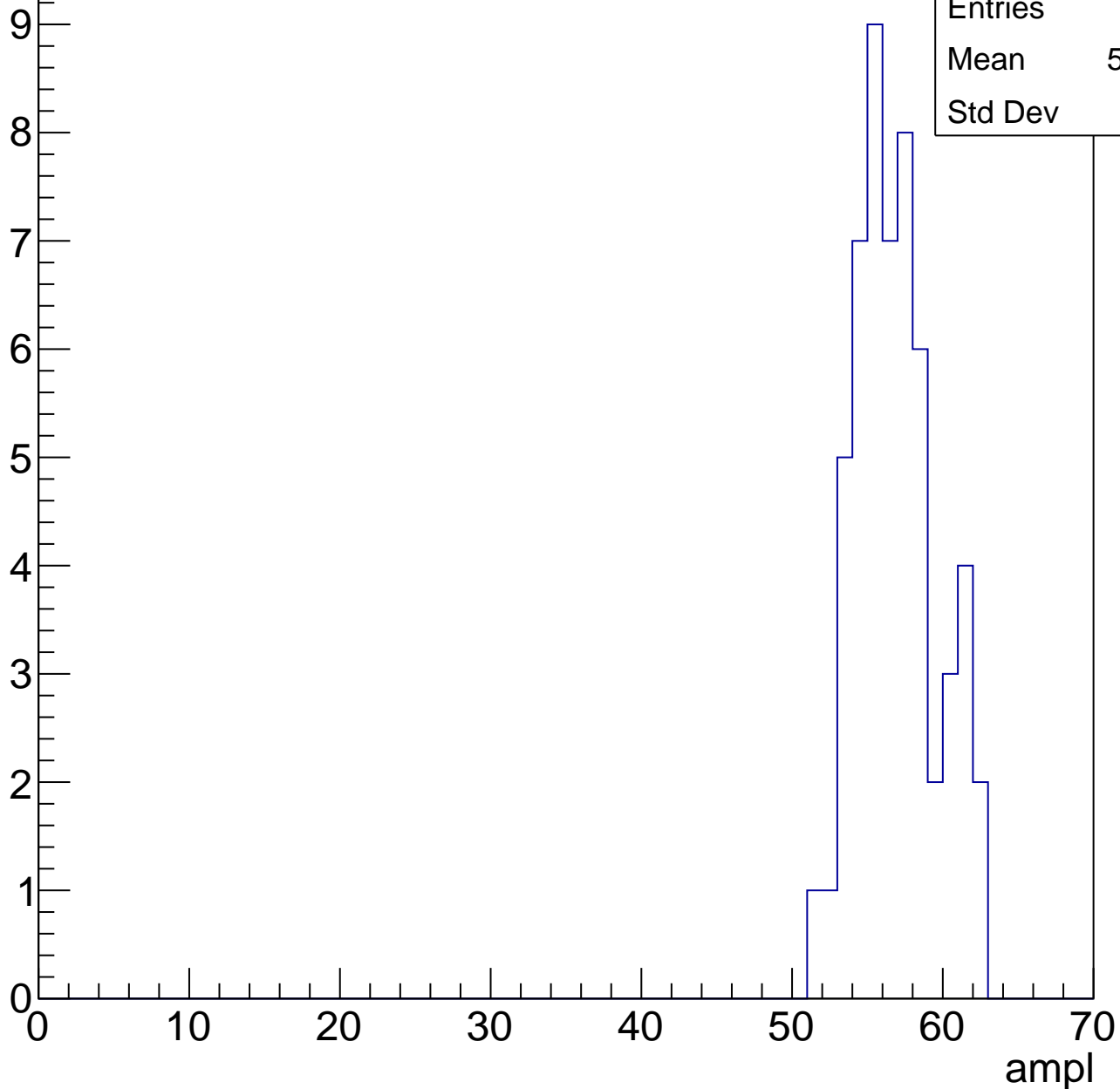
Entries	67
Mean	49.43
Std Dev	3.27



B1L101S, U21-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	55
Mean	56.42
Std Dev	2.64

B1L101S, U21-ch81, adc5

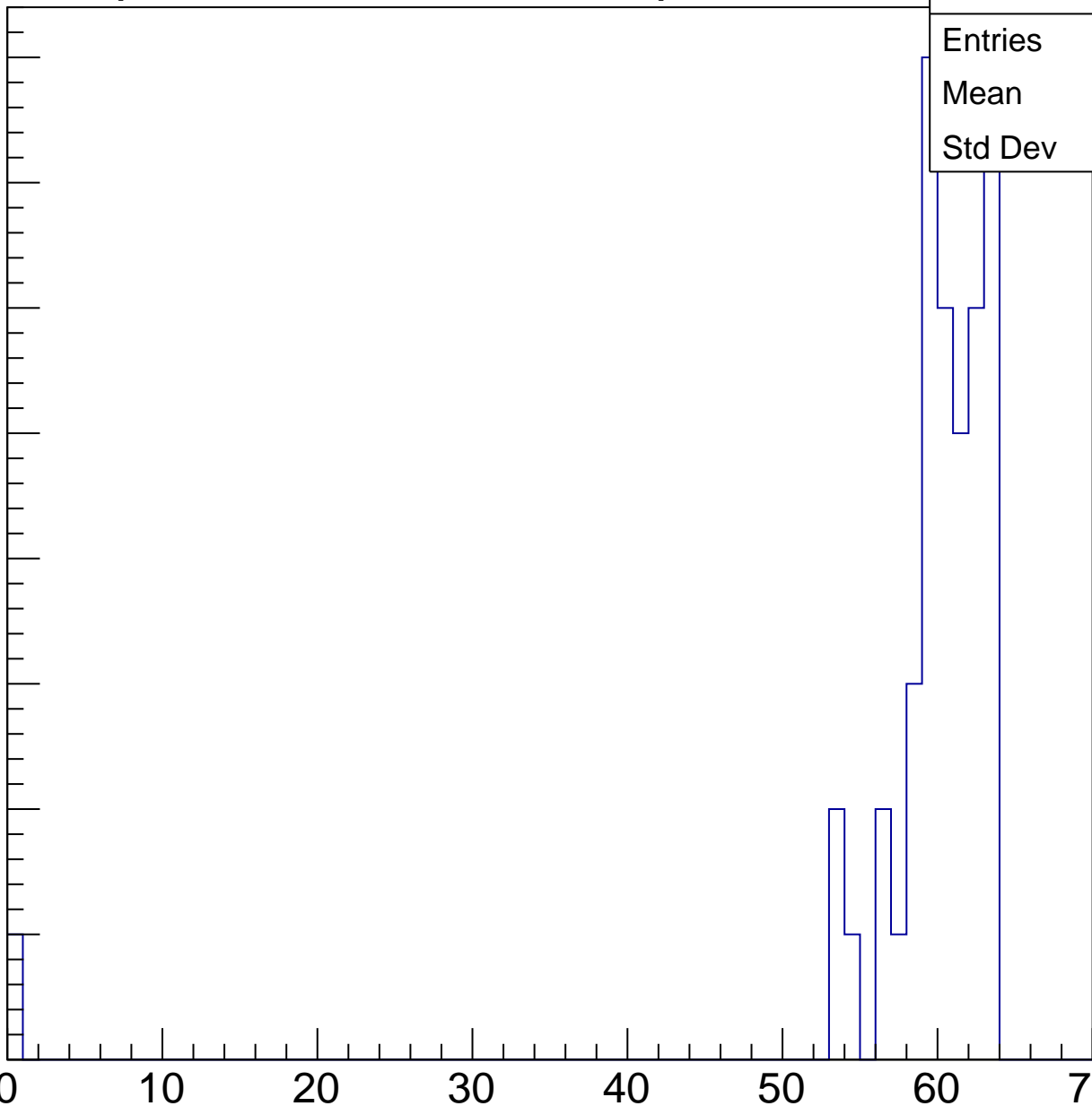
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	58.51
Std Dev	9.402

ampl



B1L101S, U21-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.83
Std Dev	1.067

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch82, adc0

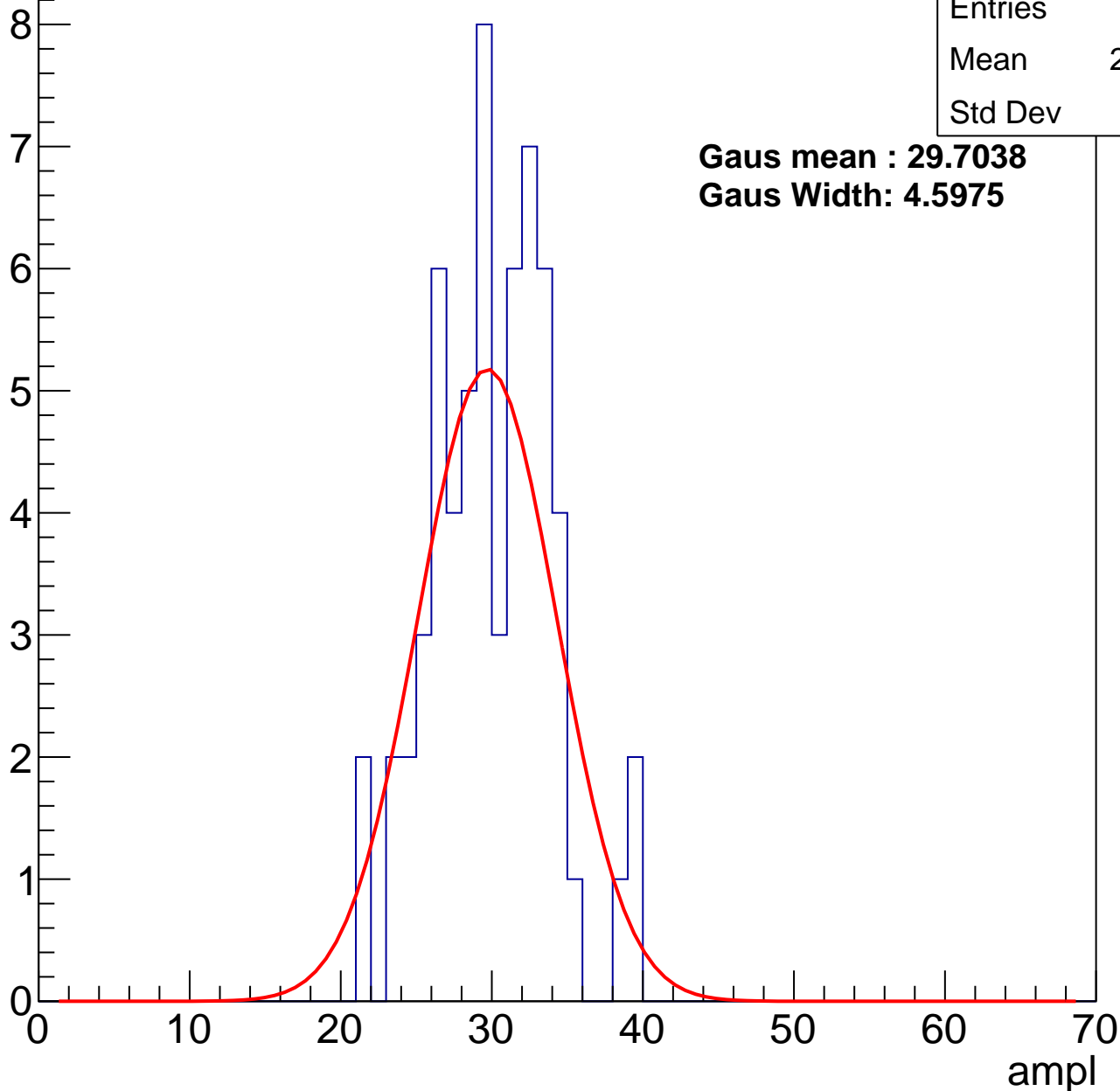
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.55
Std Dev	3.93

Gaus mean : 29.7038

Gaus Width: 4.5975

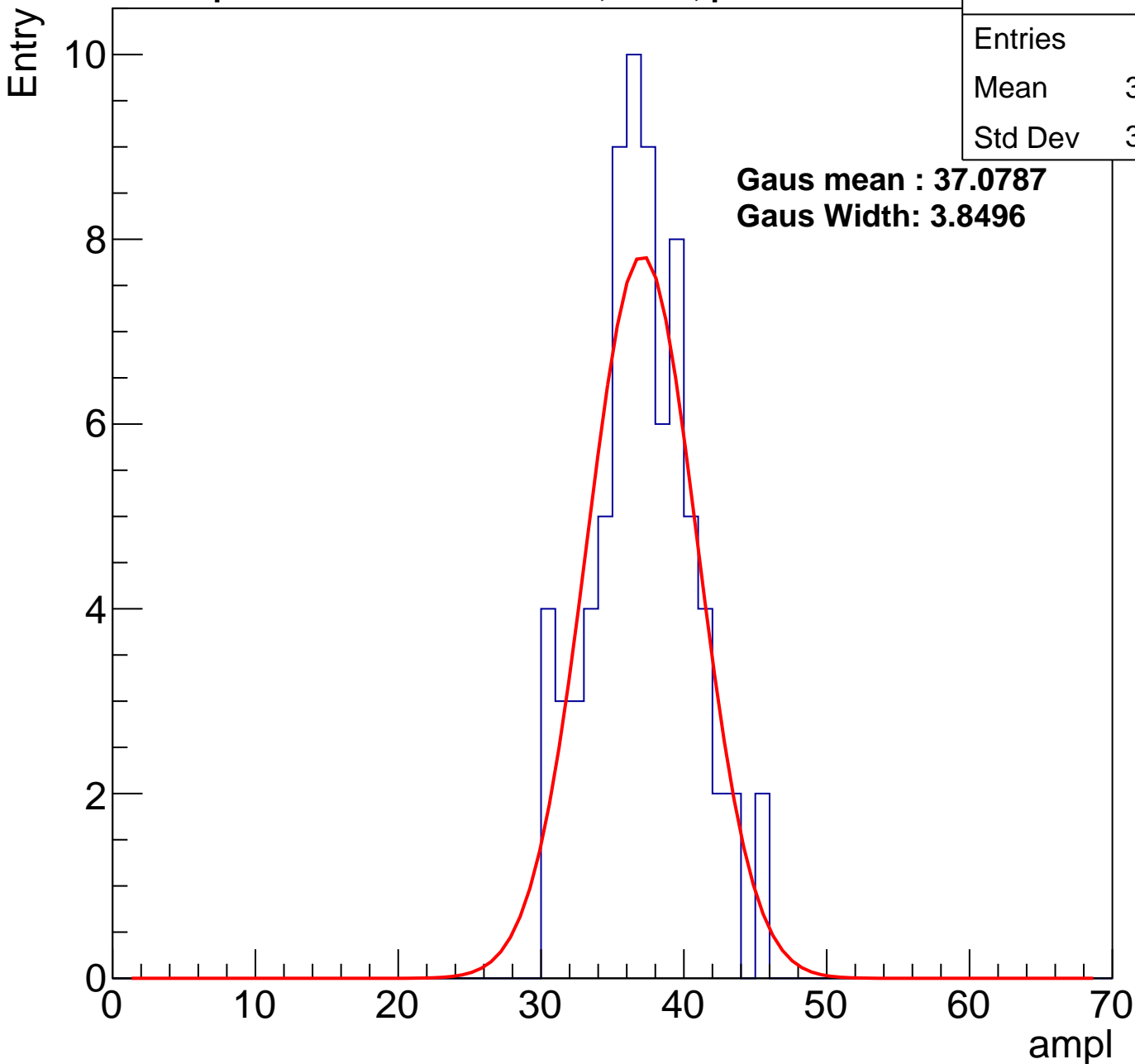


B1L101S, U21-ch82, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.62
Std Dev	3.479

Gaus mean : 37.0787
Gaus Width: 3.8496



B1L101S, U21-ch82, adc2

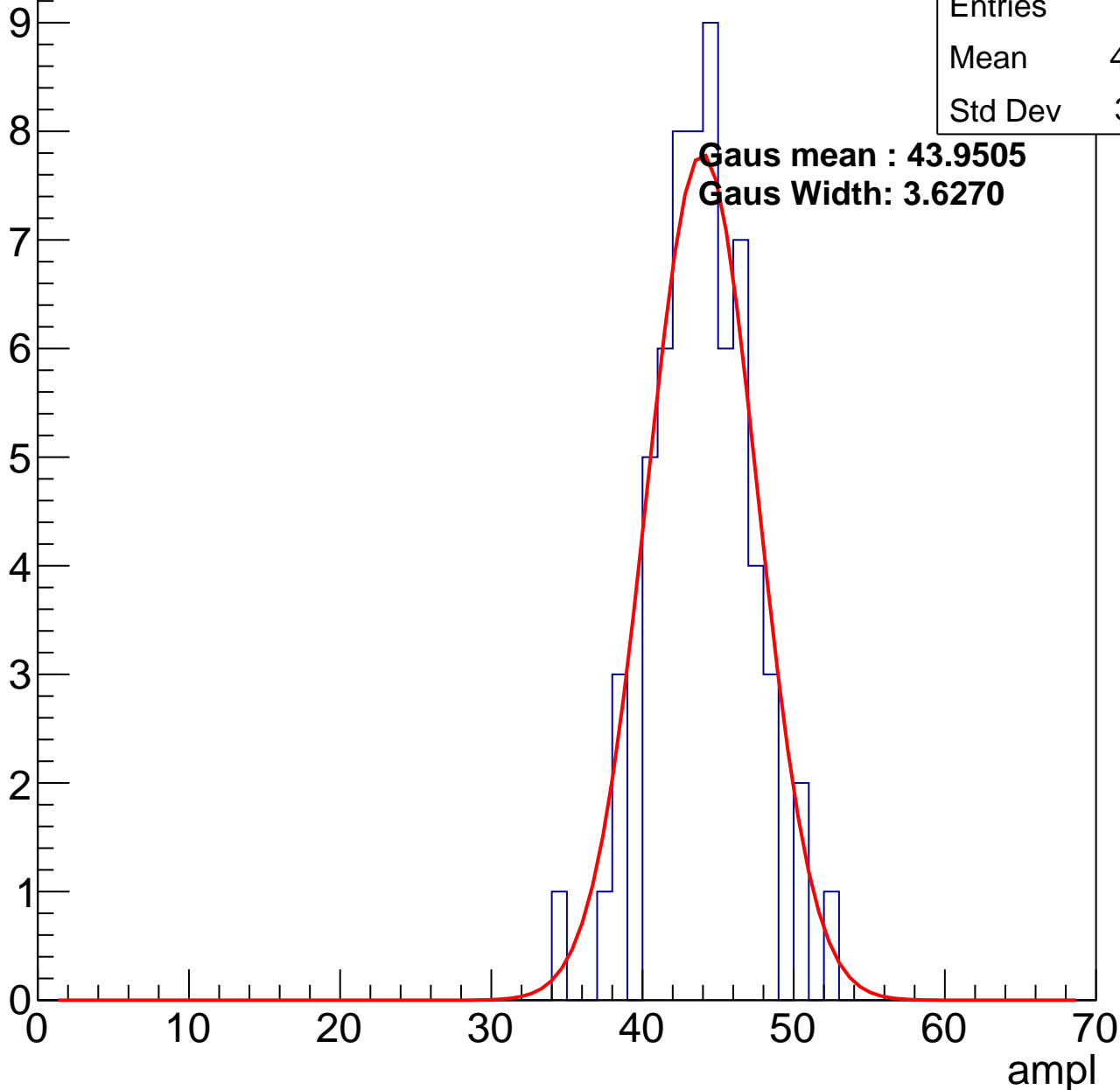
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.48
Std Dev	3.231

Gaus mean : 43.9505

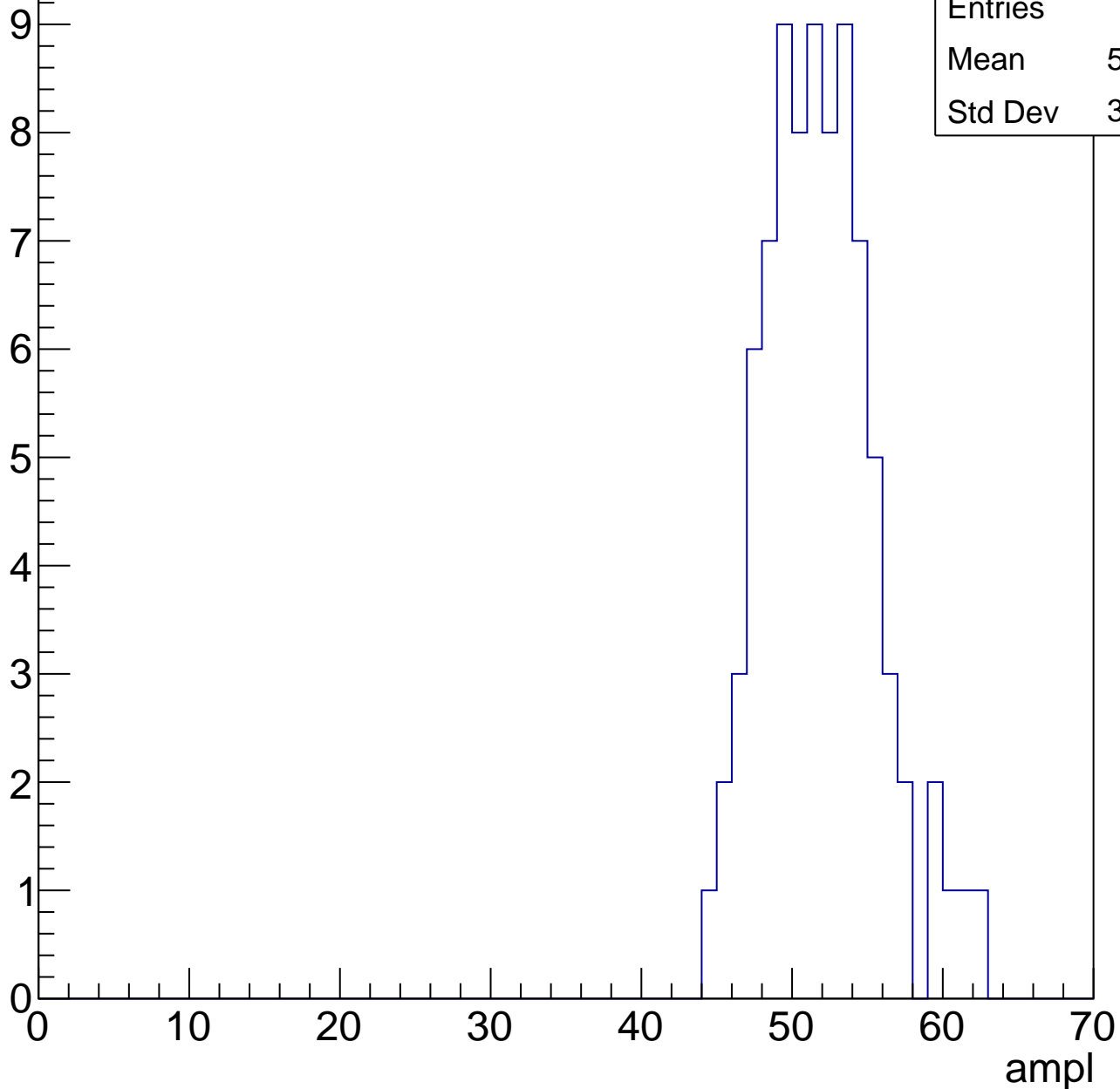
Gaus Width: 3.6270



B1L101S, U21-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

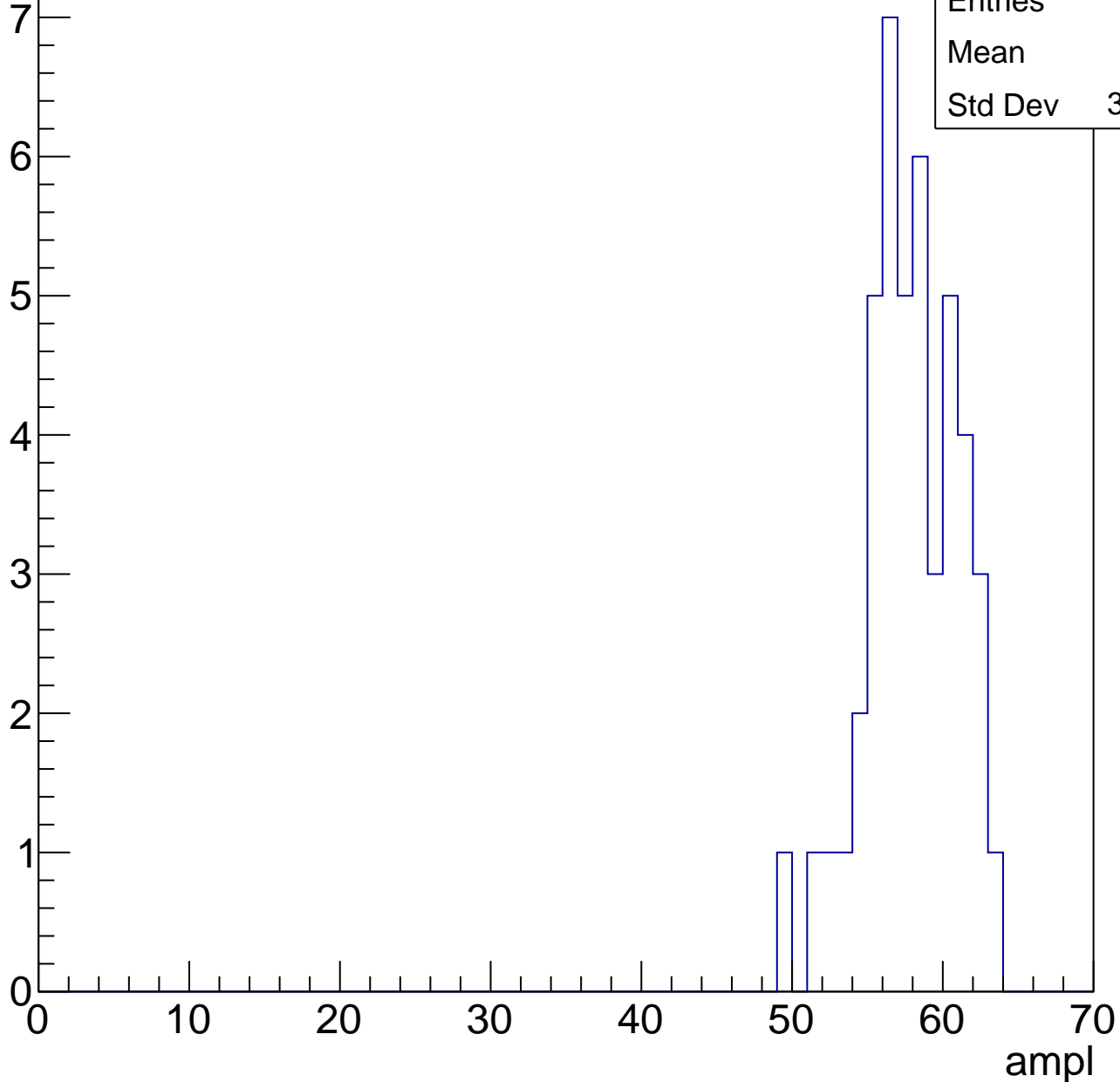


Entries	84
Mean	51.42
Std Dev	3.698

B1L101S, U21-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

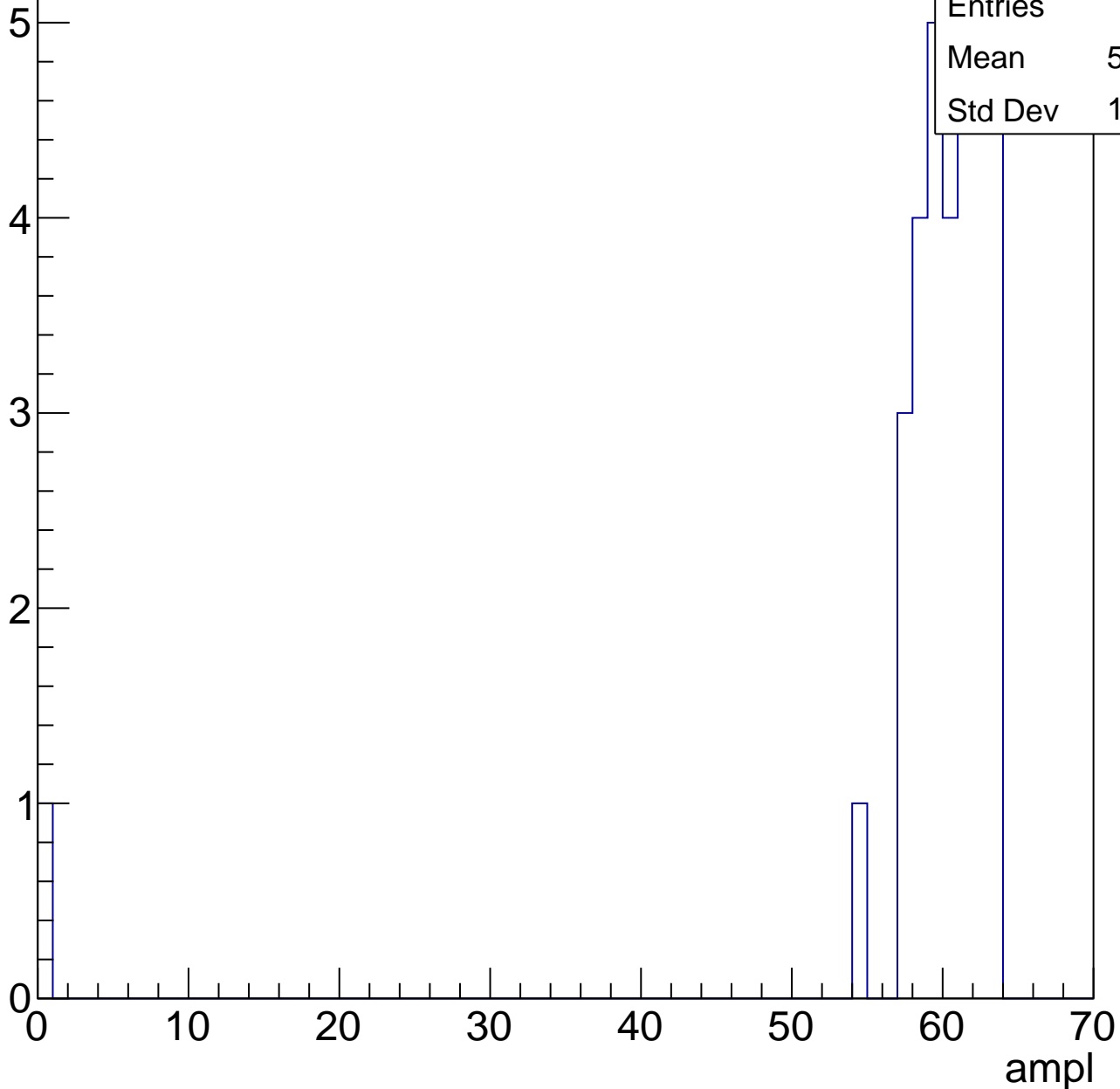


B1L101S, U21-ch82, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.24
Std Dev	10.52



B1L101S, U21-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	62.8
Std Dev	0.4

B1L101S, U21-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch83, adc0

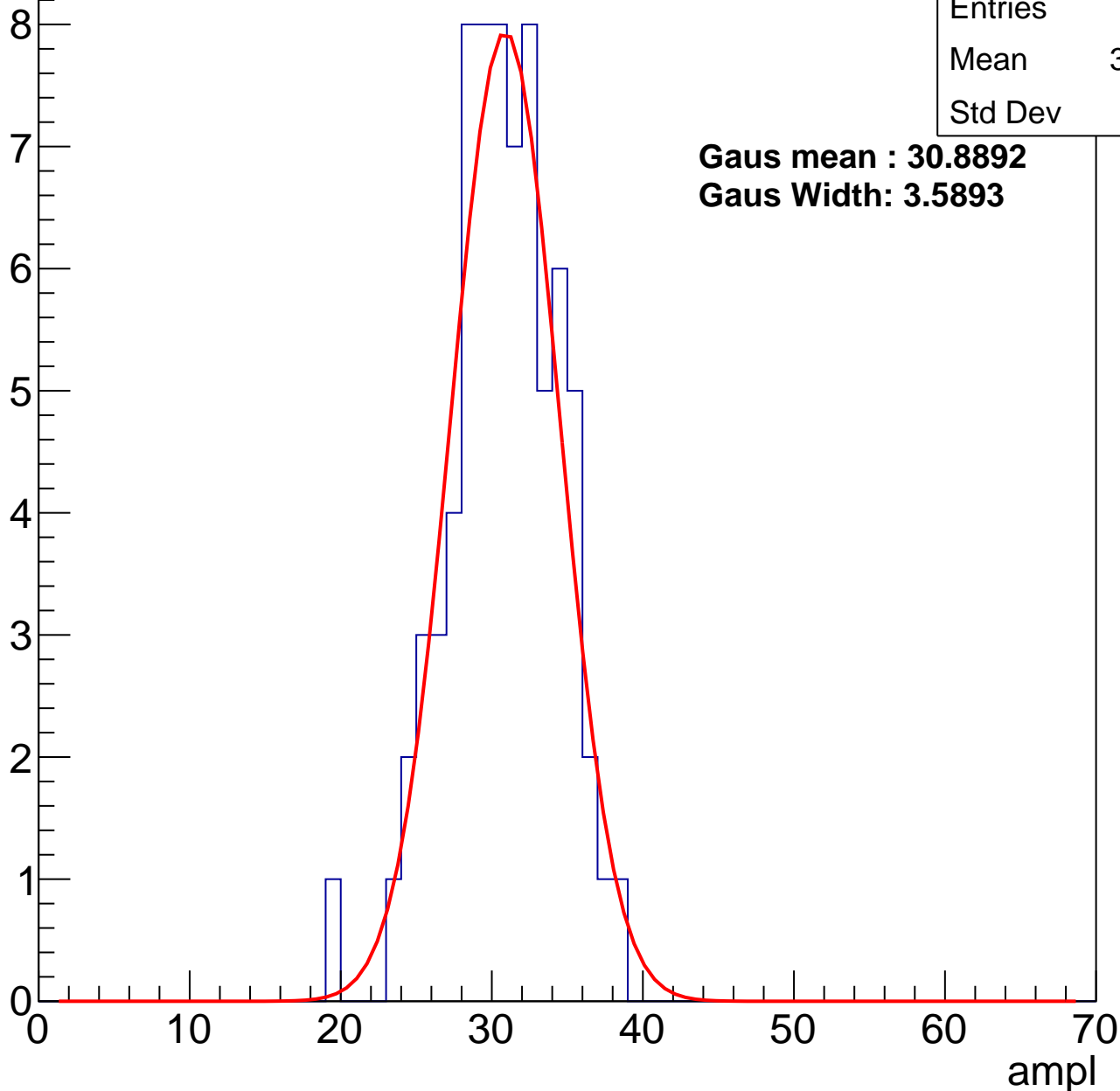
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.29
Std Dev	3.56

Gaus mean : 30.8892

Gaus Width: 3.5893



B1L101S, U21-ch83, adc1

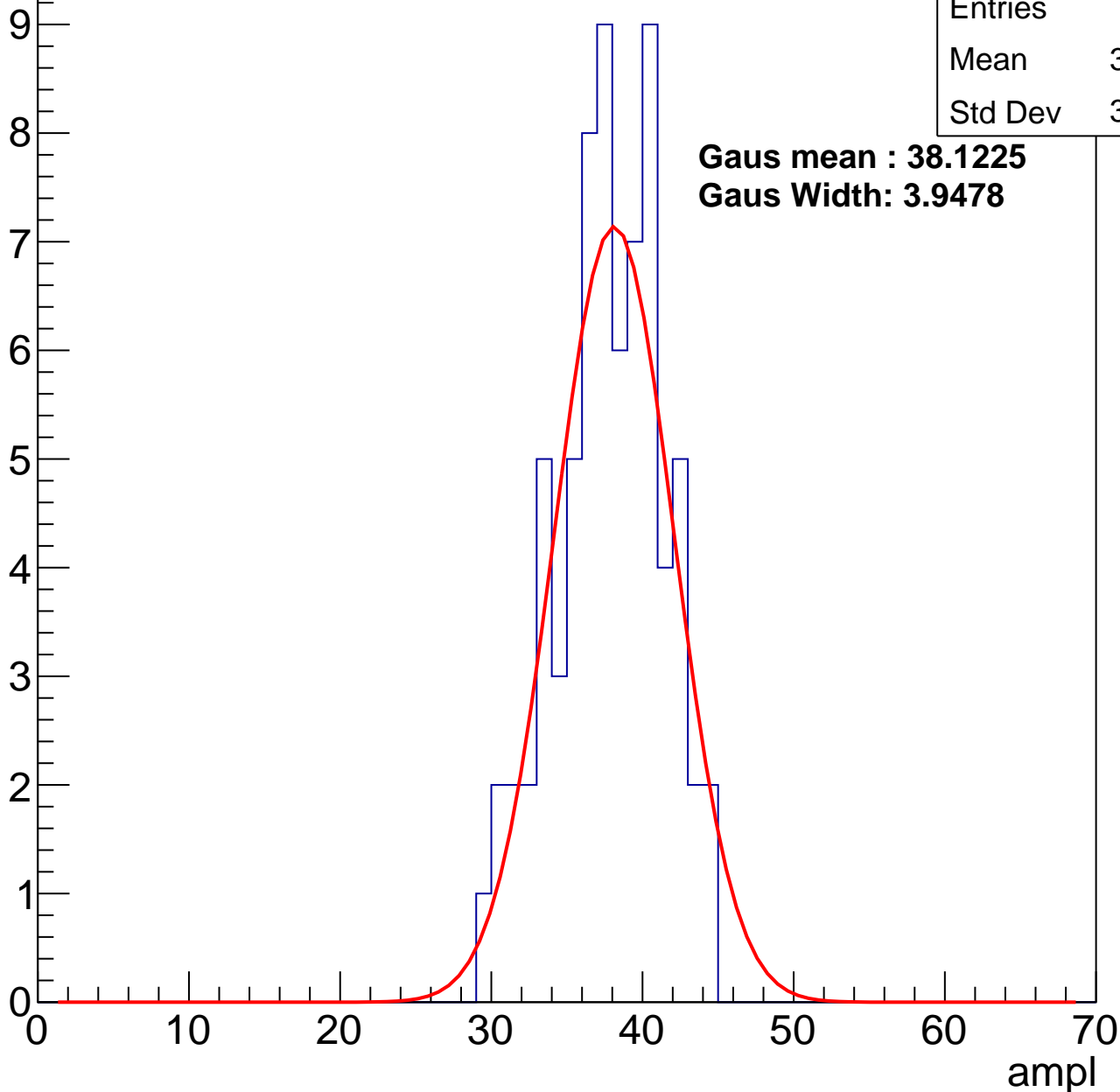
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.32
Std Dev	3.507

Gaus mean : 38.1225

Gaus Width: 3.9478



B1L101S, U21-ch83, adc2

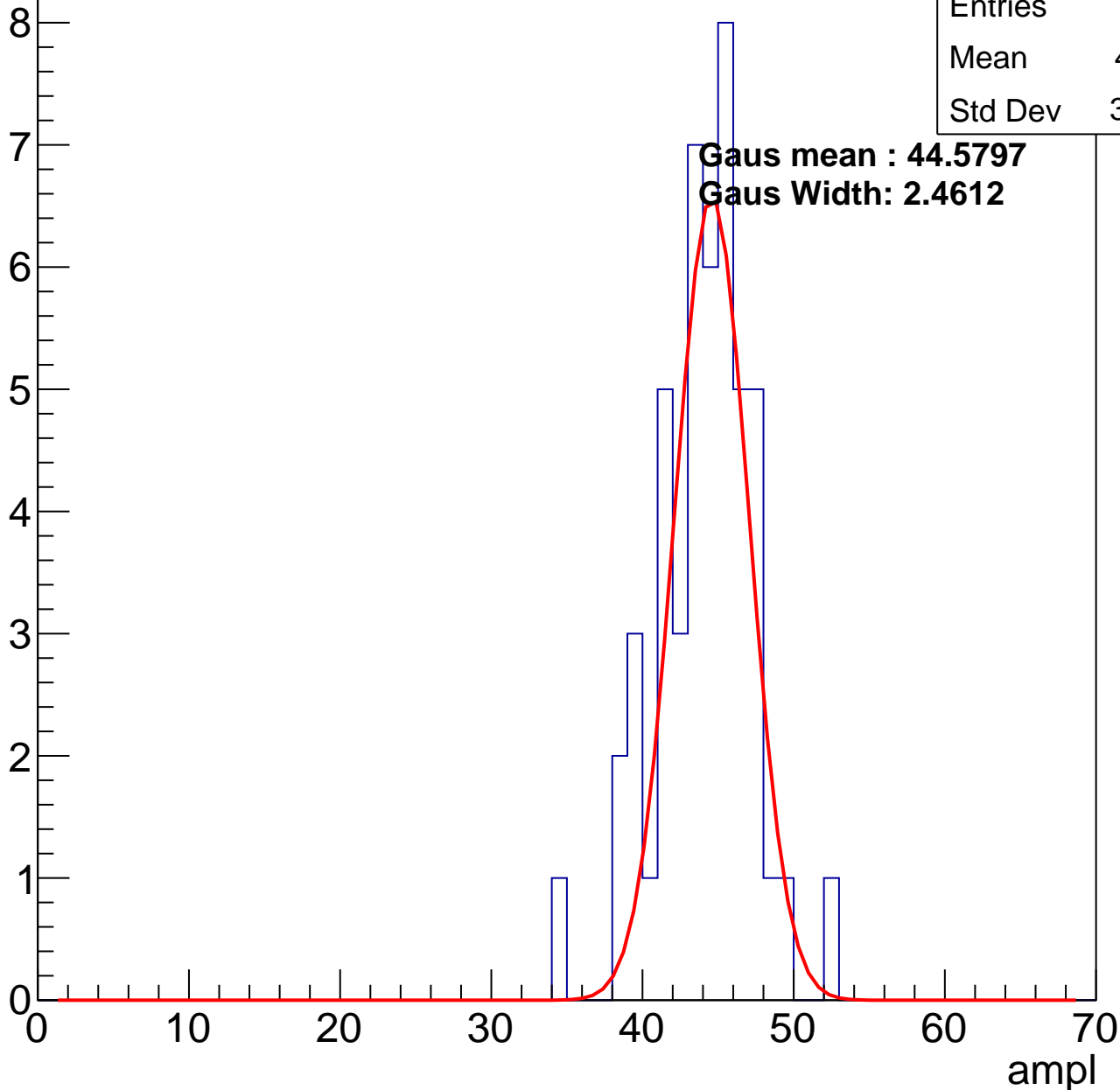
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	43.61
Std Dev	3.187

Gaus mean : 44.5797

Gaus Width: 2.4612



B1L101S, U21-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	49.7
Std Dev	4.053

Entry

10

8

6

4

2

0

0

10

20

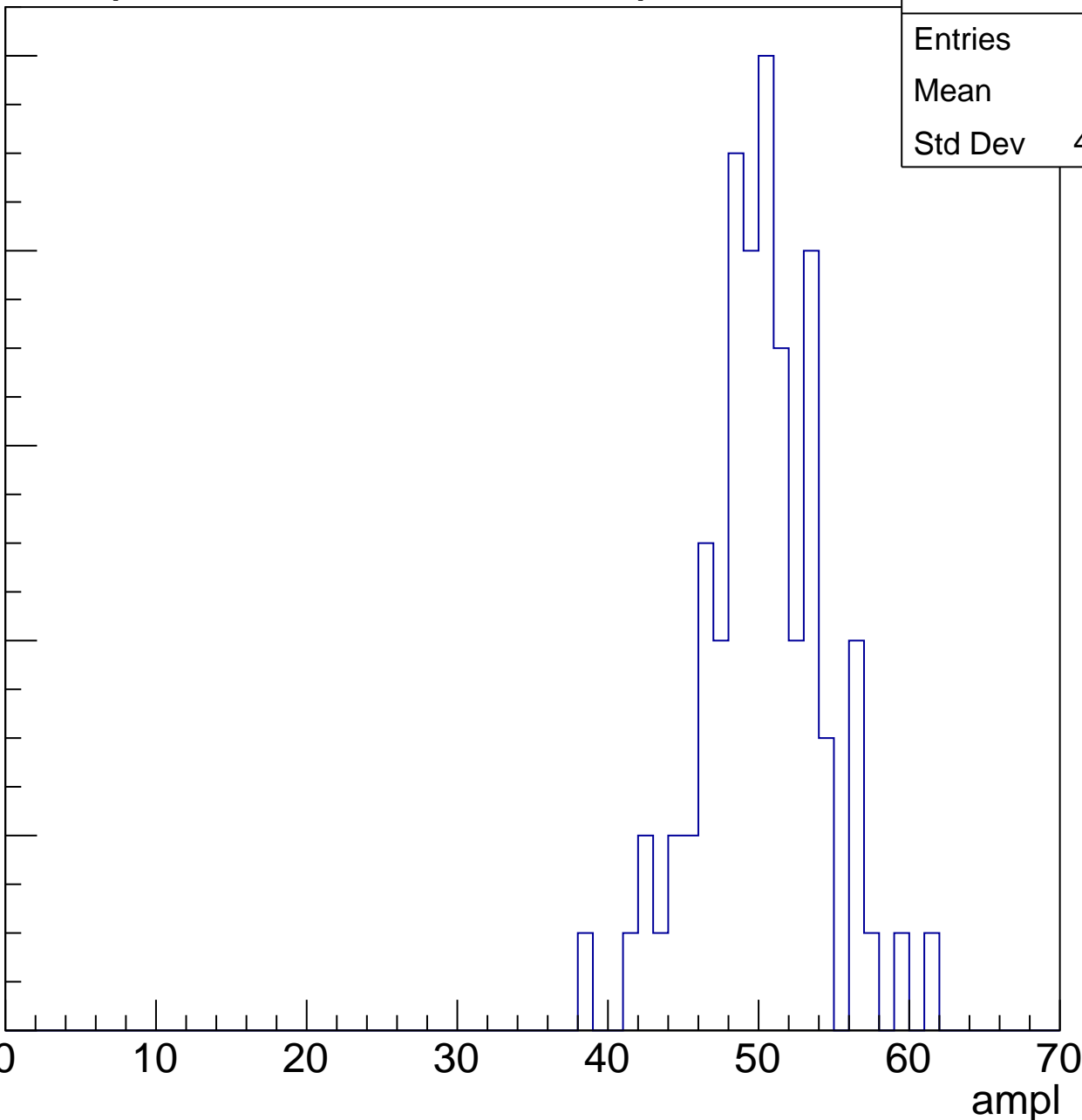
30

40

50

60

ampl

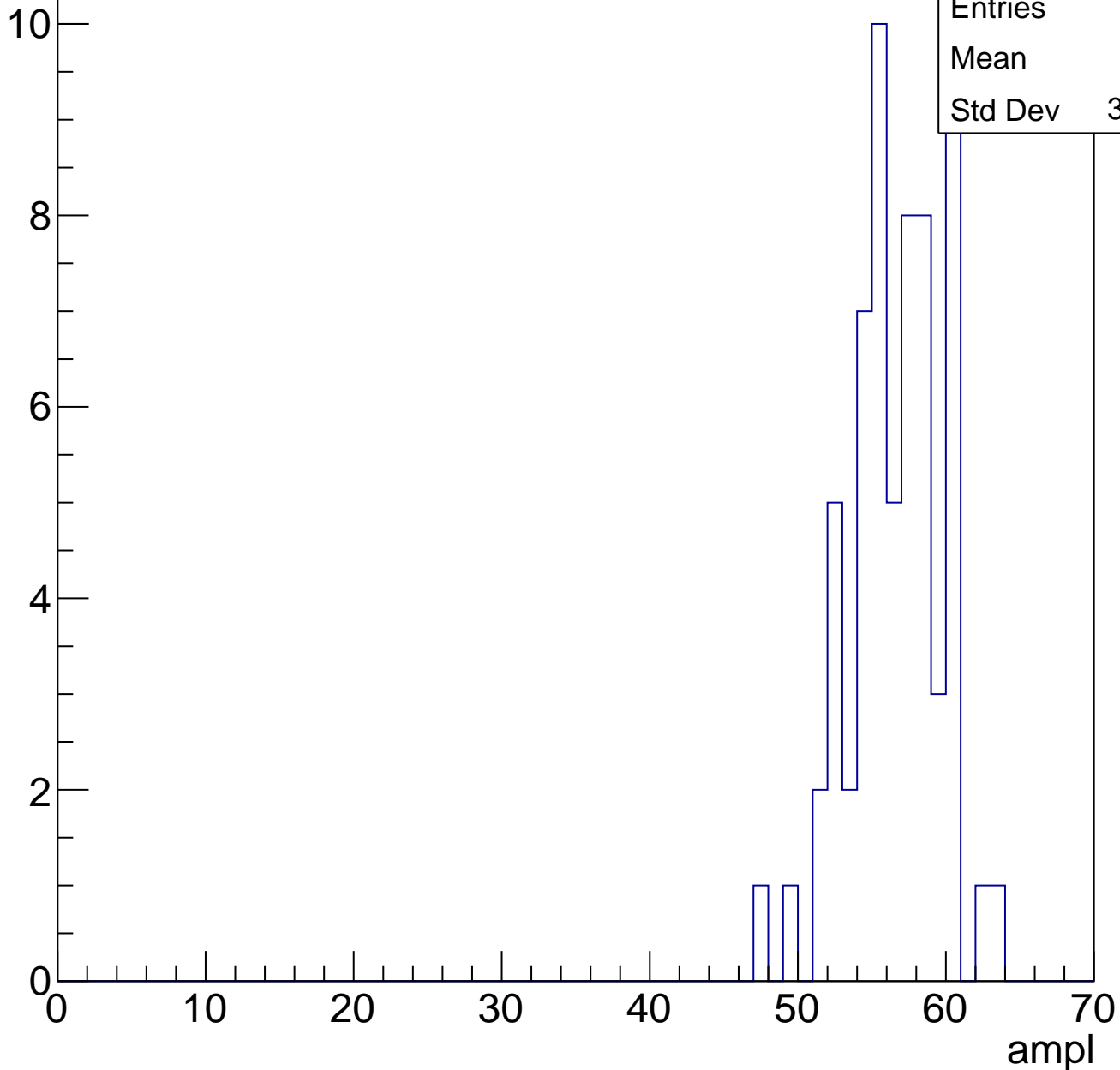


B1L101S, U21-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	56.1
Std Dev	3.115

Entry

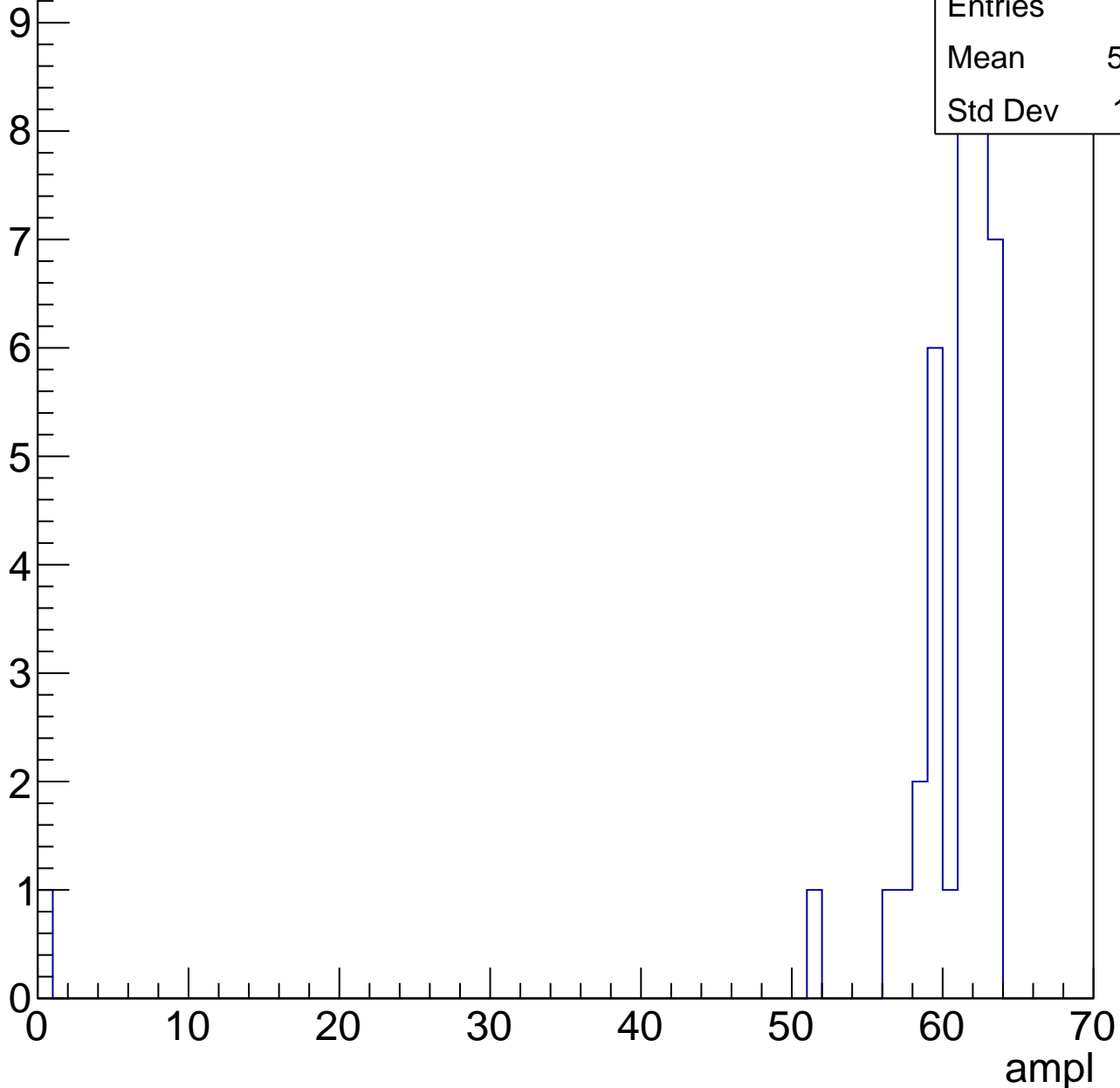


B1L101S, U21-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.92
Std Dev	10.11



B1L101S, U21-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch84, adc0

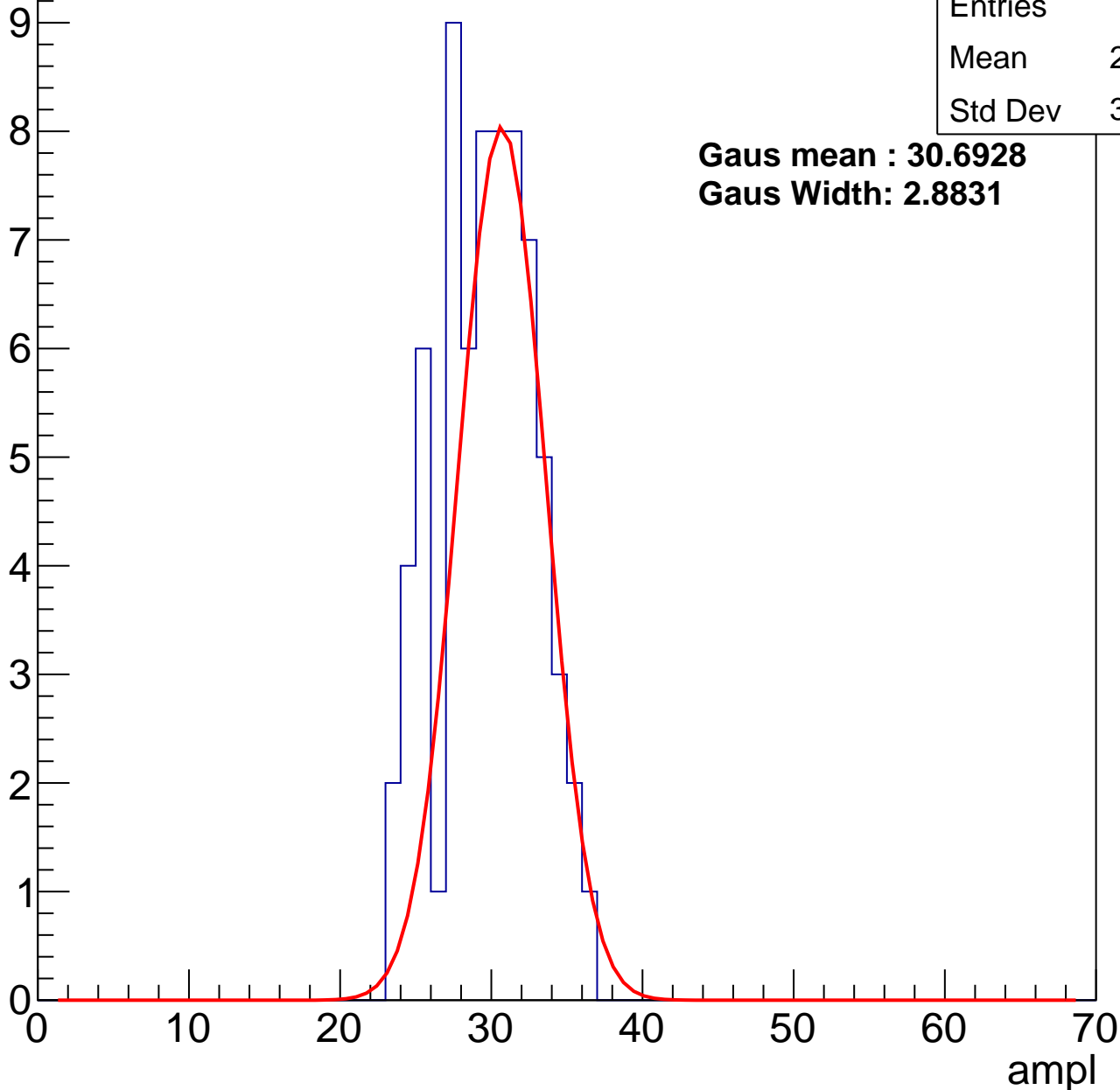
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.23
Std Dev	3.149

Gaus mean : 30.6928

Gaus Width: 2.8831



B1L101S, U21-ch84, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.47
Std Dev	3.847

Gaus mean : 37.1580

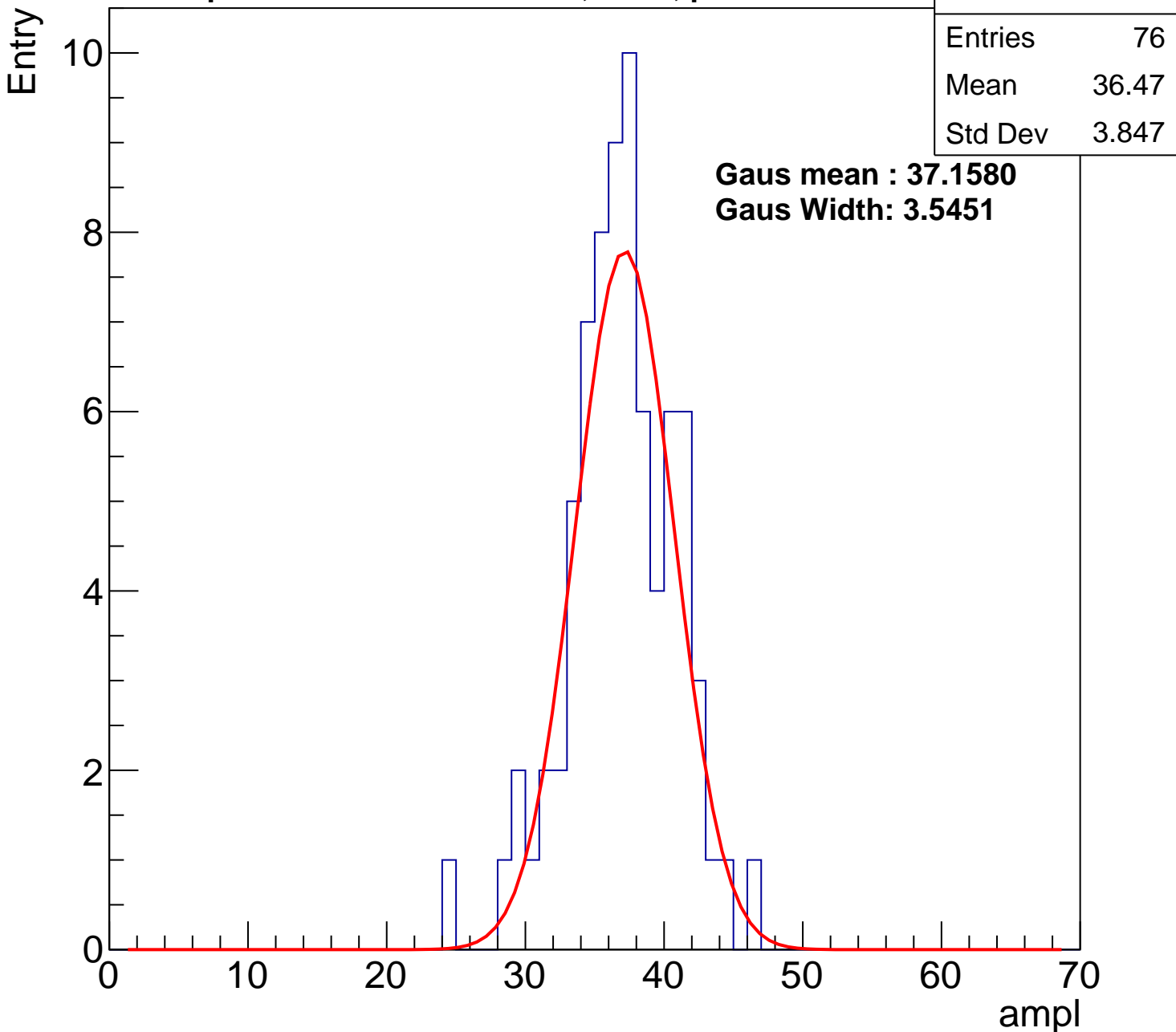
Gaus Width: 3.5451

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch84, adc2

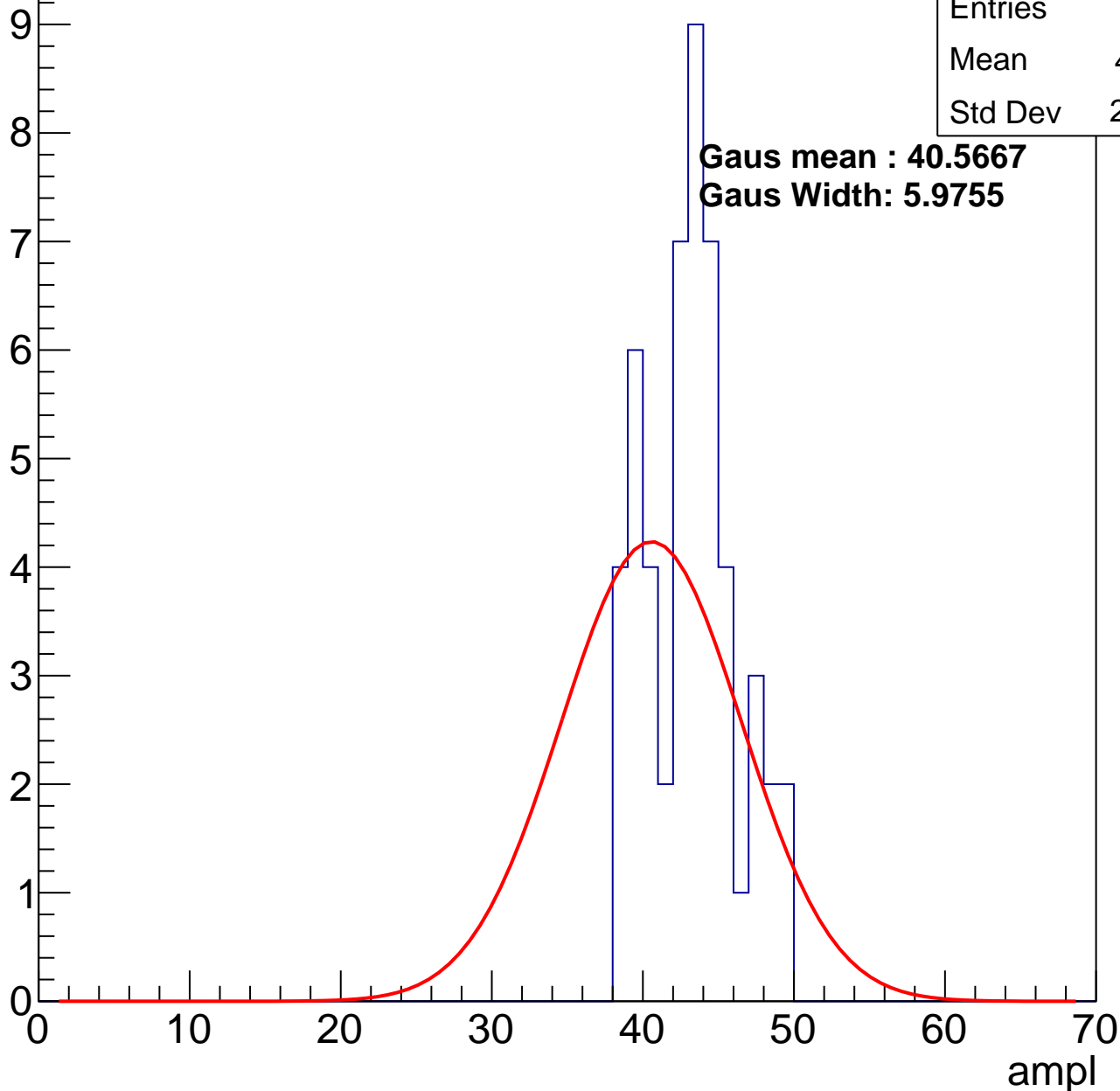
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.71
Std Dev	2.953

Gaus mean : 40.5667

Gaus Width: 5.9755

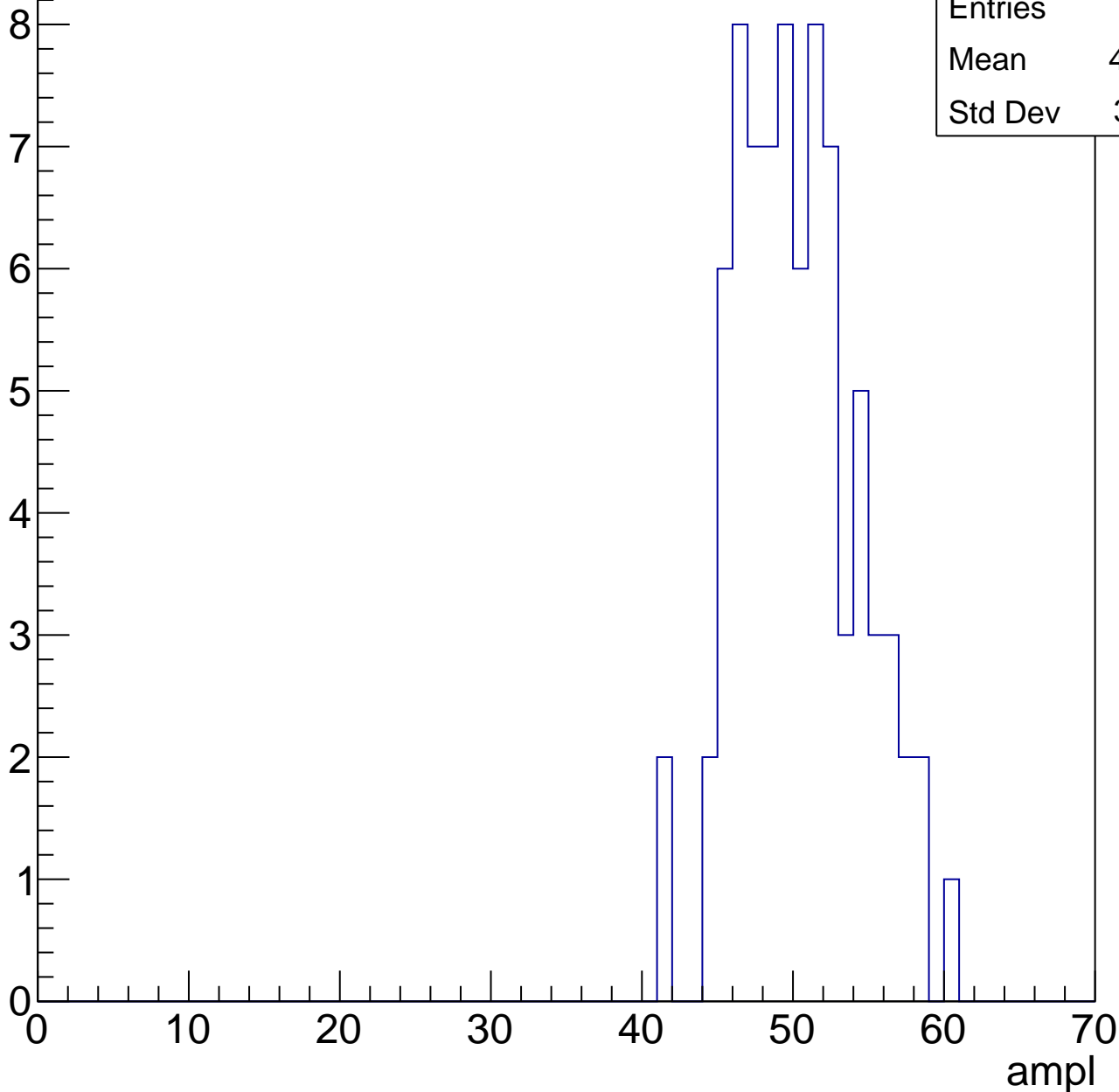


B1L101S, U21-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	49.86
Std Dev	3.971

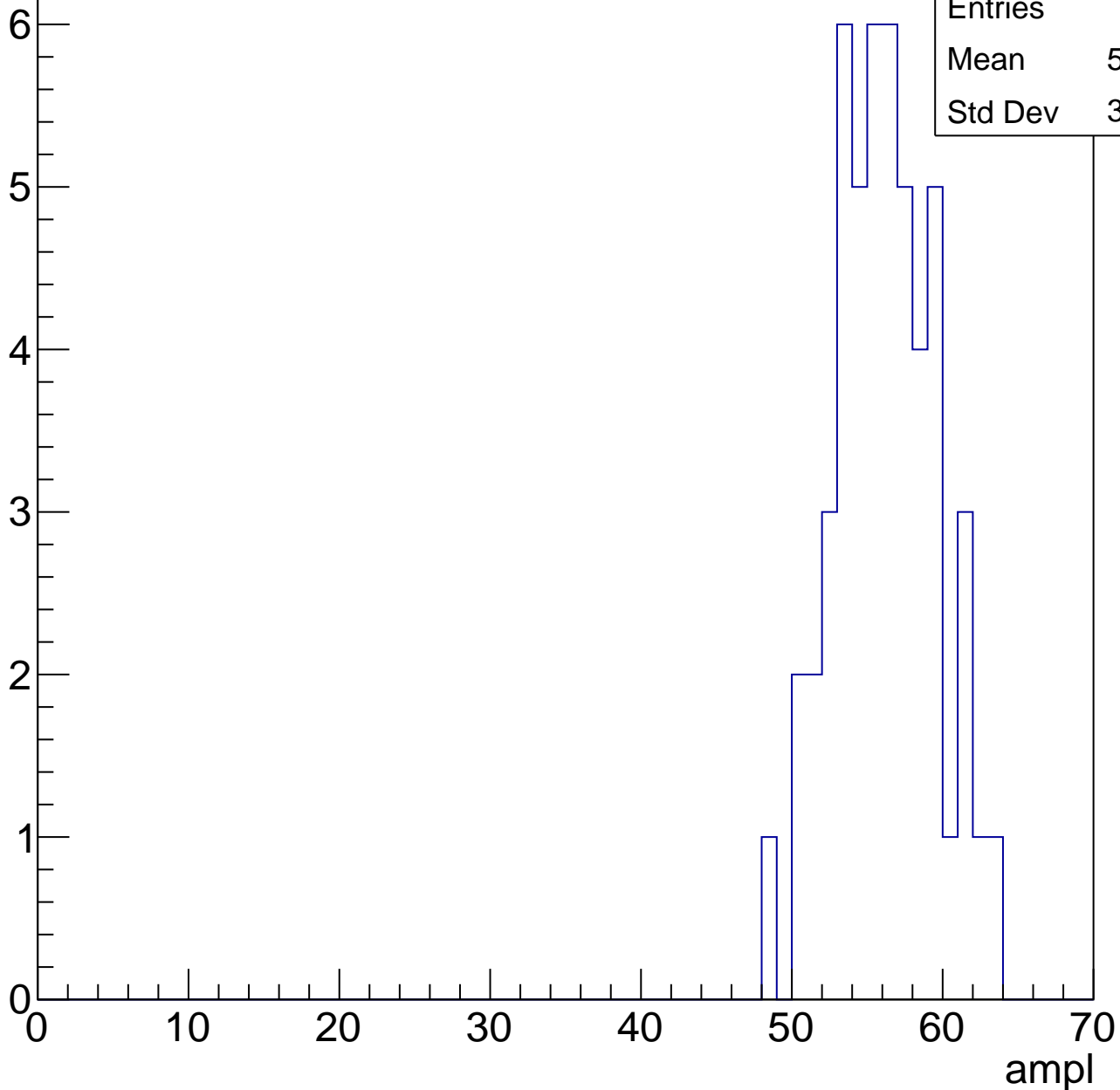


B1L101S, U21-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.69
Std Dev	3.287

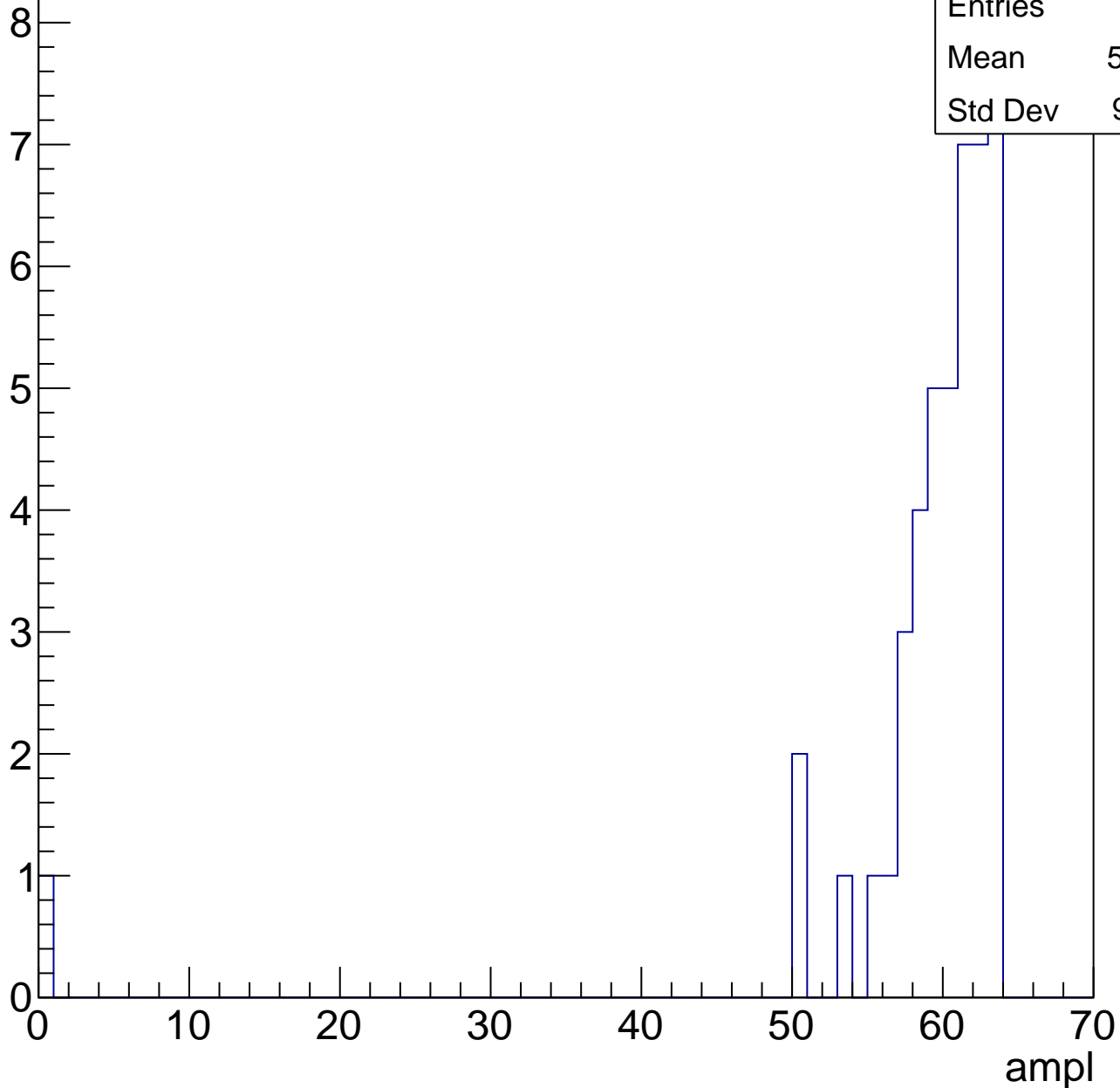


B1L101S, U21-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

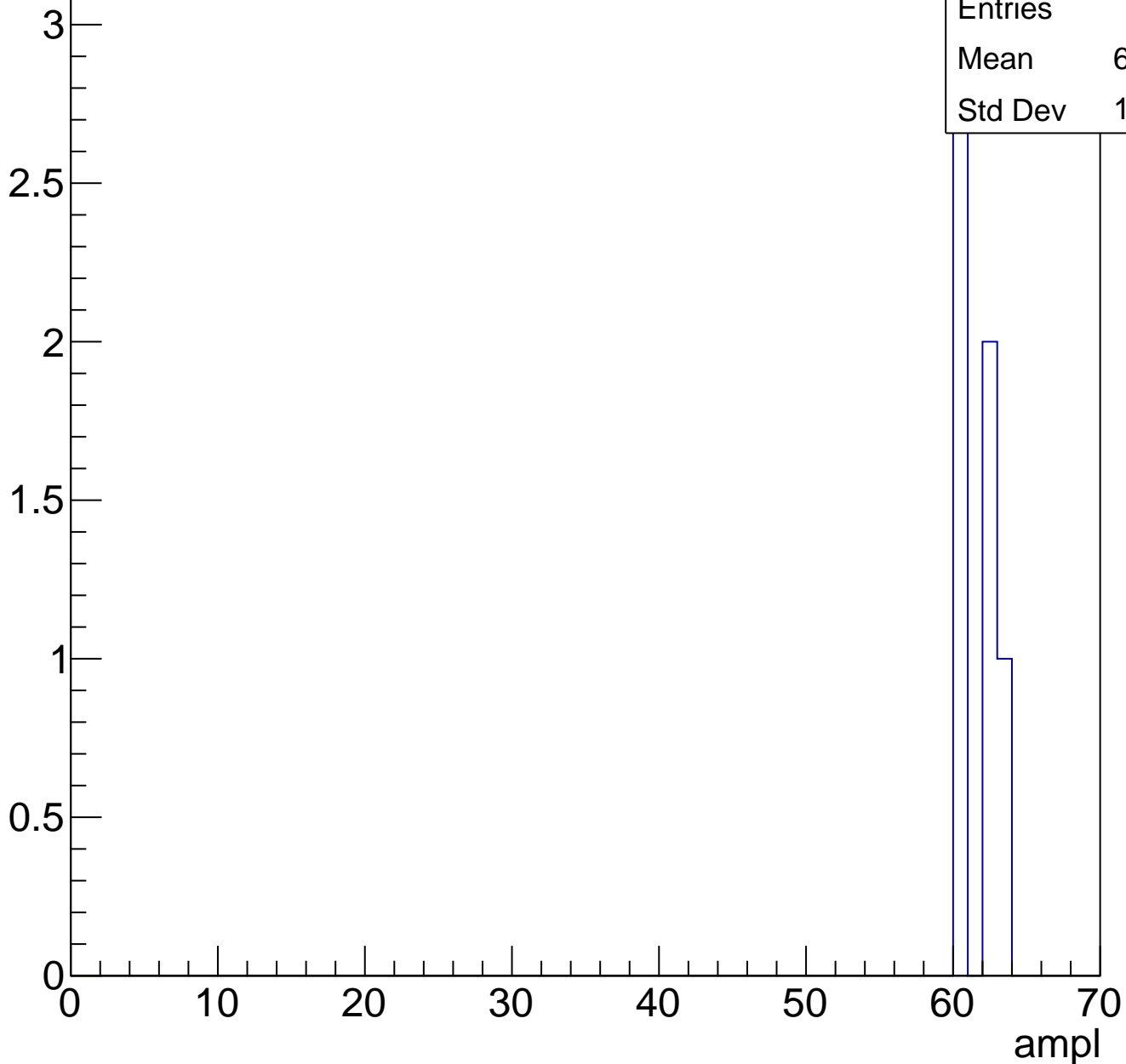
Entries	45
Mean	58.38
Std Dev	9.341



B1L101S, U21-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch85, adc0

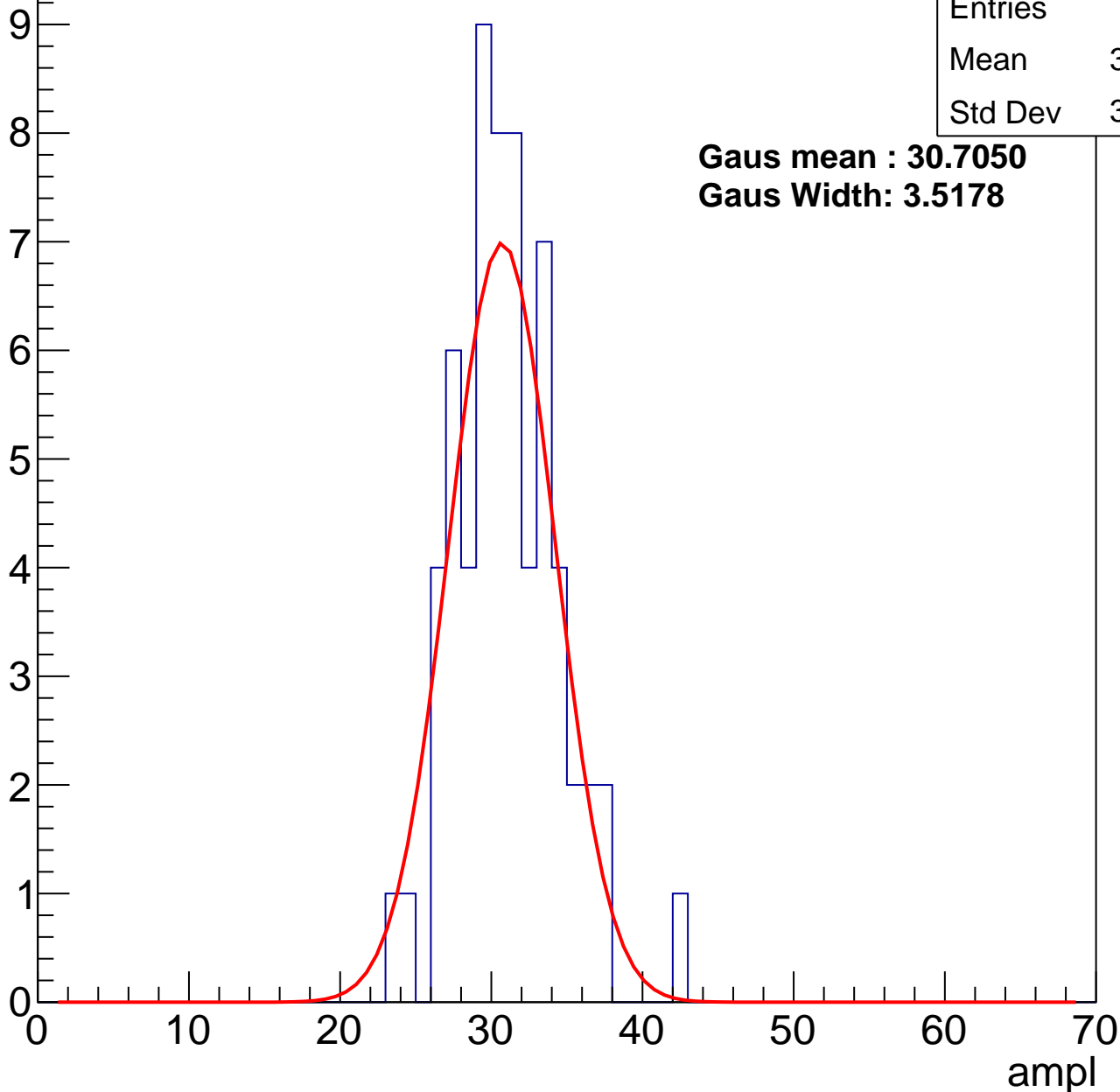
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.59
Std Dev	3.384

Gaus mean : 30.7050

Gaus Width: 3.5178



B1L101S, U21-ch85, adc1

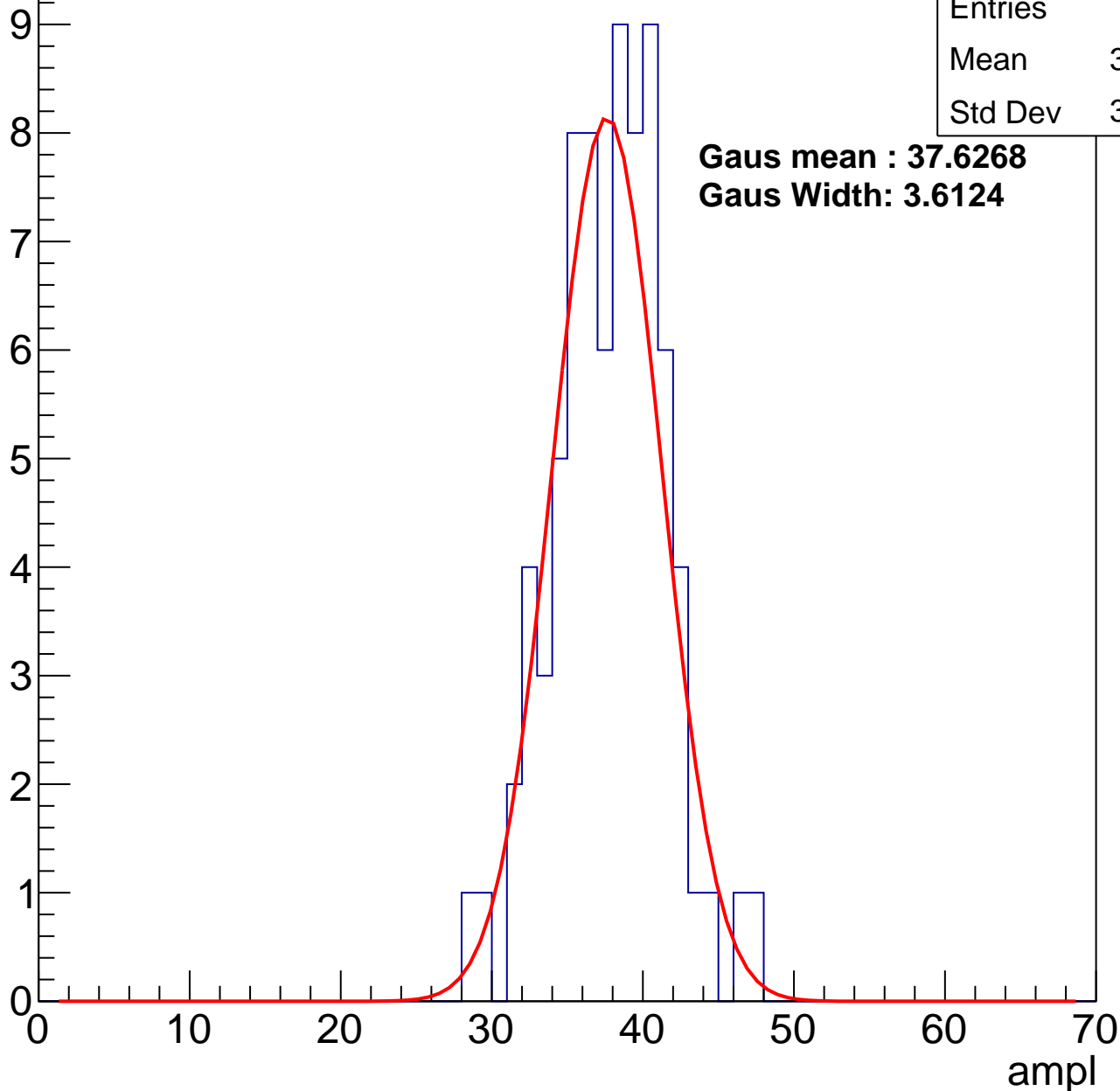
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	37.36
Std Dev	3.623

Gaus mean : 37.6268

Gaus Width: 3.6124



B1L101S, U21-ch85, adc2

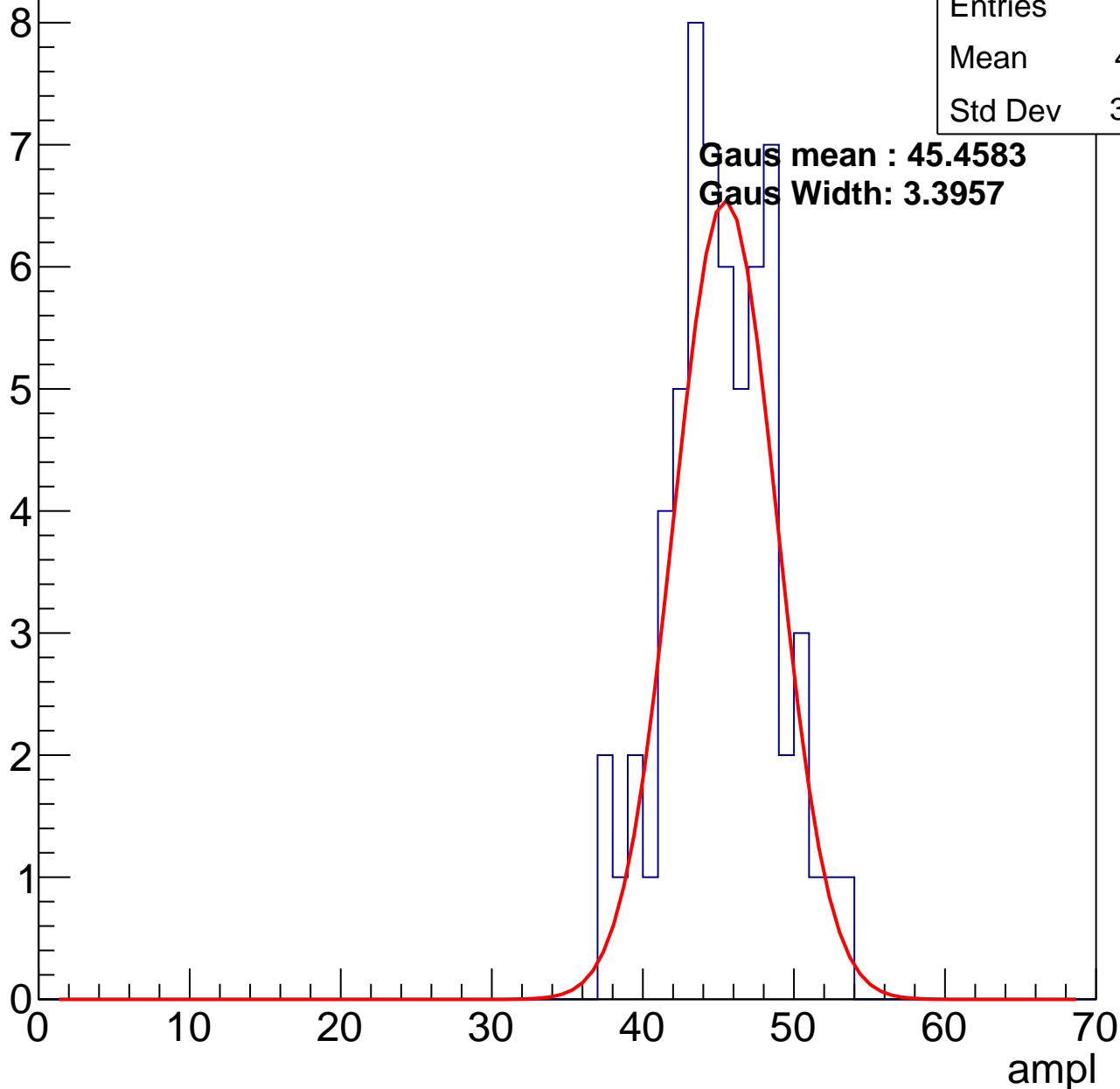
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.81
Std Dev	3.519

Gaus mean : 45.4583

Gaus Width: 3.3957

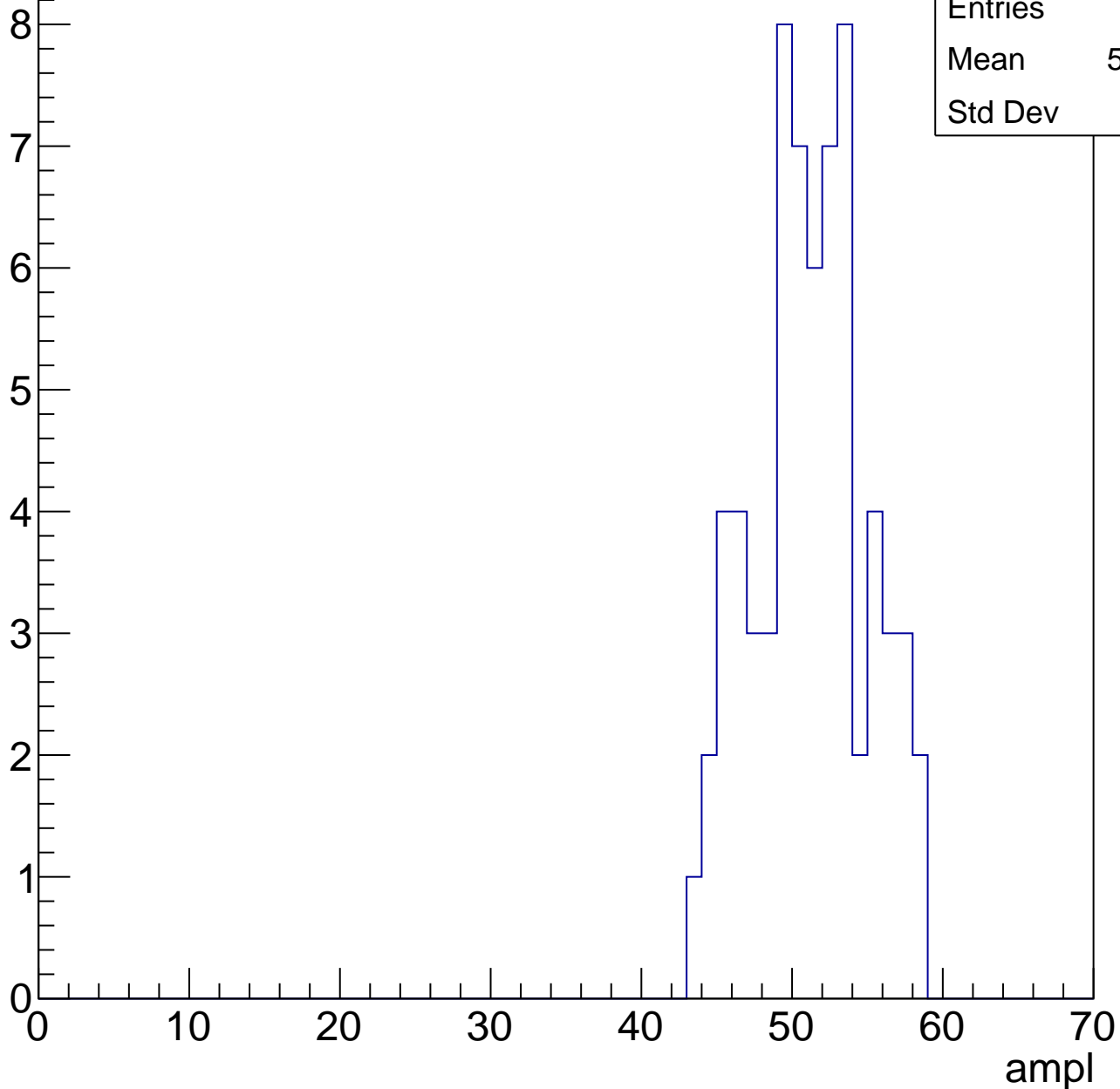


B1L101S, U21-ch85, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.73
Std Dev	3.7



B1L101S, U21-ch85, adc4

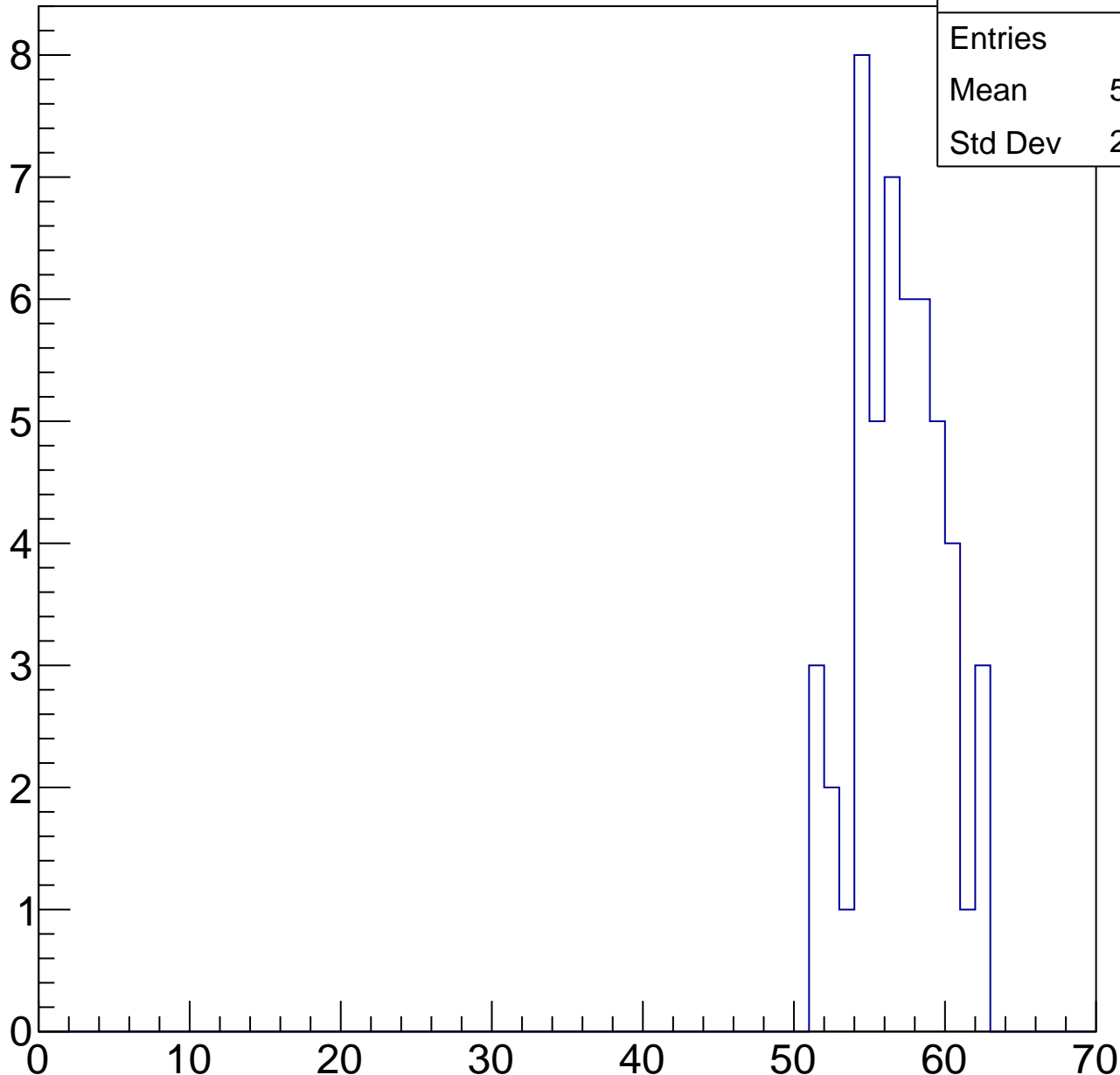
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	56.49
Std Dev	2.845

ampl

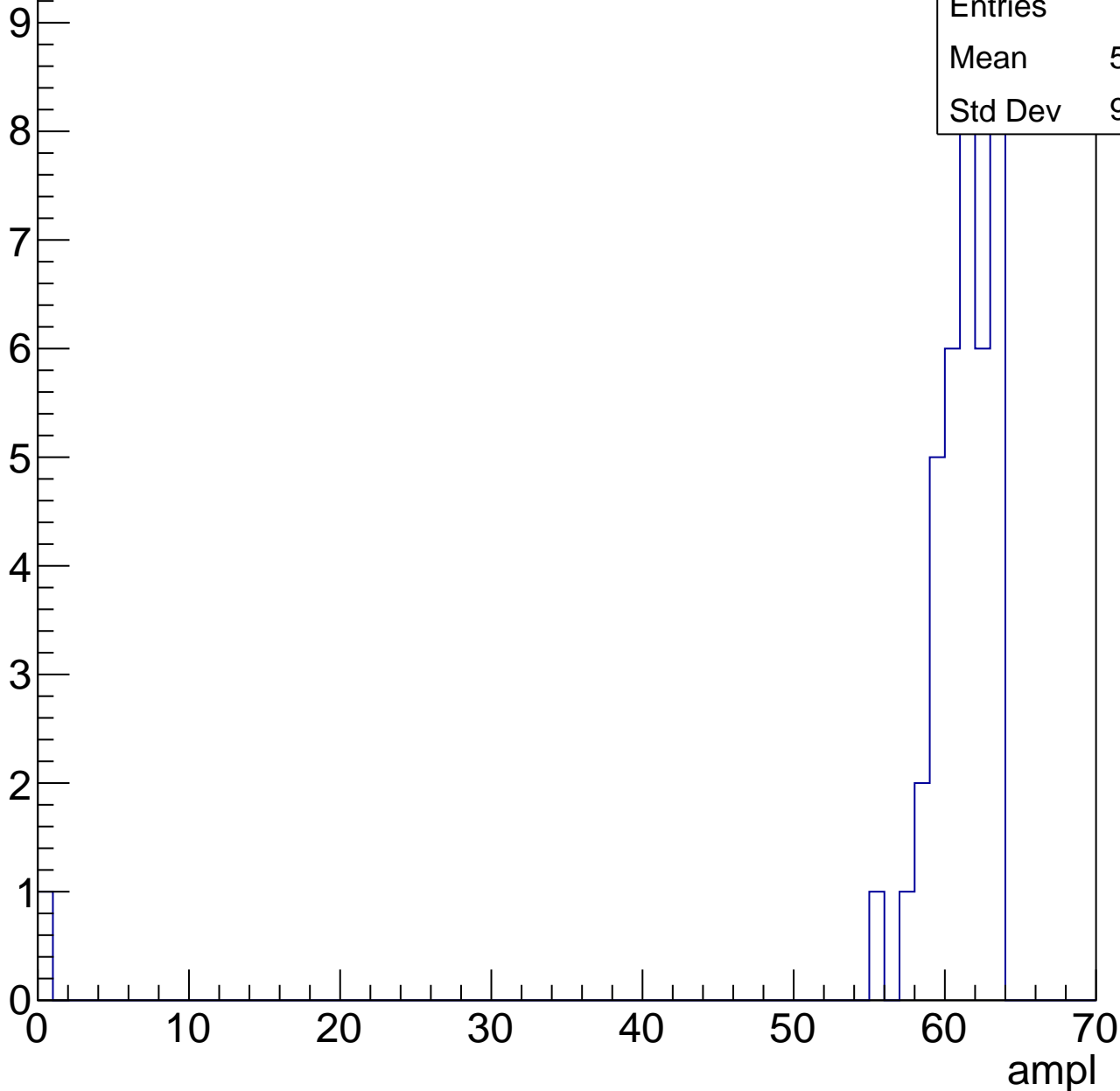


B1L101S, U21-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	59.18
Std Dev	9.774



B1L101S, U21-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch86, adc0

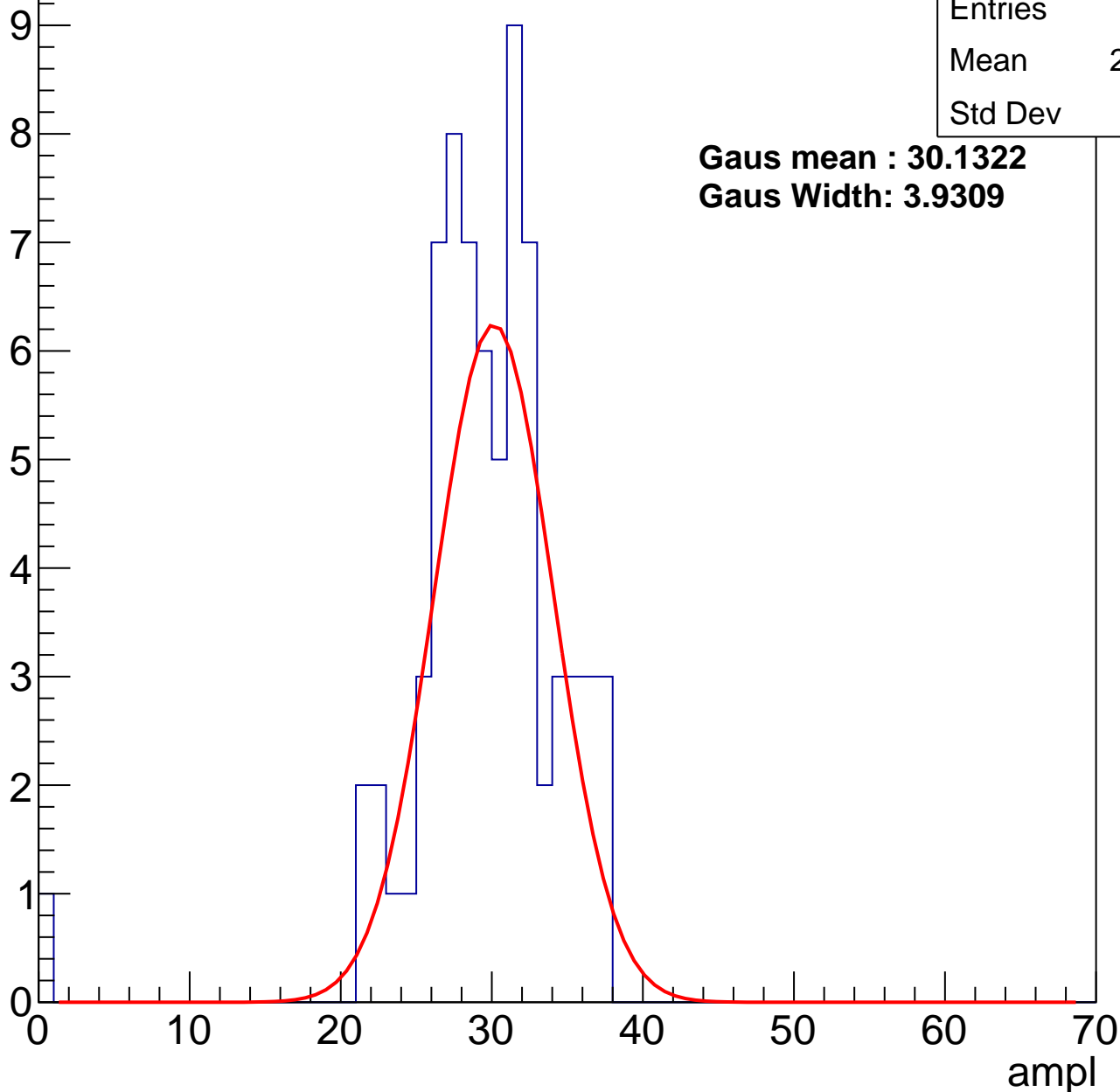
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	29.05
Std Dev	5.15

Gaus mean : 30.1322

Gaus Width: 3.9309



B1L101S, U21-ch86, adc1

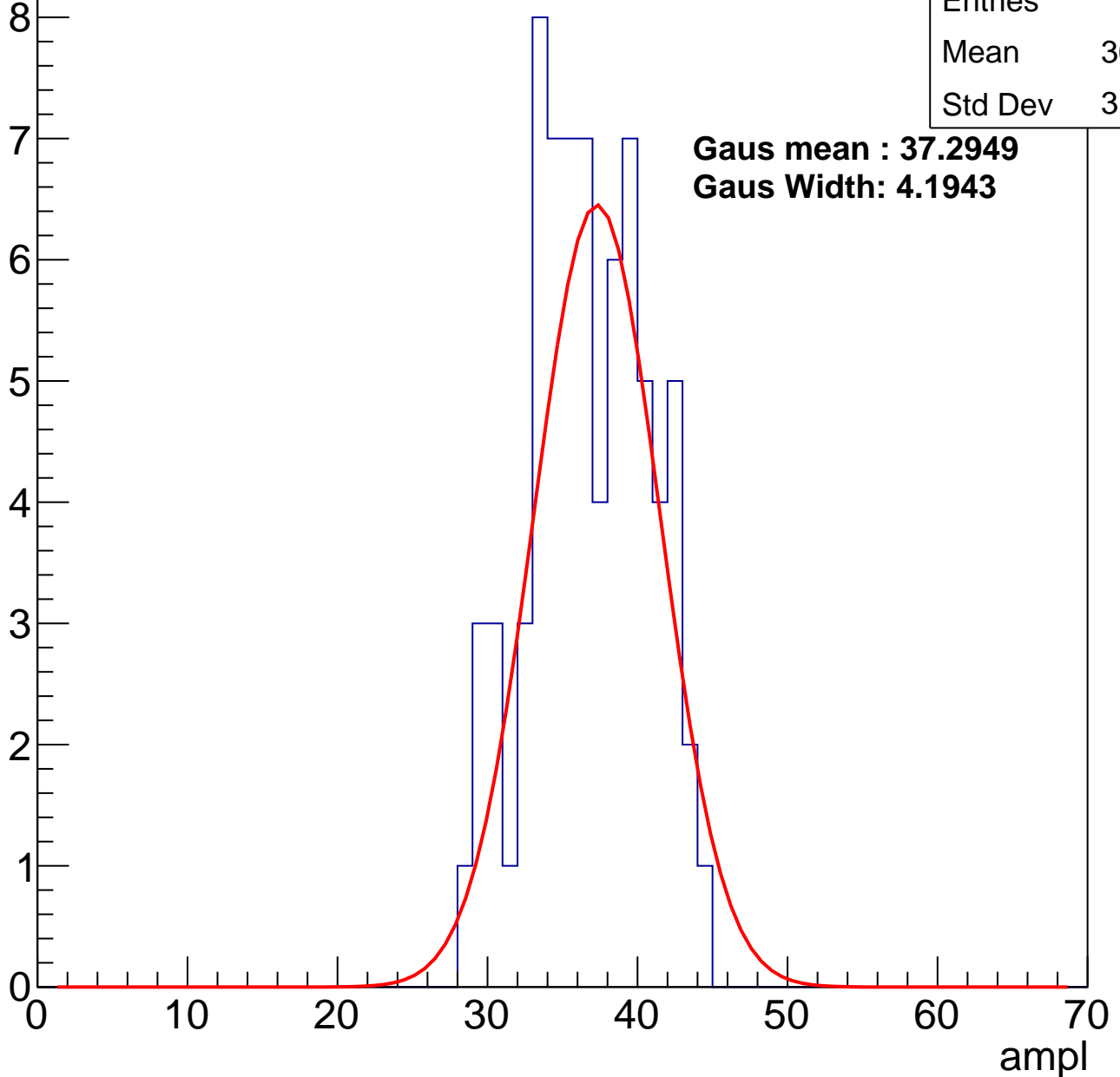
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.27
Std Dev	3.867

Gaus mean : 37.2949

Gaus Width: 4.1943



B1L101S, U21-ch86, adc2

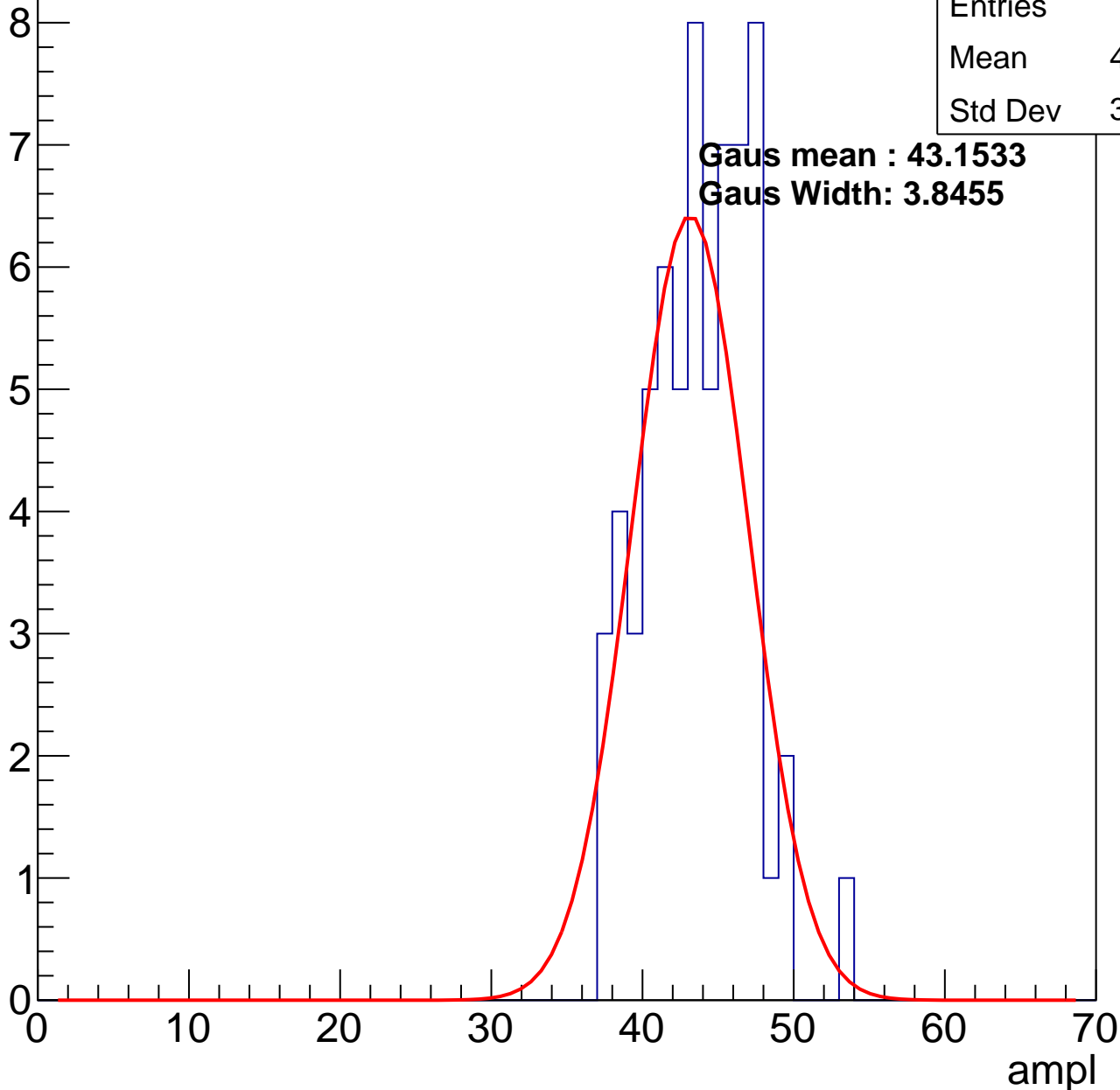
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	43.26
Std Dev	3.389

Gaus mean : 43.1533

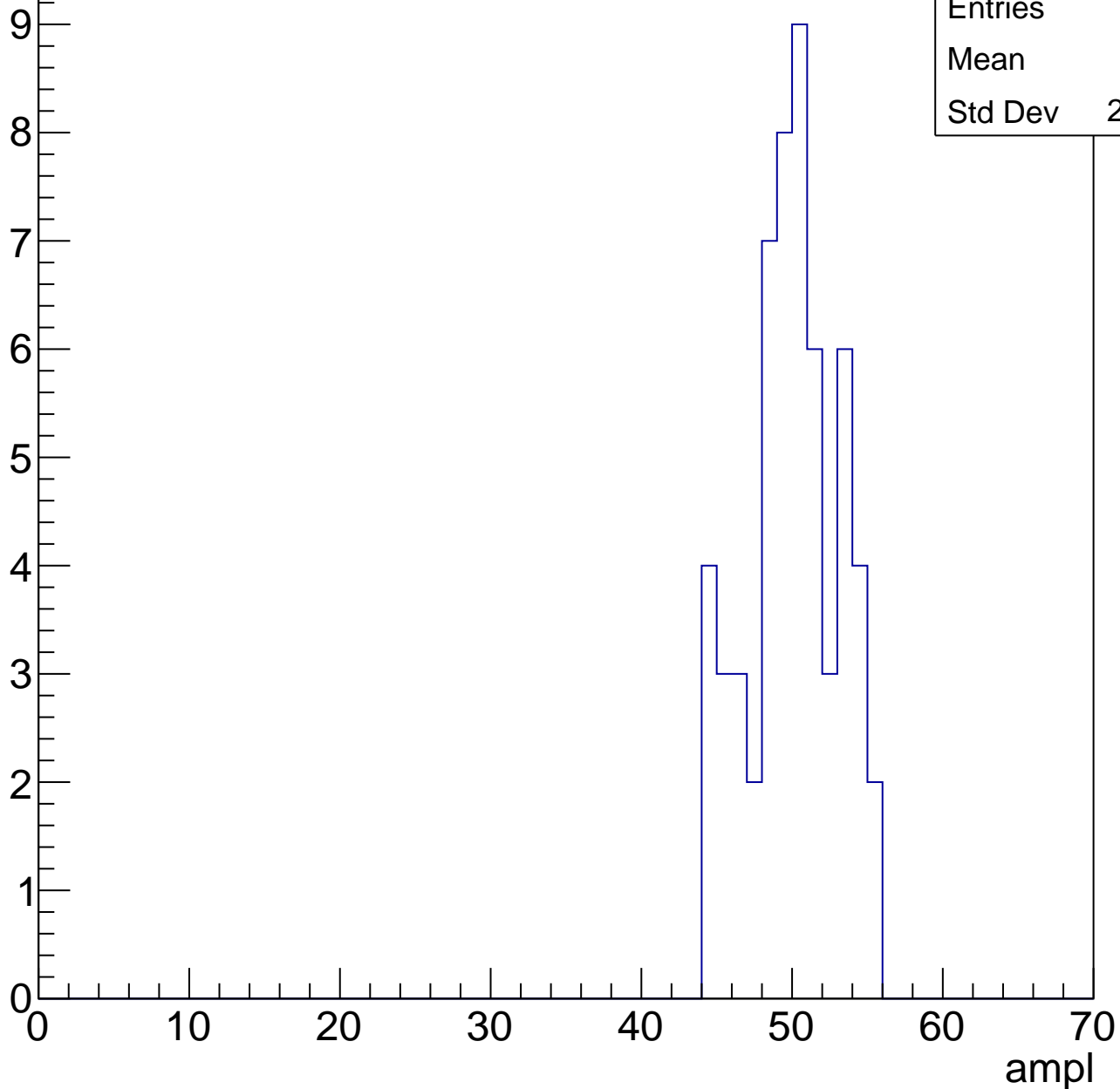
Gaus Width: 3.8455



B1L101S, U21-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

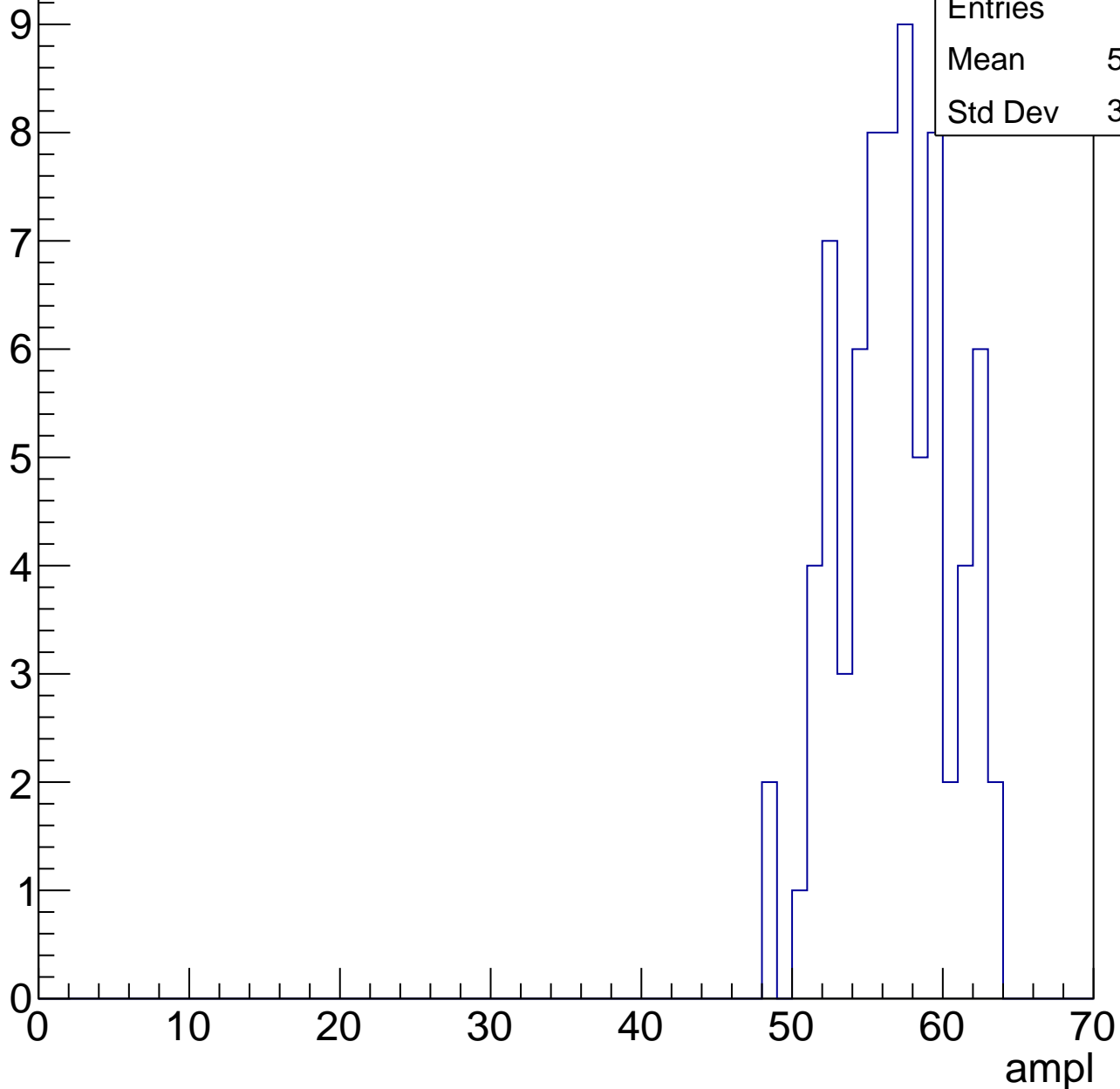
Entry



B1L101S, U21-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

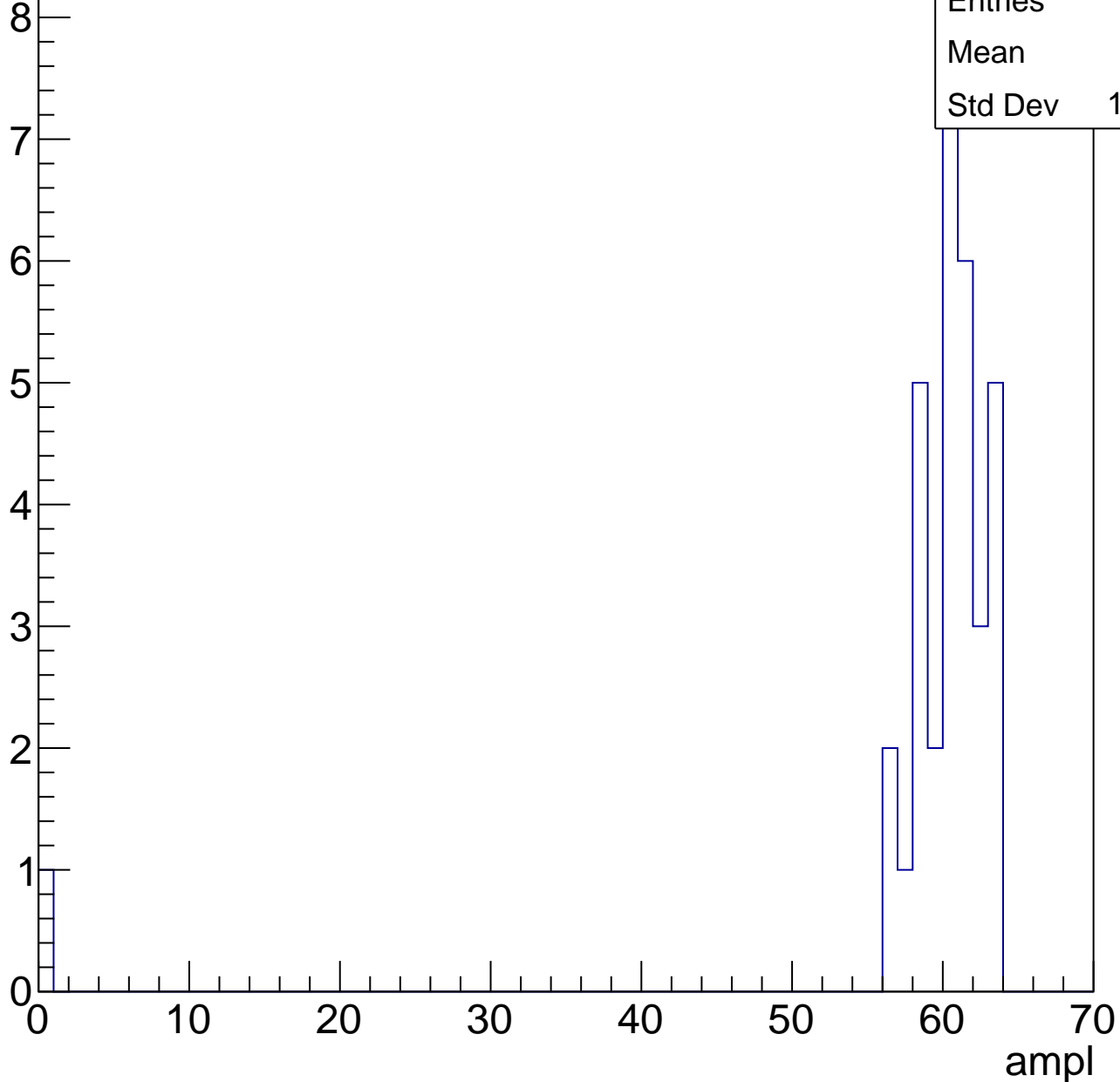


Entries	75
Mean	56.29
Std Dev	3.607

B1L101S, U21-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

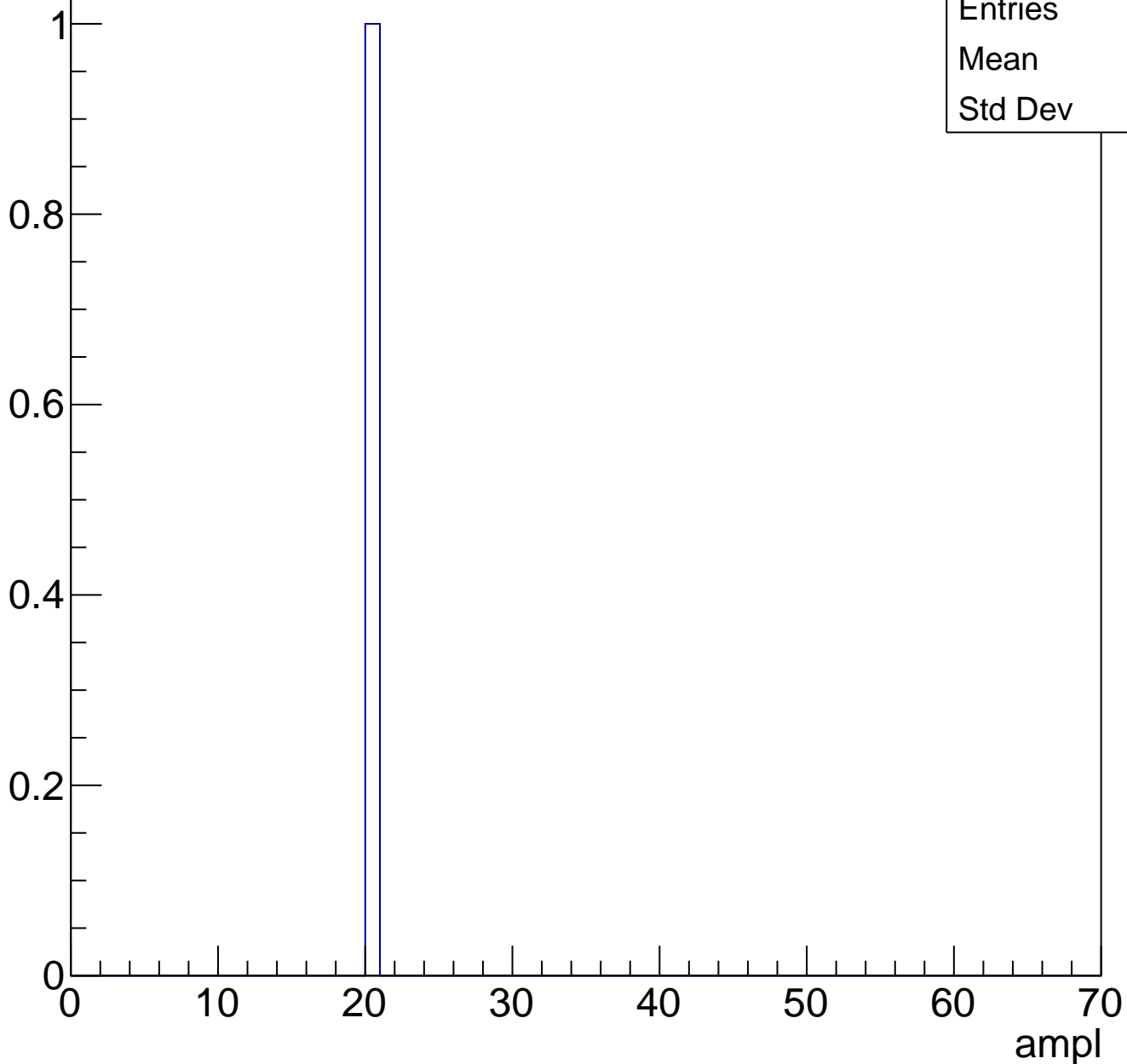
Entry



B1L101S, U21-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L101S, U21-ch87, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	30.16
Std Dev	3.777

Gaus mean : 30.4715

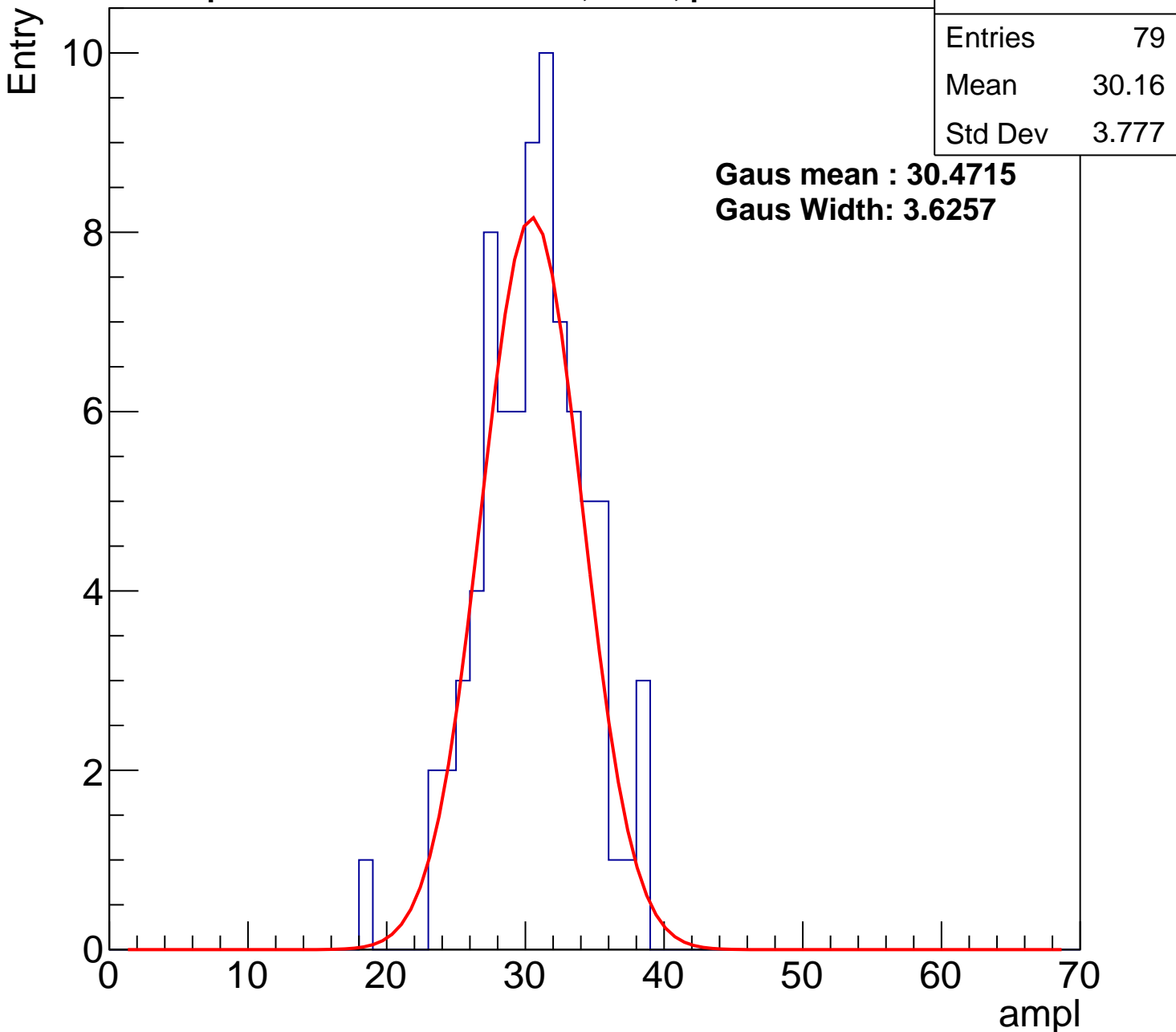
Gaus Width: 3.6257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch87, adc1

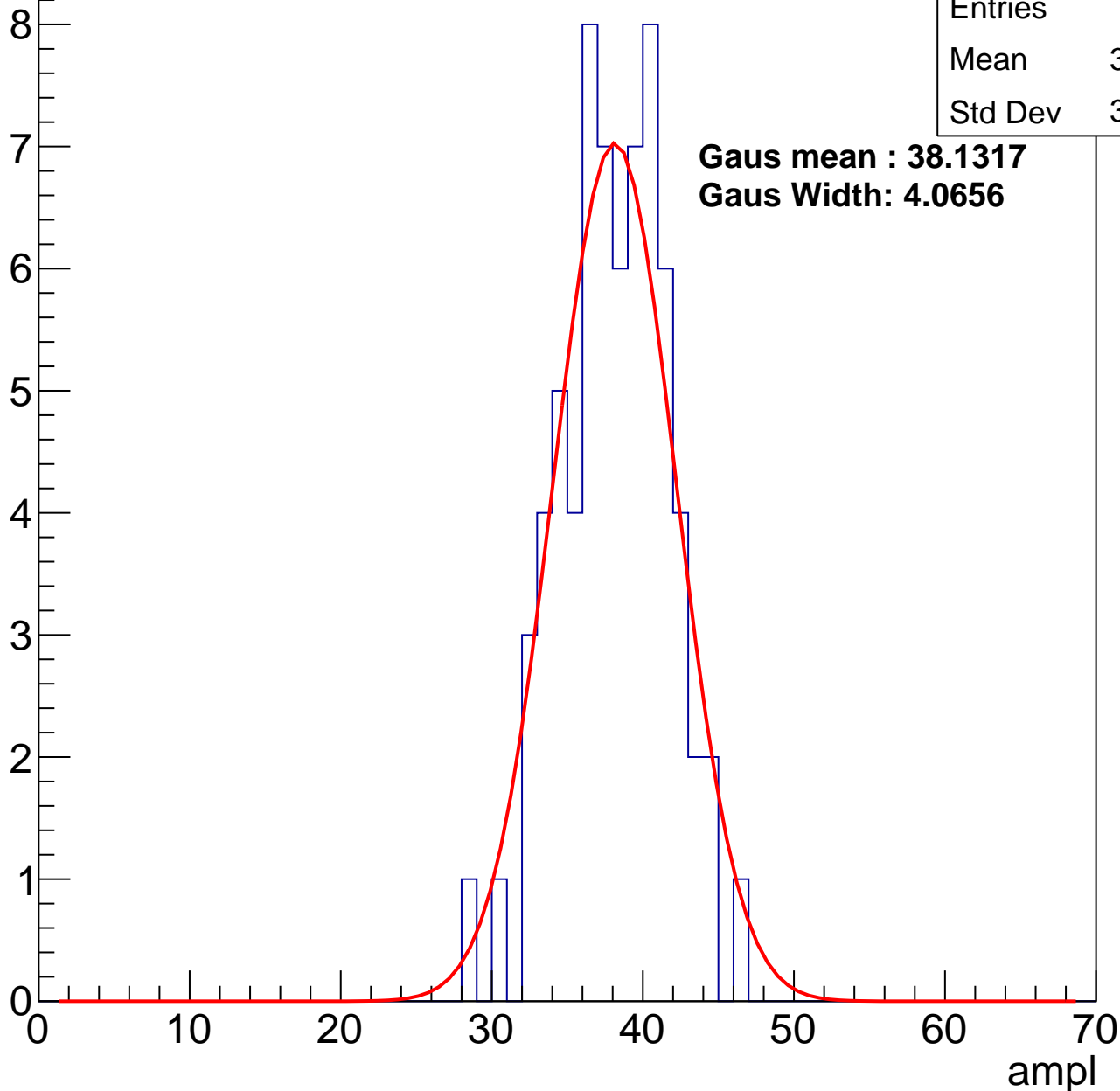
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.65
Std Dev	3.534

Gaus mean : 38.1317

Gaus Width: 4.0656



B1L101S, U21-ch87, adc2

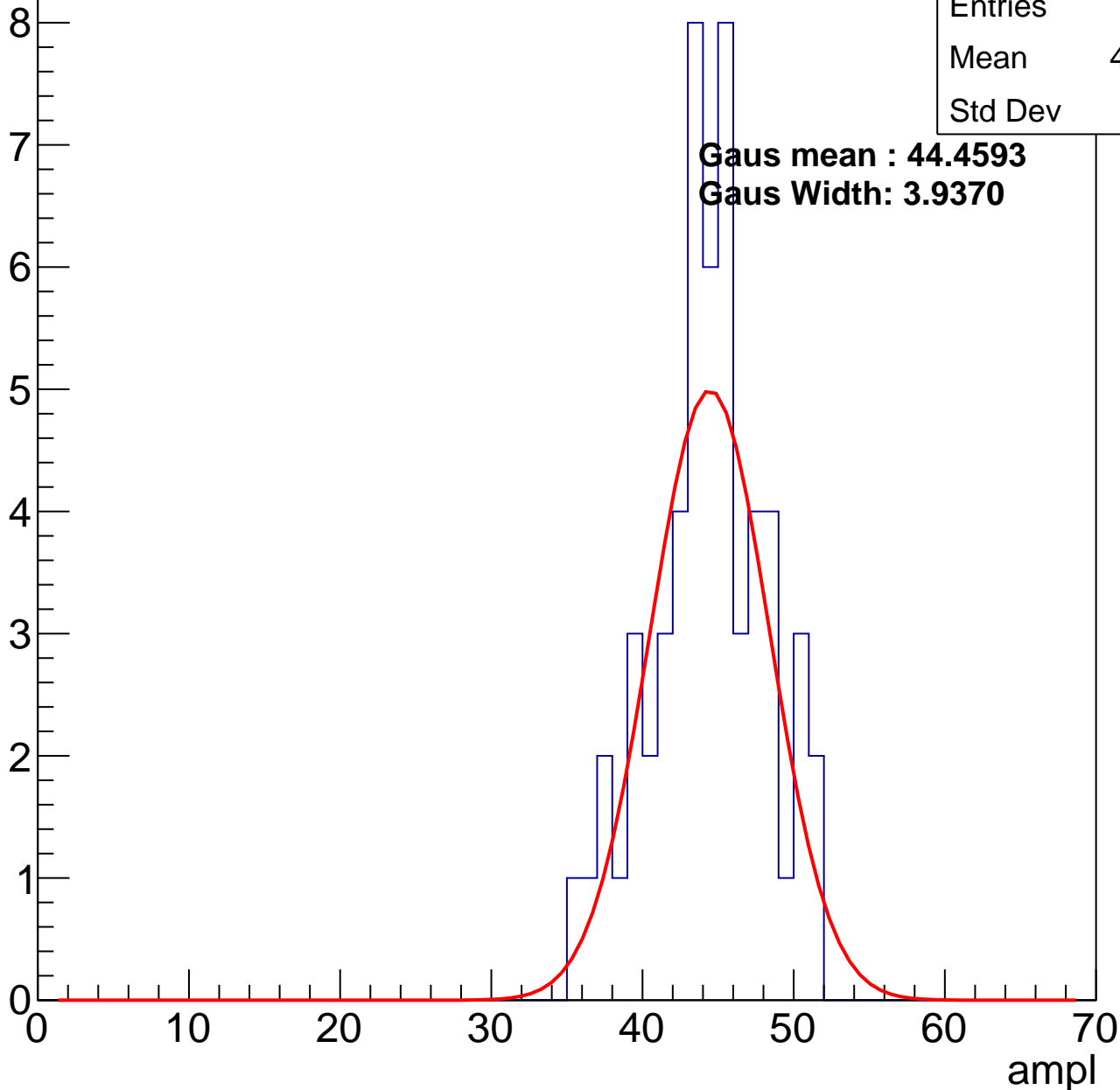
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.89
Std Dev	3.75

Gaus mean : 44.4593

Gaus Width: 3.9370

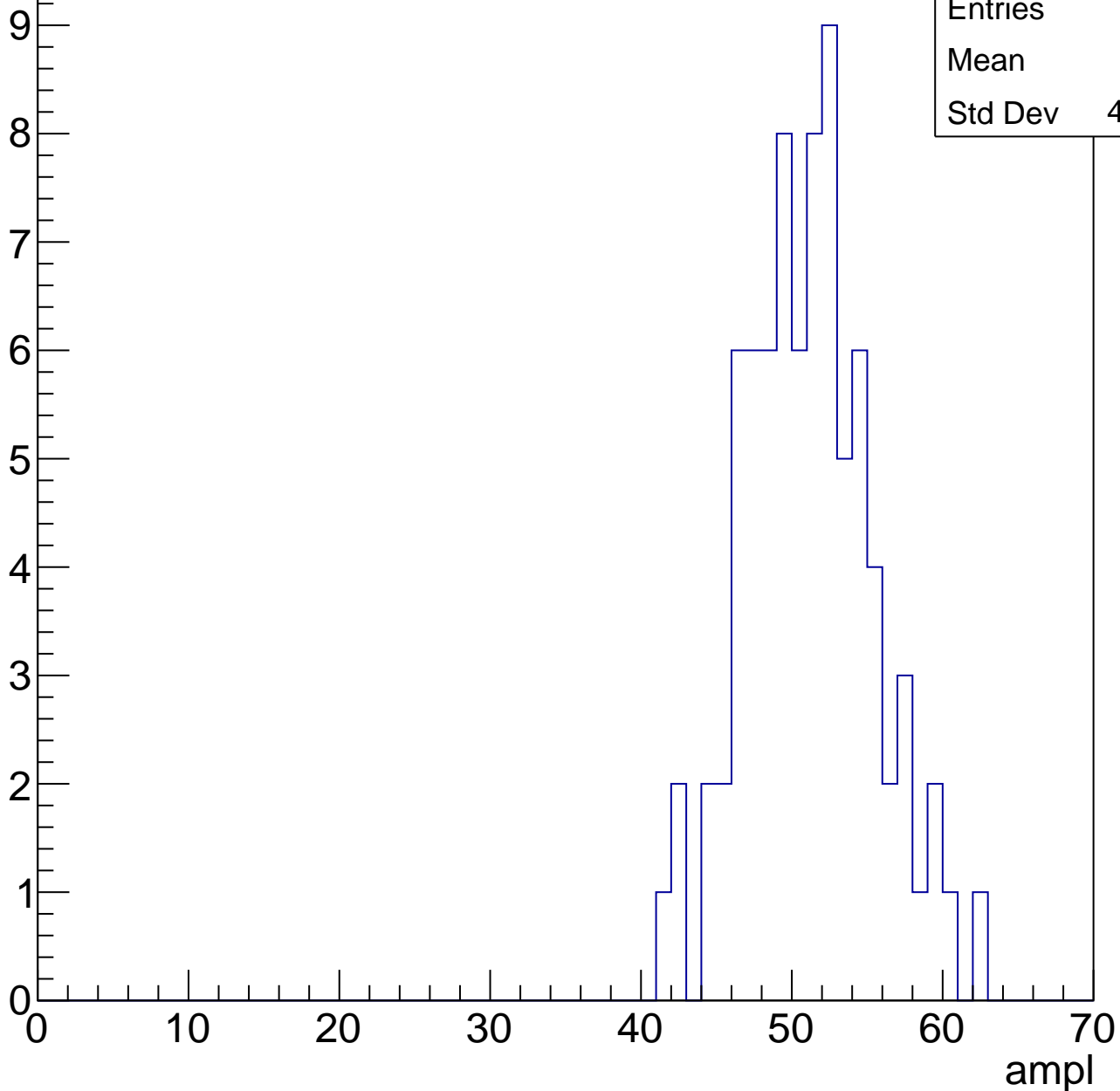


B1L101S, U21-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

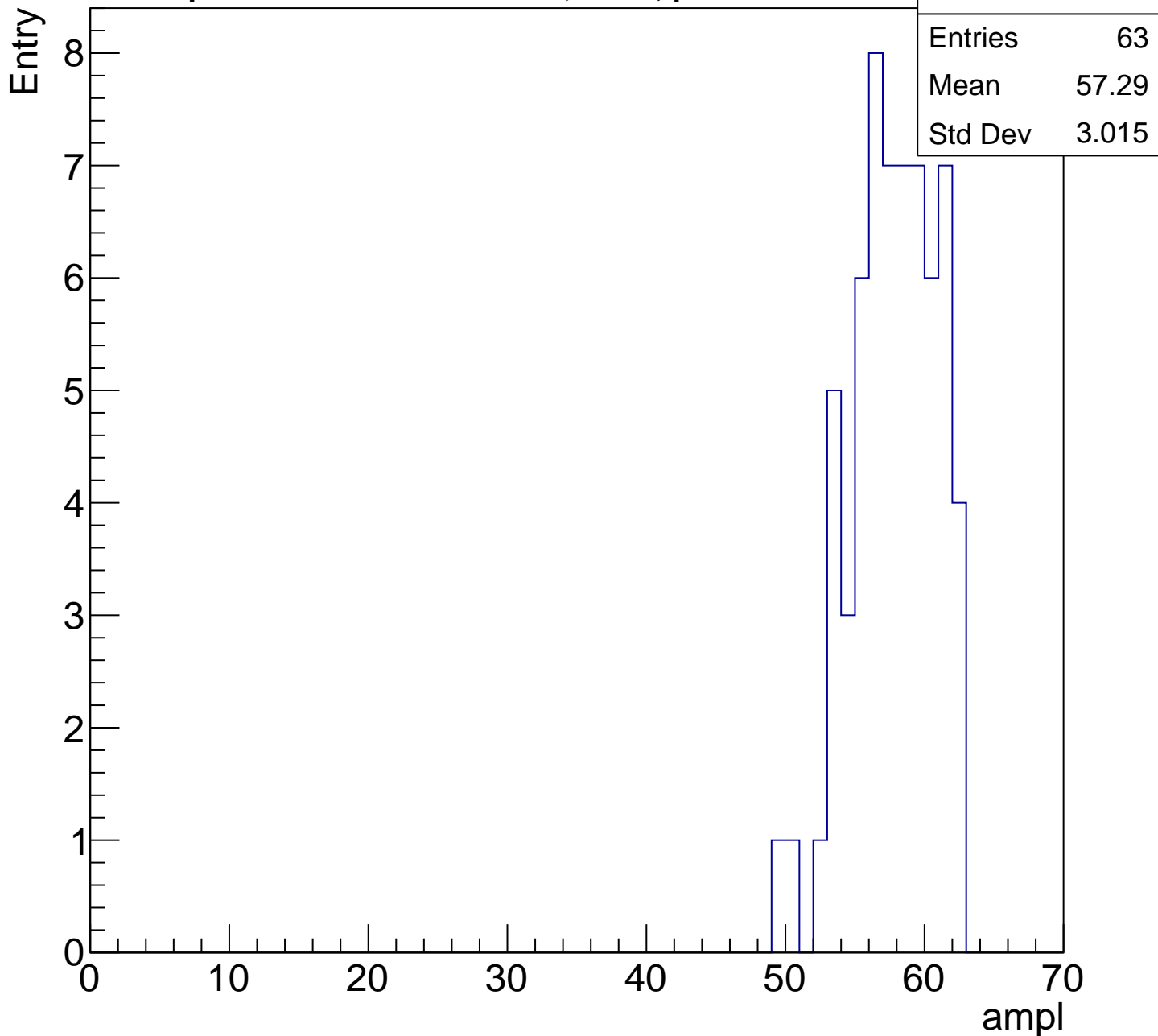
Entry

Entries	81
Mean	50.7
Std Dev	4.226



B1L101S, U21-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

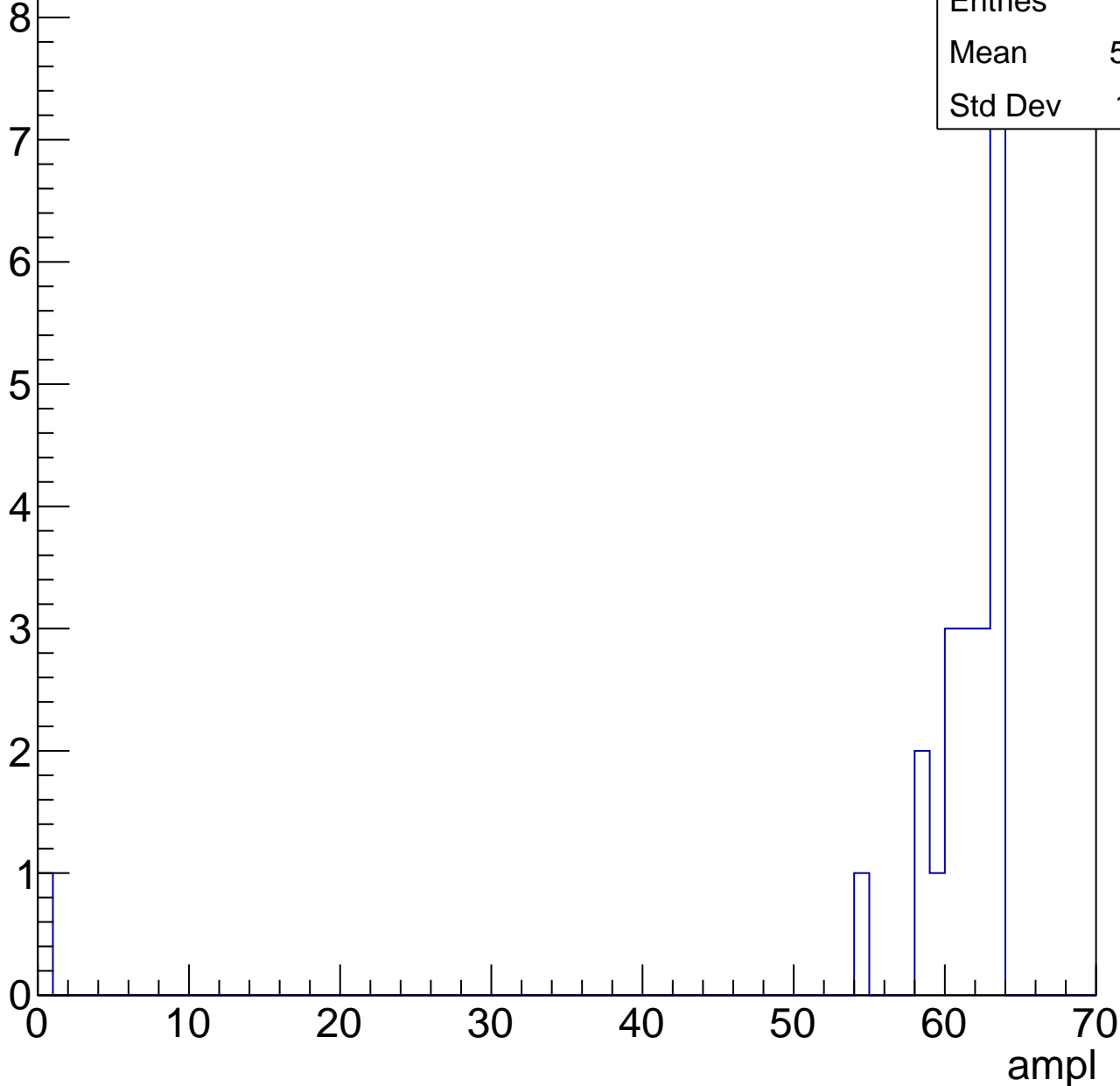


B1L101S, U21-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	58.27
Std Dev	12.91



B1L101S, U21-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch88, adc0

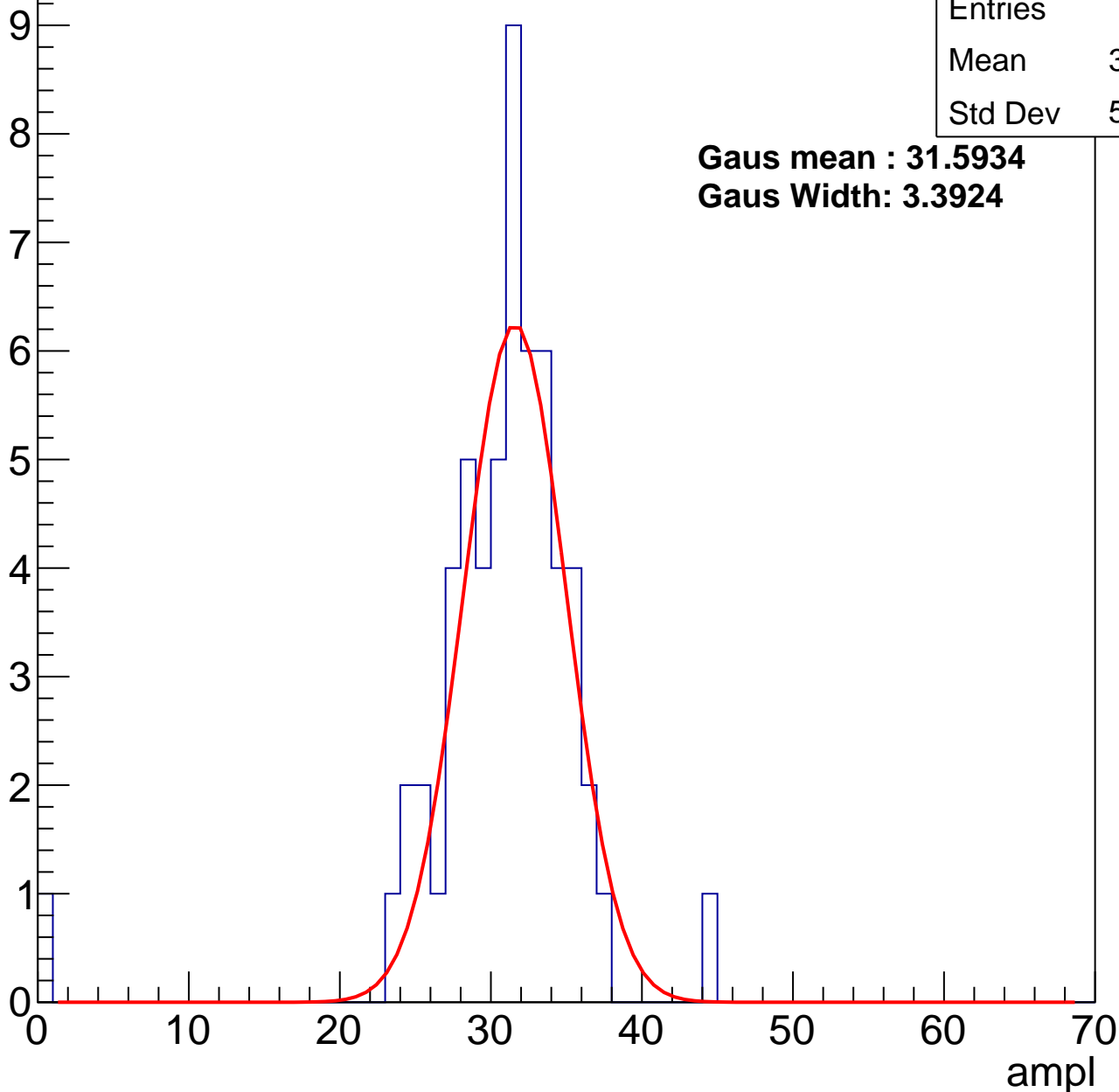
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	30.33
Std Dev	5.428

Gaus mean : 31.5934

Gaus Width: 3.3924



B1L101S, U21-ch88, adc1

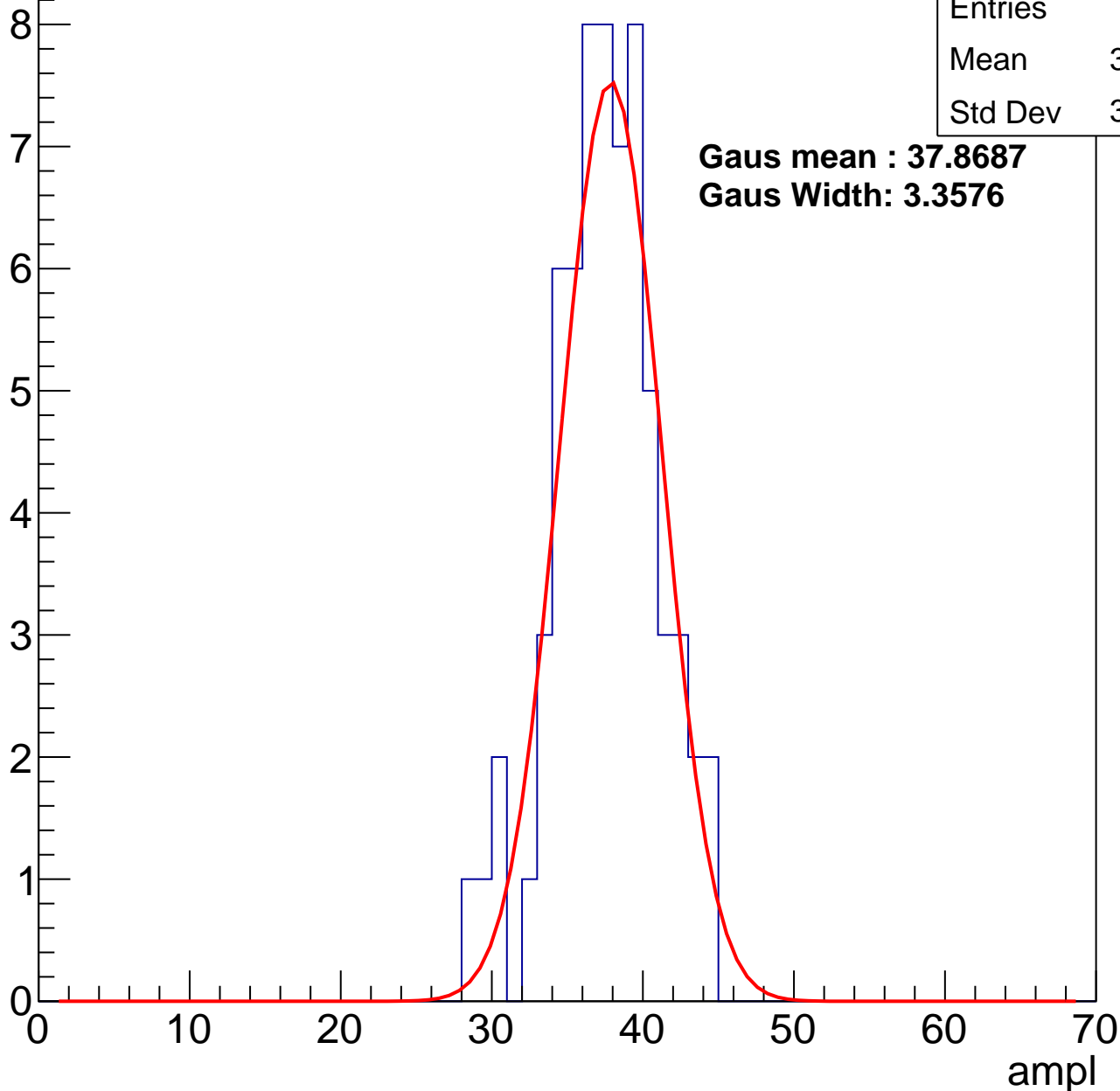
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.08
Std Dev	3.435

Gaus mean : 37.8687

Gaus Width: 3.3576



B1L101S, U21-ch88, adc2

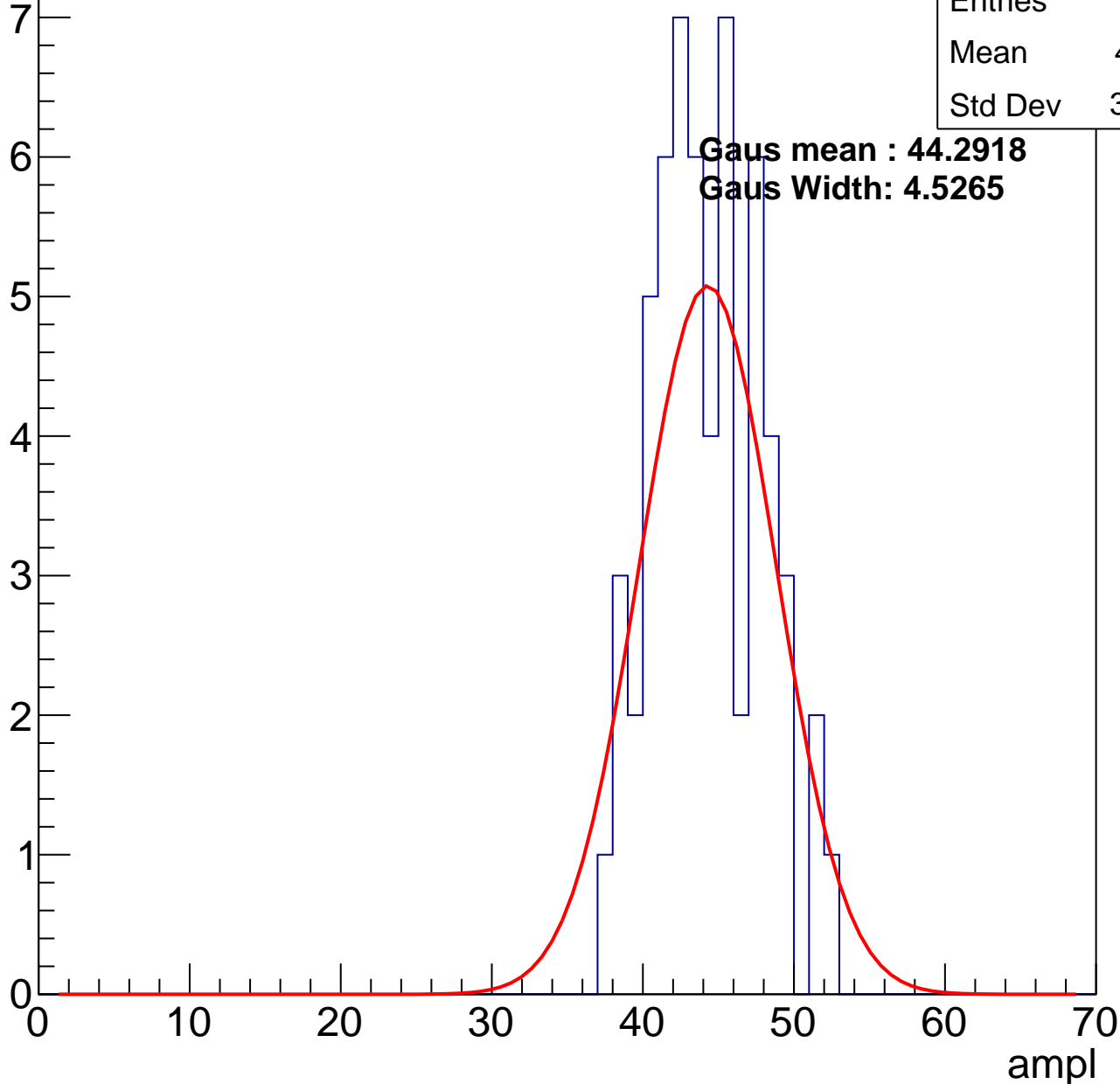
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.81
Std Dev	3.549

Gaus mean : 44.2918

Gaus Width: 4.5265

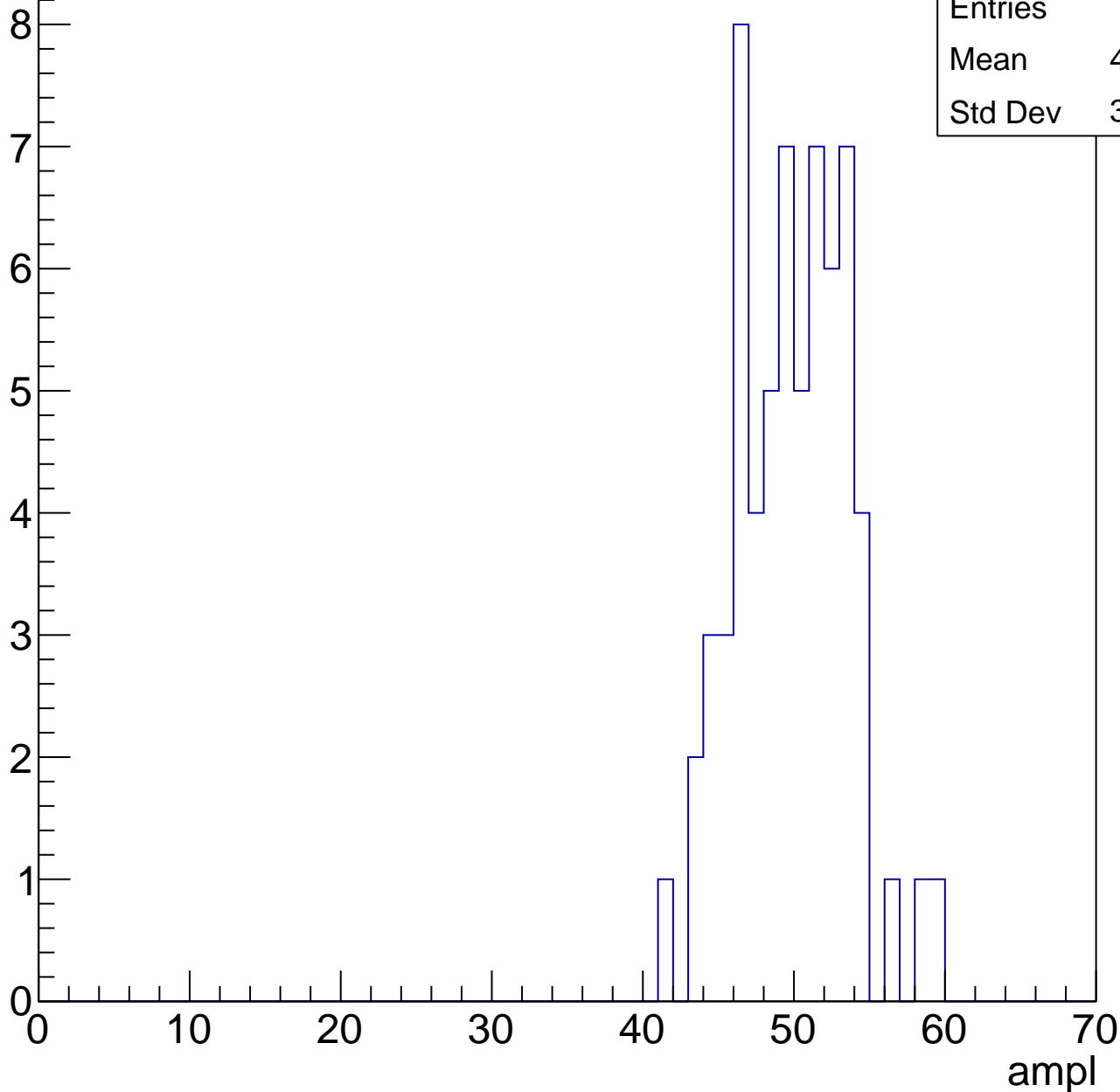


B1L101S, U21-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.42
Std Dev	3.658

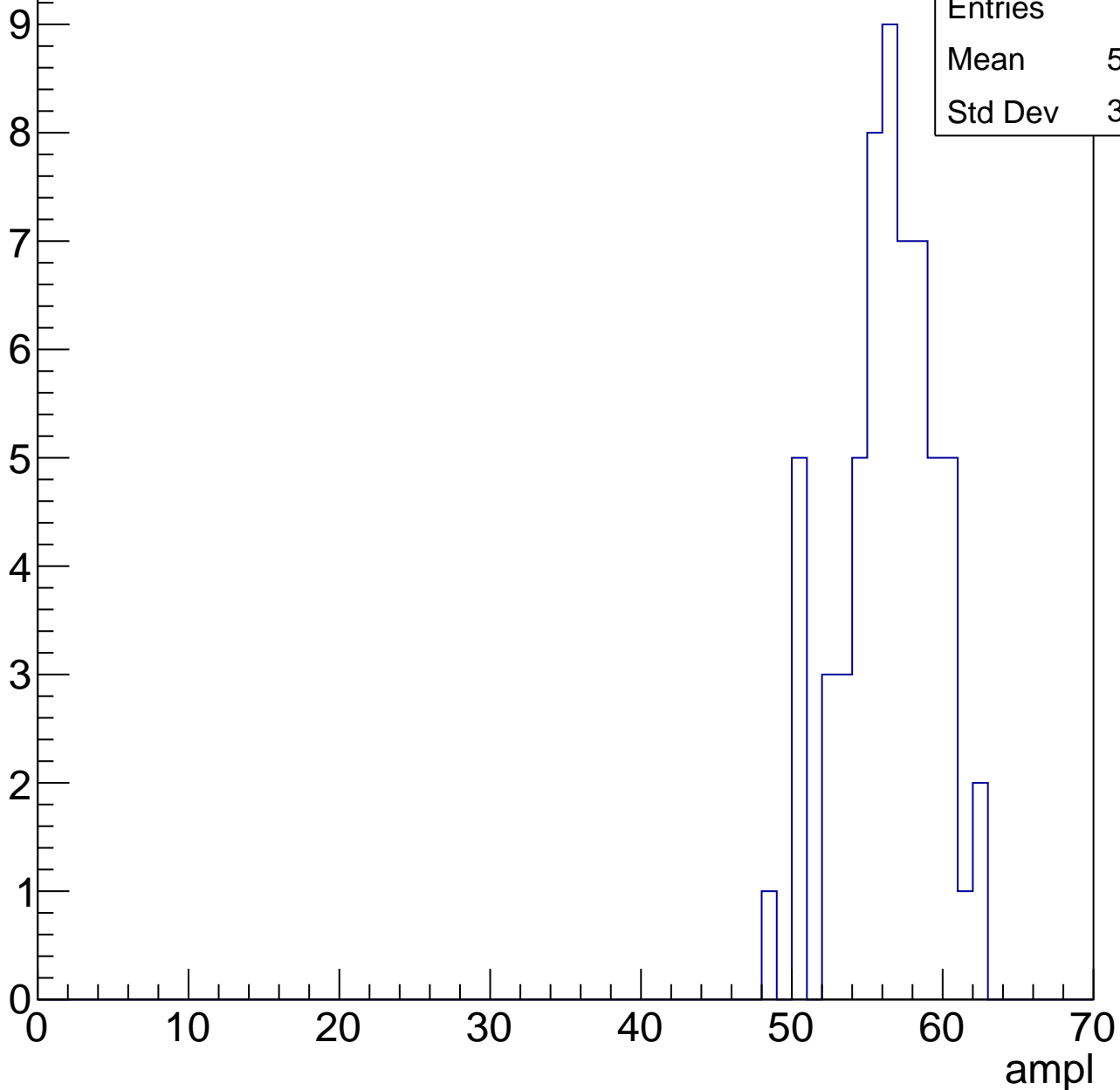


B1L101S, U21-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.93
Std Dev	3.146

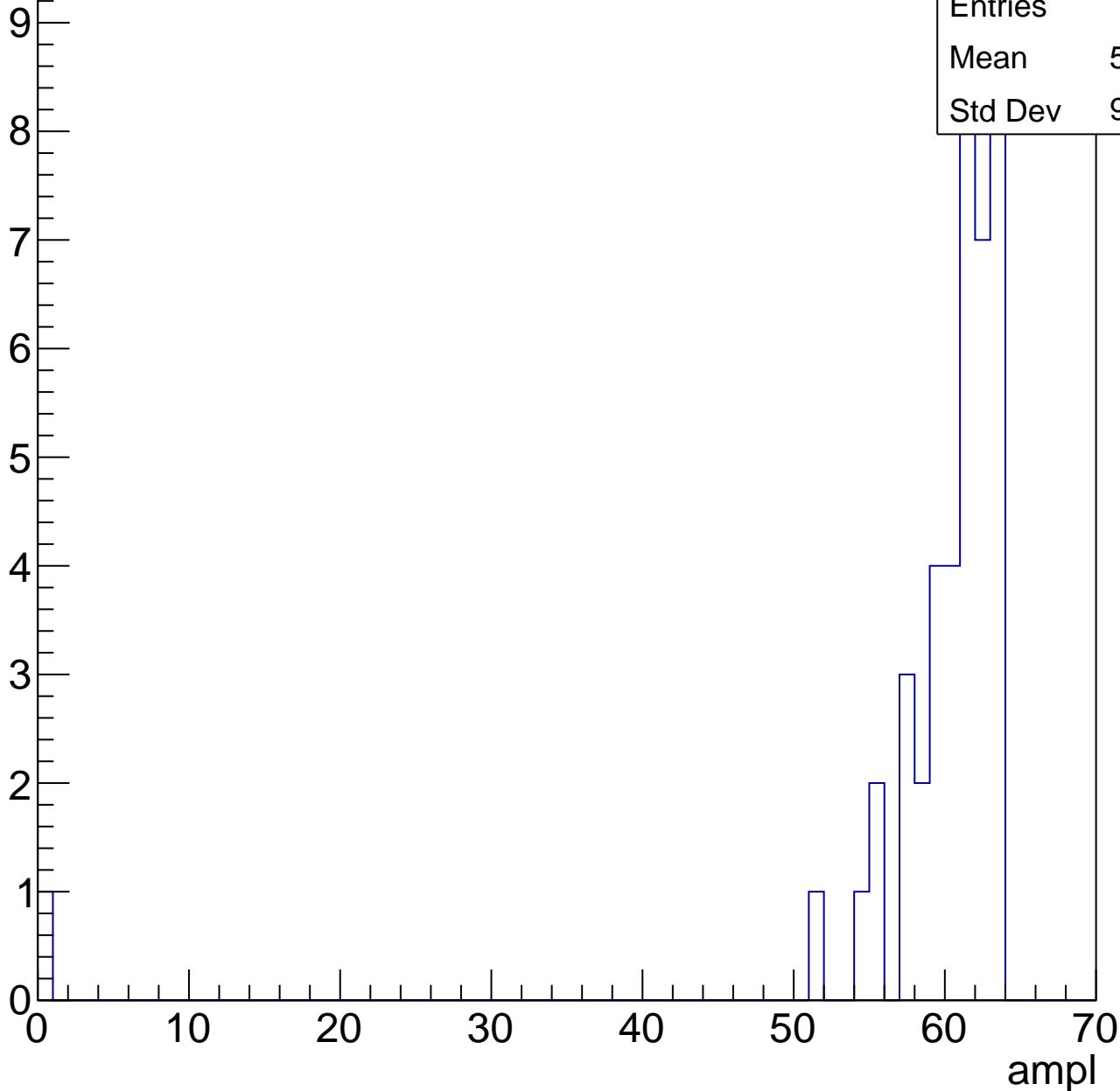


B1L101S, U21-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

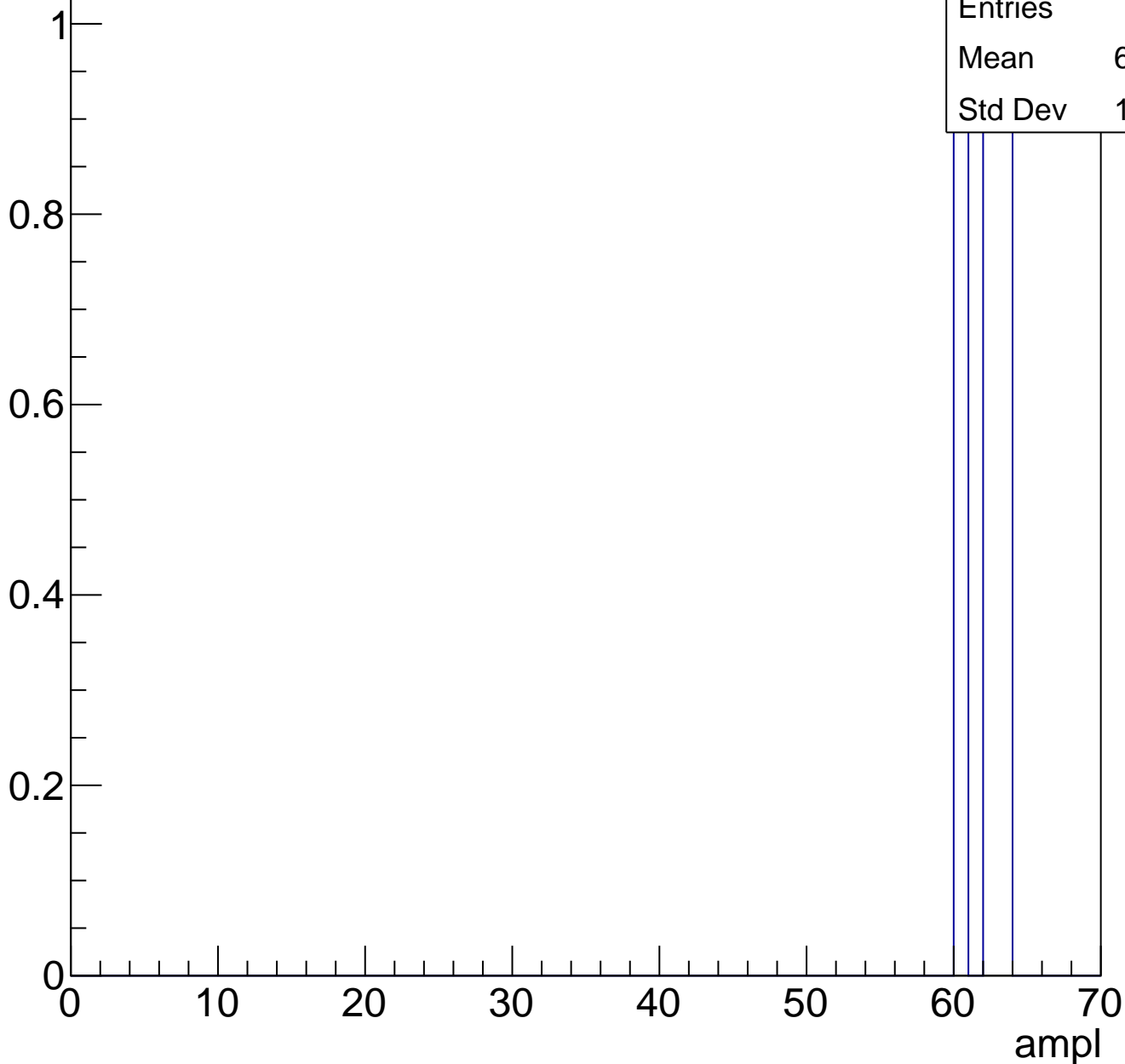
Entries	43
Mean	58.79
Std Dev	9.476



B1L101S, U21-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	22
Std Dev	0

B1L101S, U21-ch89, adc0

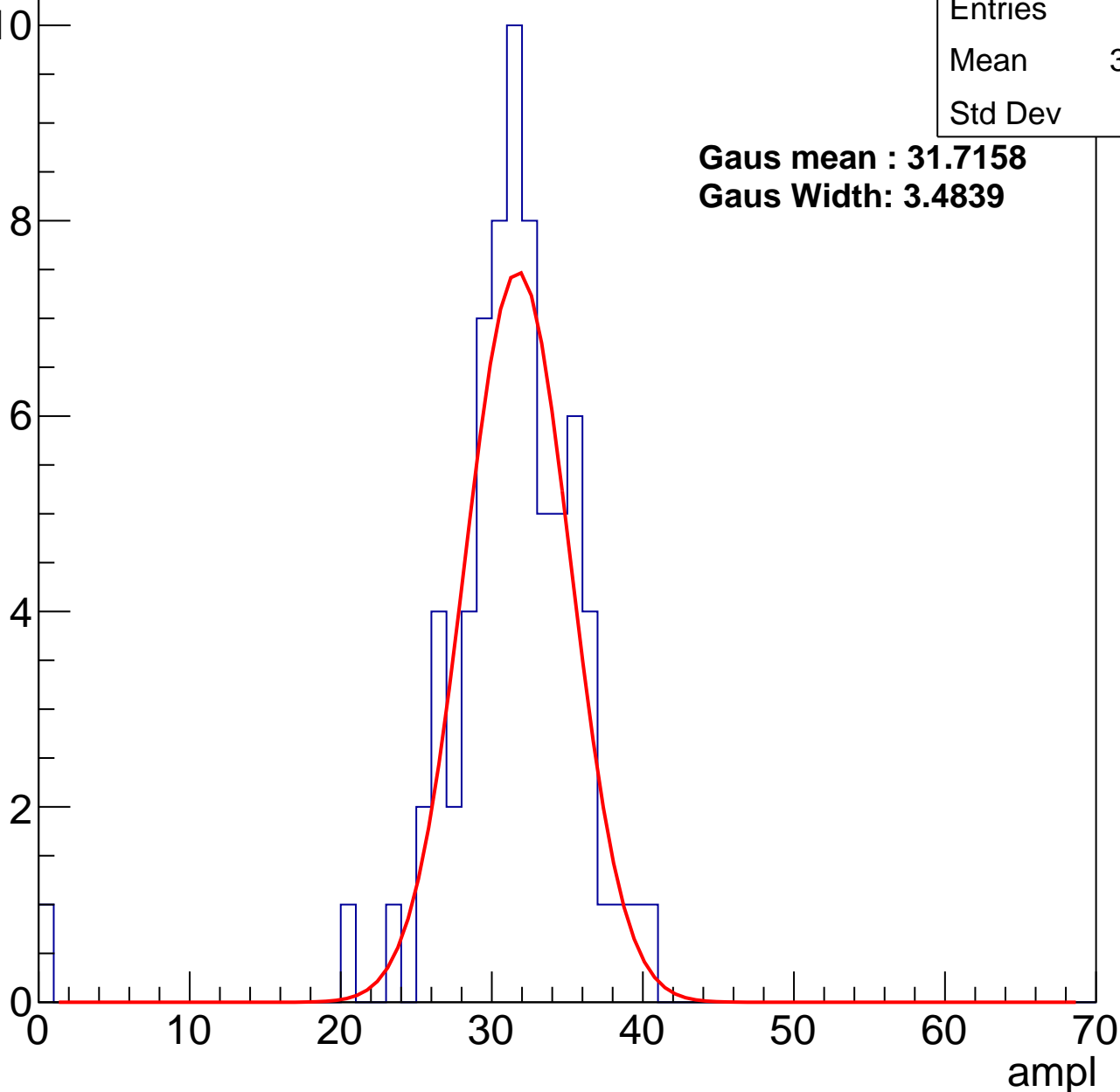
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	30.76
Std Dev	5.16

Gaus mean : 31.7158

Gaus Width: 3.4839



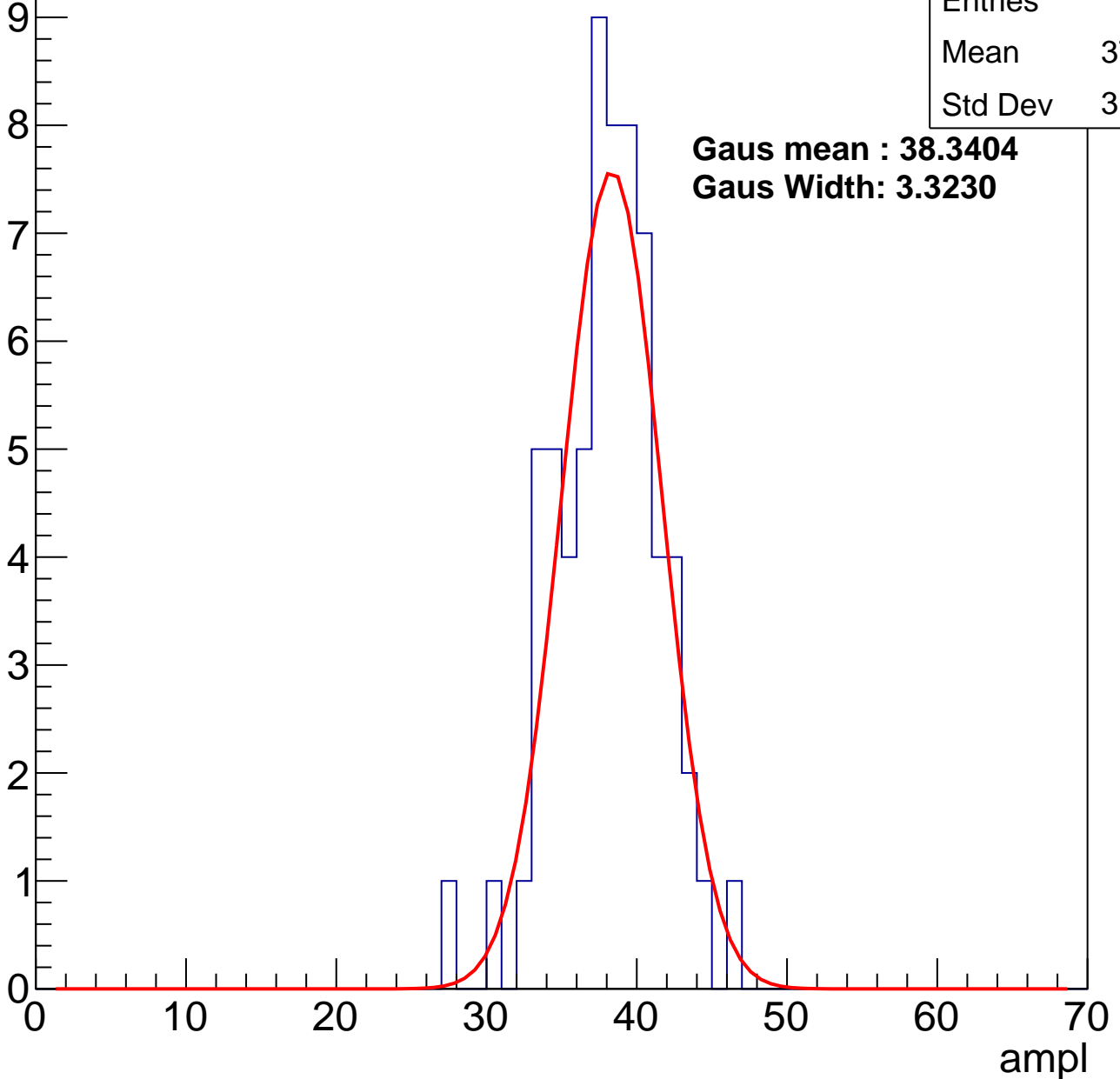
B1L101S, U21-ch89, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.59
Std Dev	3.407

Gaus mean : 38.3404
Gaus Width: 3.3230



B1L101S, U21-ch89, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	45.04
Std Dev	3.486

Gaus mean : 44.9528

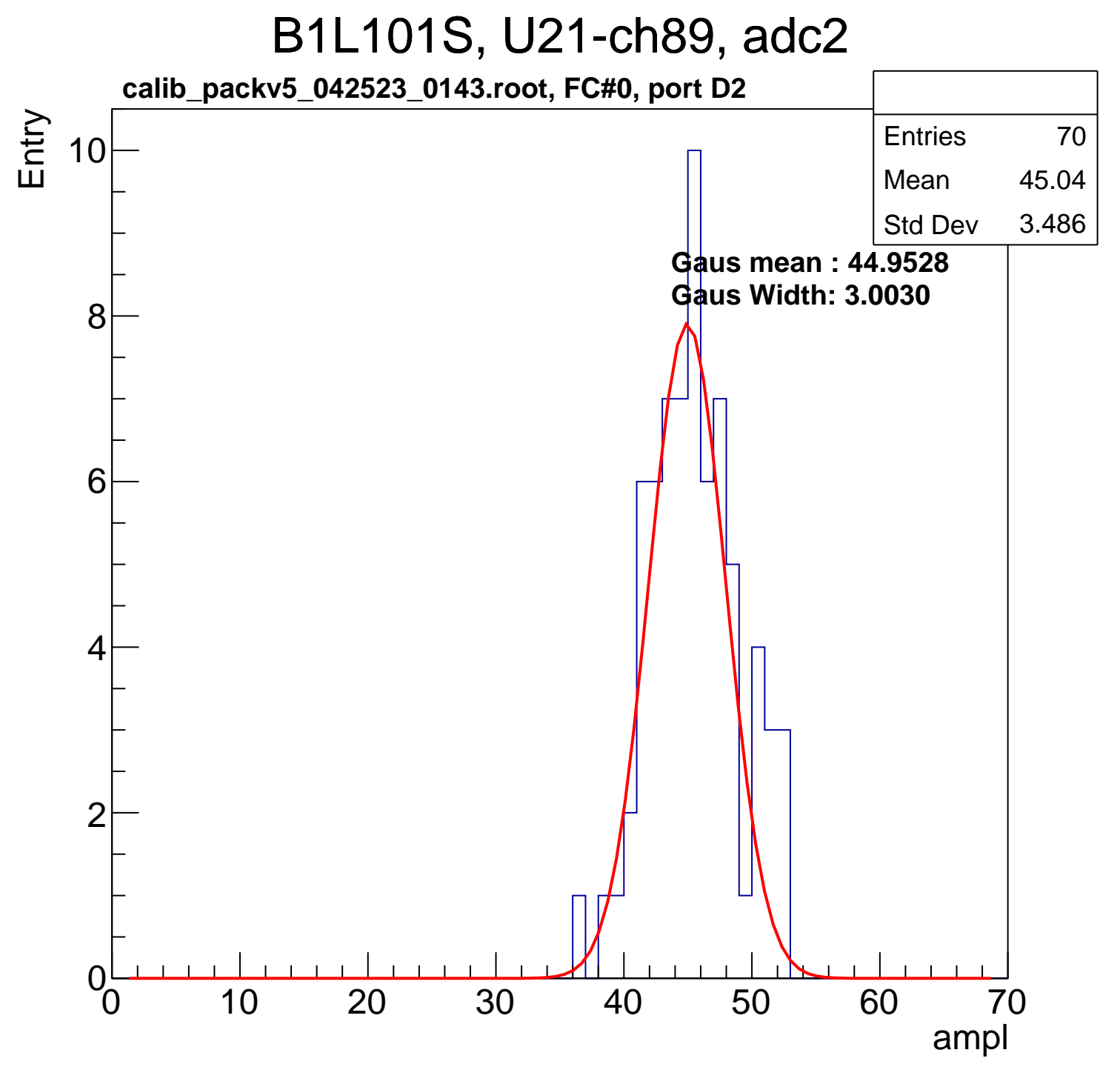
Gaus Width: 3.0030

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

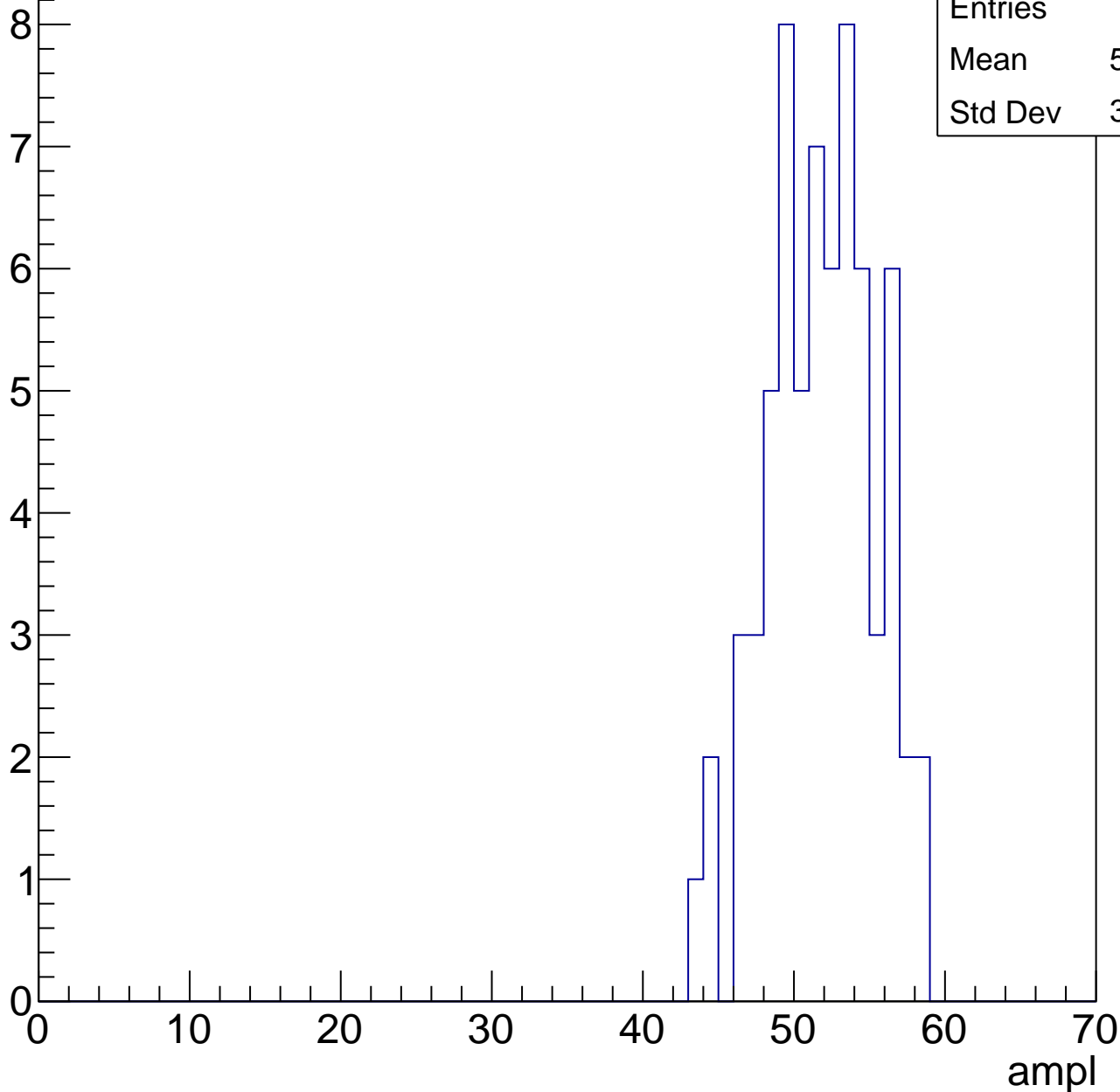


B1L101S, U21-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.34
Std Dev	3.496

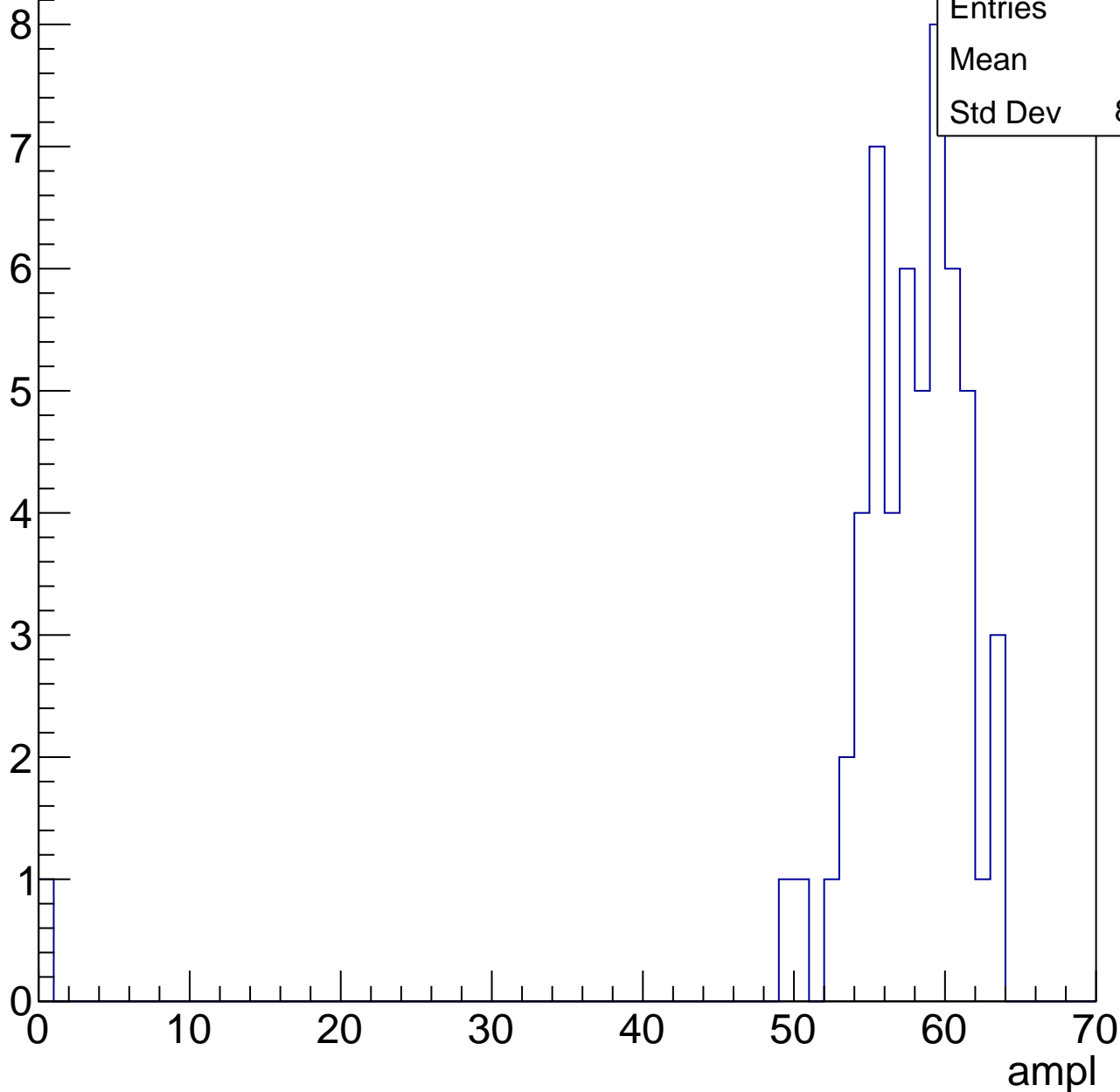


B1L101S, U21-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.4
Std Dev	8.281

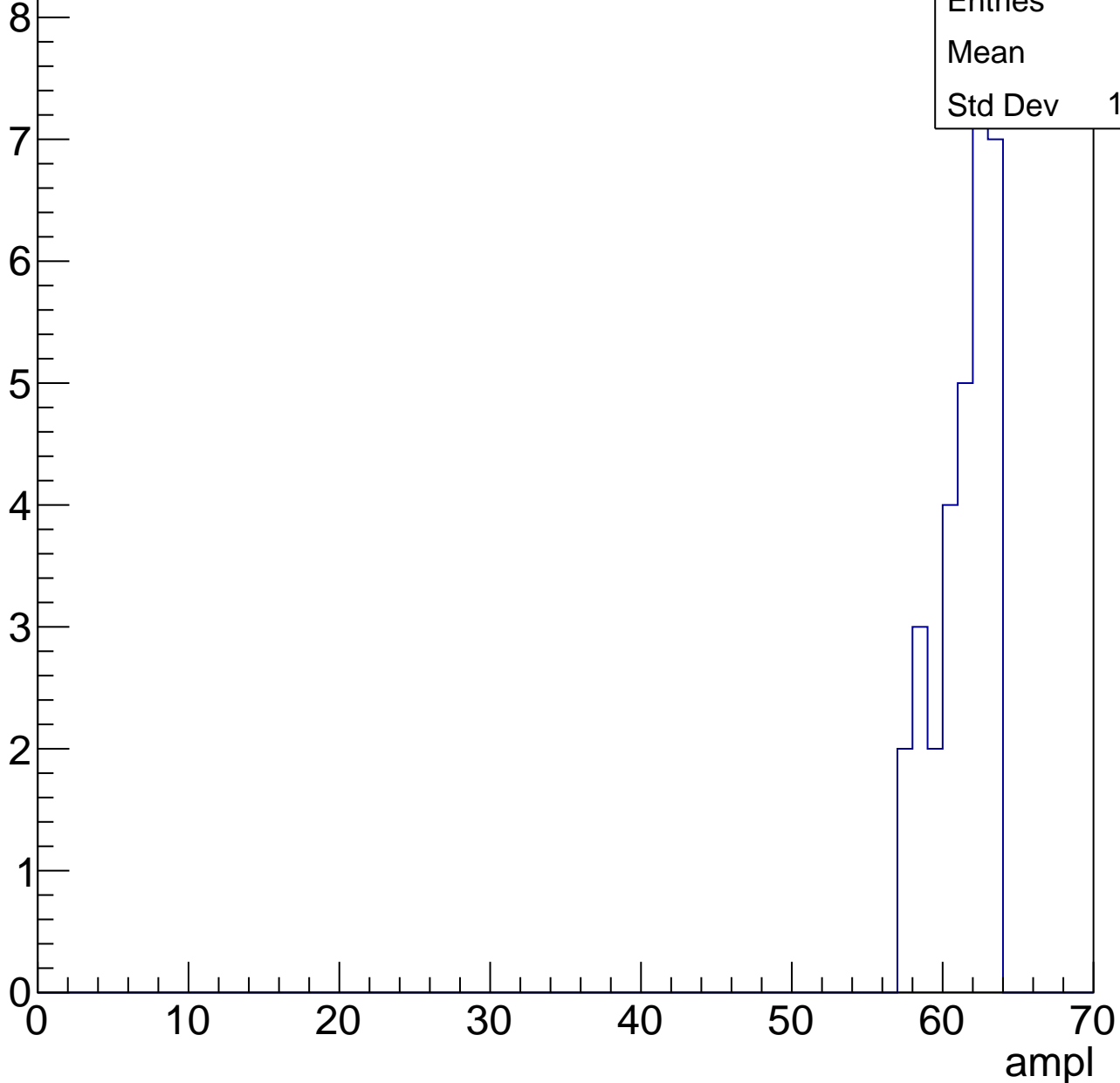


B1L101S, U21-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.9
Std Dev	1.855



B1L101S, U21-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch90, adc0

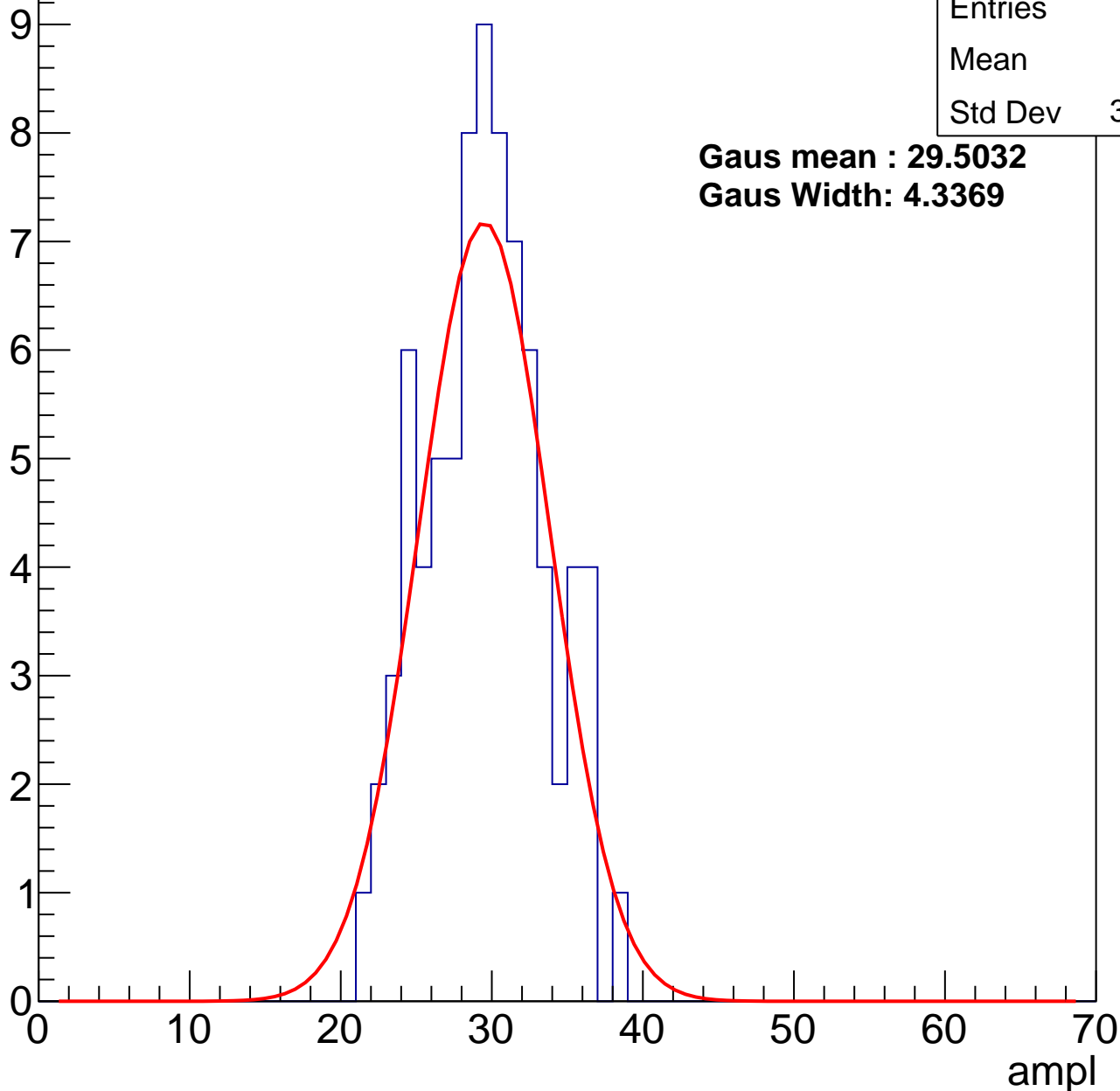
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	29.1
Std Dev	3.867

Gaus mean : 29.5032

Gaus Width: 4.3369



B1L101S, U21-ch90, adc1

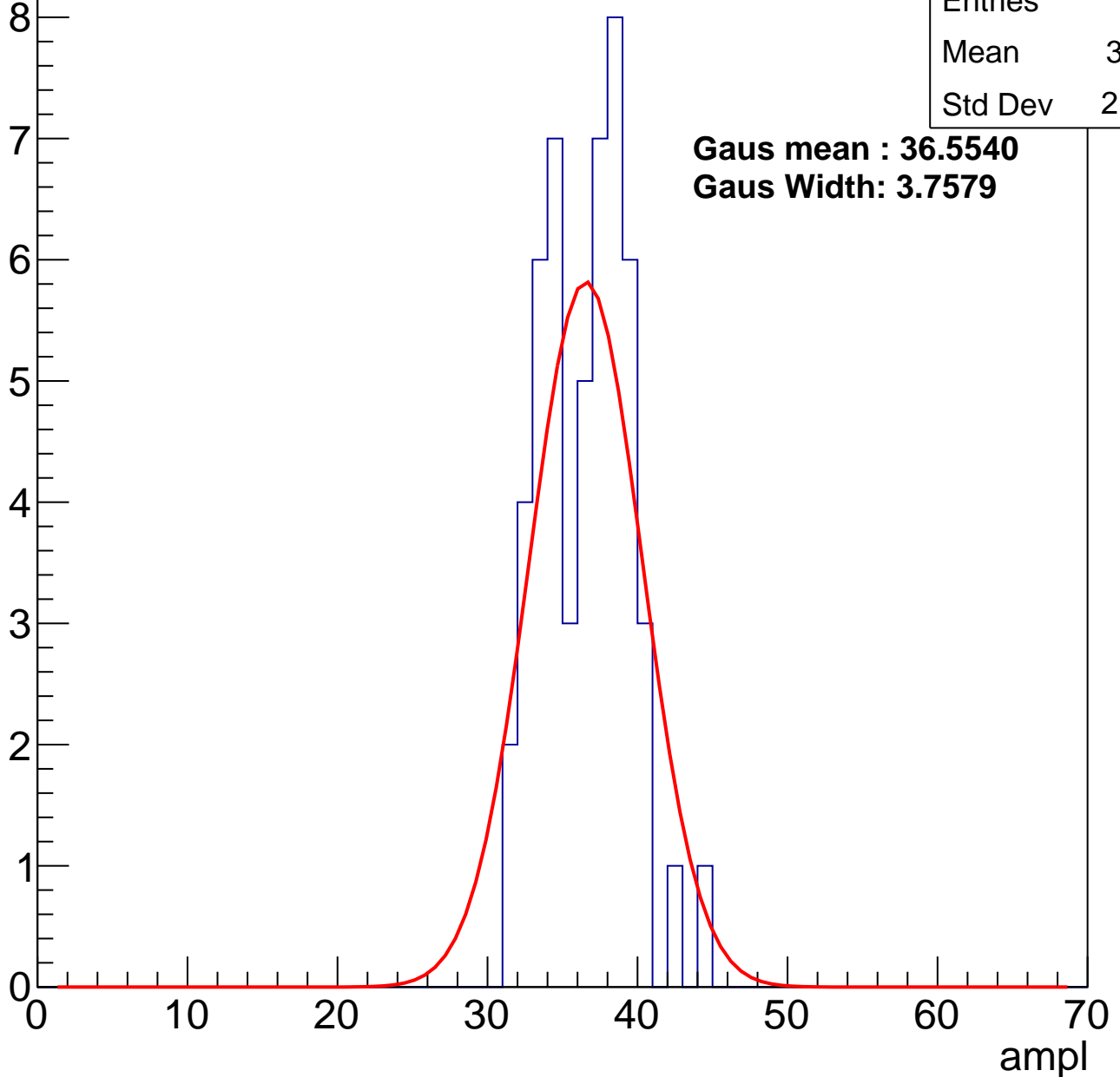
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	36.11
Std Dev	2.879

Gaus mean : 36.5540

Gaus Width: 3.7579



B1L101S, U21-ch90, adc2

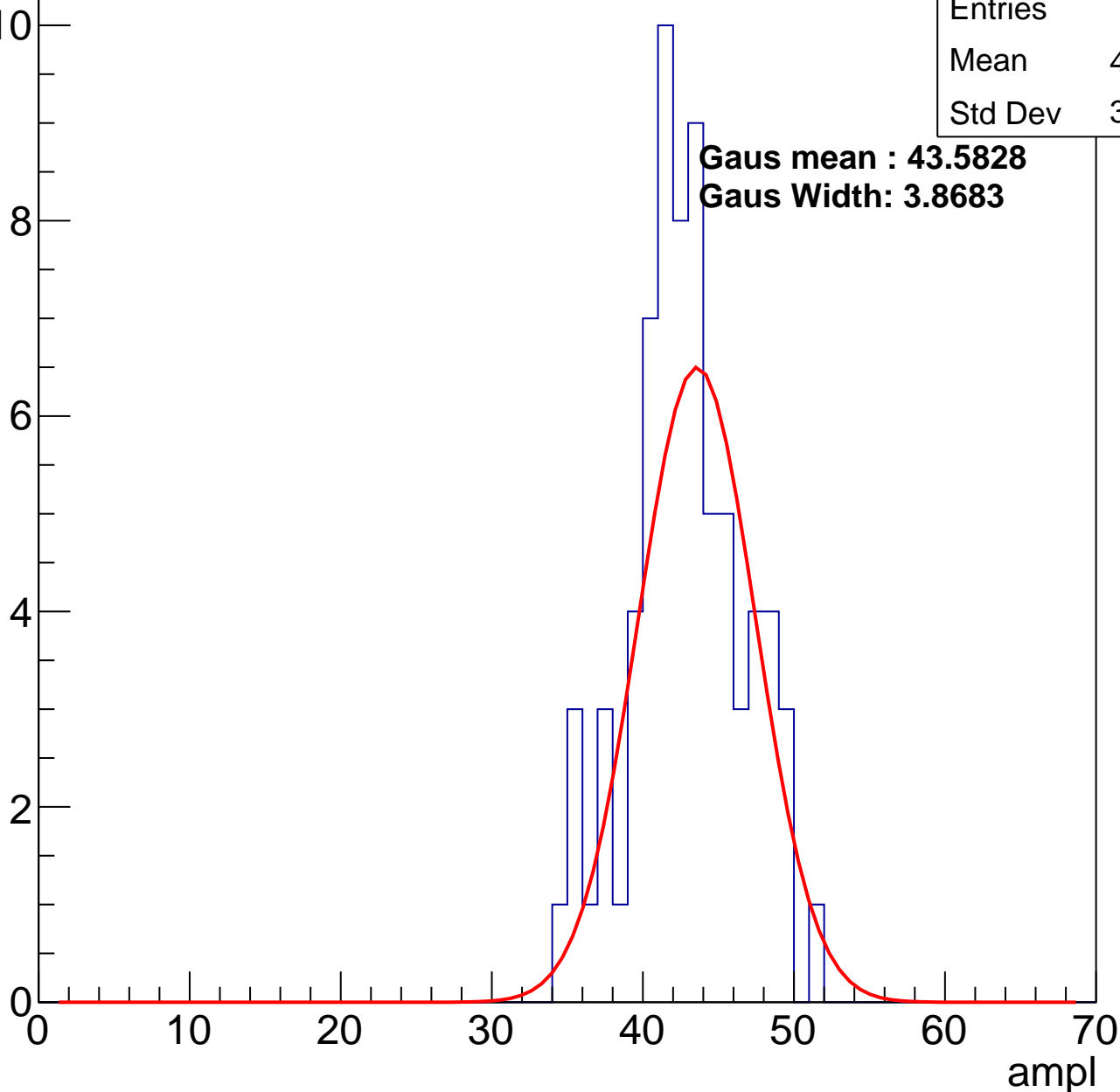
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.42
Std Dev	3.715

Gaus mean : 43.5828

Gaus Width: 3.8683



B1L101S, U21-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

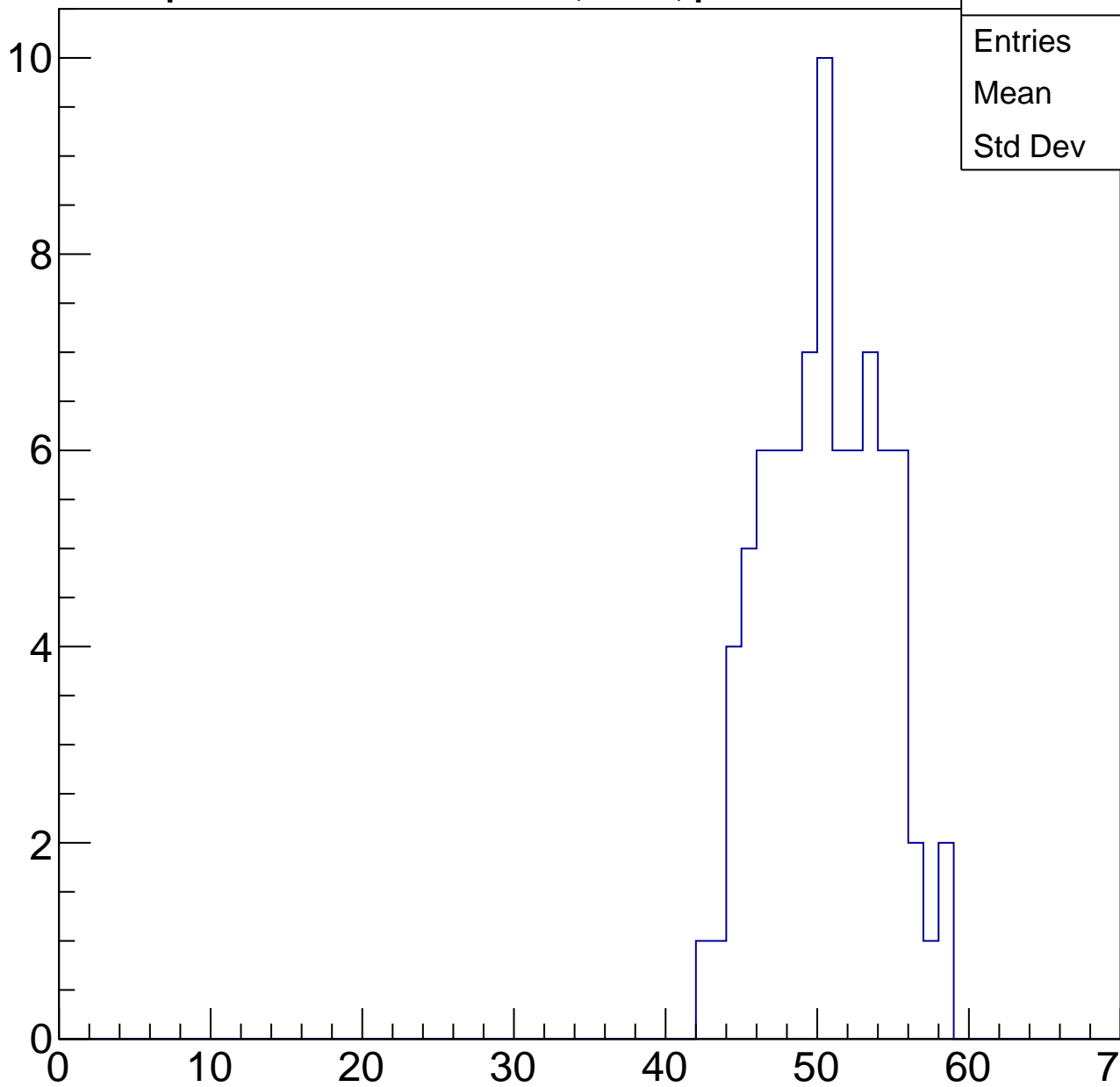
Entries	82
Mean	50.04
Std Dev	3.746

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

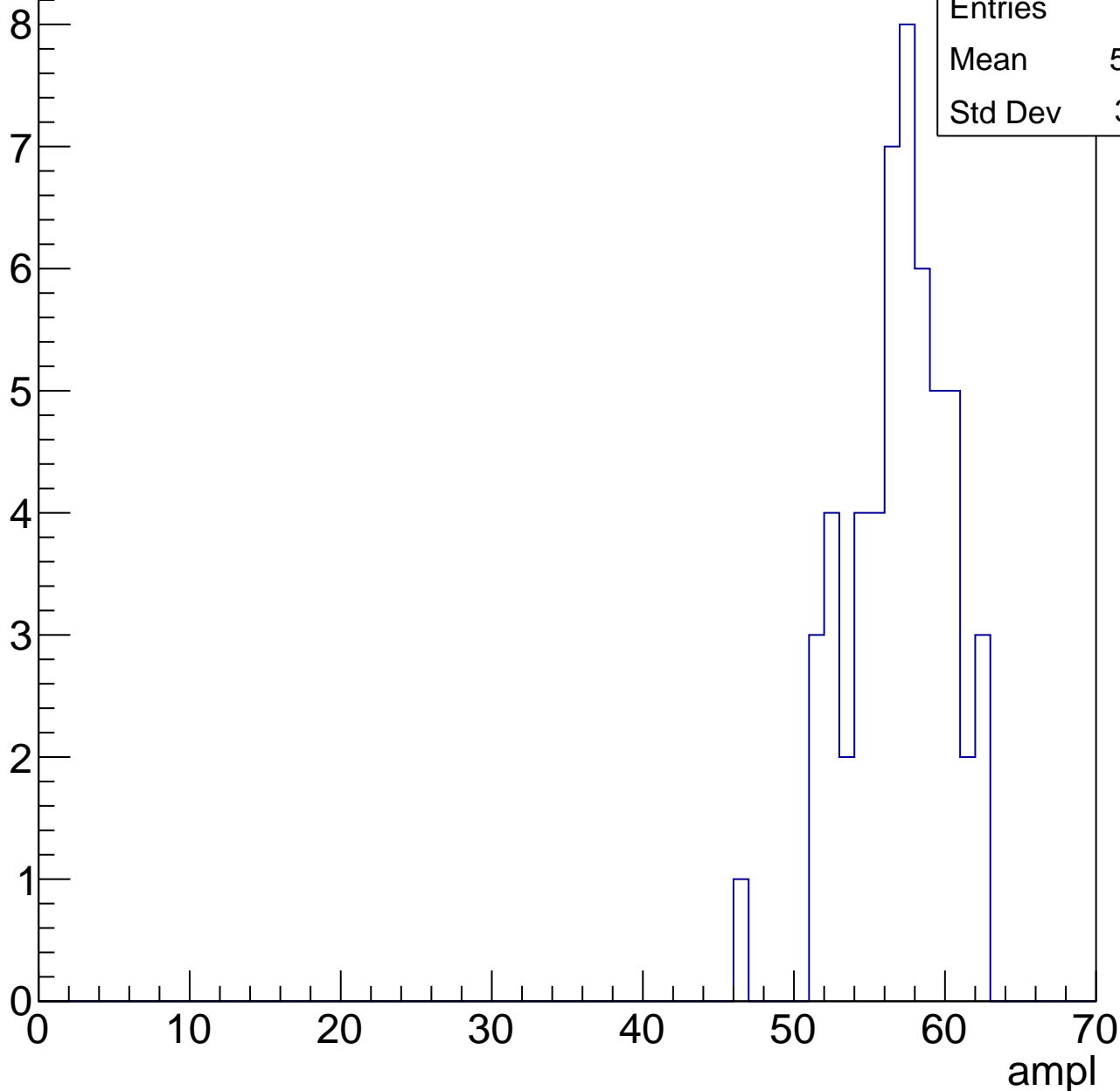


B1L101S, U21-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.44
Std Dev	3.281

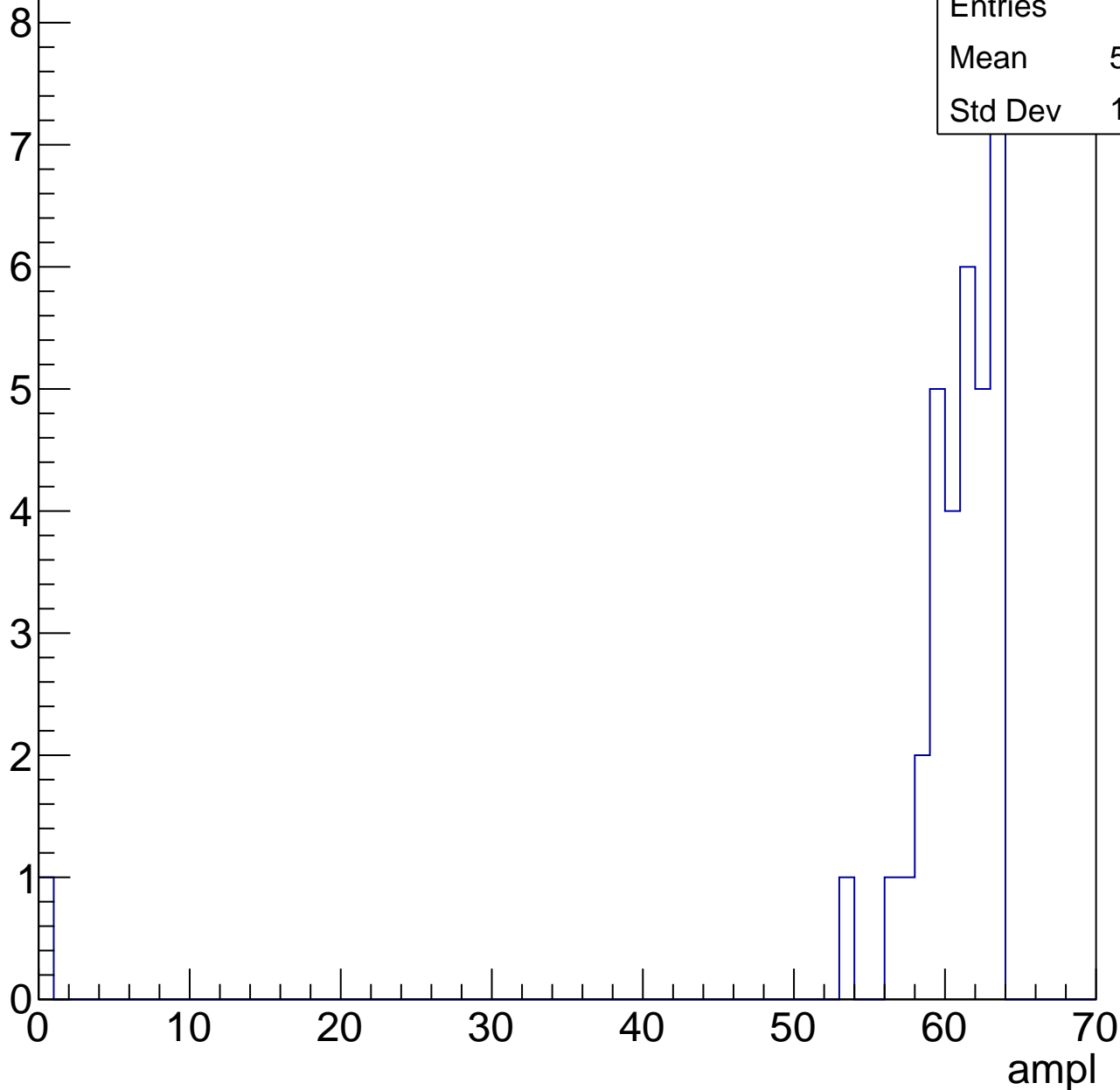


B1L101S, U21-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.74
Std Dev	10.47



B1L101S, U21-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

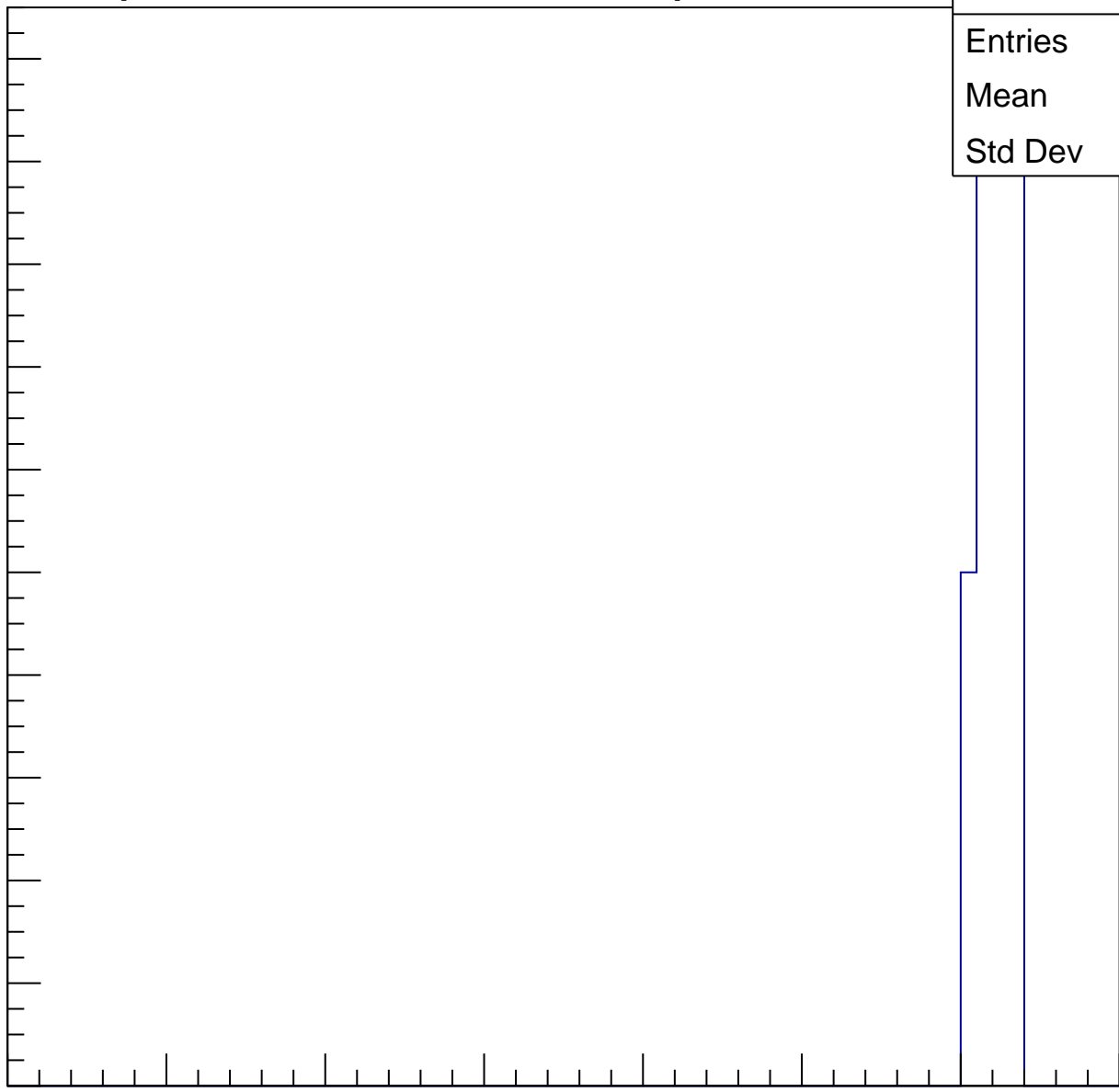
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.71
Std Dev	1.03

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch91, adc0

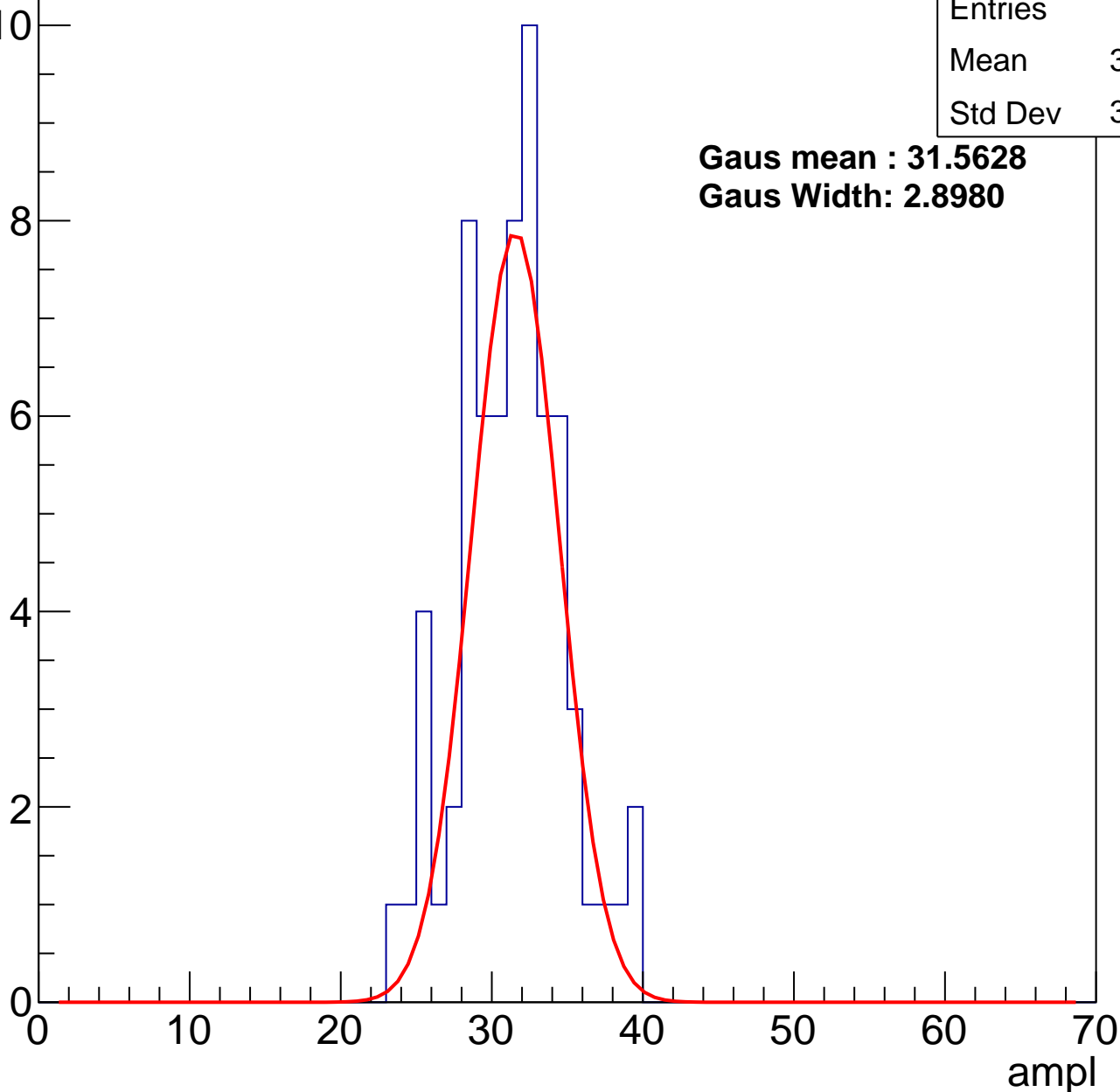
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.88
Std Dev	3.432

Gaus mean : 31.5628

Gaus Width: 2.8980



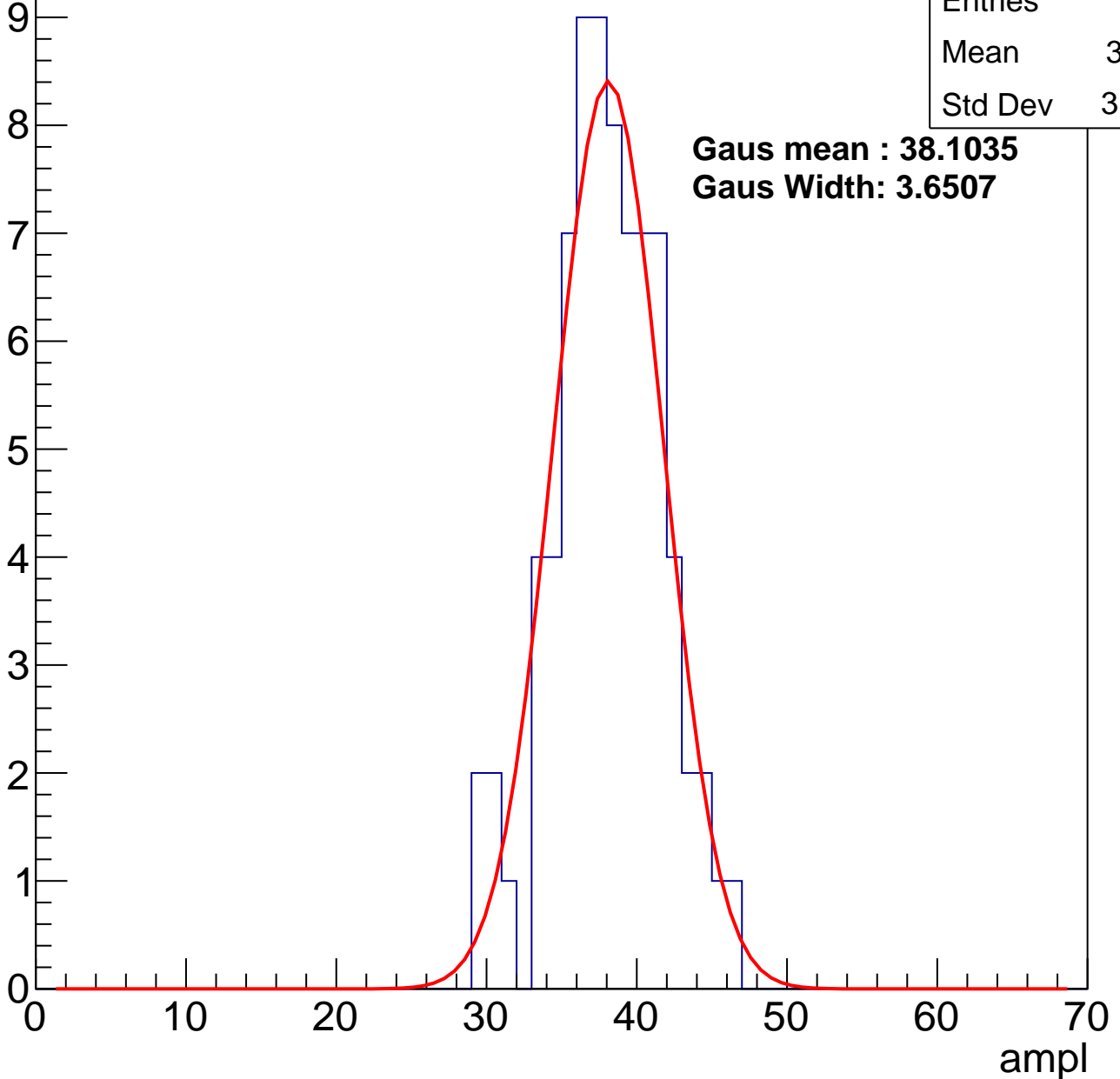
B1L101S, U21-ch91, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

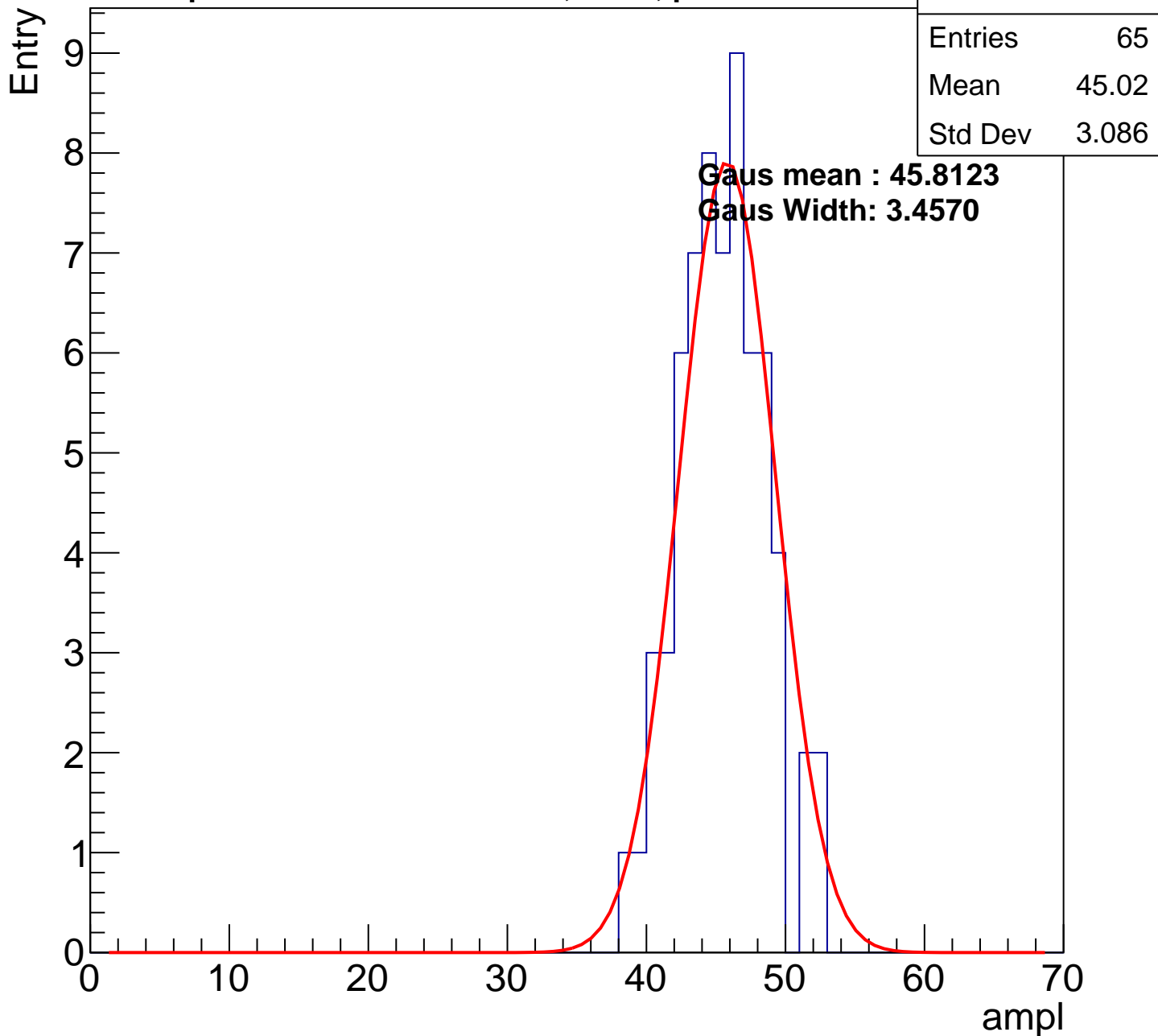
Entries	77
Mean	37.61
Std Dev	3.597

Gaus mean : 38.1035
Gaus Width: 3.6507



B1L101S, U21-ch91, adc2

calib_packv5_042523_0143.root, FC#0, port D2

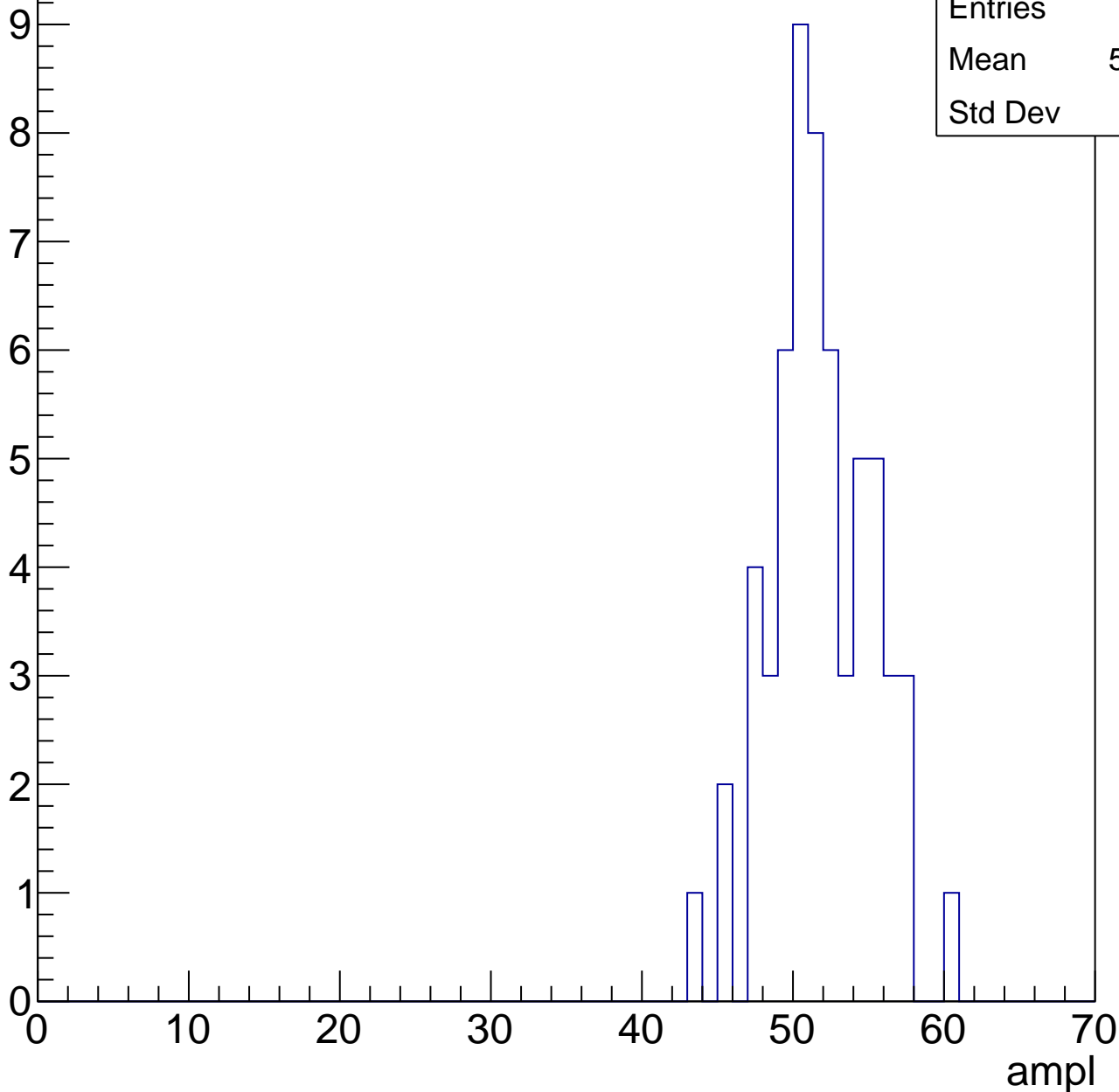


B1L101S, U21-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	51.39
Std Dev	3.34

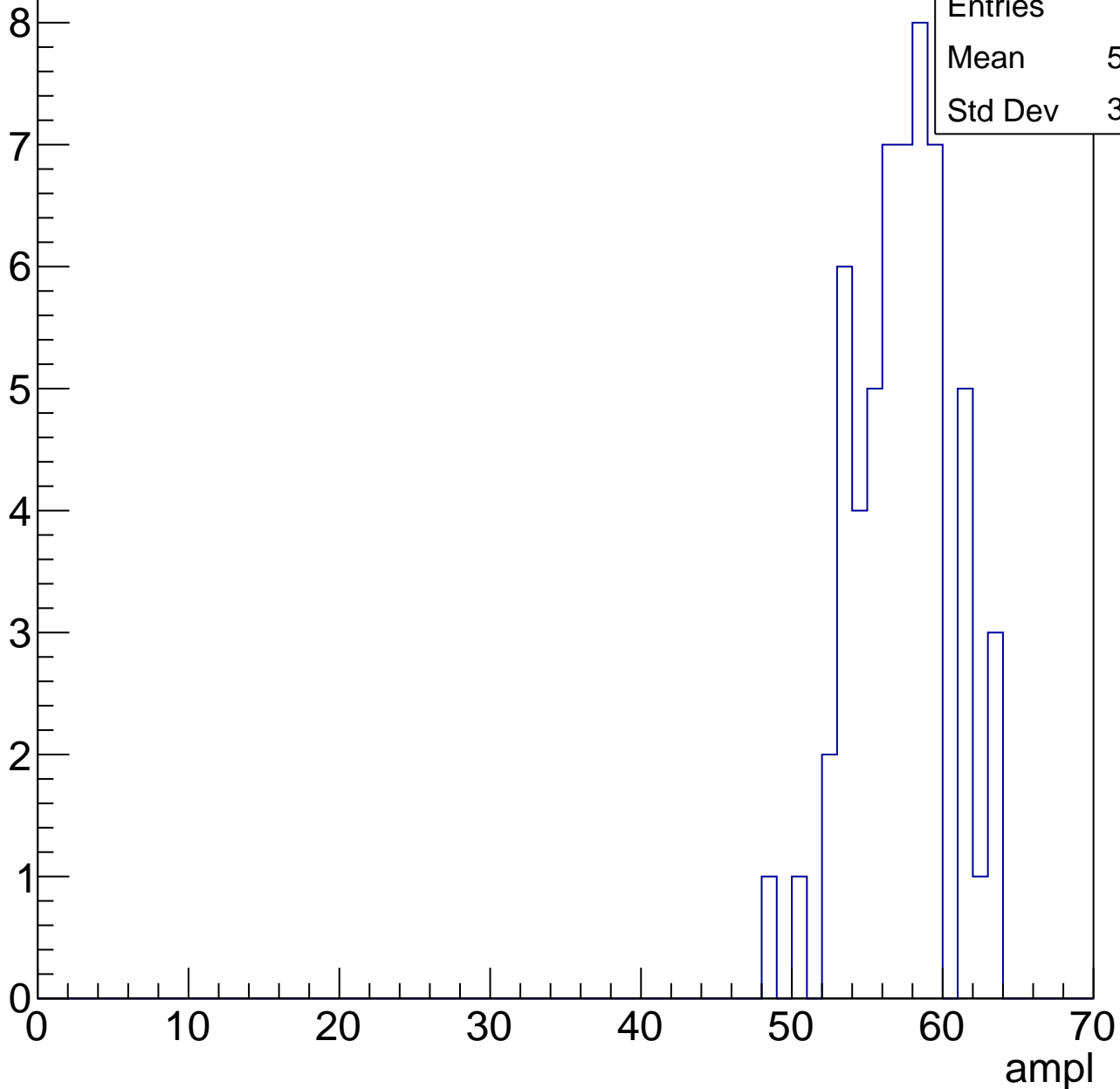


B1L101S, U21-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

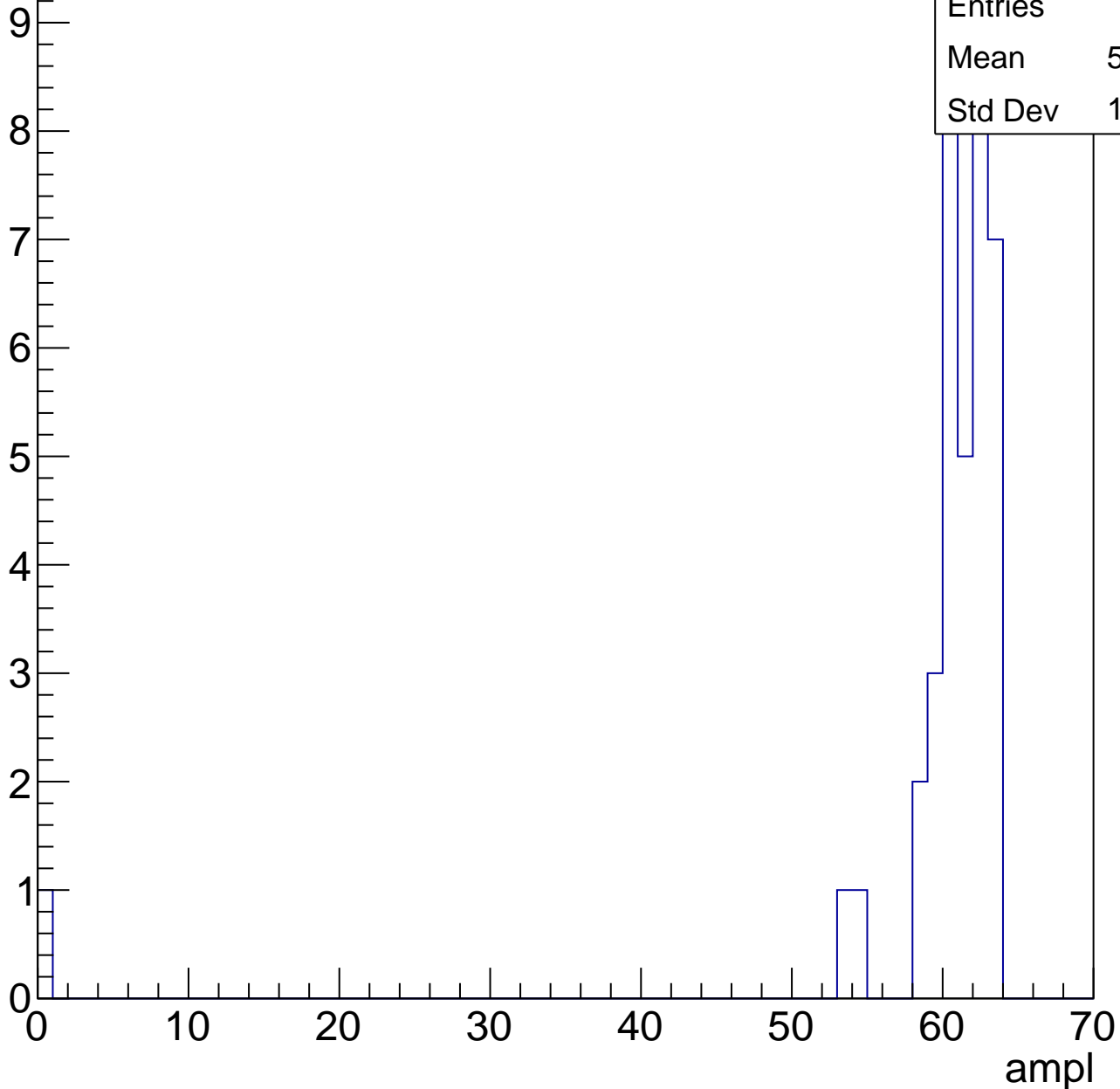
Entries	57
Mean	56.75
Std Dev	3.202



B1L101S, U21-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch92, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	30.21
Std Dev	3.254

Gaus mean : 30.5765

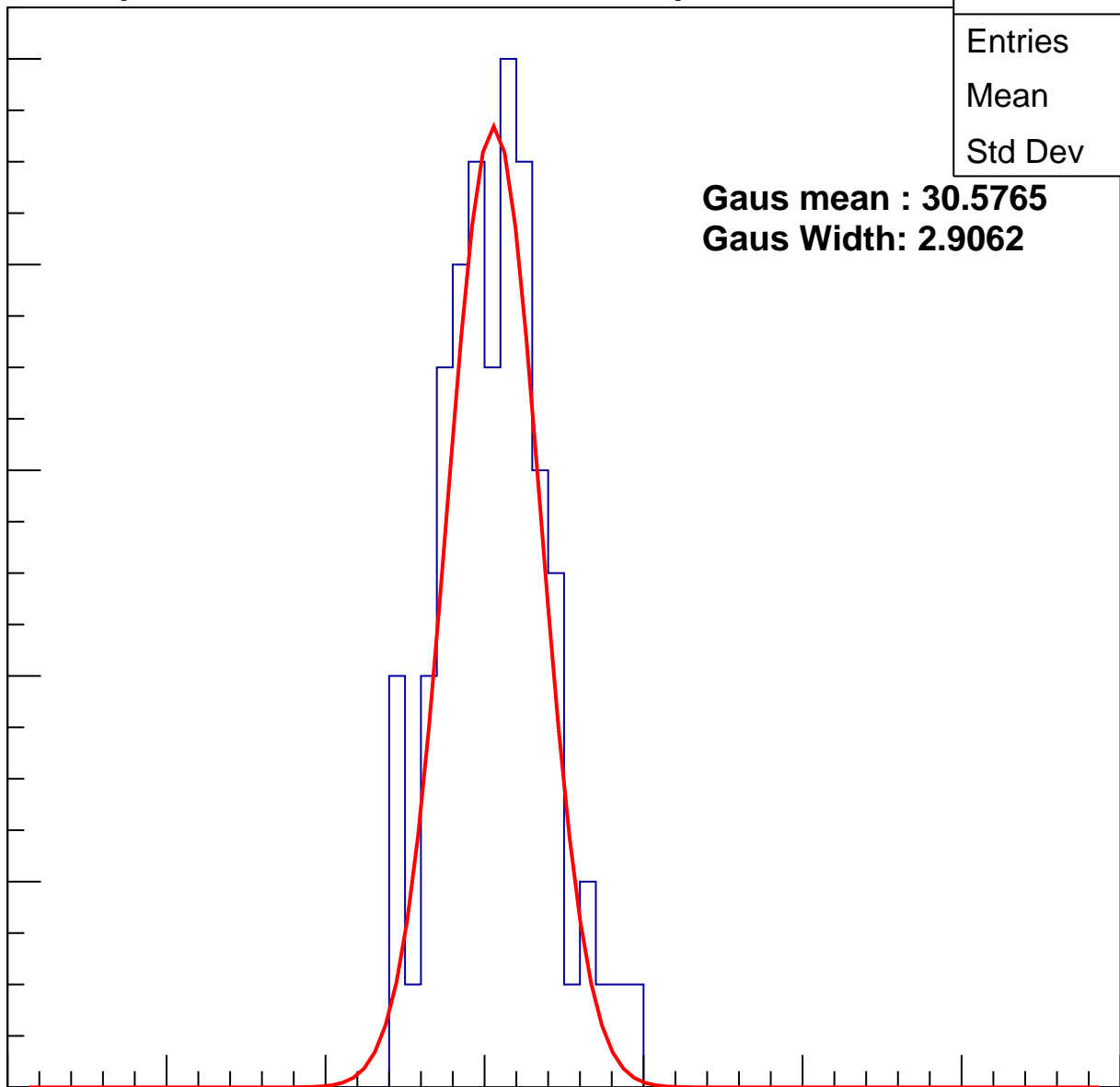
Gaus Width: 2.9062

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch92, adc1

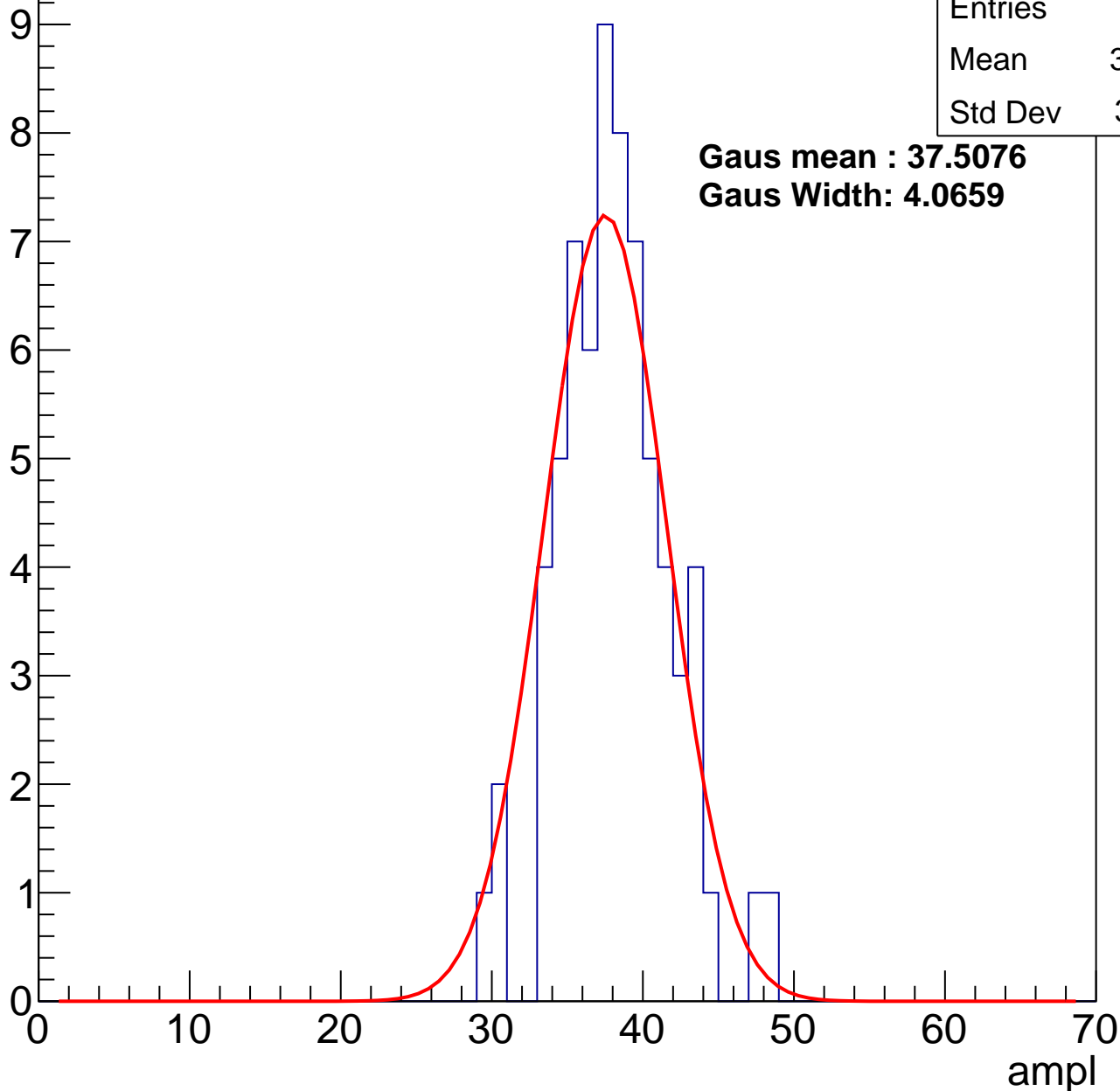
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	37.69
Std Dev	3.651

Gaus mean : 37.5076

Gaus Width: 4.0659



B1L101S, U21-ch92, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.65
Std Dev	3.513

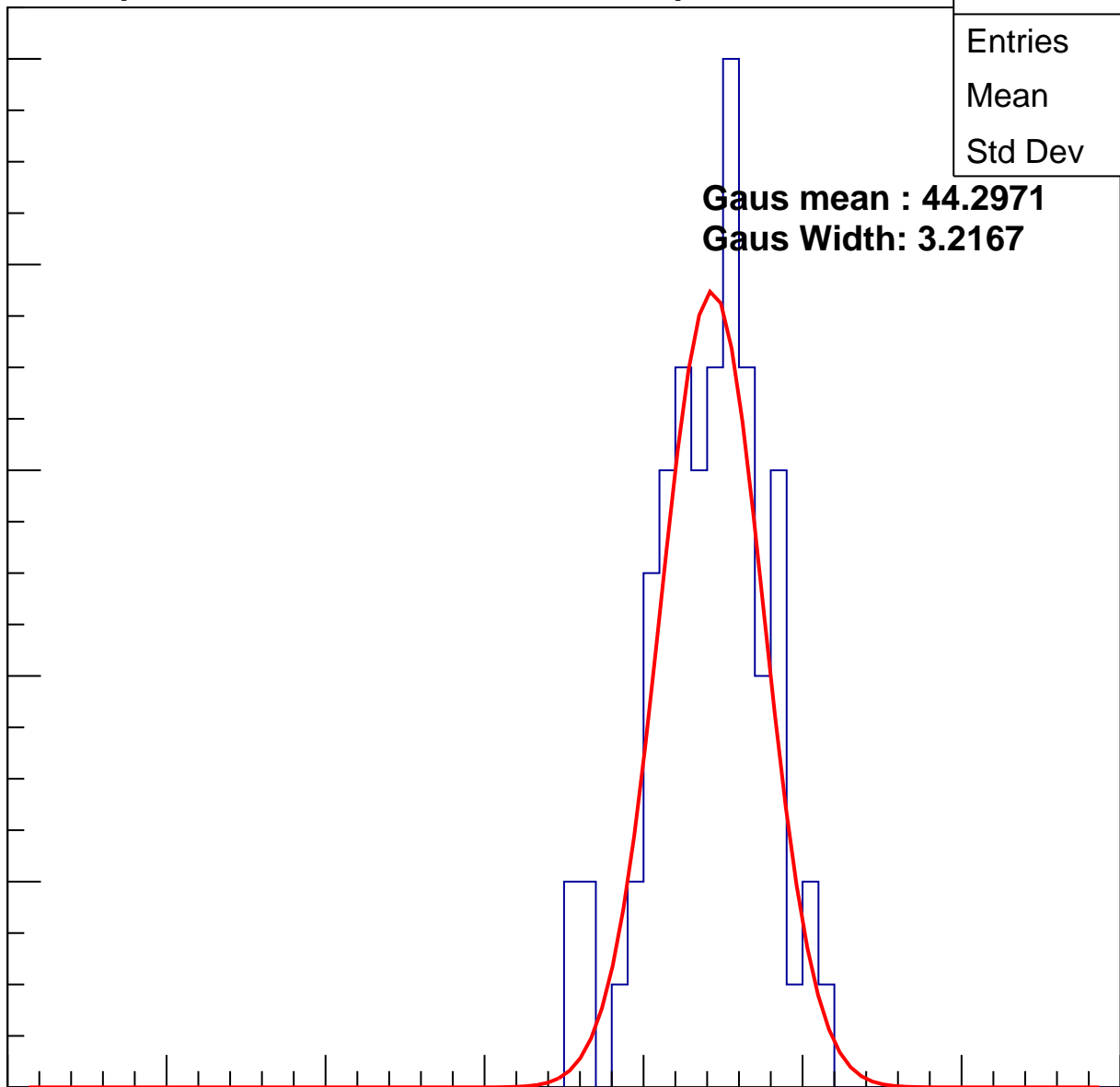
Gaus mean : 44.2971

Gaus Width: 3.2167

10
8
6
4
2
0

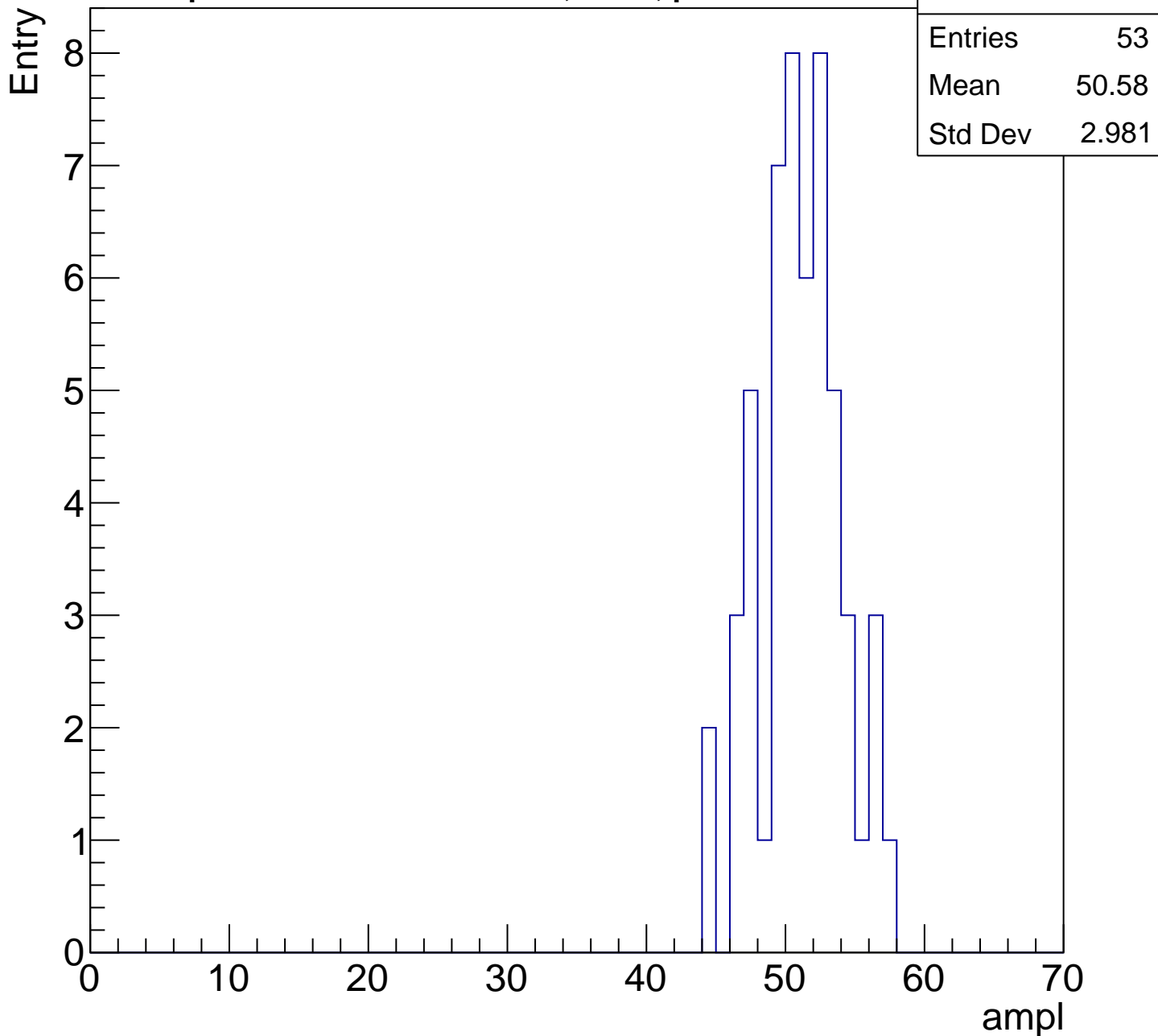
ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

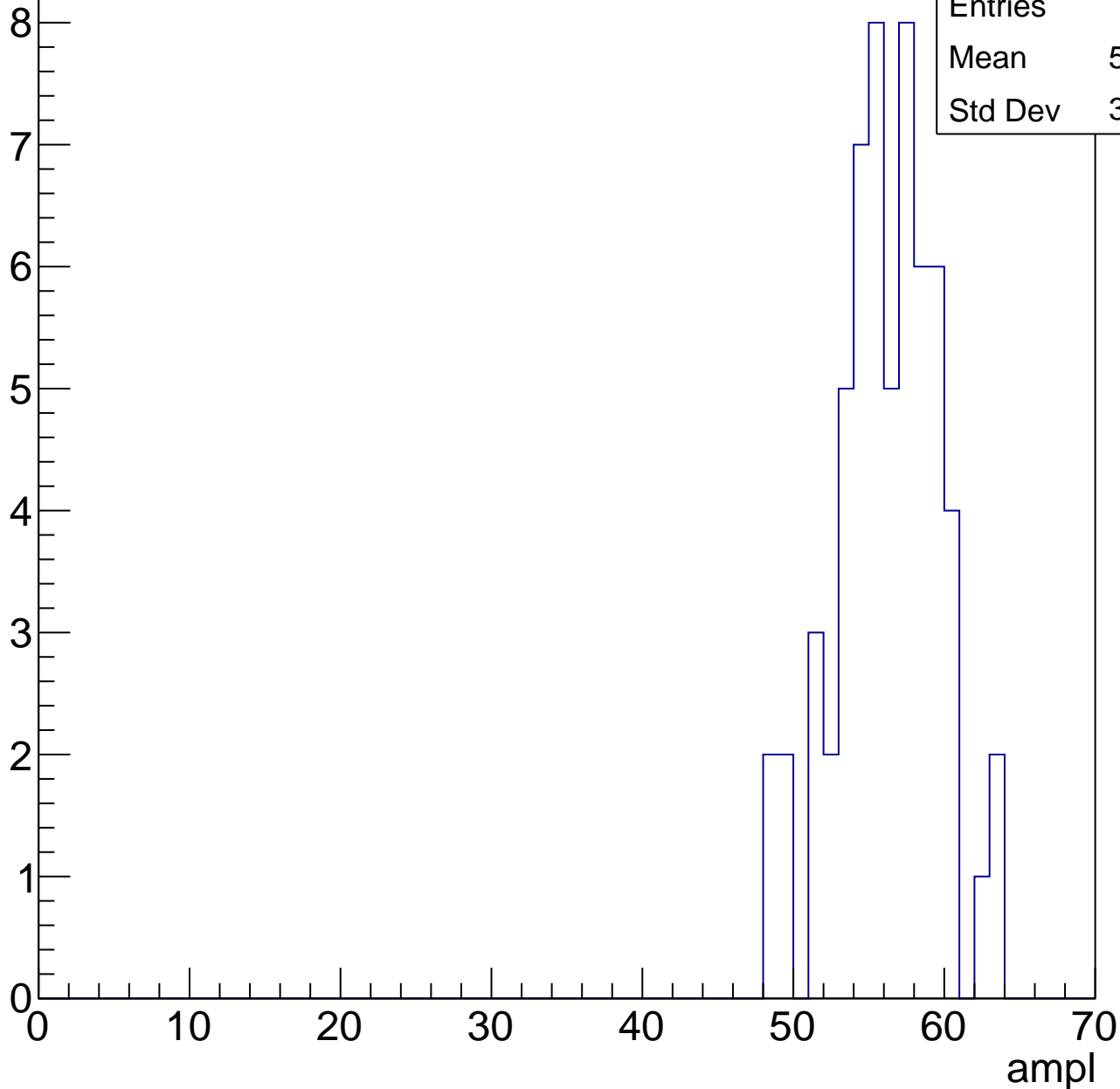


B1L101S, U21-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.74
Std Dev	3.373

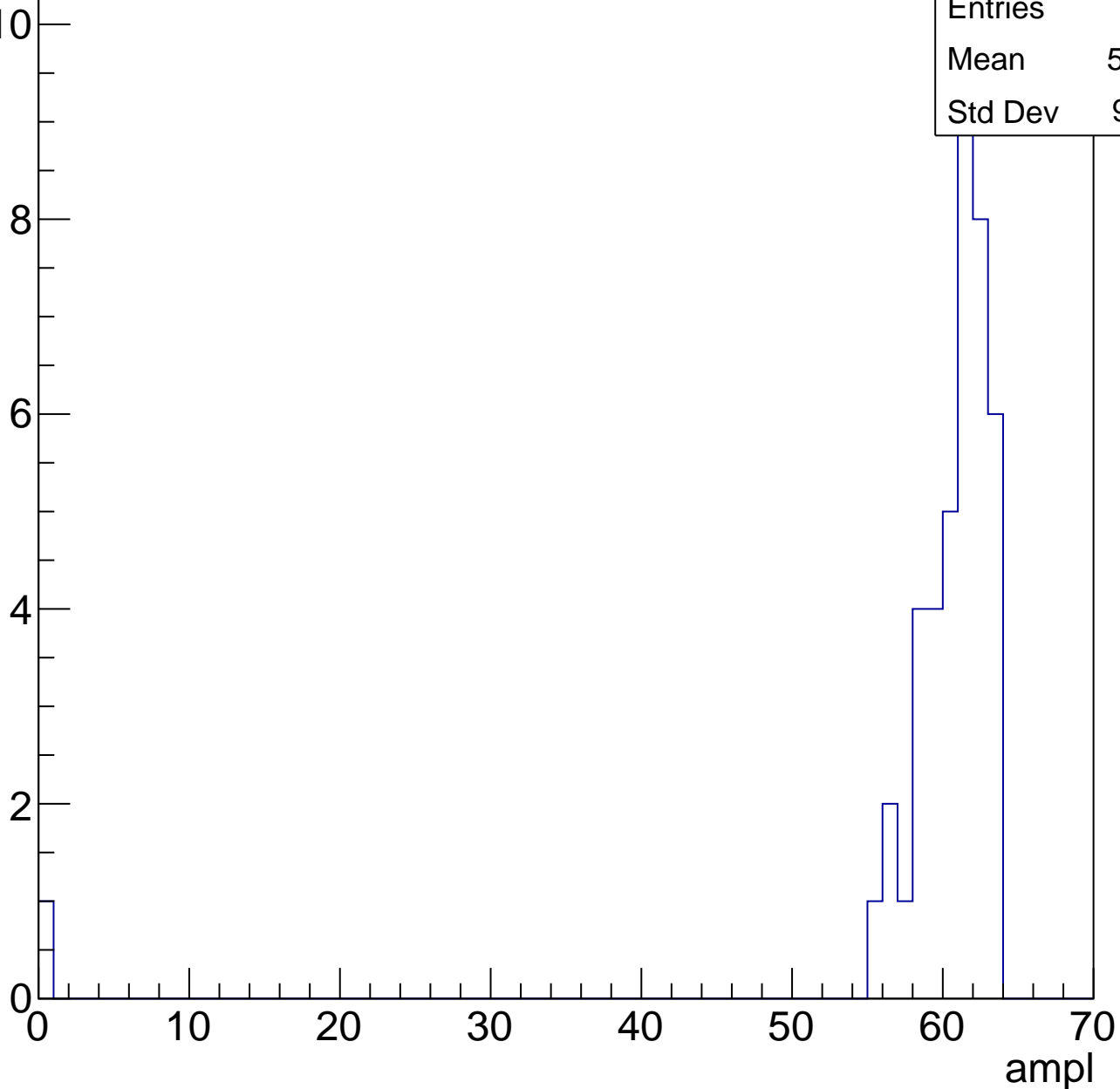


B1L101S, U21-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

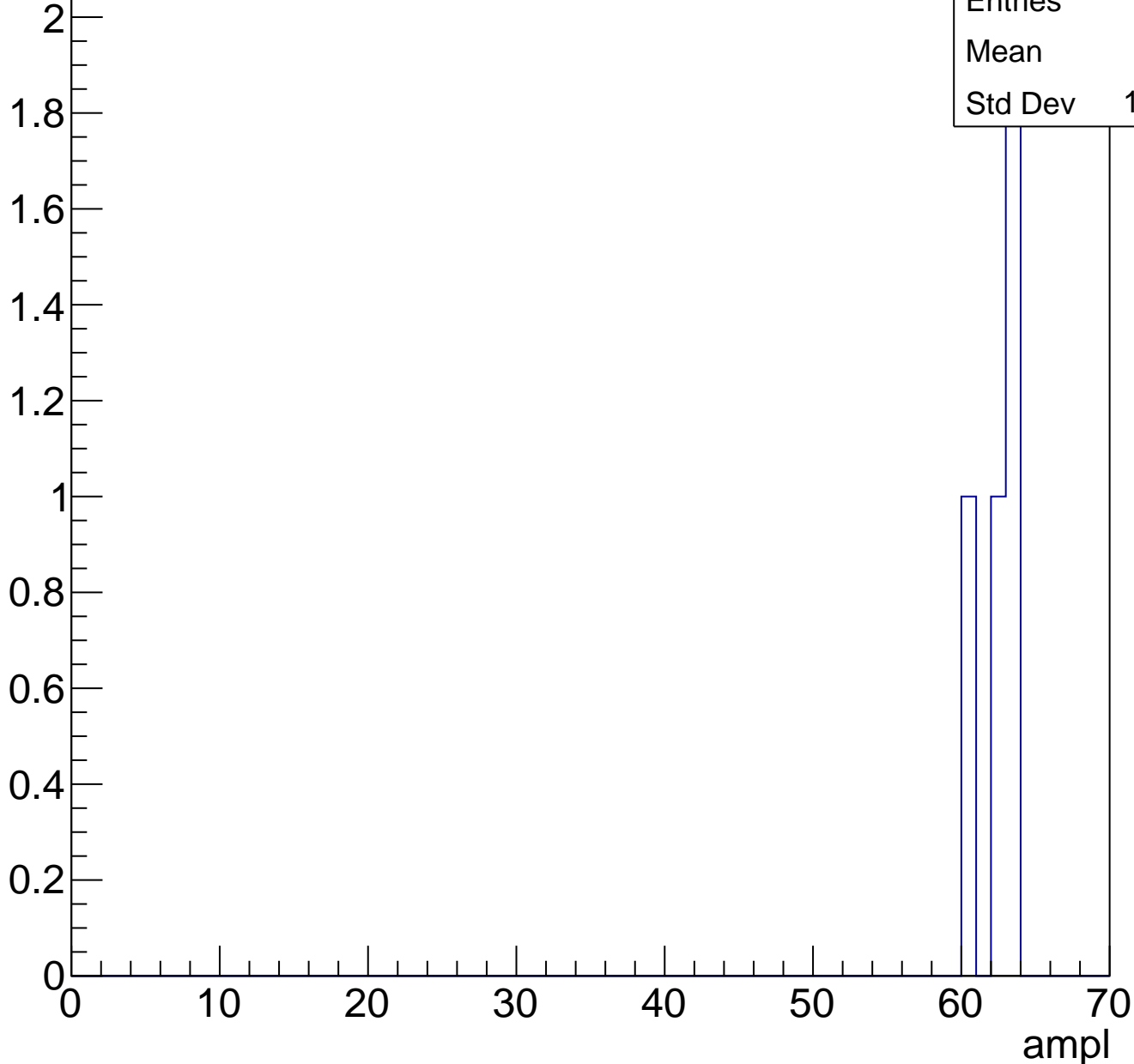
Entries	42
Mean	58.95
Std Dev	9.431



B1L101S, U21-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch93, adc0

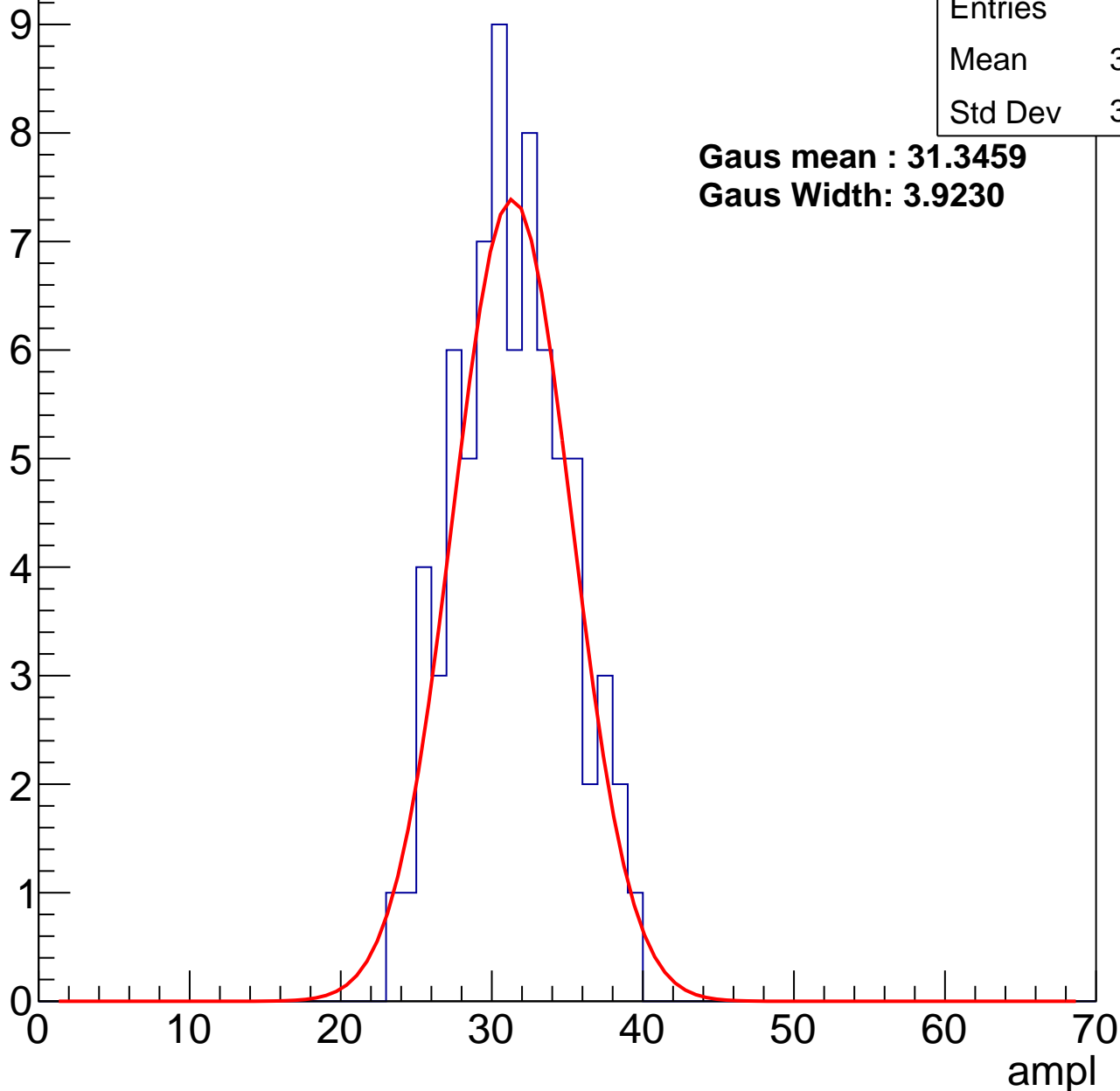
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.85
Std Dev	3.649

Gaus mean : 31.3459

Gaus Width: 3.9230



B1L101S, U21-ch93, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	93
Mean	38.65
Std Dev	4.123

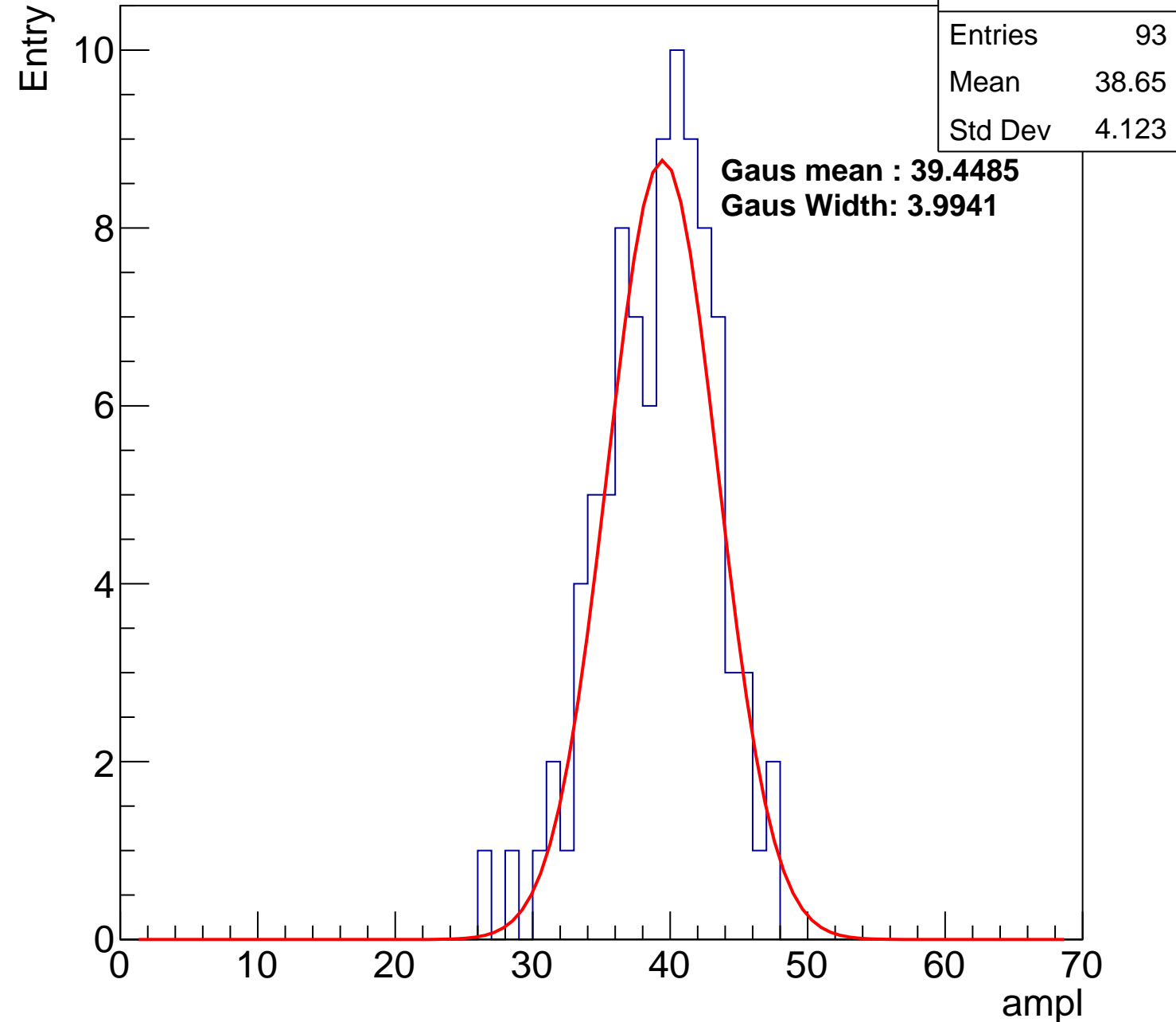
Gaus mean : 39.4485
Gaus Width: 3.9941

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch93, adc2

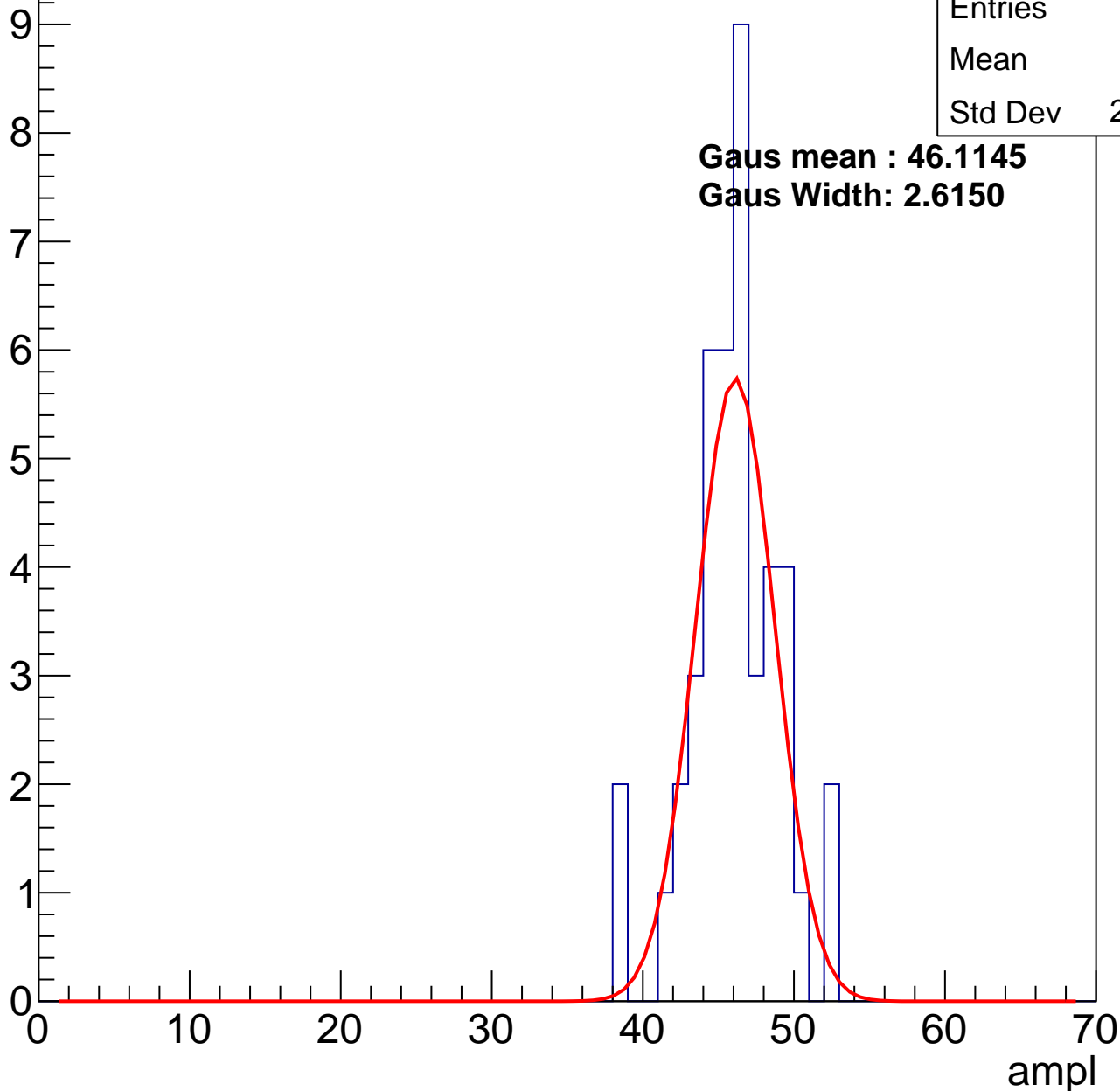
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	45.6
Std Dev	2.966

Gaus mean : 46.1145

Gaus Width: 2.6150



B1L101S, U21-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	51.32
Std Dev	2.94

Entry

10

8

6

4

2

0

0

10

20

30

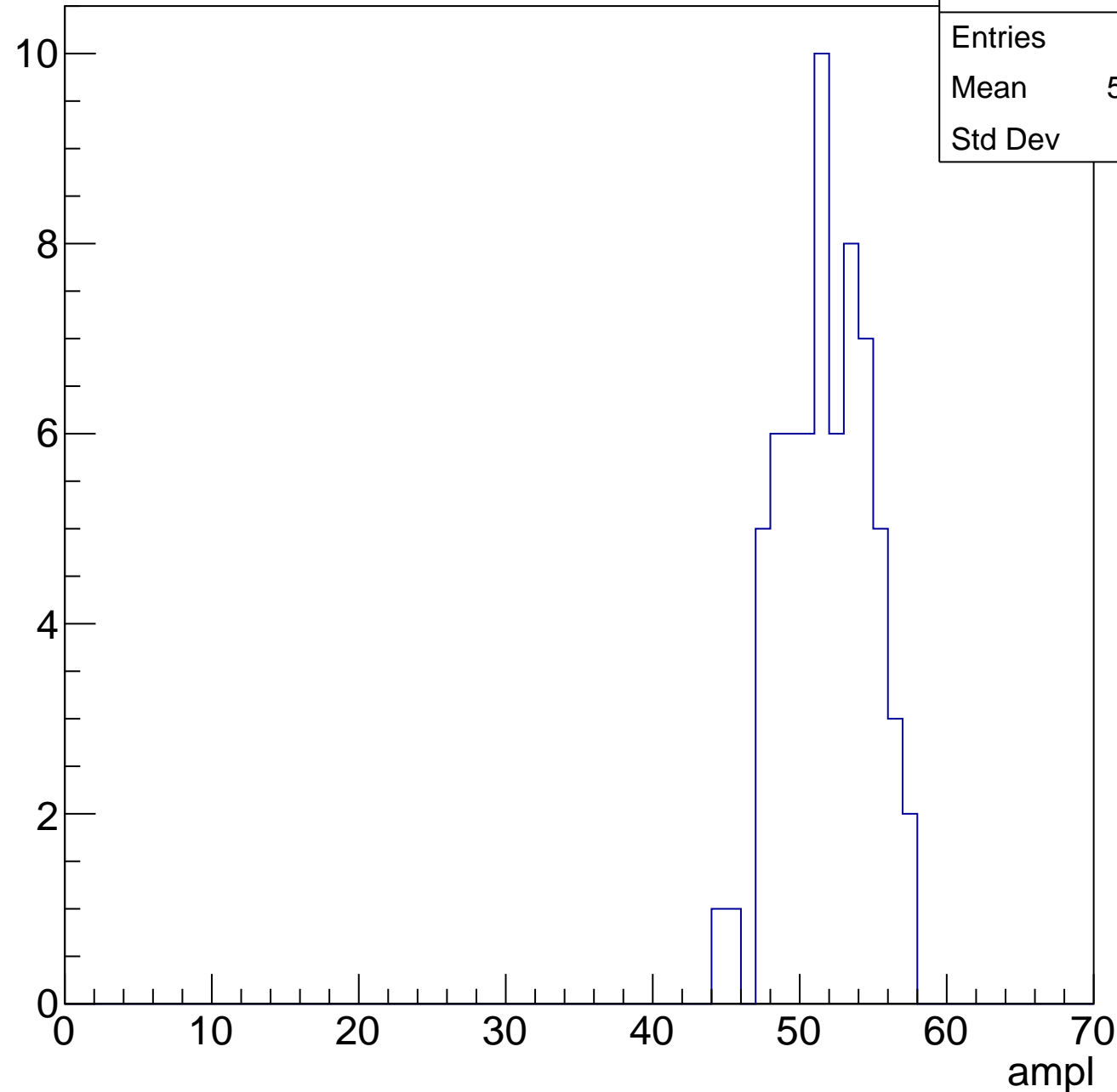
40

50

60

70

ampl

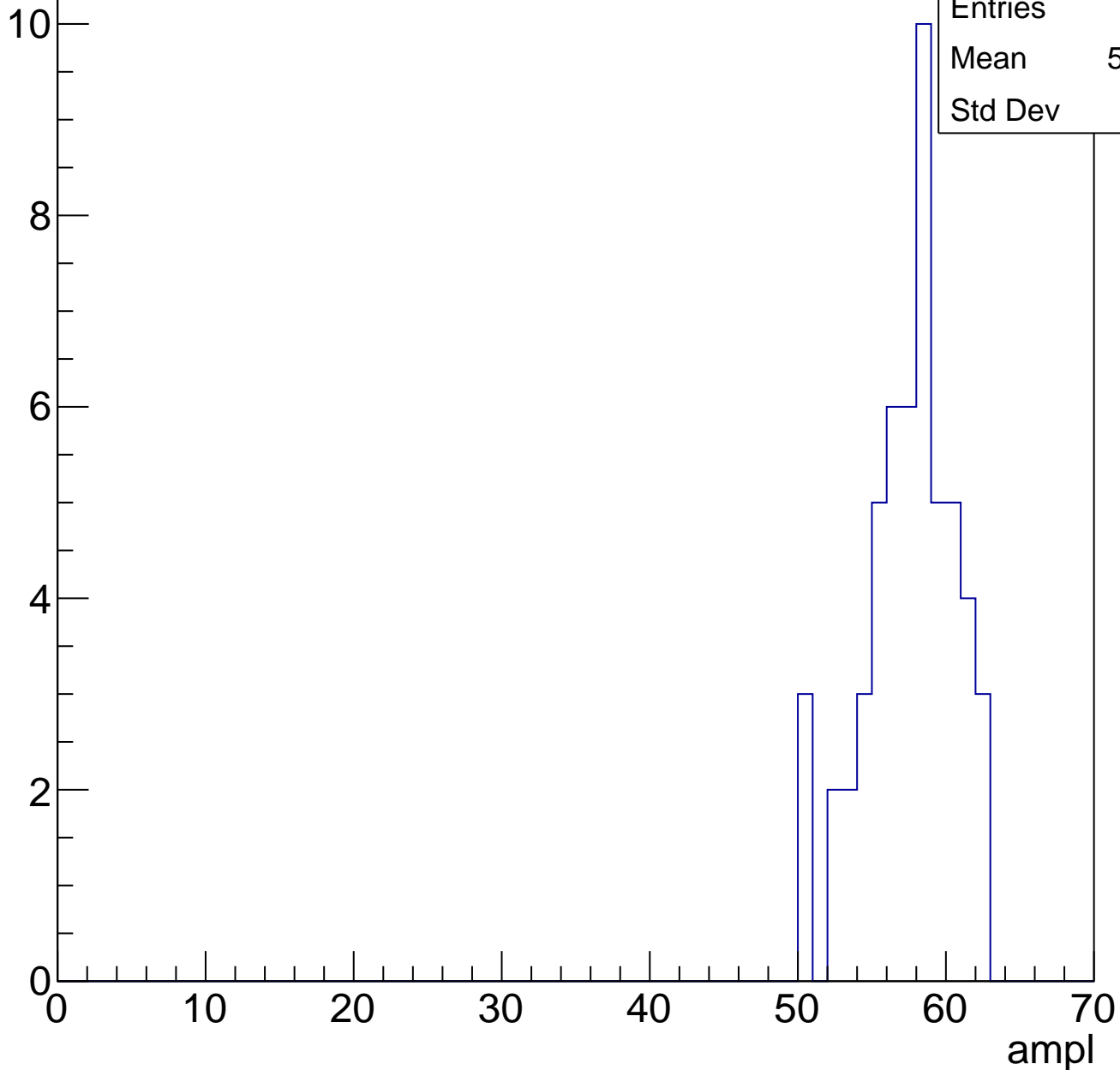


B1L101S, U21-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	54
Mean	57.04
Std Dev	3.03

Entry

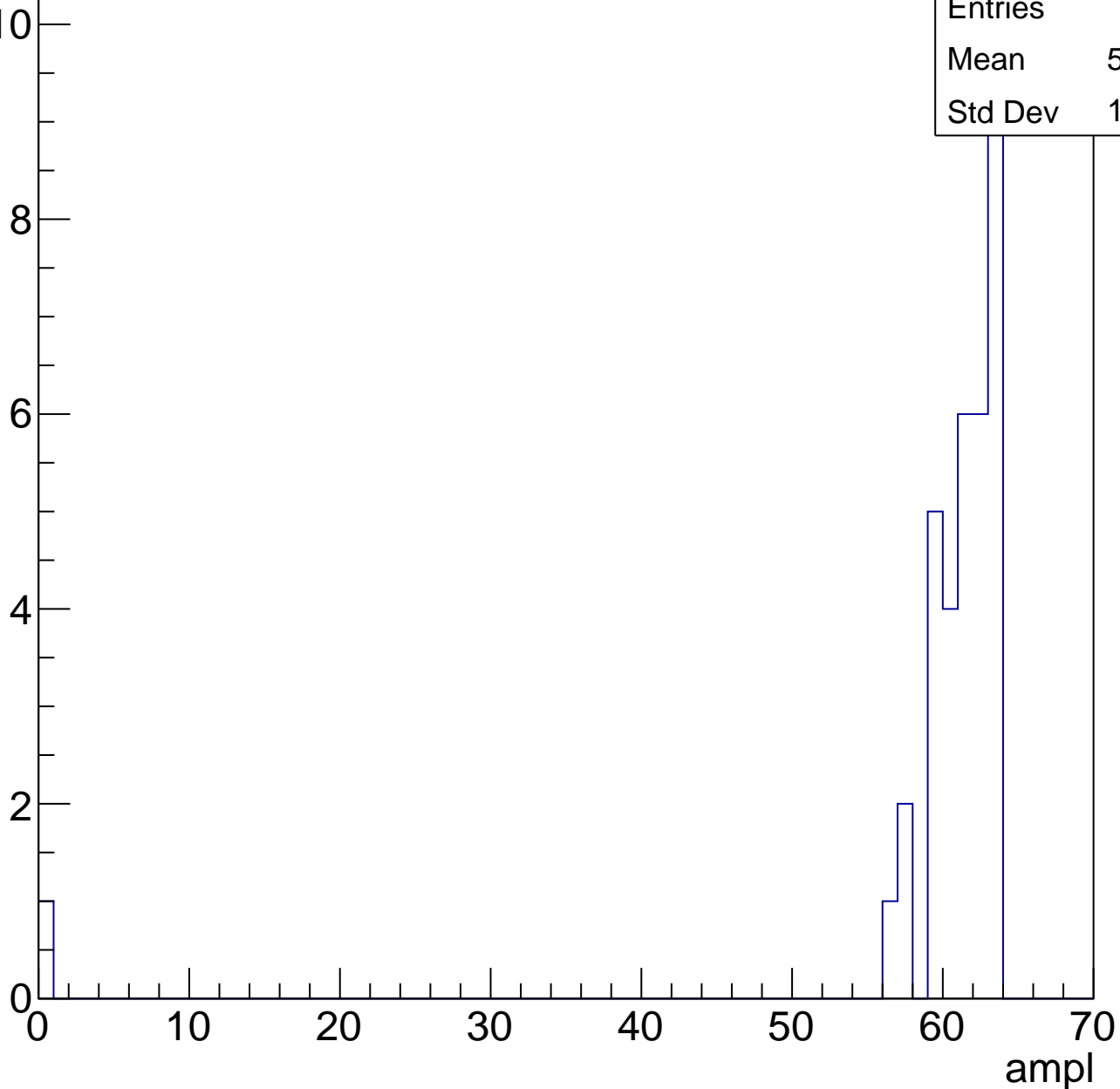


B1L101S, U21-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

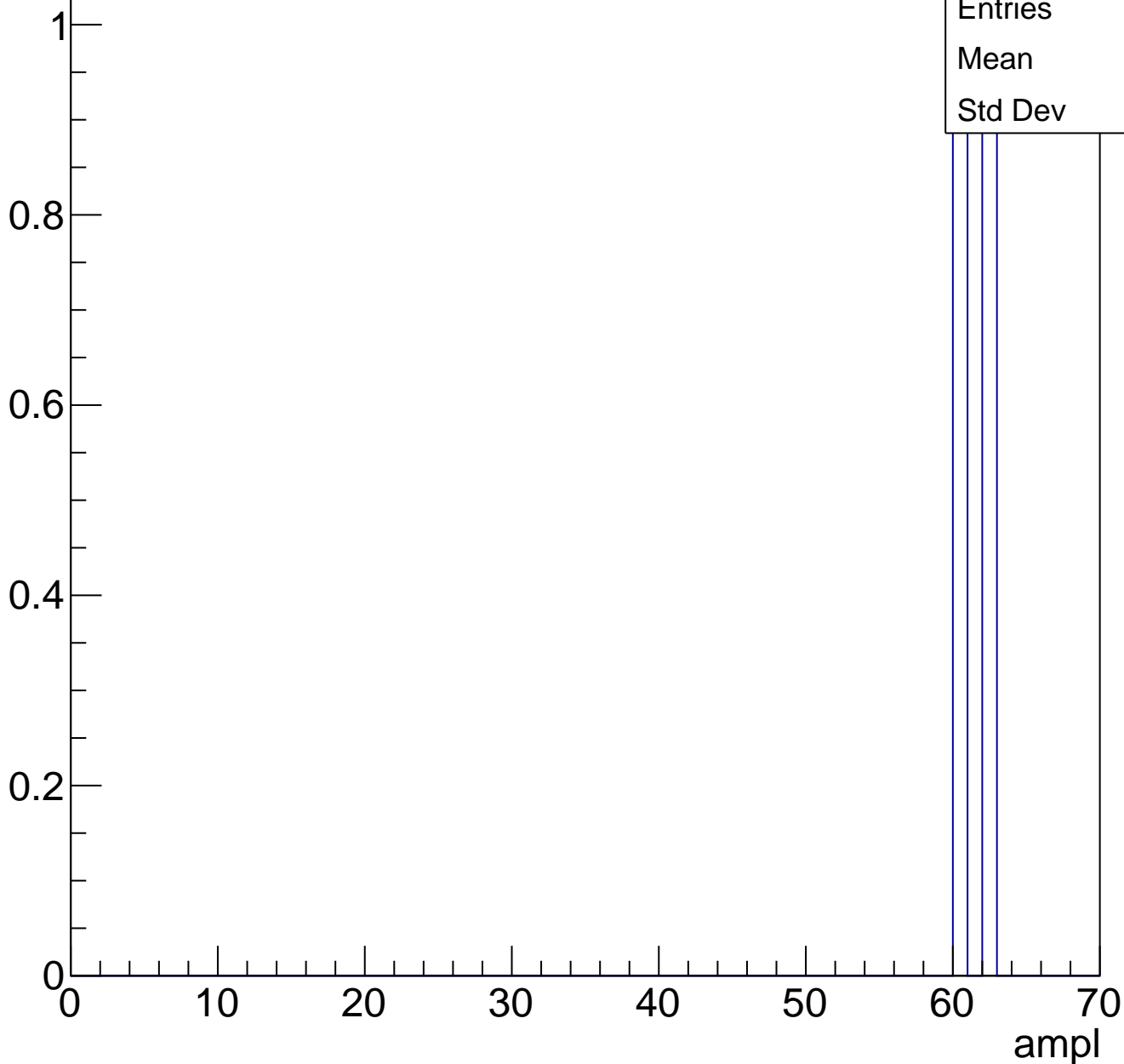
Entries	35
Mean	59.23
Std Dev	10.33



B1L101S, U21-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch94, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	28.6
Std Dev	5.924

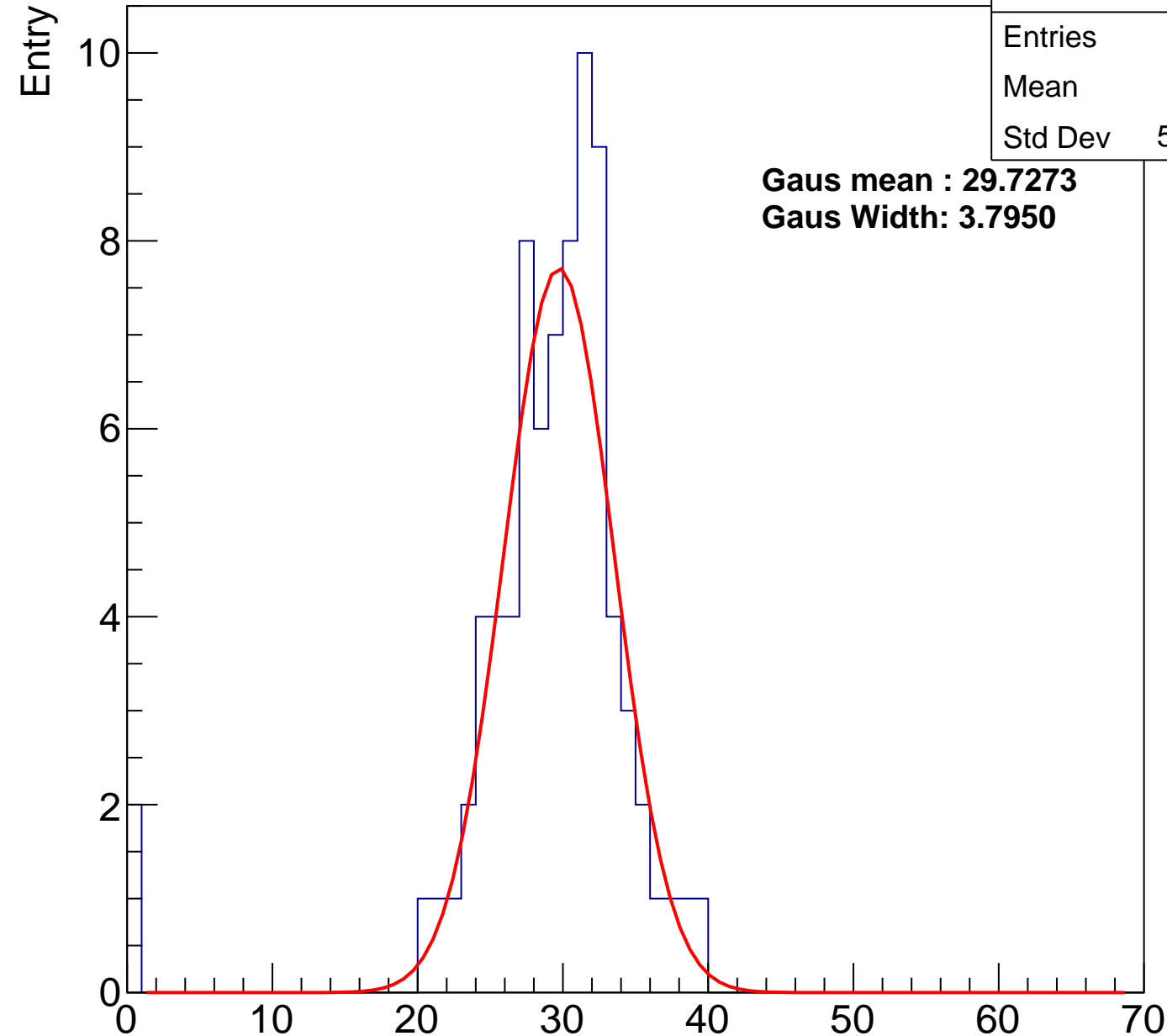
Gaus mean : 29.7273

Gaus Width: 3.7950

Entry

10
8
6
4
2
0

ampl



B1L101S, U21-ch94, adc1

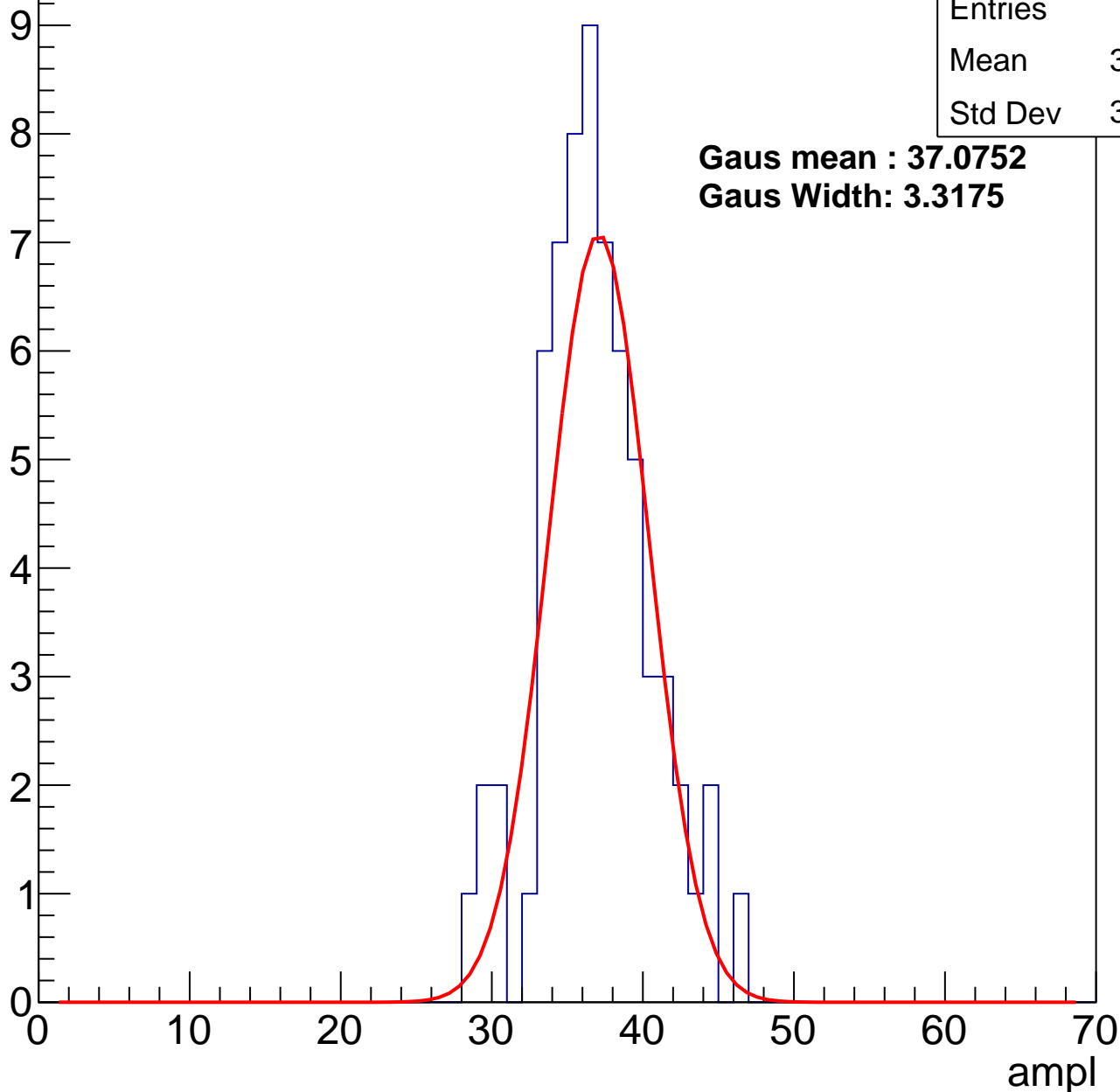
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.42
Std Dev	3.643

Gaus mean : 37.0752

Gaus Width: 3.3175



B1L101S, U21-ch94, adc2

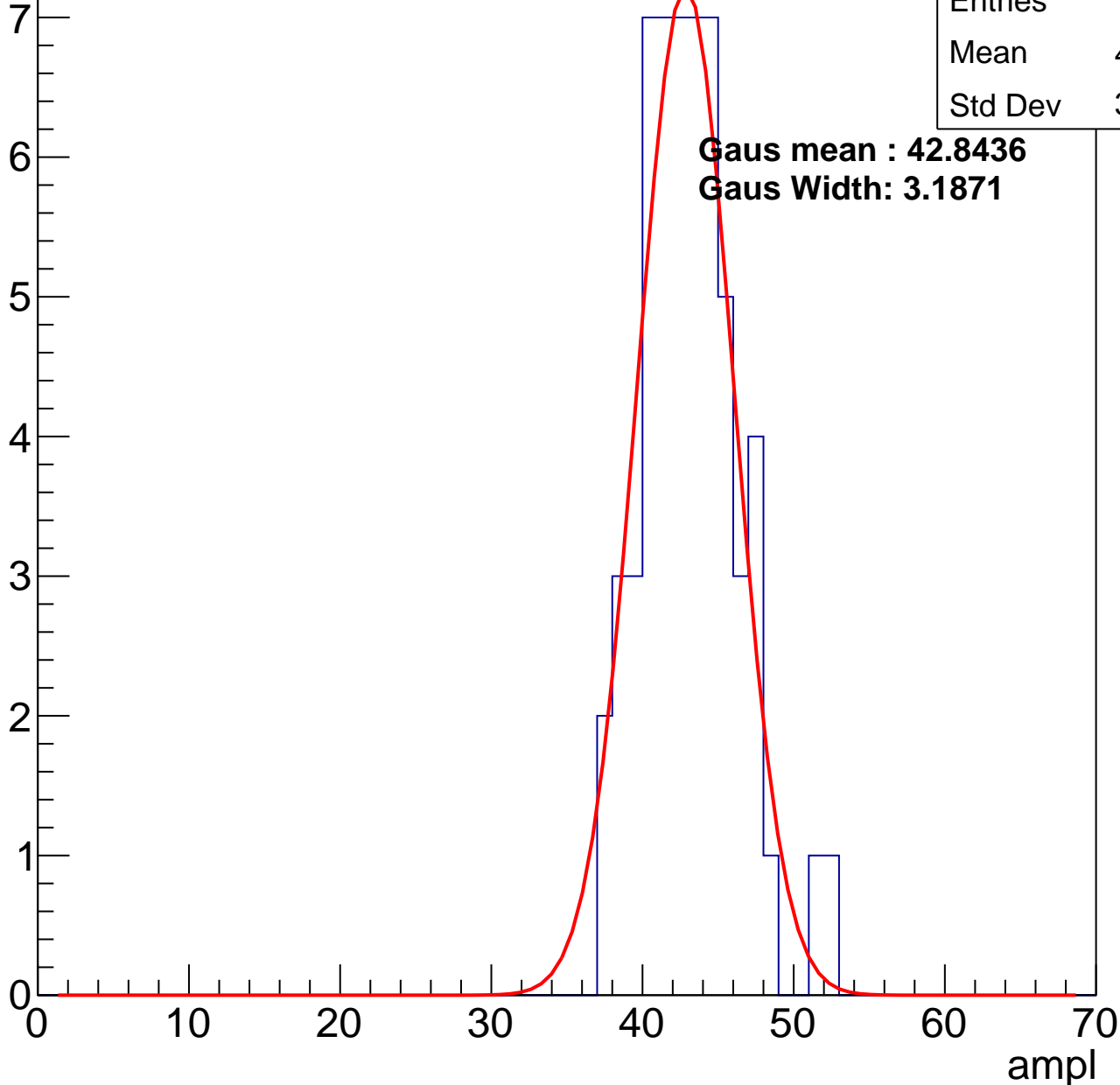
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	42.71
Std Dev	3.151

Gaus mean : 42.8436

Gaus Width: 3.1871

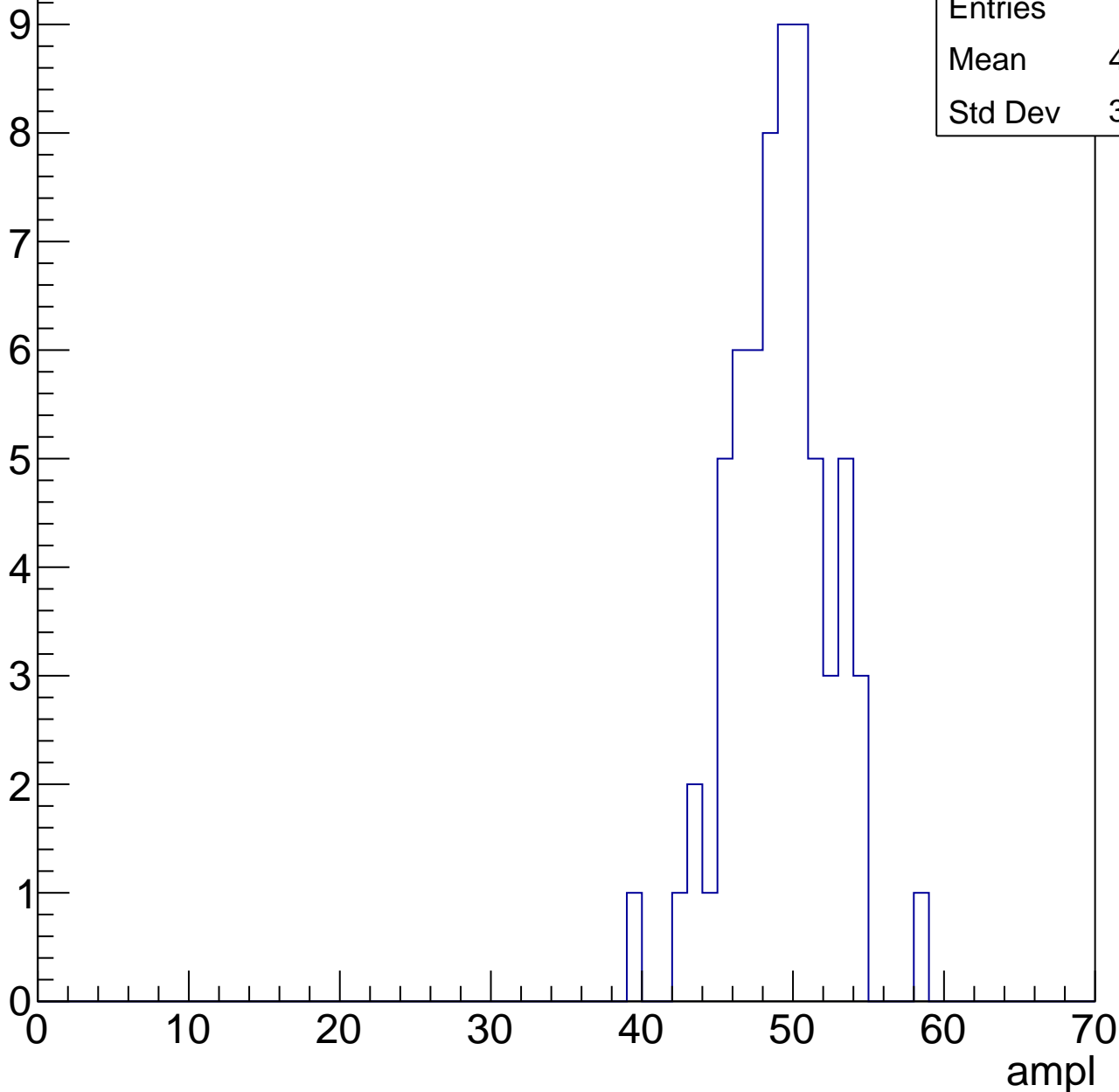


B1L101S, U21-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.69
Std Dev	3.277

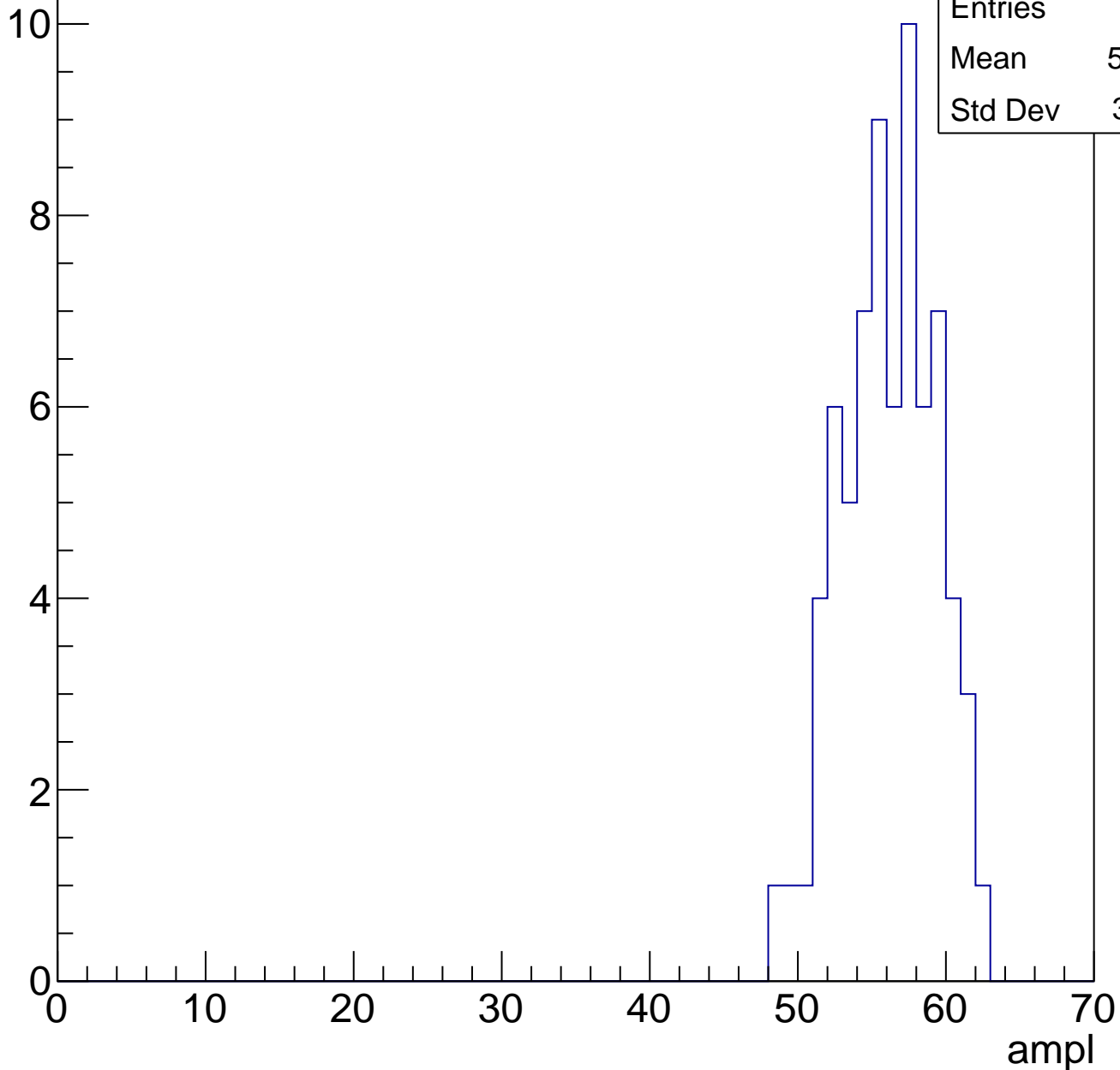


B1L101S, U21-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	55.68
Std Dev	3.121

Entry

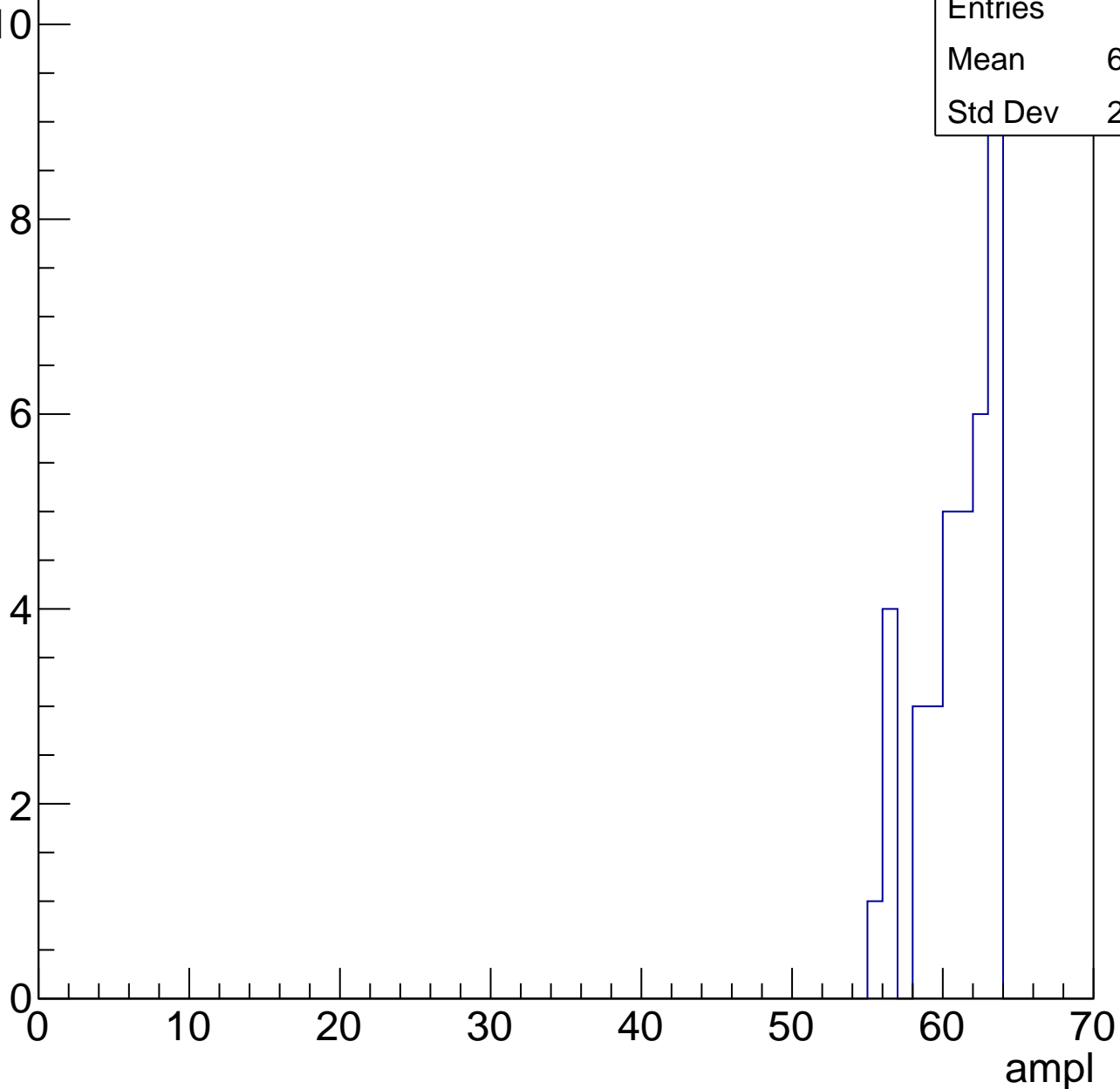


B1L101S, U21-ch94, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

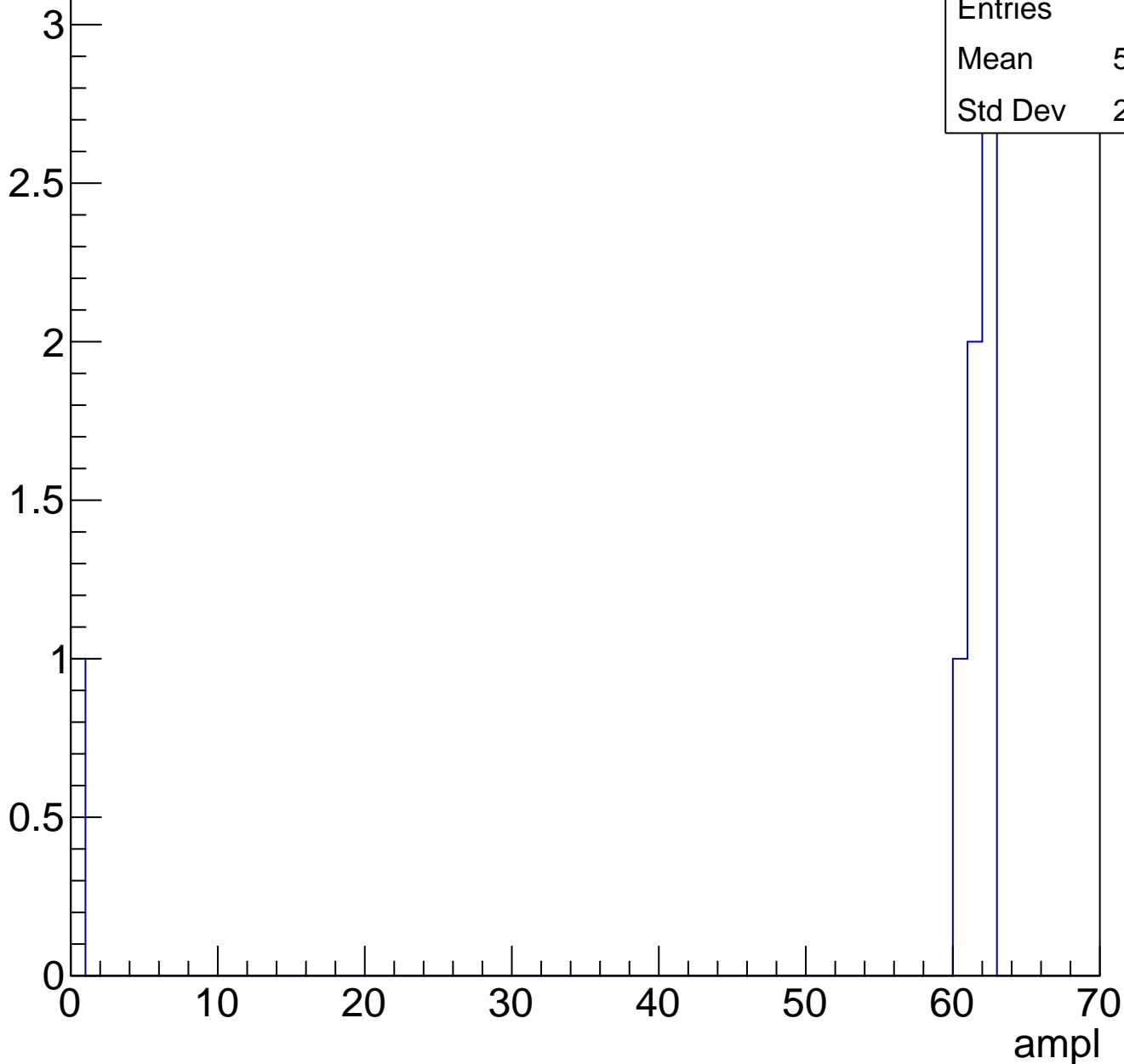
Entries	37
Mean	60.46
Std Dev	2.412



B1L101S, U21-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	7
Mean	52.57
Std Dev	21.47

B1L101S, U21-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch95, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	31.55
Std Dev	3.693

Gaus mean : 32.2638

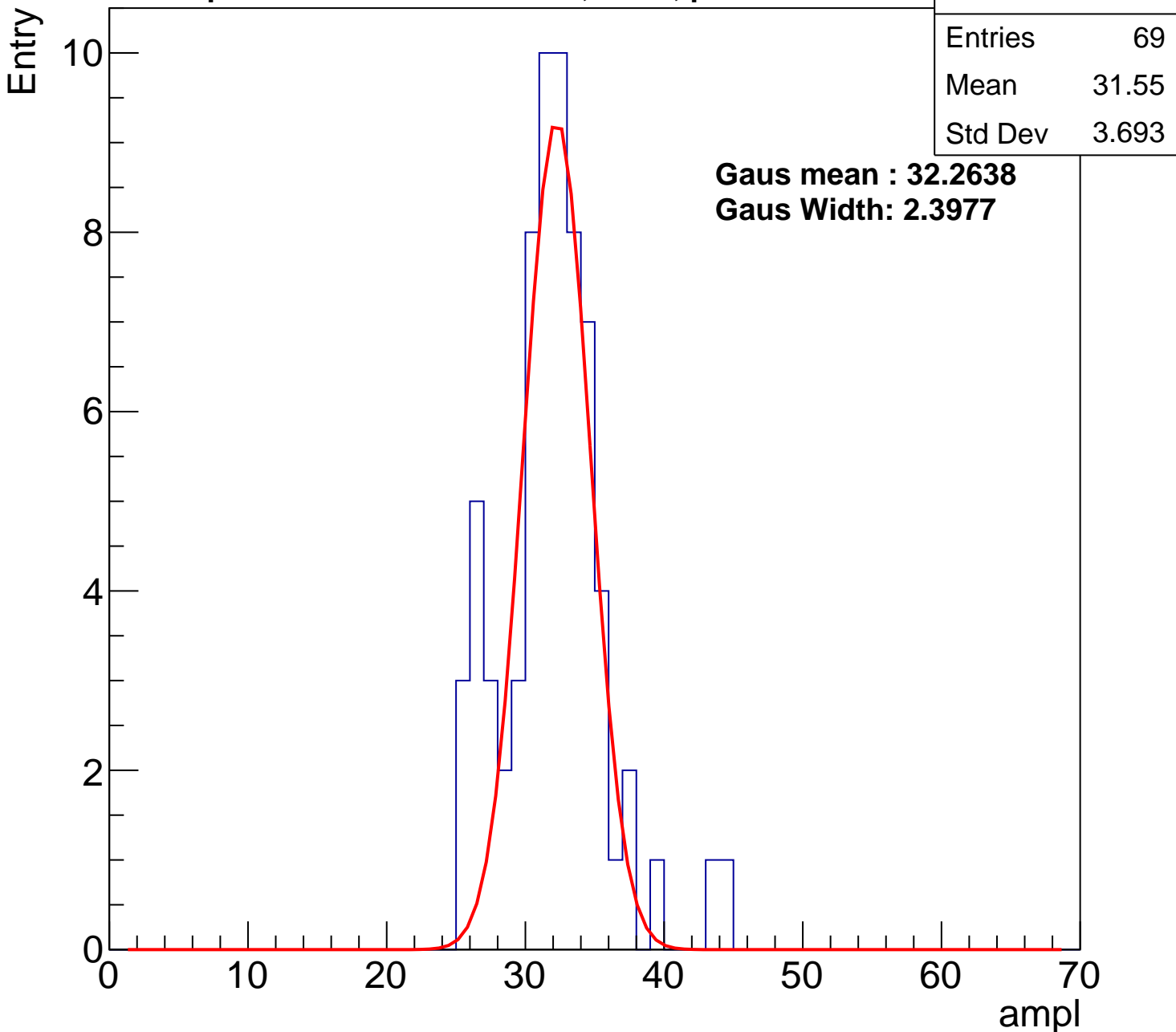
Gaus Width: 2.3977

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch95, adc1

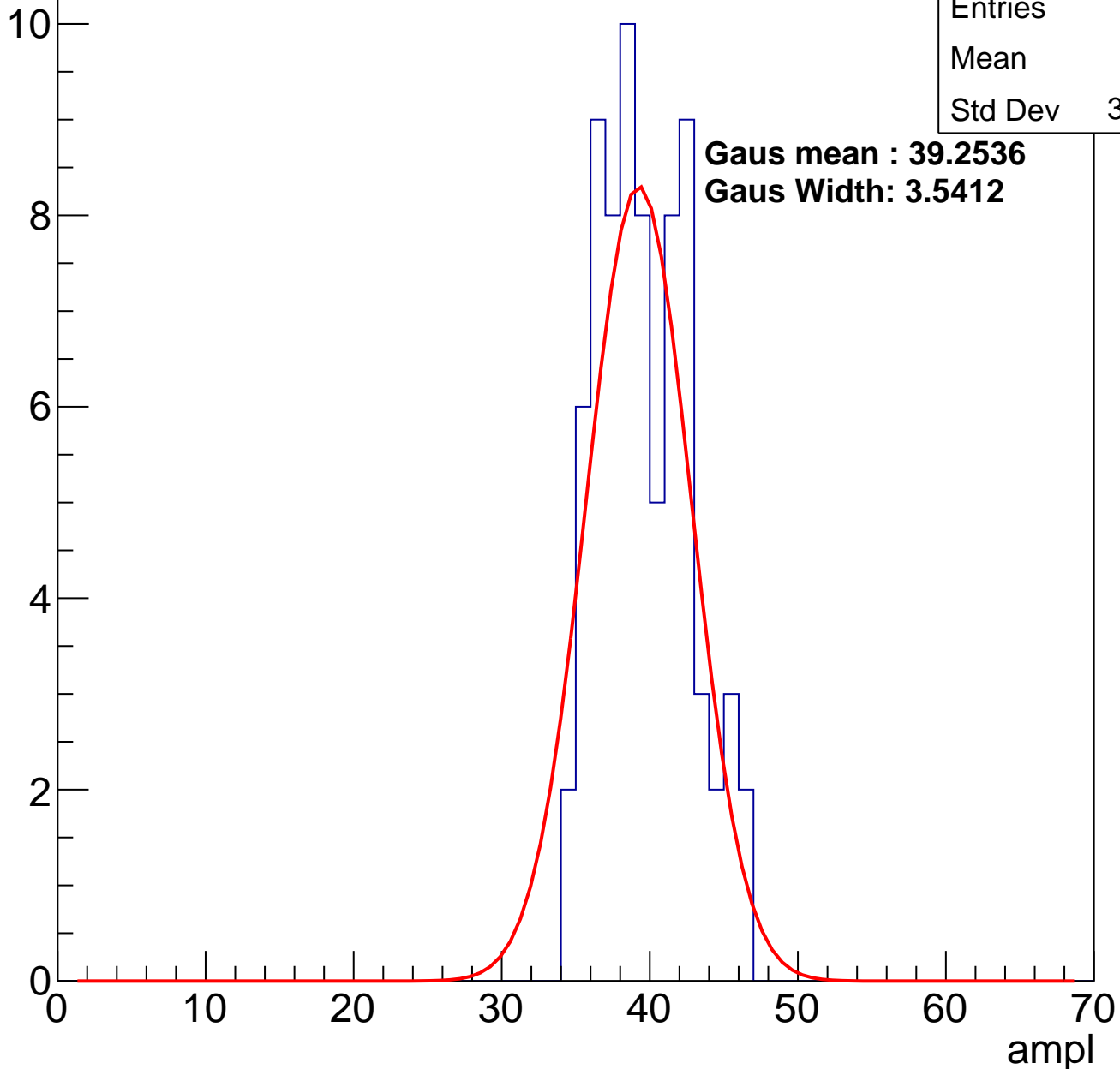
calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	39.2
Std Dev	3.029

Gaus mean : 39.2536

Gaus Width: 3.5412

Entry



B1L101S, U21-ch95, adc2

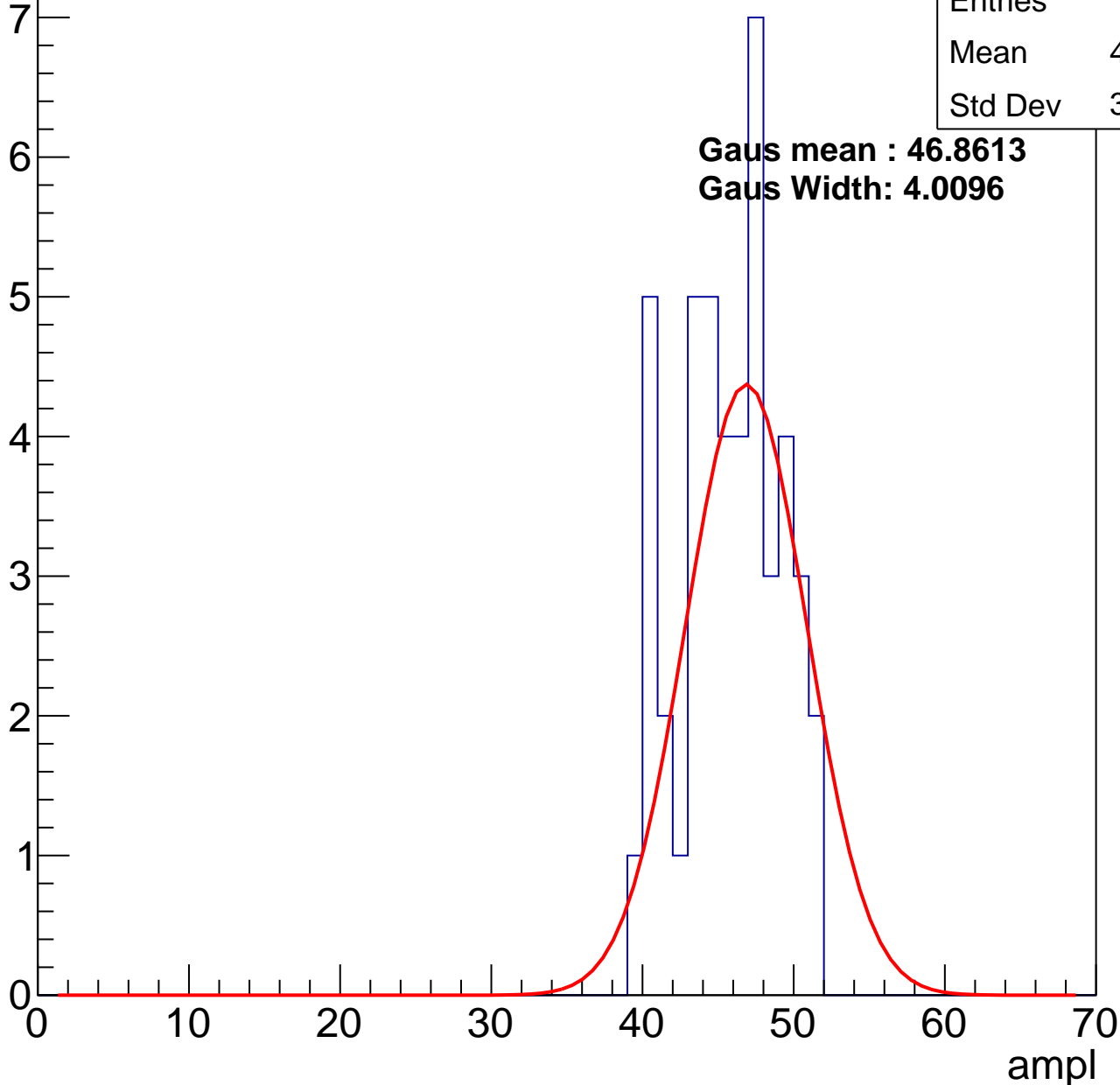
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	45.28
Std Dev	3.275

Gaus mean : 46.8613

Gaus Width: 4.0096

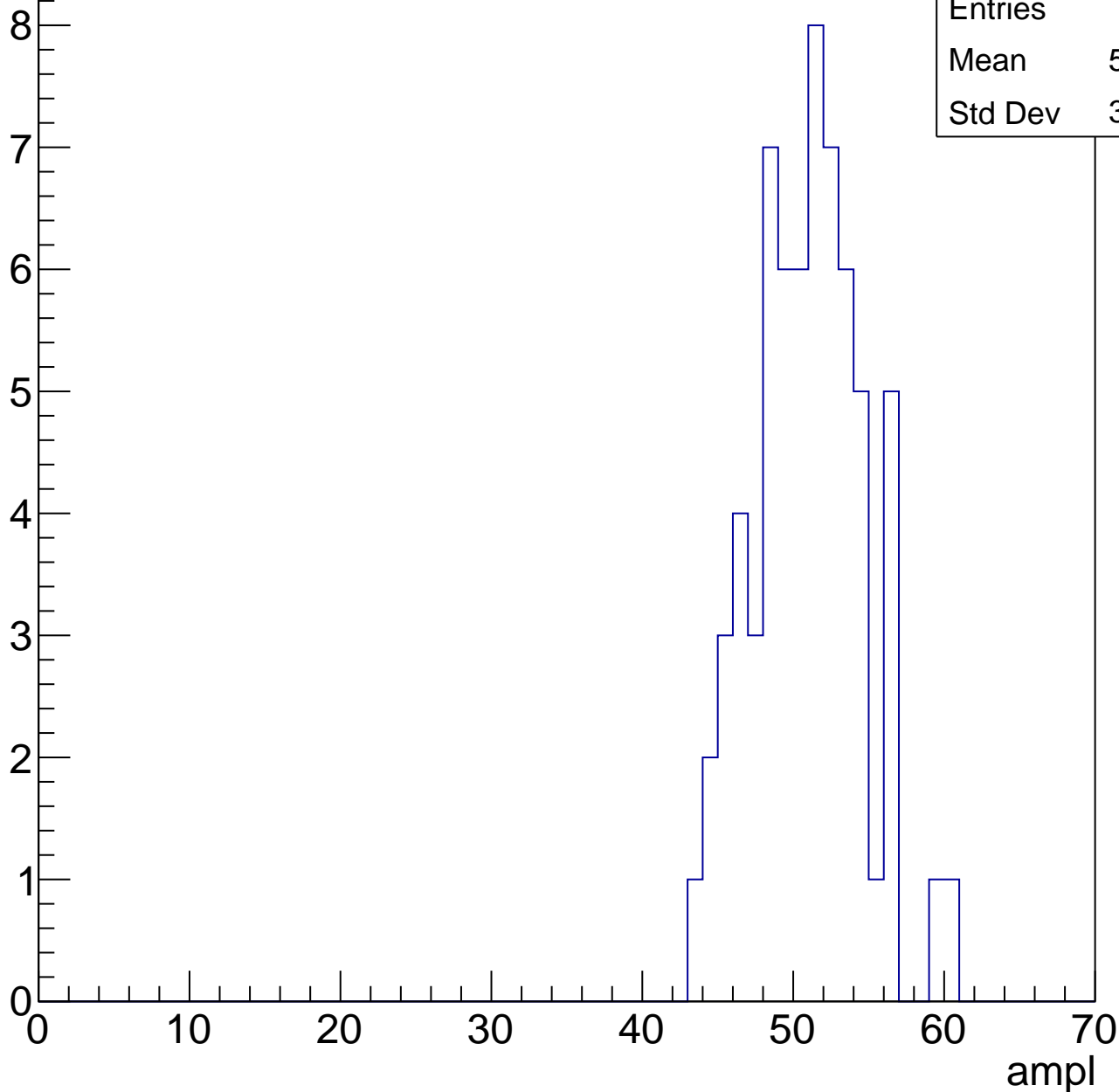


B1L101S, U21-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	50.53
Std Dev	3.606

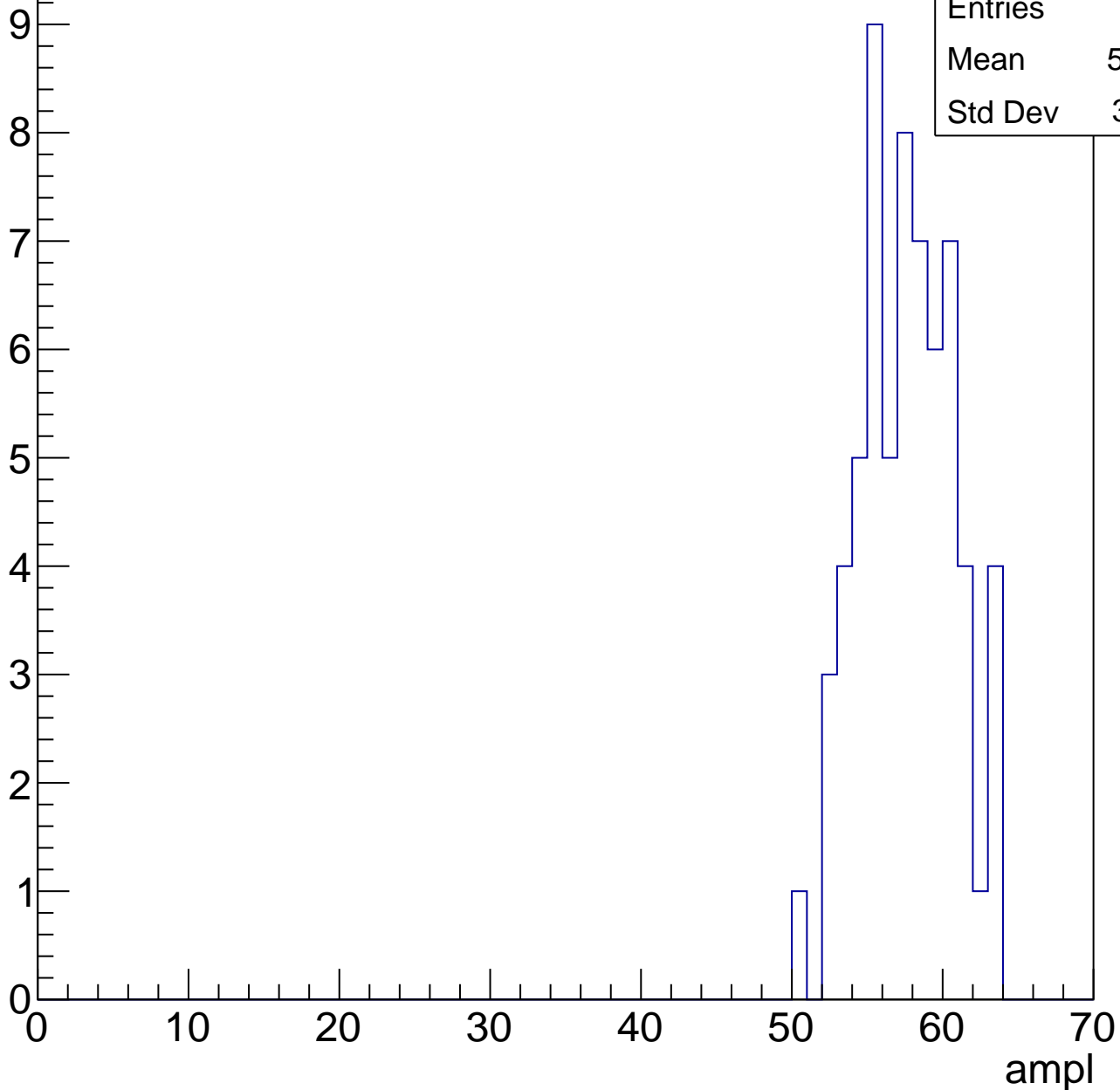


B1L101S, U21-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	57.14
Std Dev	3.061

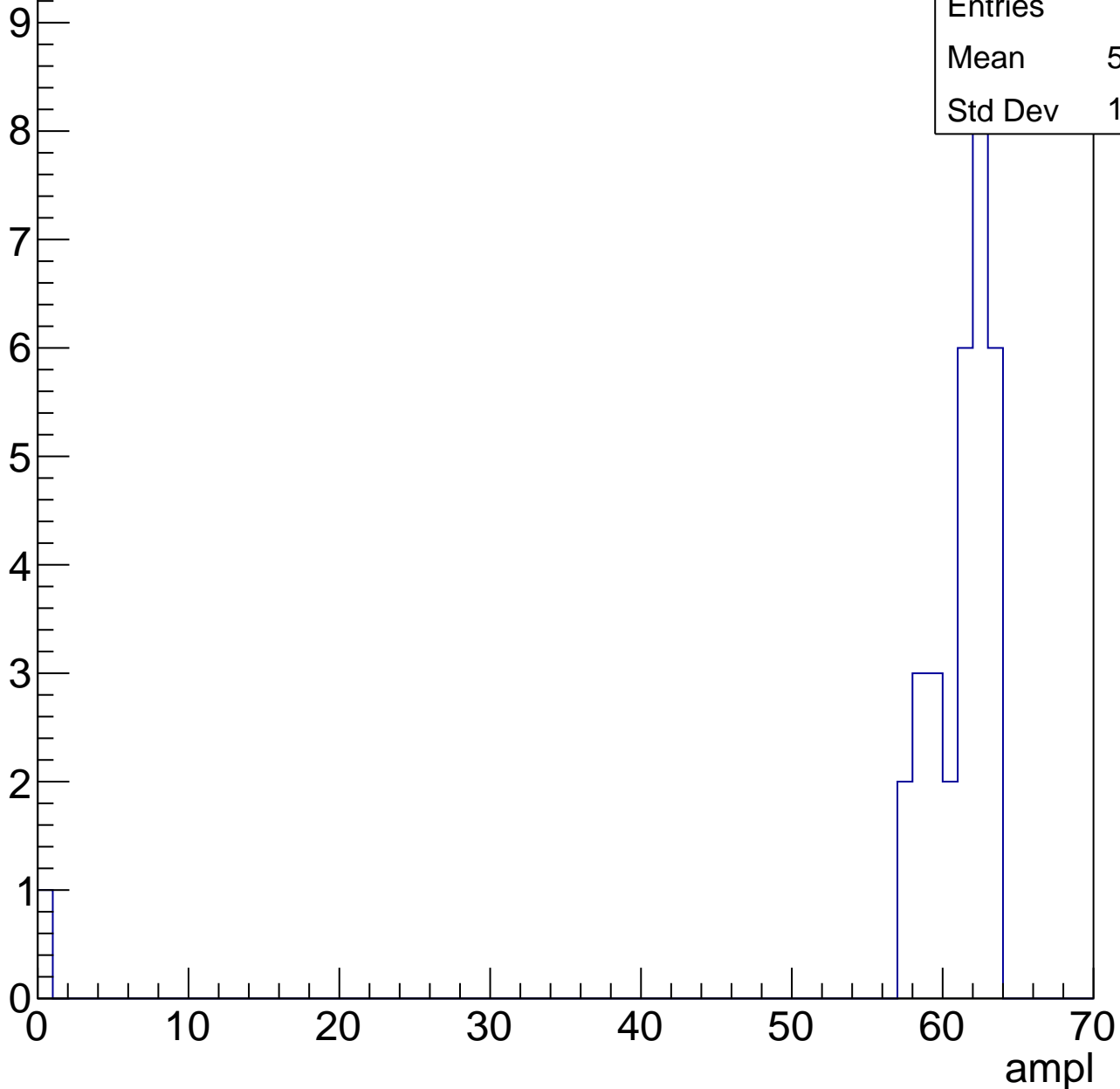


B1L101S, U21-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.97
Std Dev	10.75



B1L101S, U21-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch96, adc0

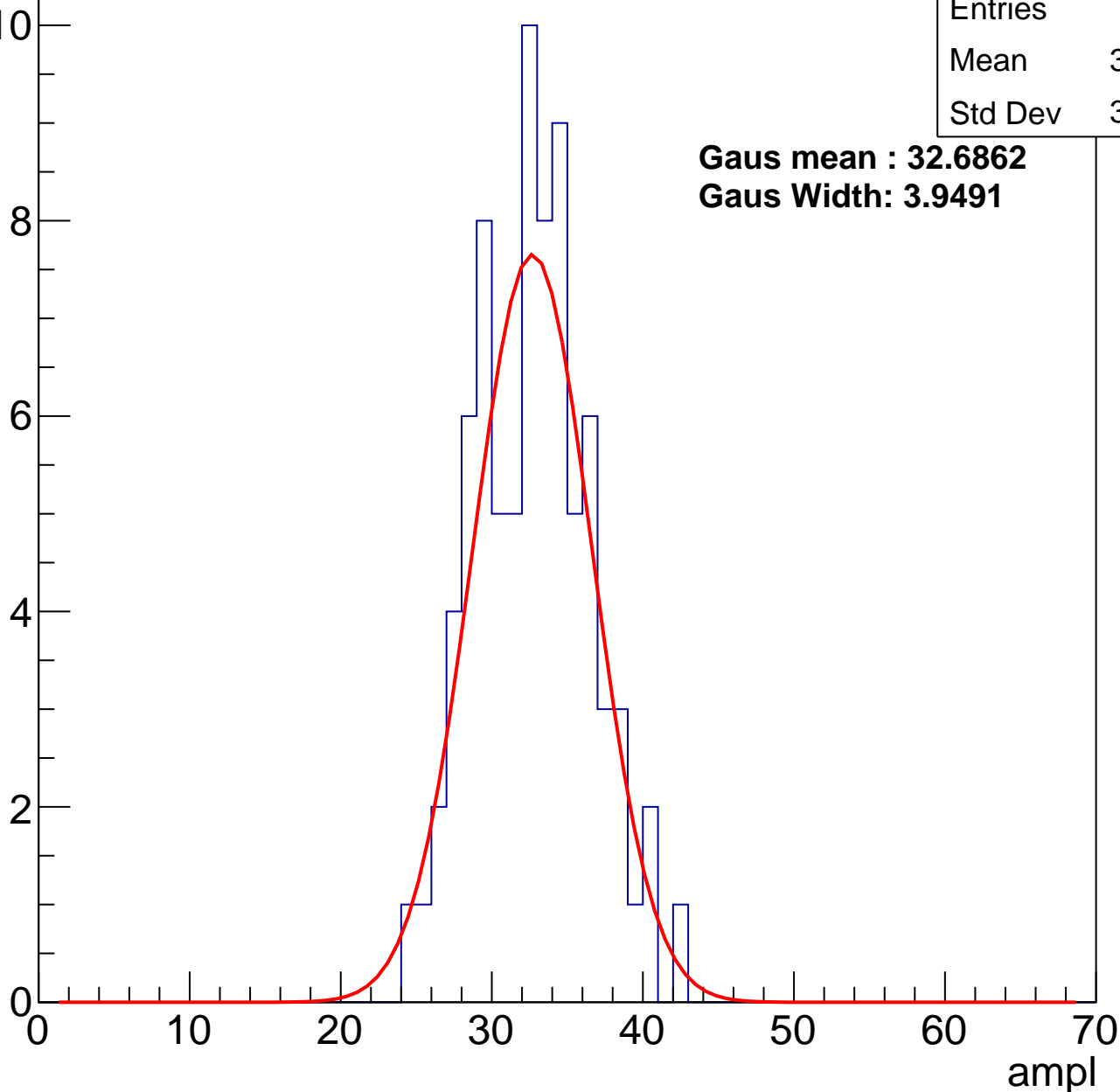
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	32.26
Std Dev	3.737

Gaus mean : 32.6862

Gaus Width: 3.9491



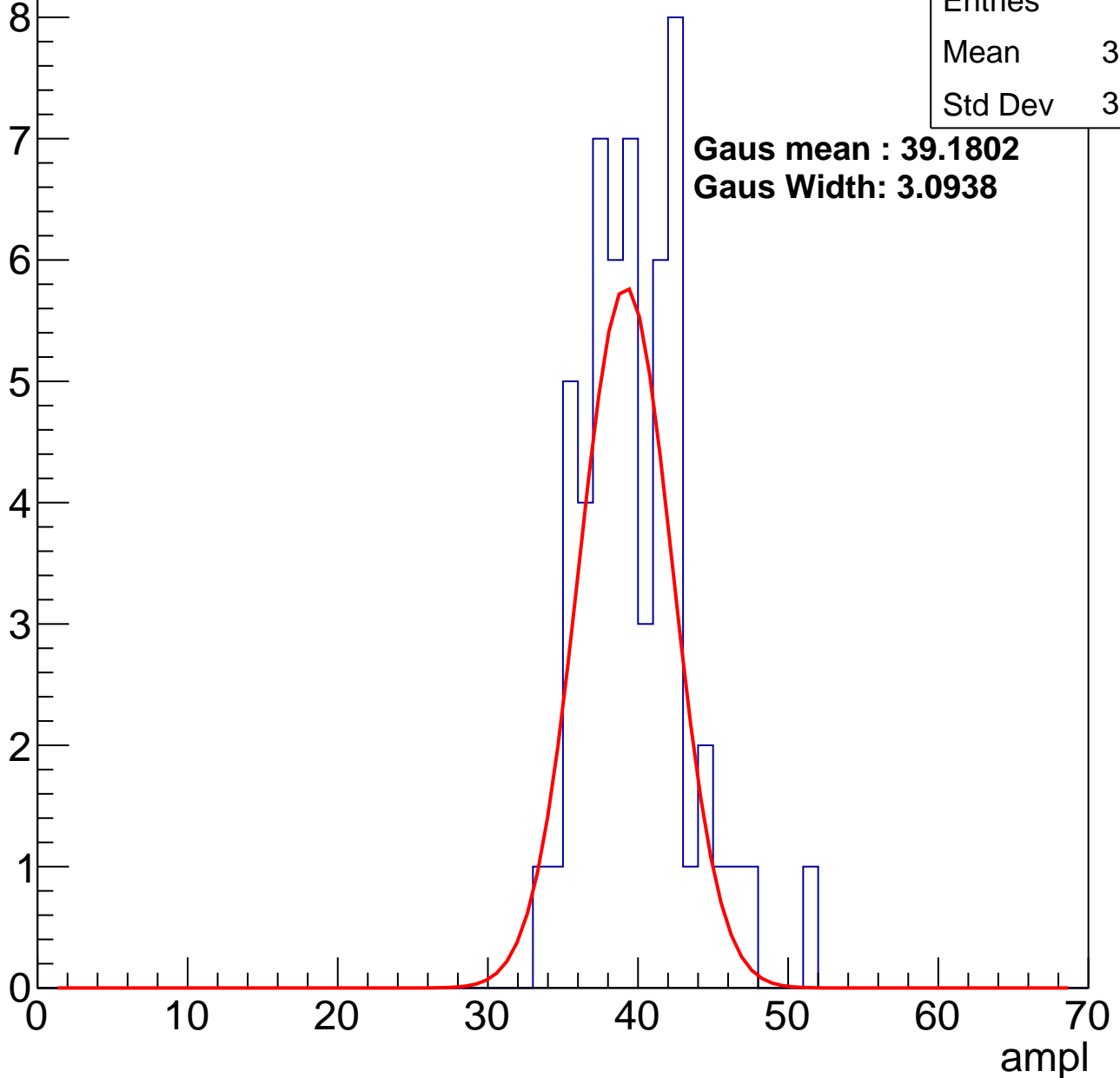
B1L101S, U21-ch96, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	39.42
Std Dev	3.462

Gaus mean : 39.1802
Gaus Width: 3.0938



B1L101S, U21-ch96, adc2

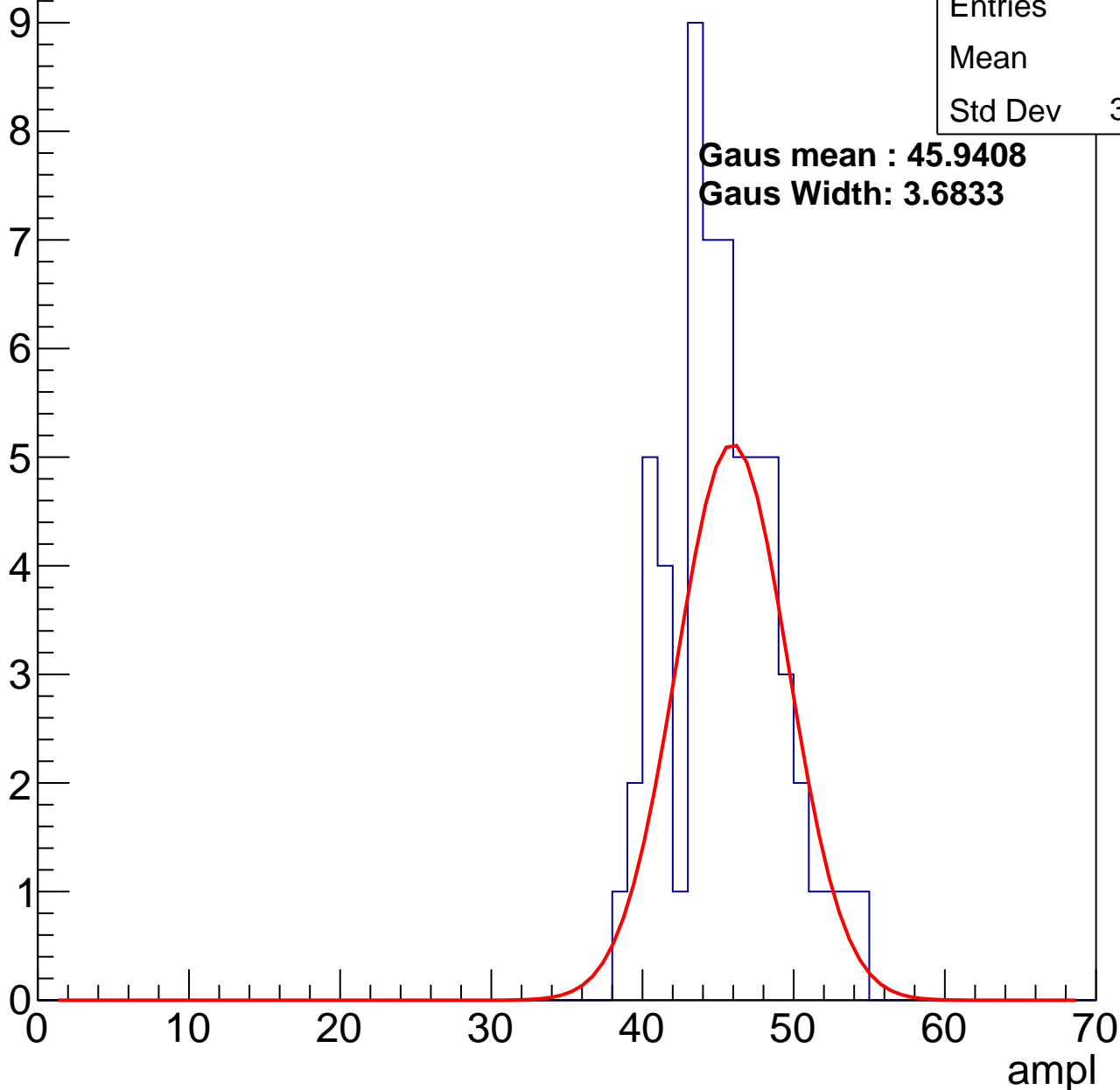
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	44.9
Std Dev	3.558

Gaus mean : 45.9408

Gaus Width: 3.6833

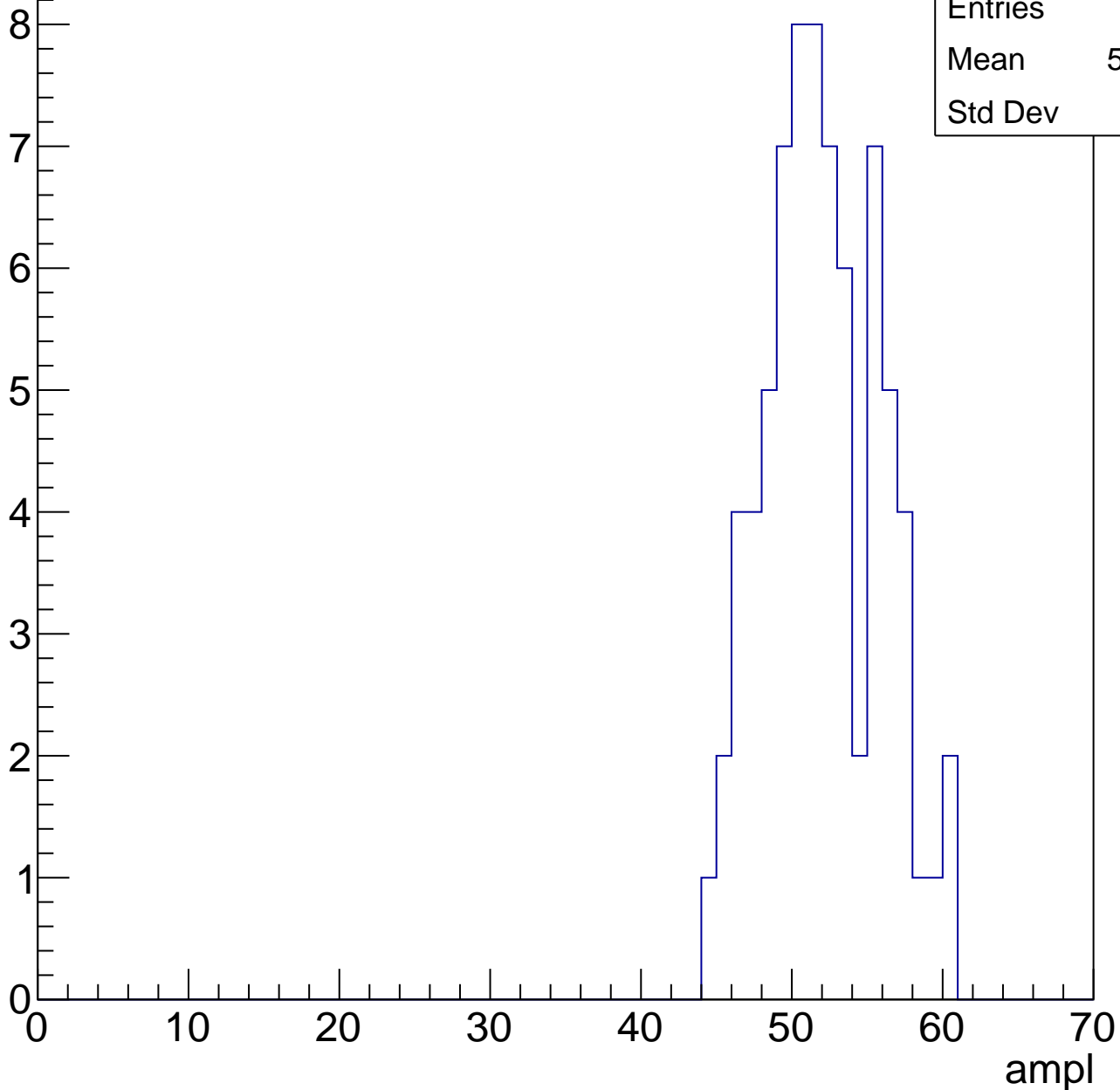


B1L101S, U21-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

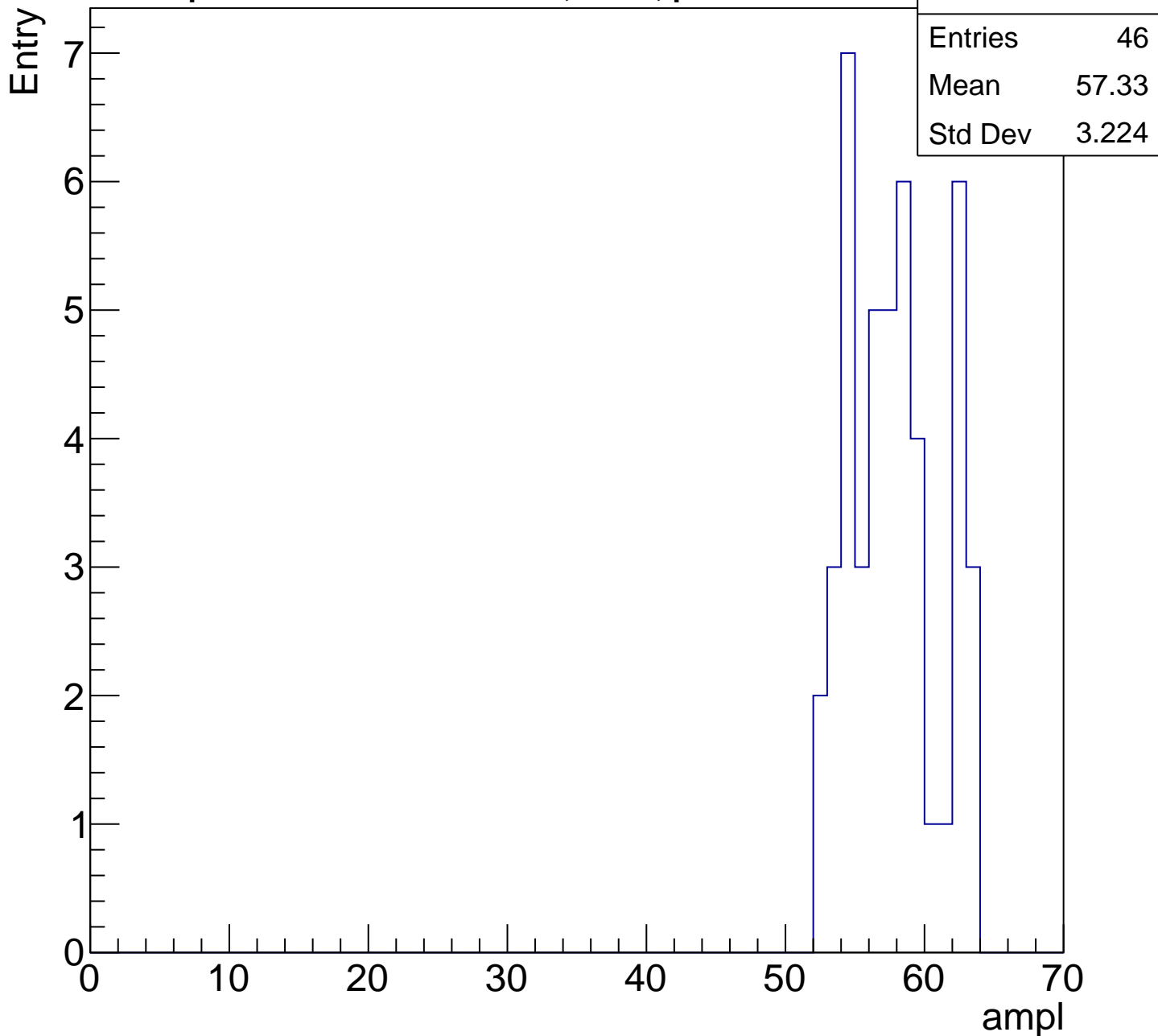
Entry

Entries	74
Mean	51.58
Std Dev	3.76



B1L101S, U21-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

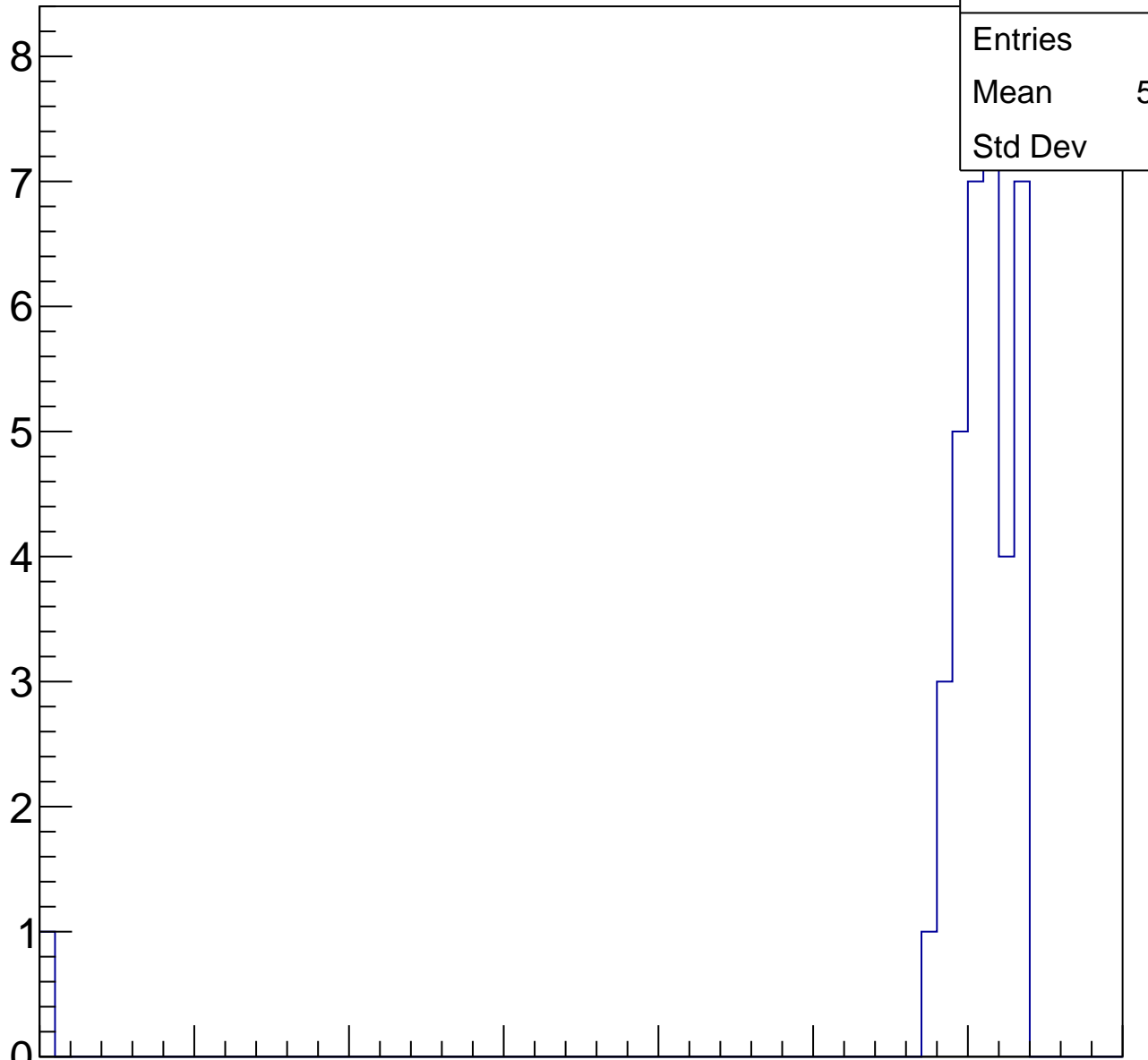
Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	58.97
Std Dev	10.1

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	23
Std Dev	0

B1L101S, U21-ch97, adc0

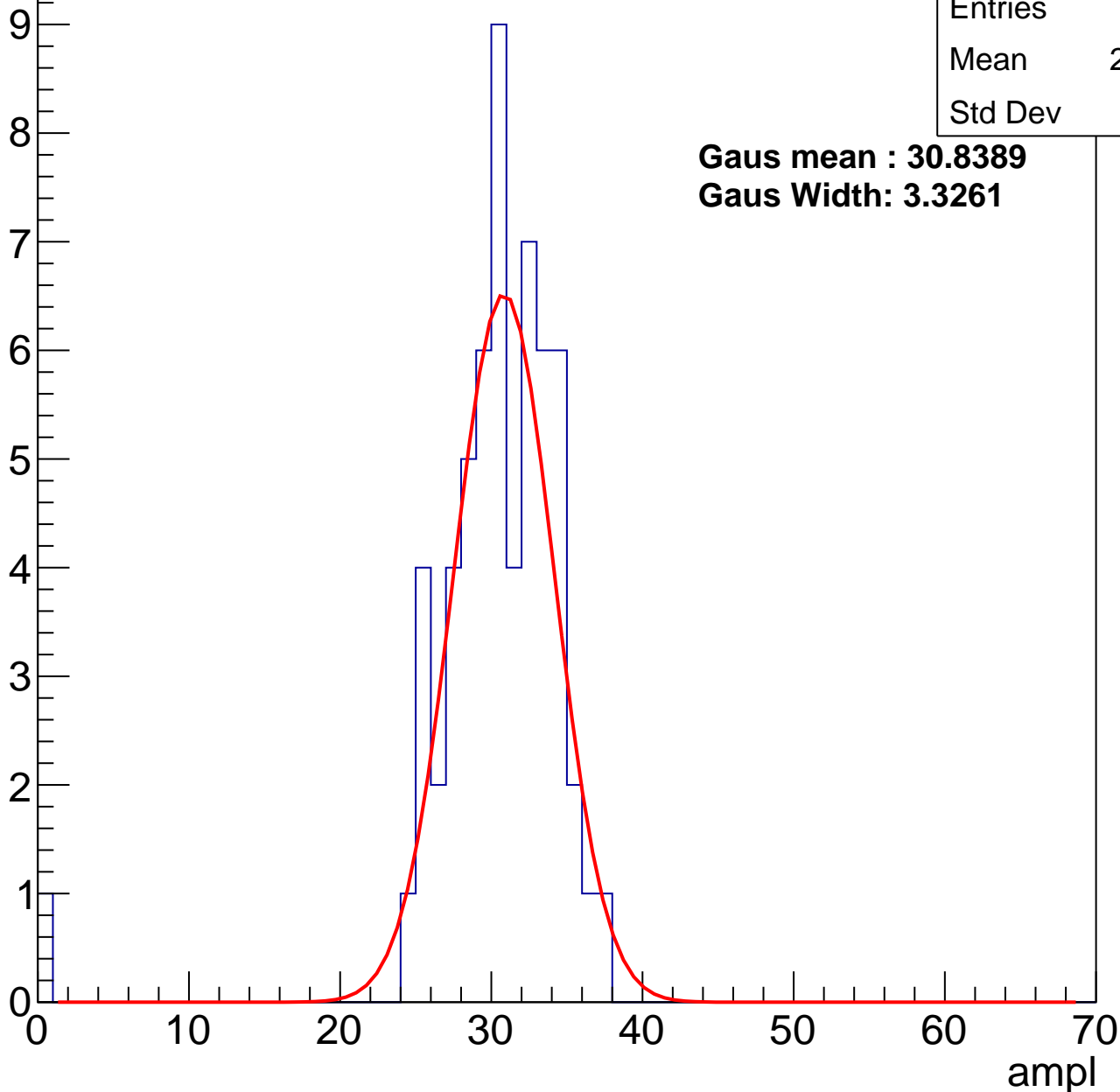
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.85
Std Dev	4.95

Gaus mean : 30.8389

Gaus Width: 3.3261



B1L101S, U21-ch97, adc1

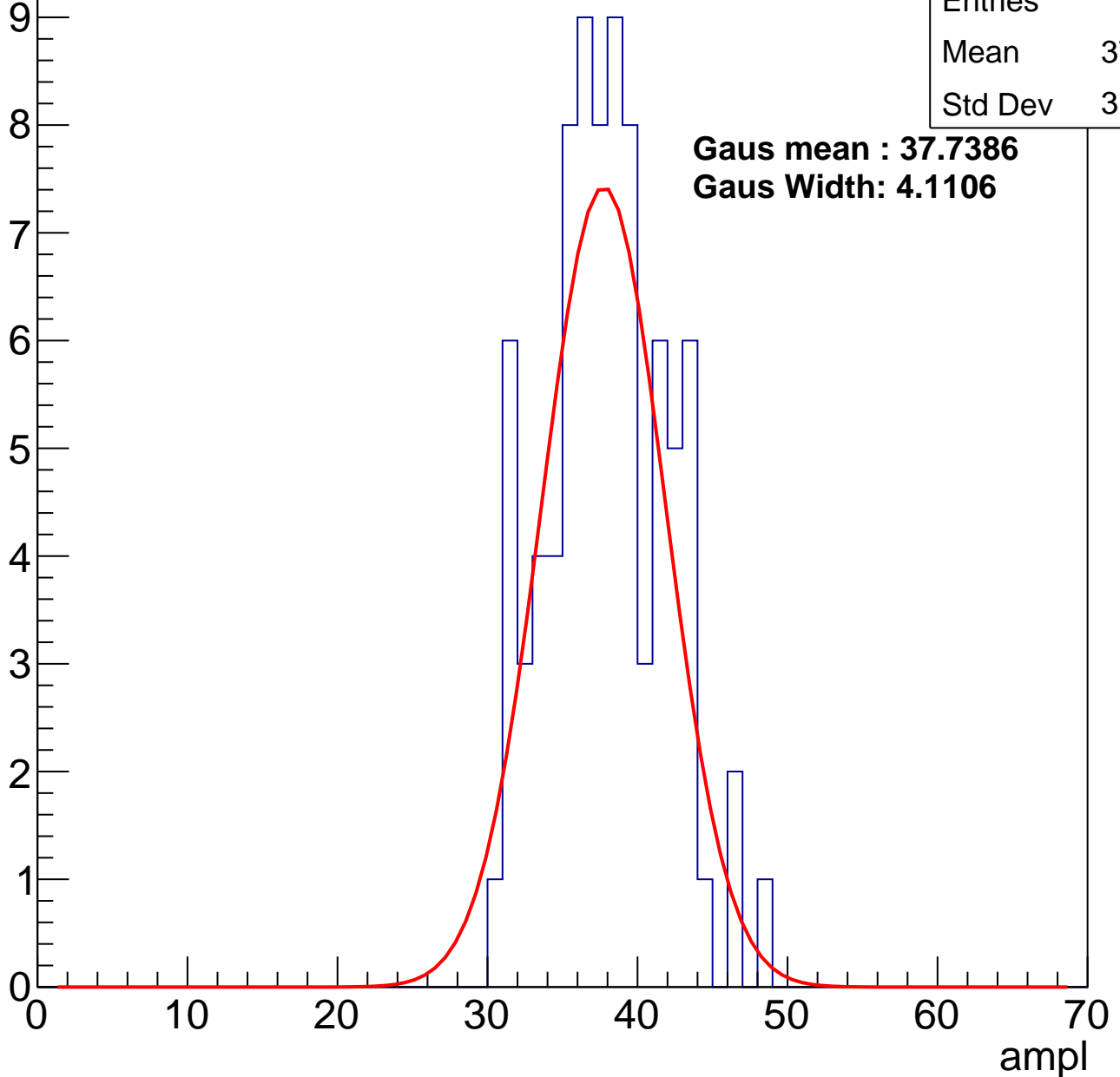
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	37.52
Std Dev	3.926

Gaus mean : 37.7386

Gaus Width: 4.1106



B1L101S, U21-ch97, adc2

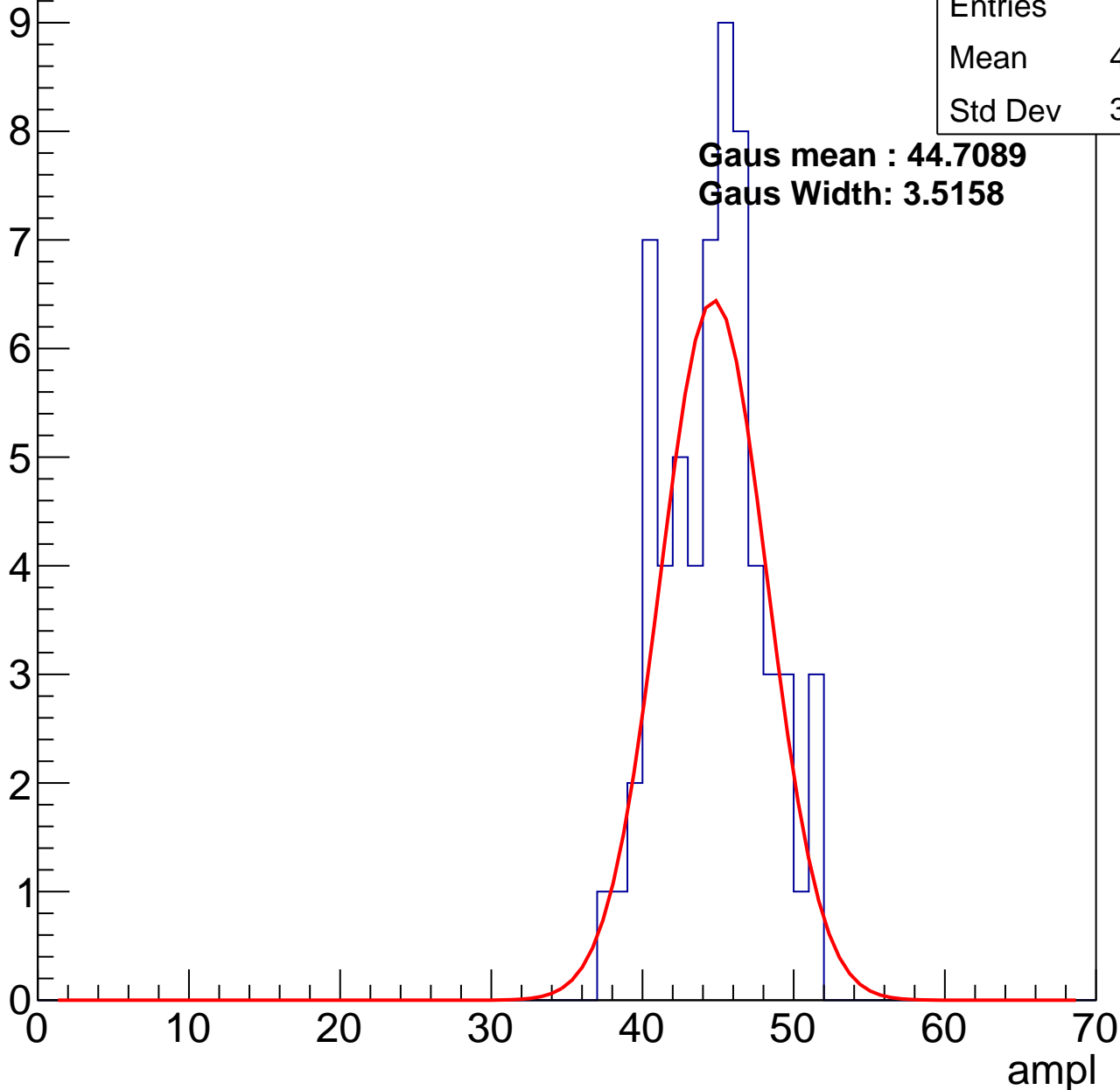
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	44.23
Std Dev	3.328

Gaus mean : 44.7089

Gaus Width: 3.5158

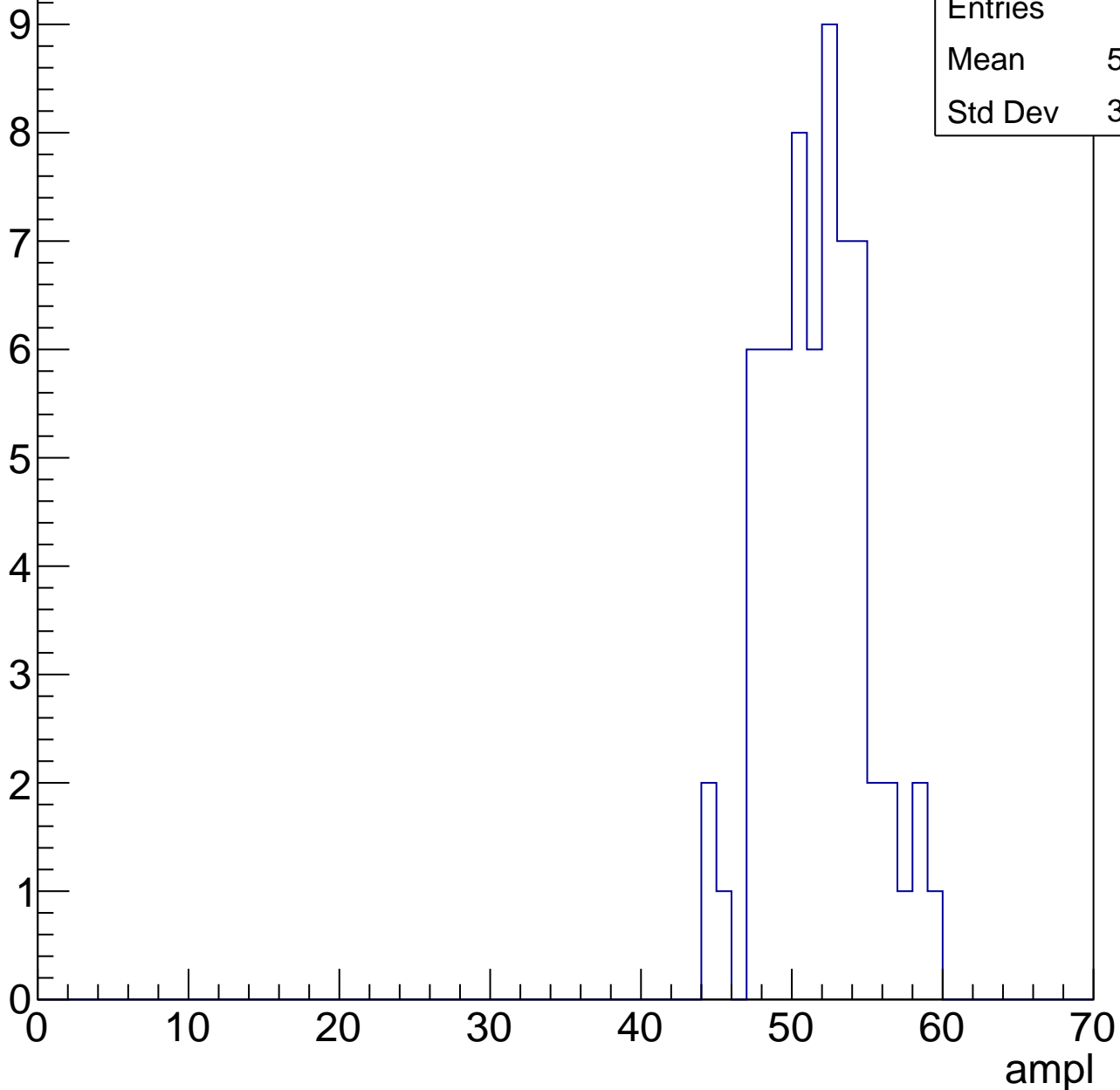


B1L101S, U21-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

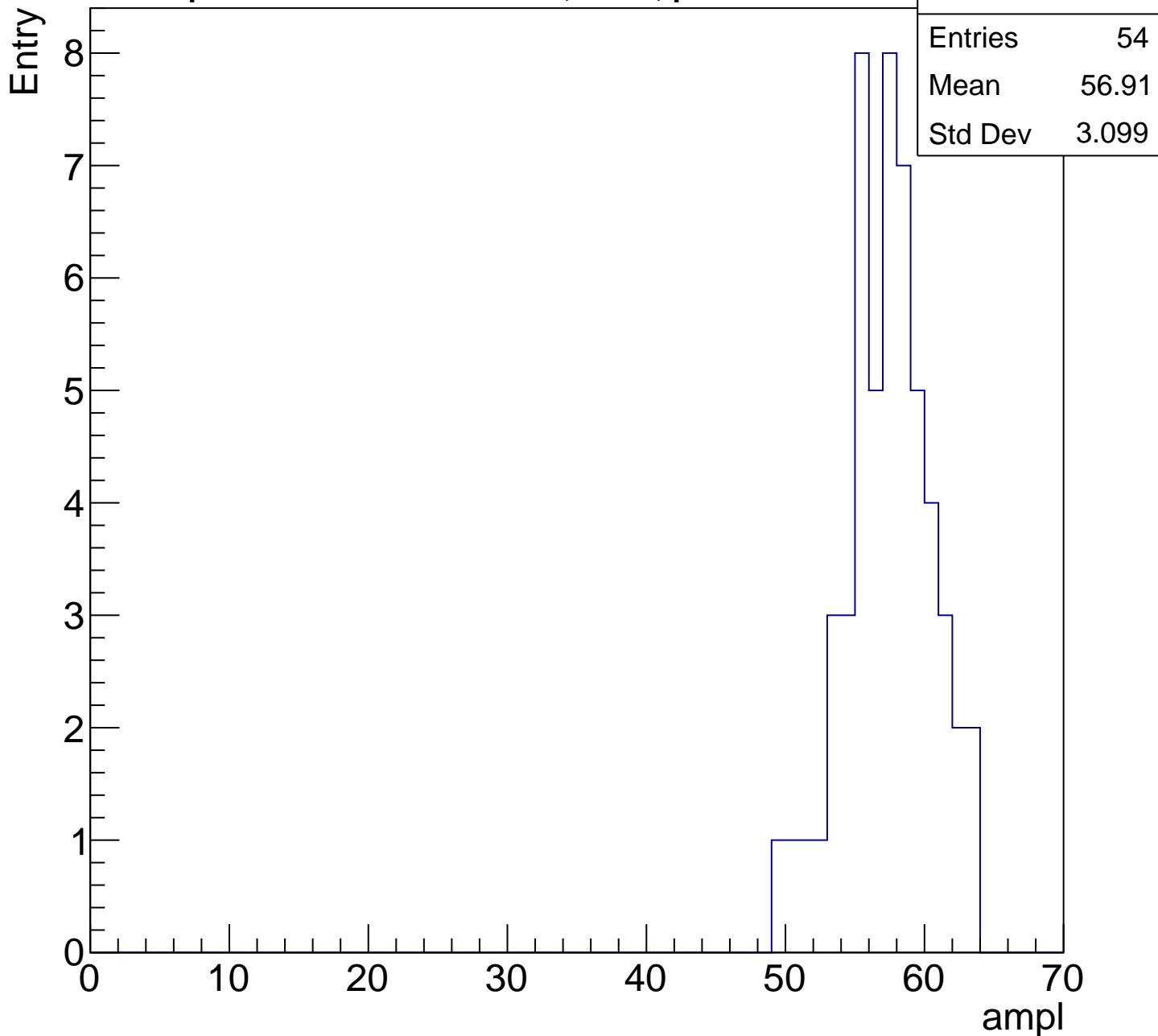
Entry

Entries	66
Mean	51.12
Std Dev	3.245



B1L101S, U21-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

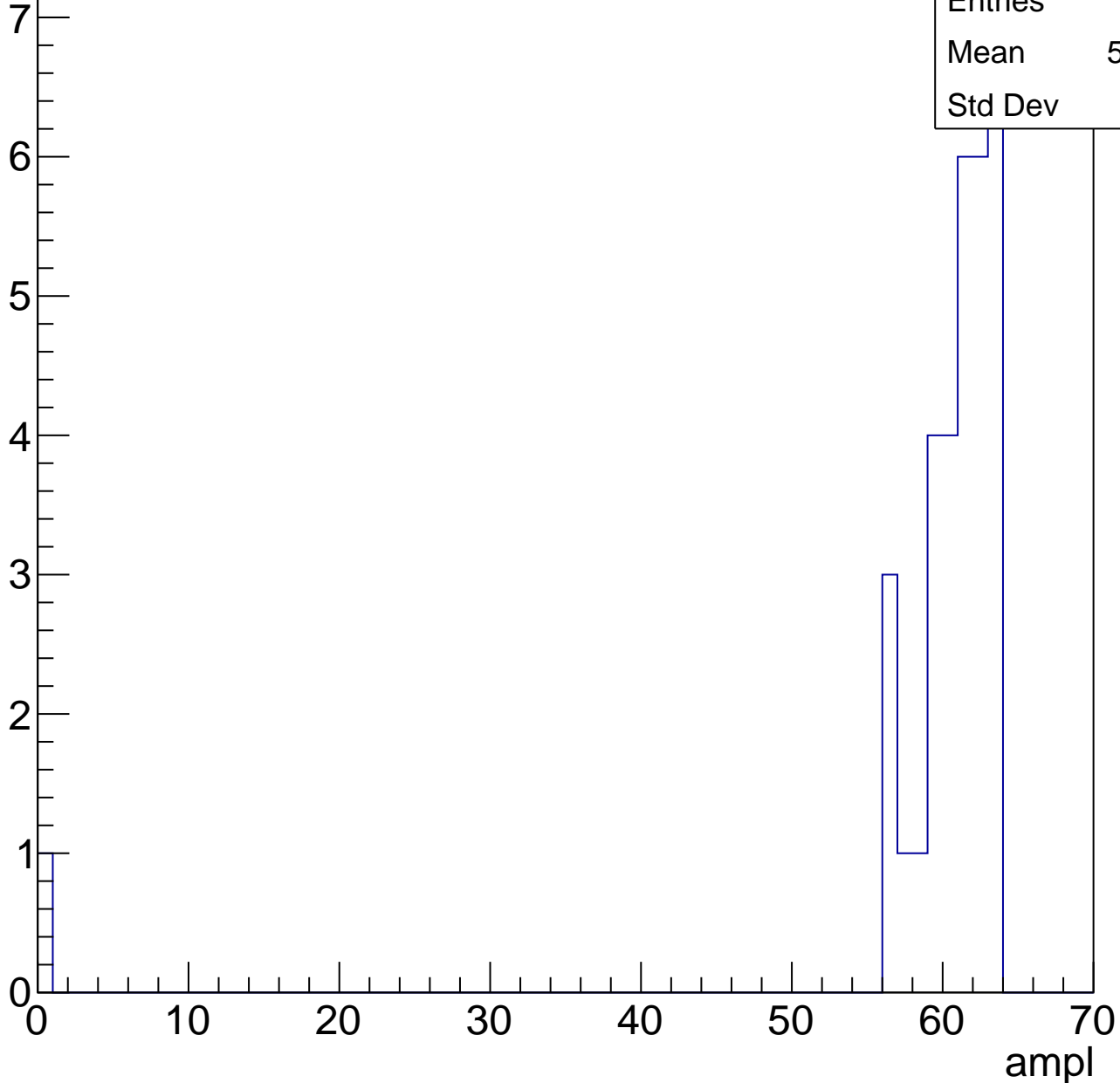


B1L101S, U21-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.73
Std Dev	10.6



B1L101S, U21-ch97, adc6

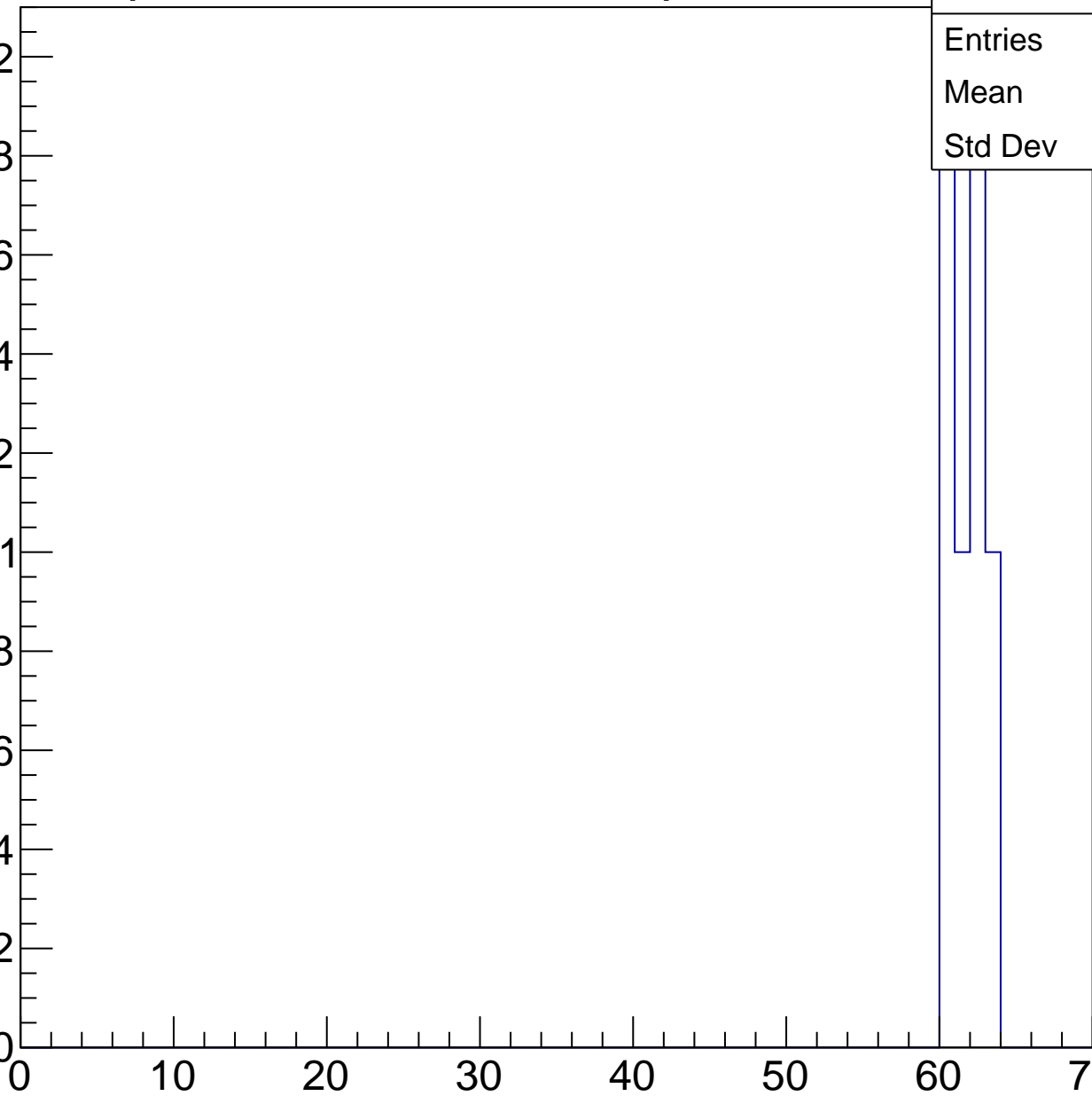
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.106

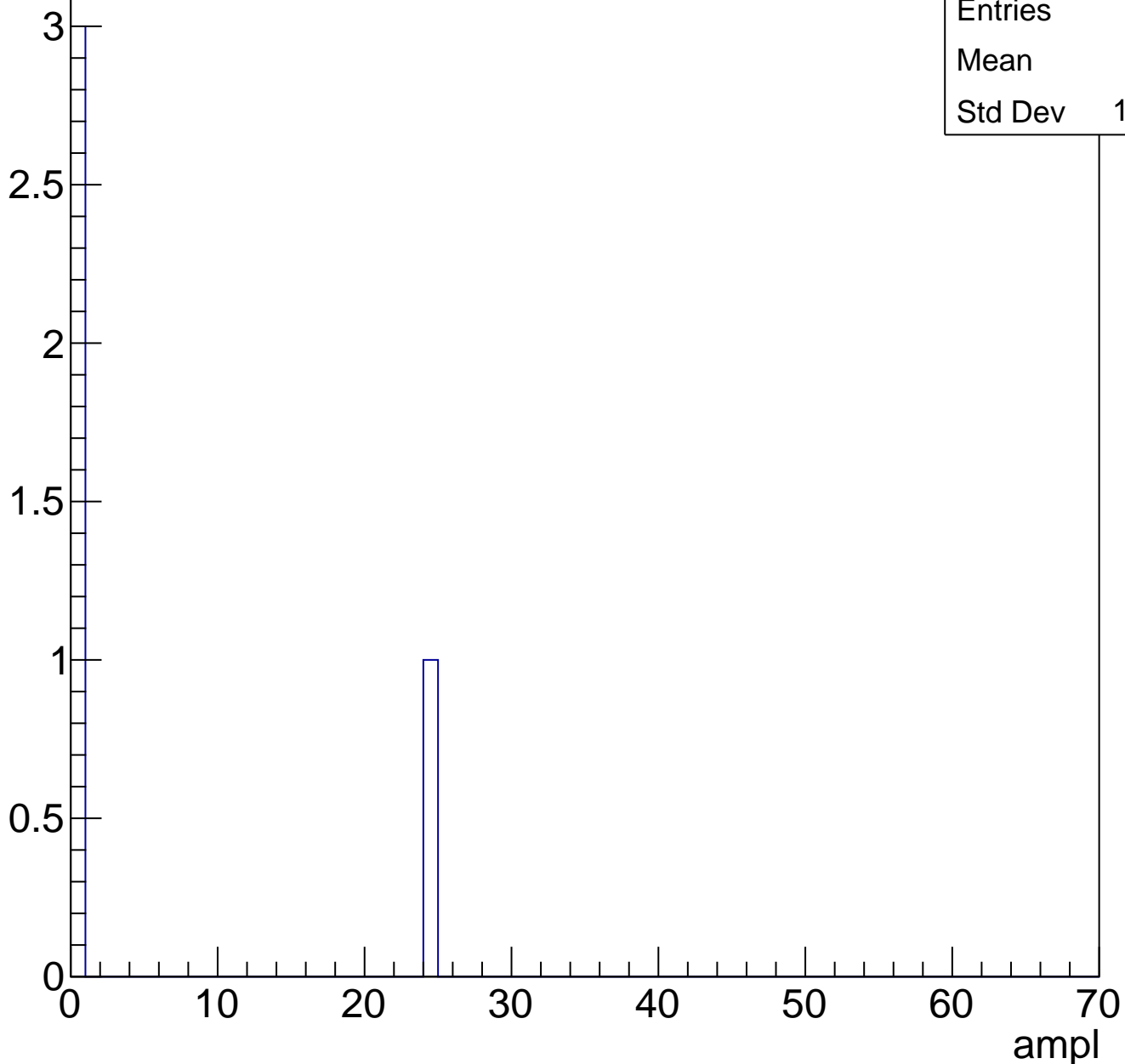
ampl



B1L101S, U21-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch98, adc0

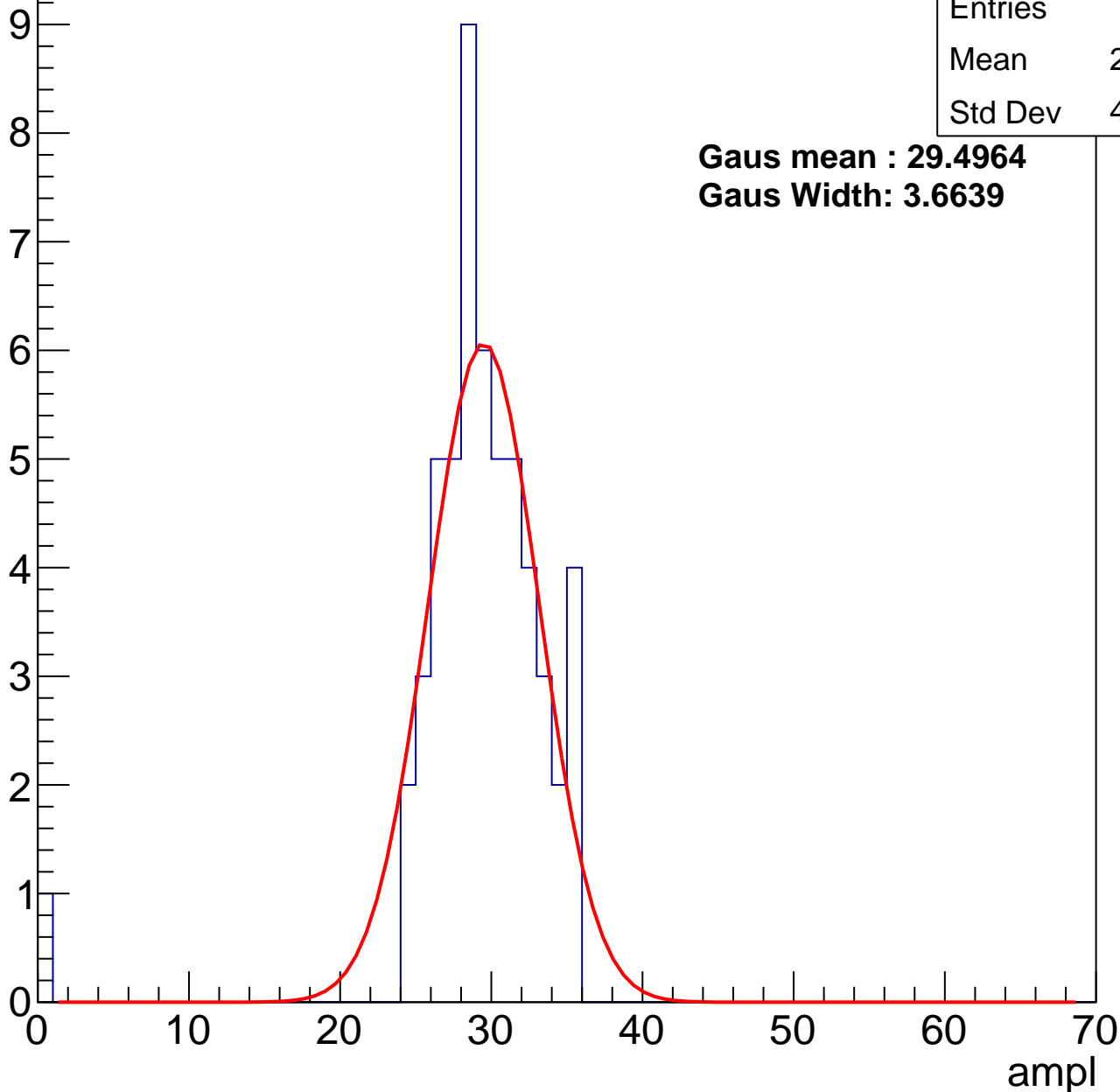
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	28.78
Std Dev	4.932

Gaus mean : 29.4964

Gaus Width: 3.6639



B1L101S, U21-ch98, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	35.44
Std Dev	3.741

Gaus mean : 35.7531

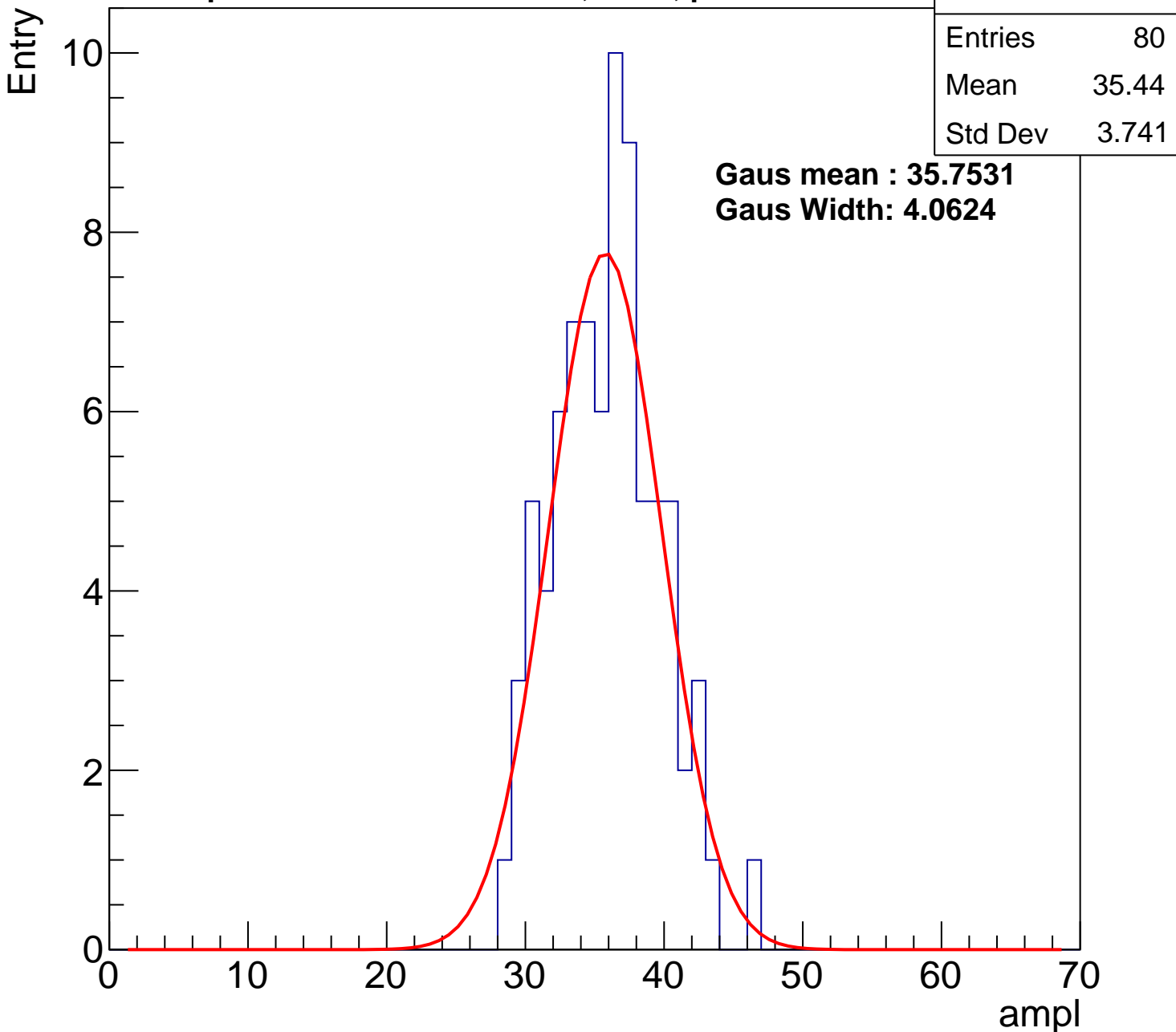
Gaus Width: 4.0624

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch98, adc2

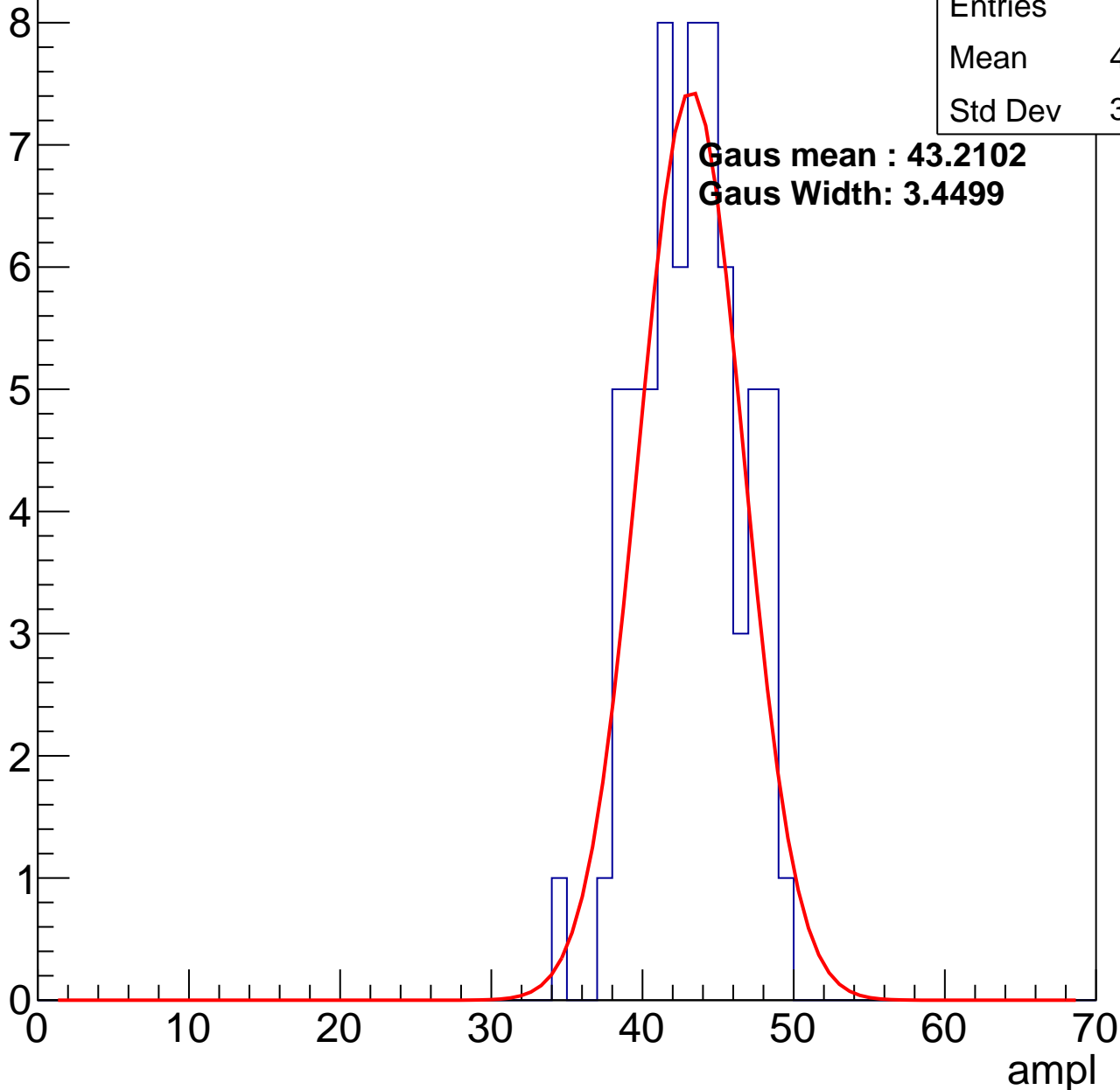
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	42.75
Std Dev	3.234

Gaus mean : 43.2102

Gaus Width: 3.4499

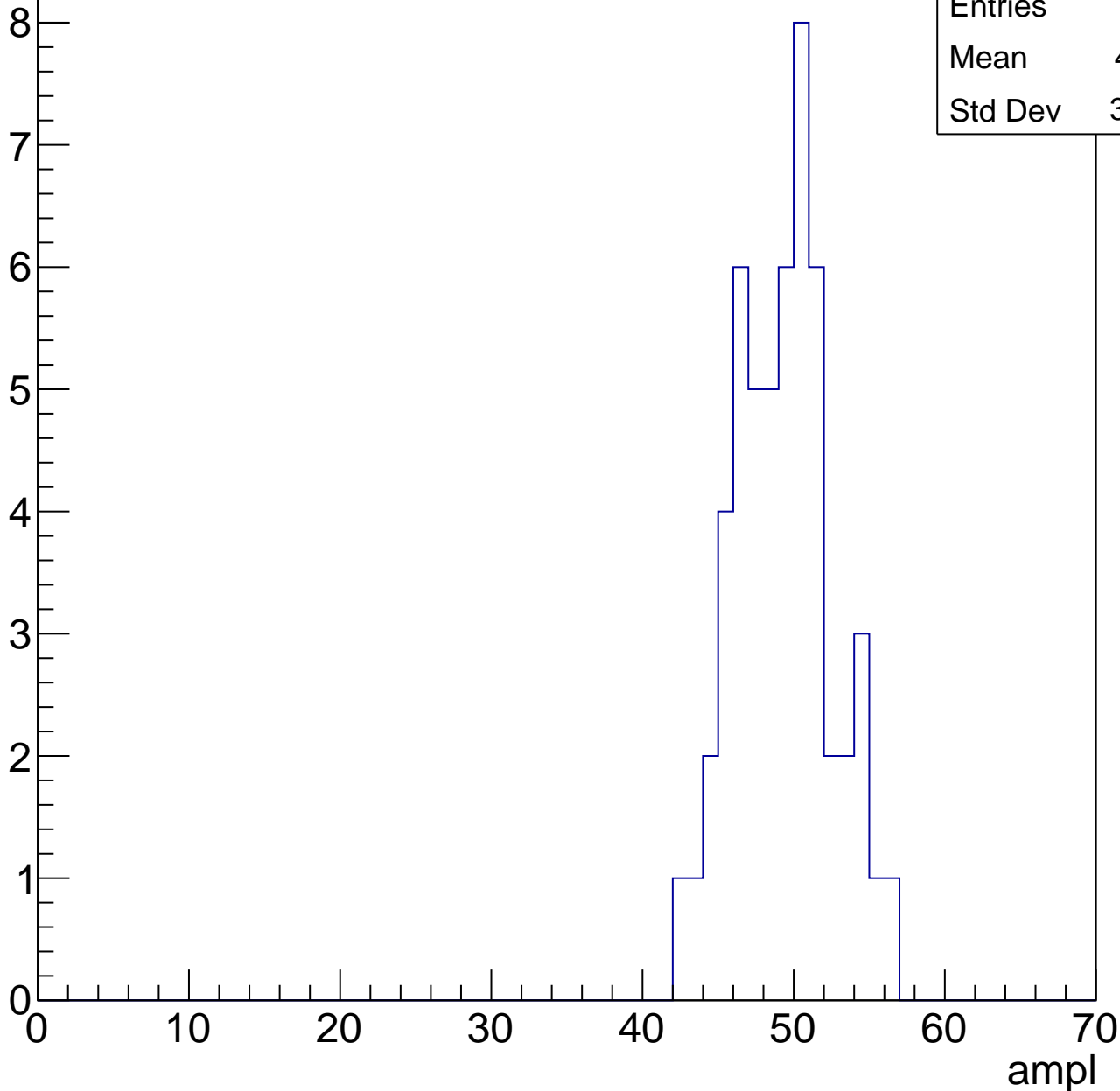


B1L101S, U21-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	48.81
Std Dev	3.127

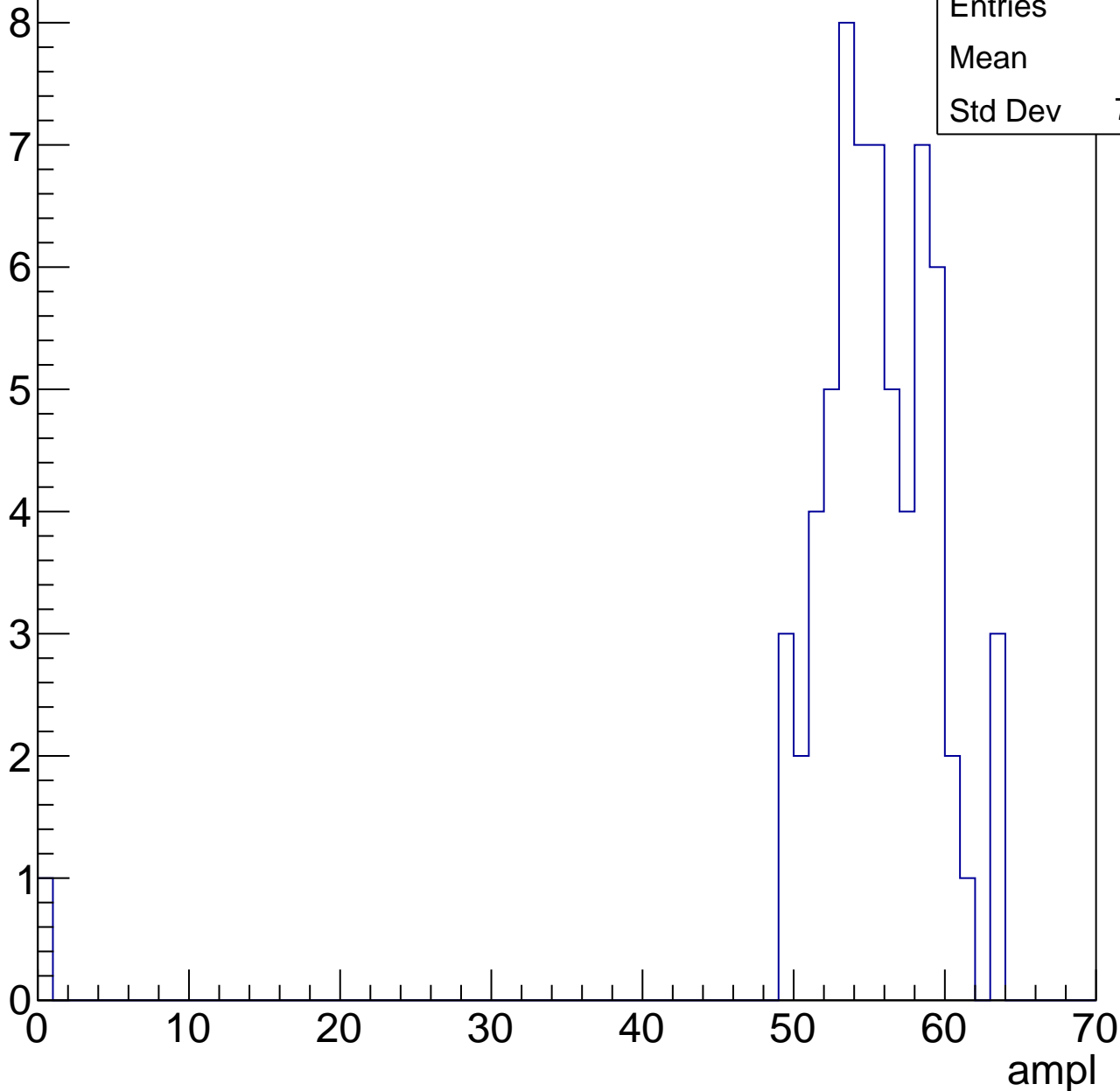


B1L101S, U21-ch98, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	54.4
Std Dev	7.611

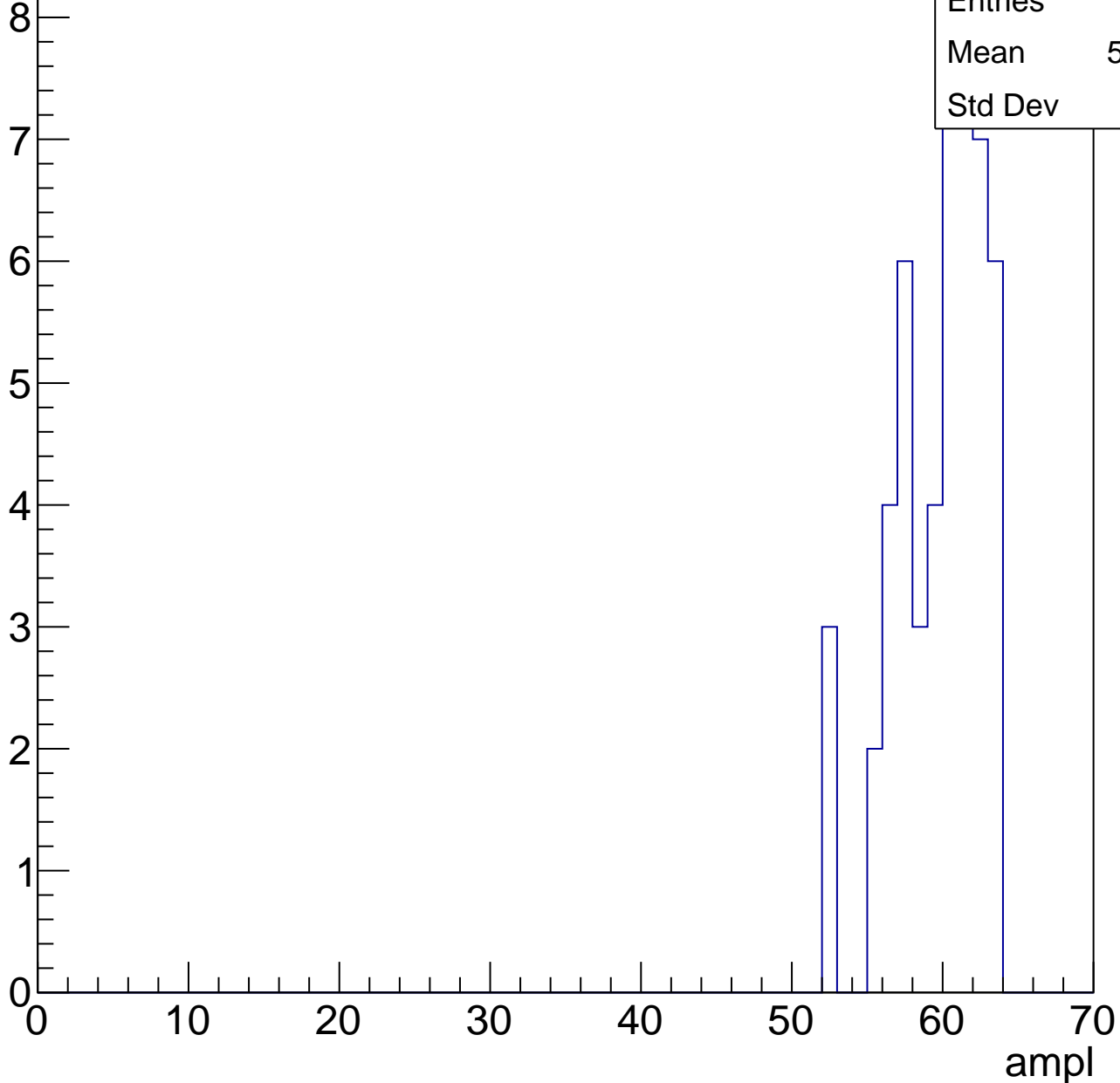


B1L101S, U21-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	59.25
Std Dev	2.93



B1L101S, U21-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	62
Std Dev	0.6325

B1L101S, U21-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch99, adc0

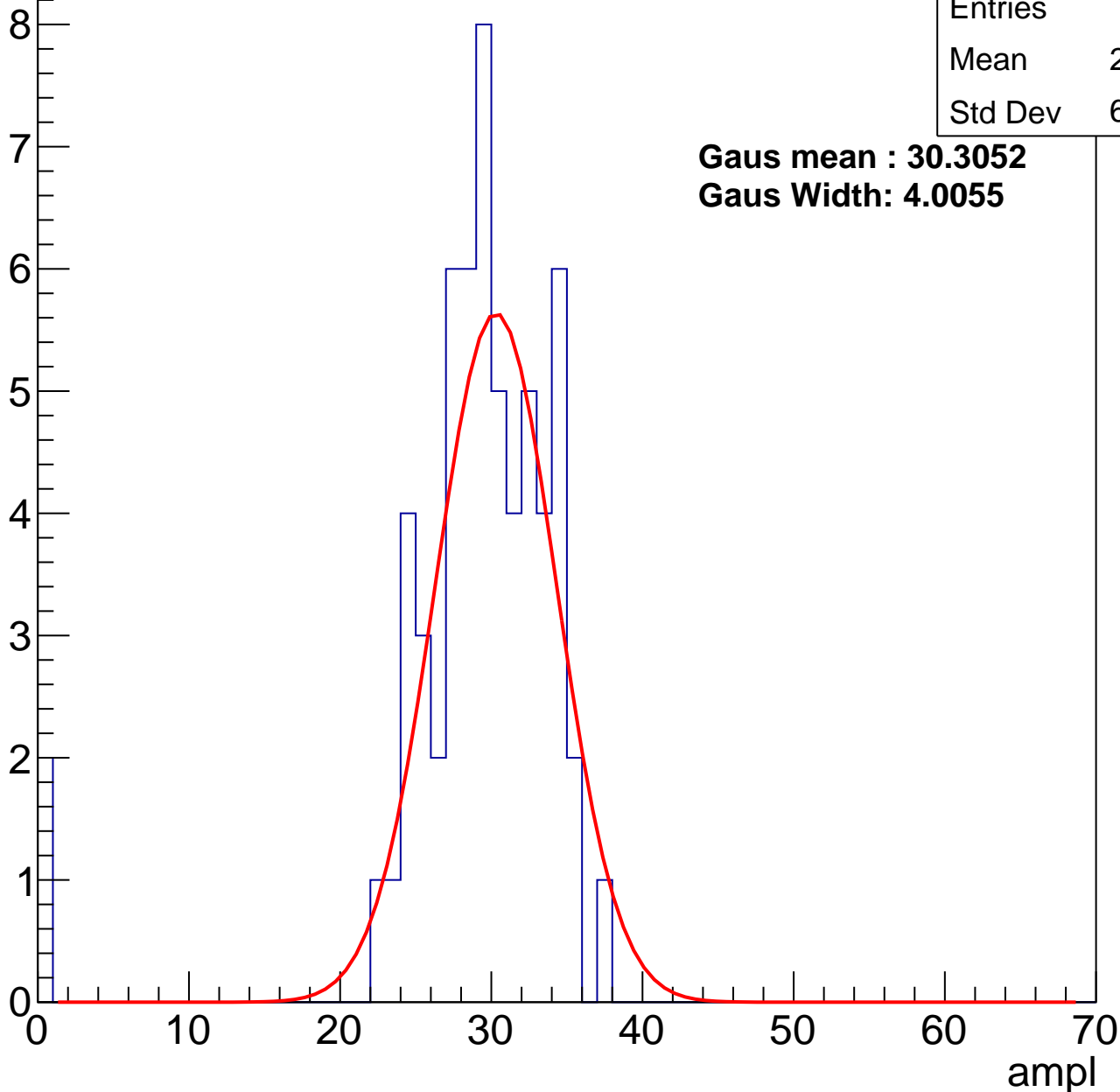
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.45
Std Dev	6.273

Gaus mean : 30.3052

Gaus Width: 4.0055



B1L101S, U21-ch99, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	85
Mean	36.42
Std Dev	5.512

Gaus mean : 37.7272

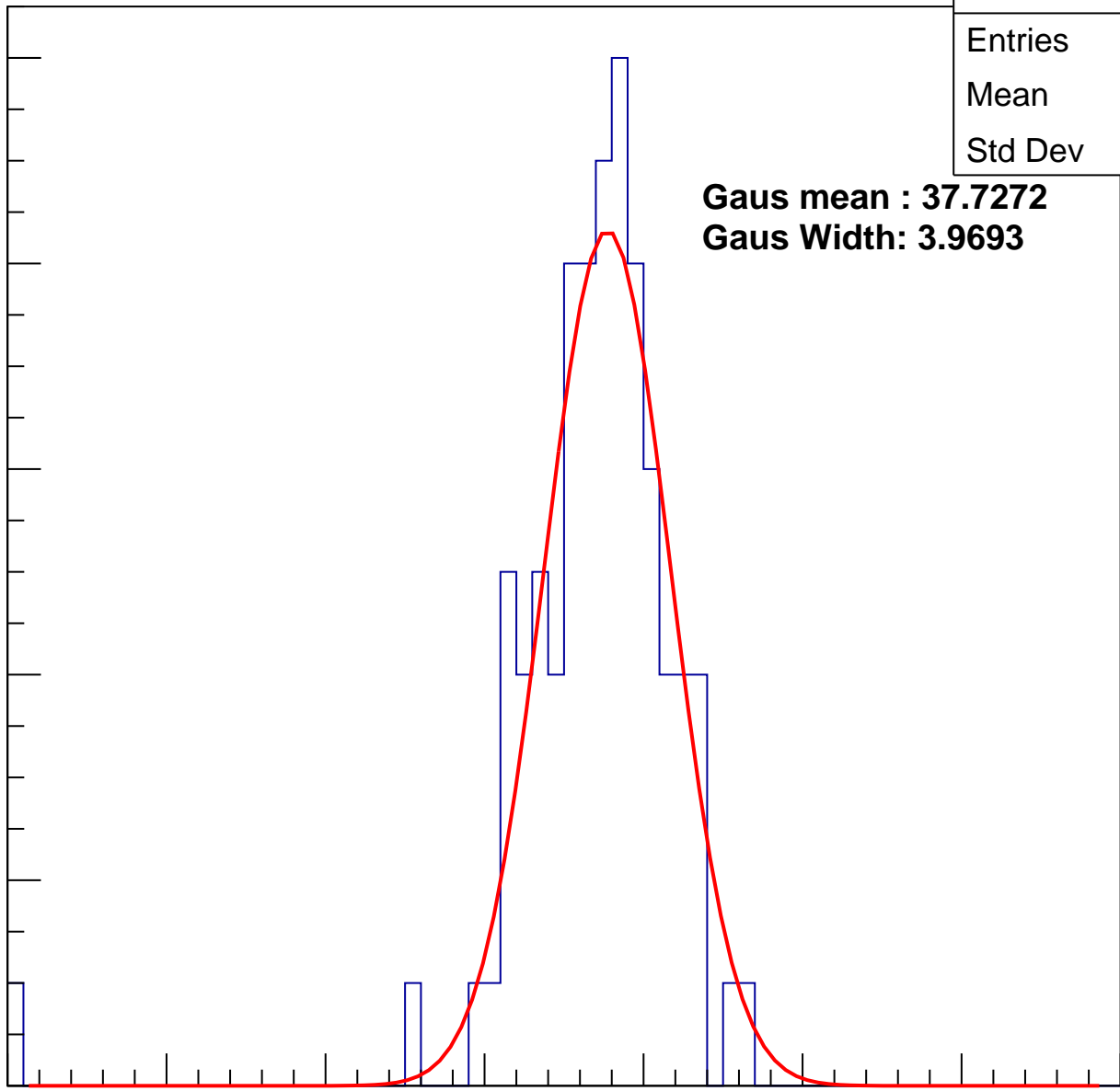
Gaus Width: 3.9693

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

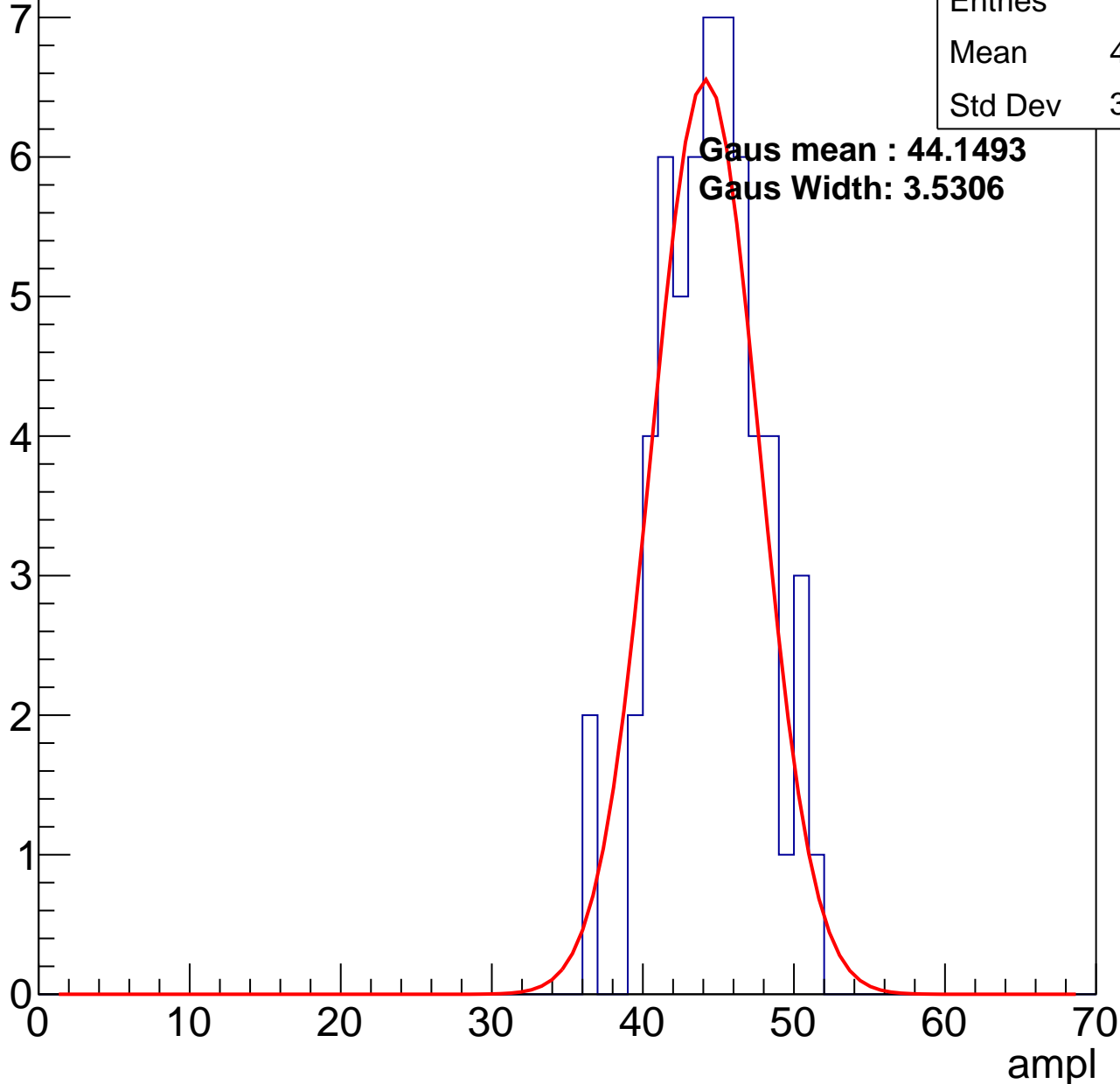


B1L101S, U21-ch99, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.02
Std Dev	3.309

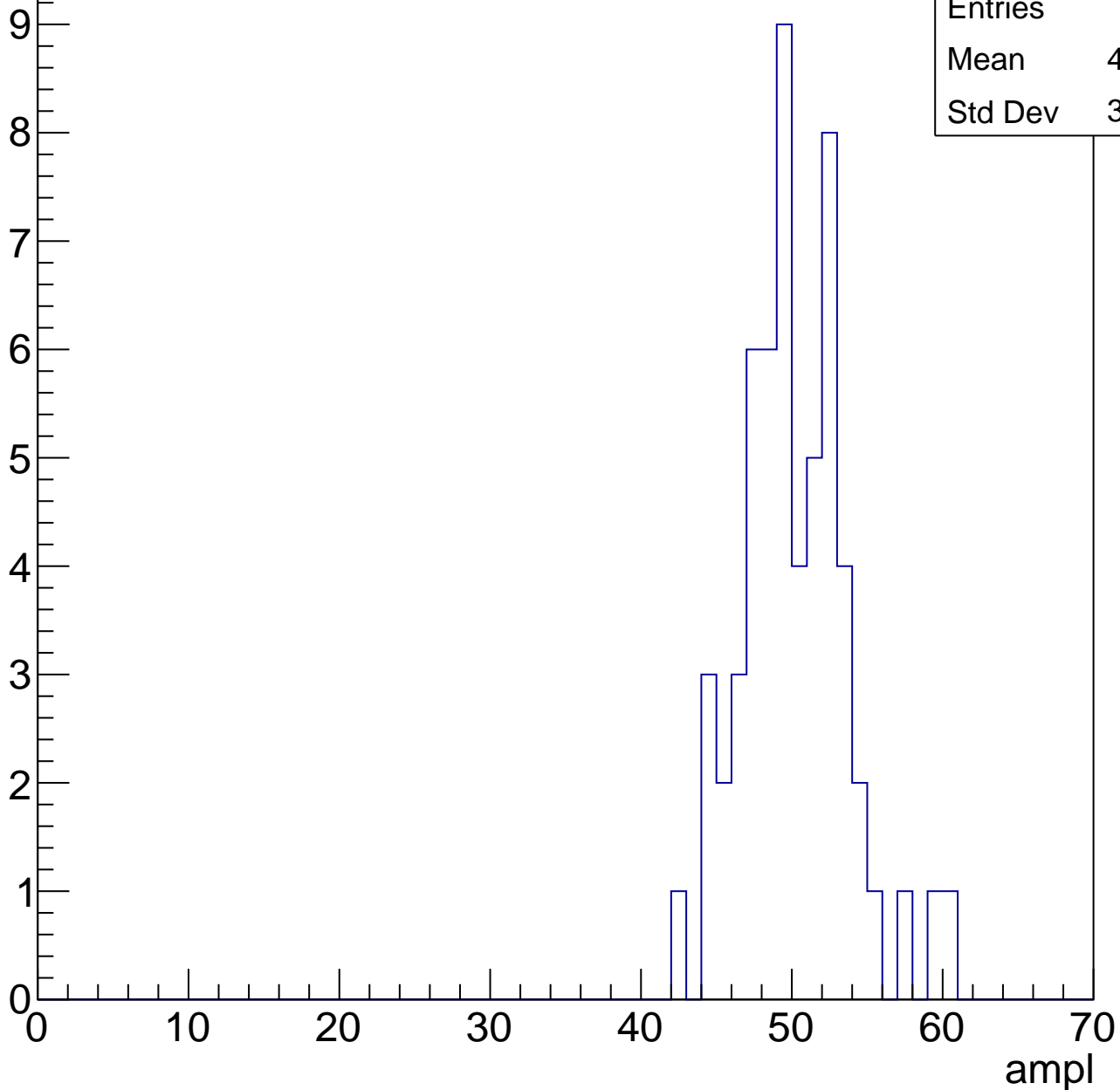


B1L101S, U21-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

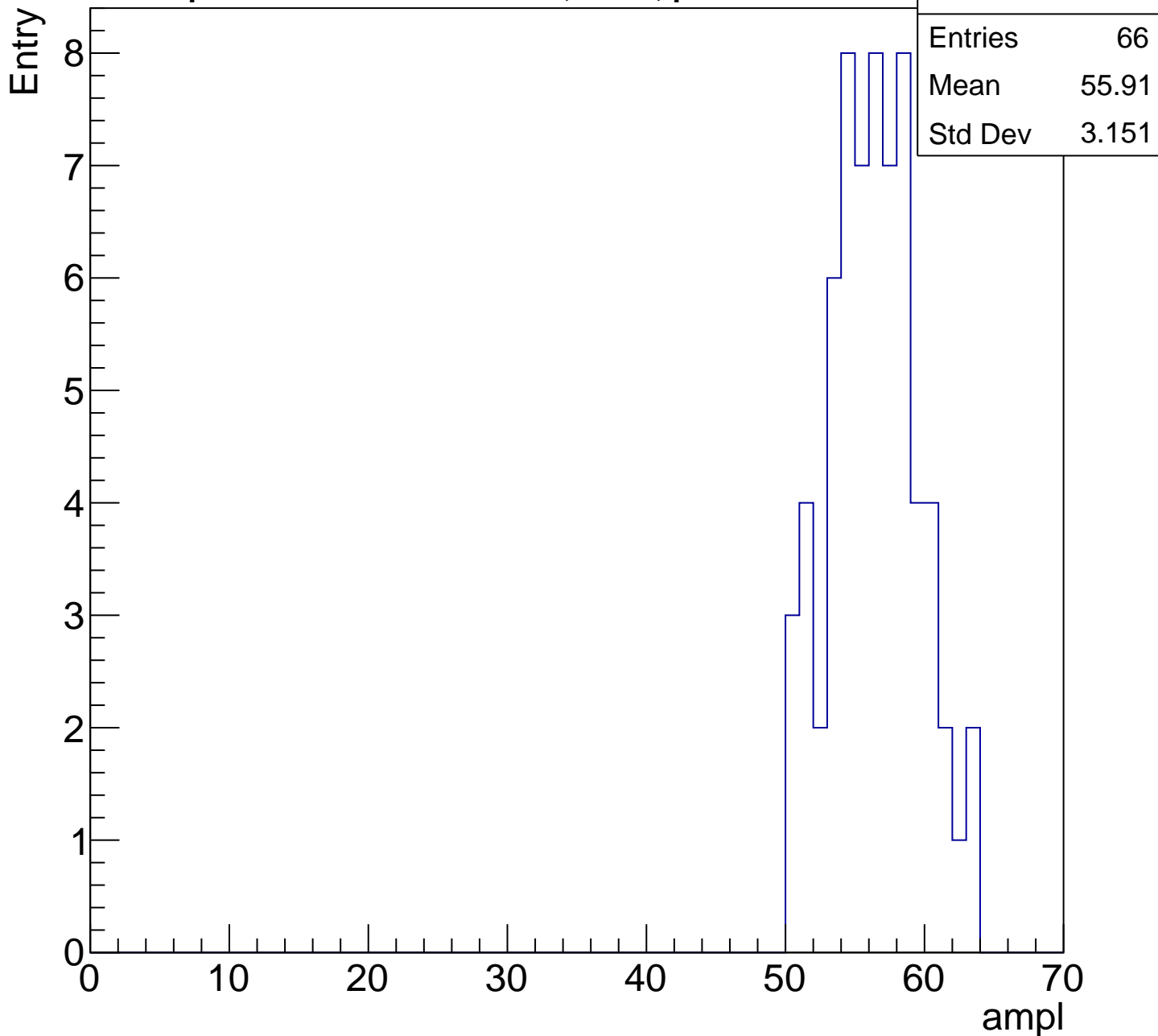
Entry

Entries	57
Mean	49.74
Std Dev	3.537



B1L101S, U21-ch99, adc4

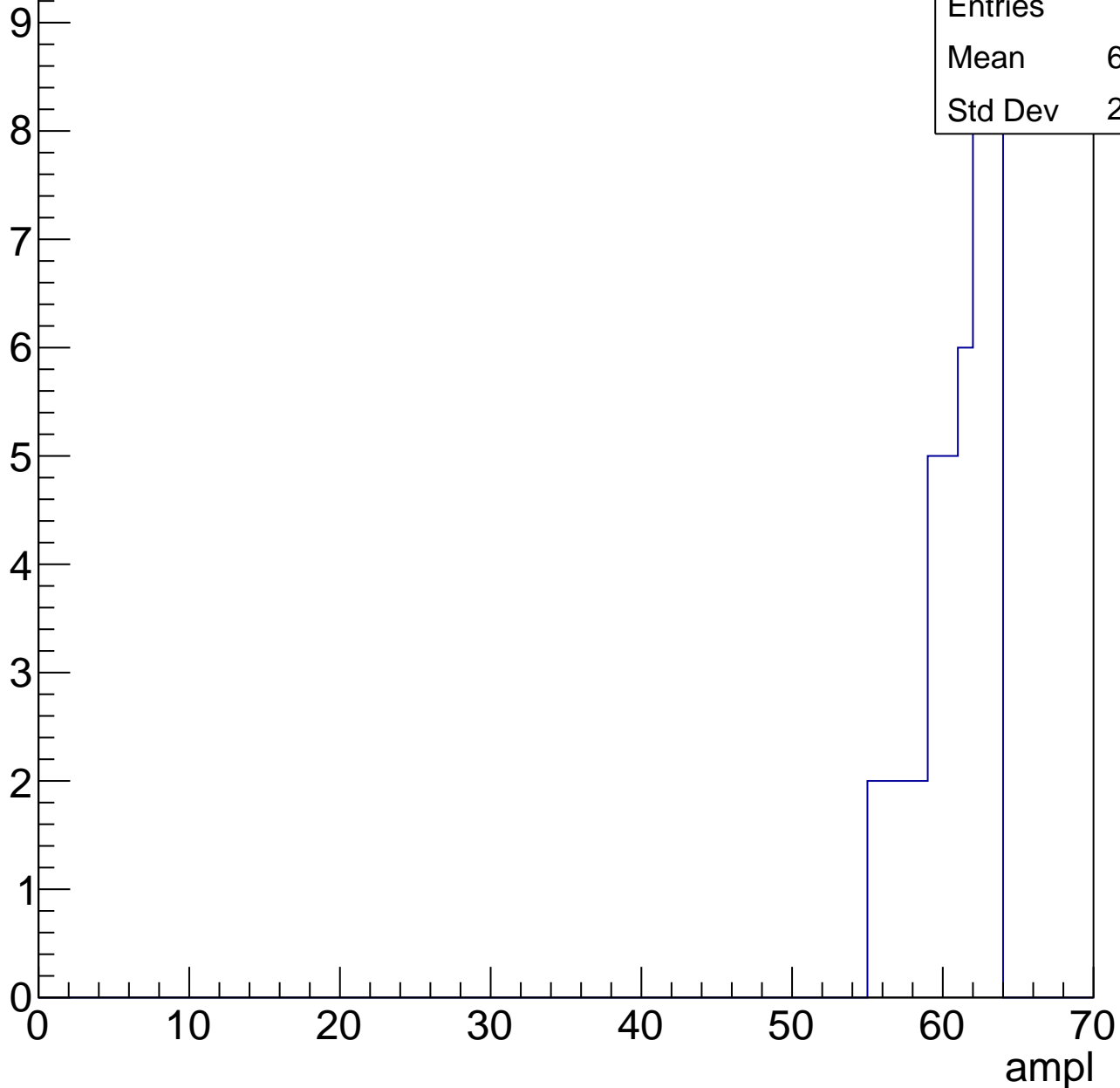
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U21-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

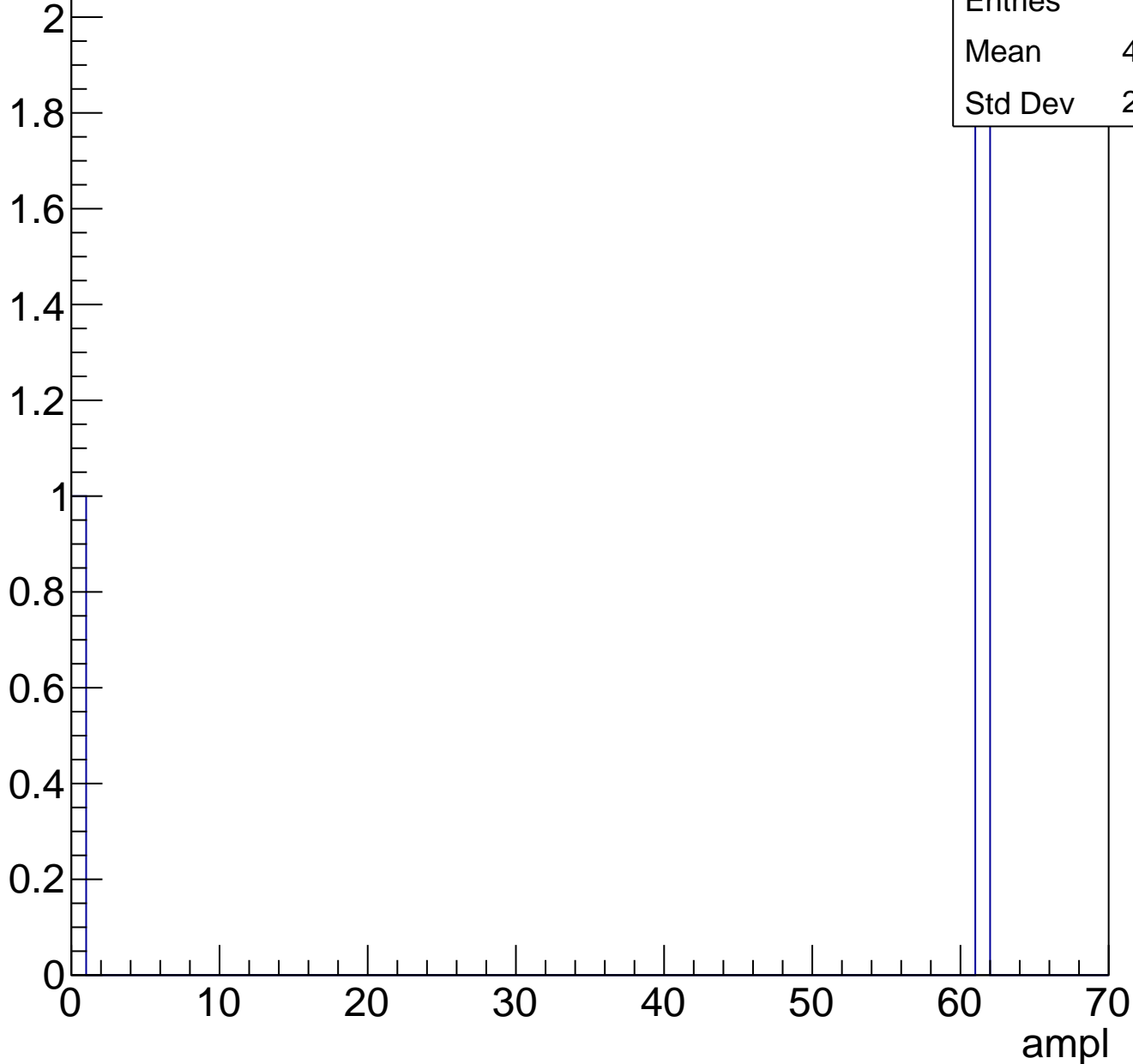
Entry



B1L101S, U21-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch100, adc0

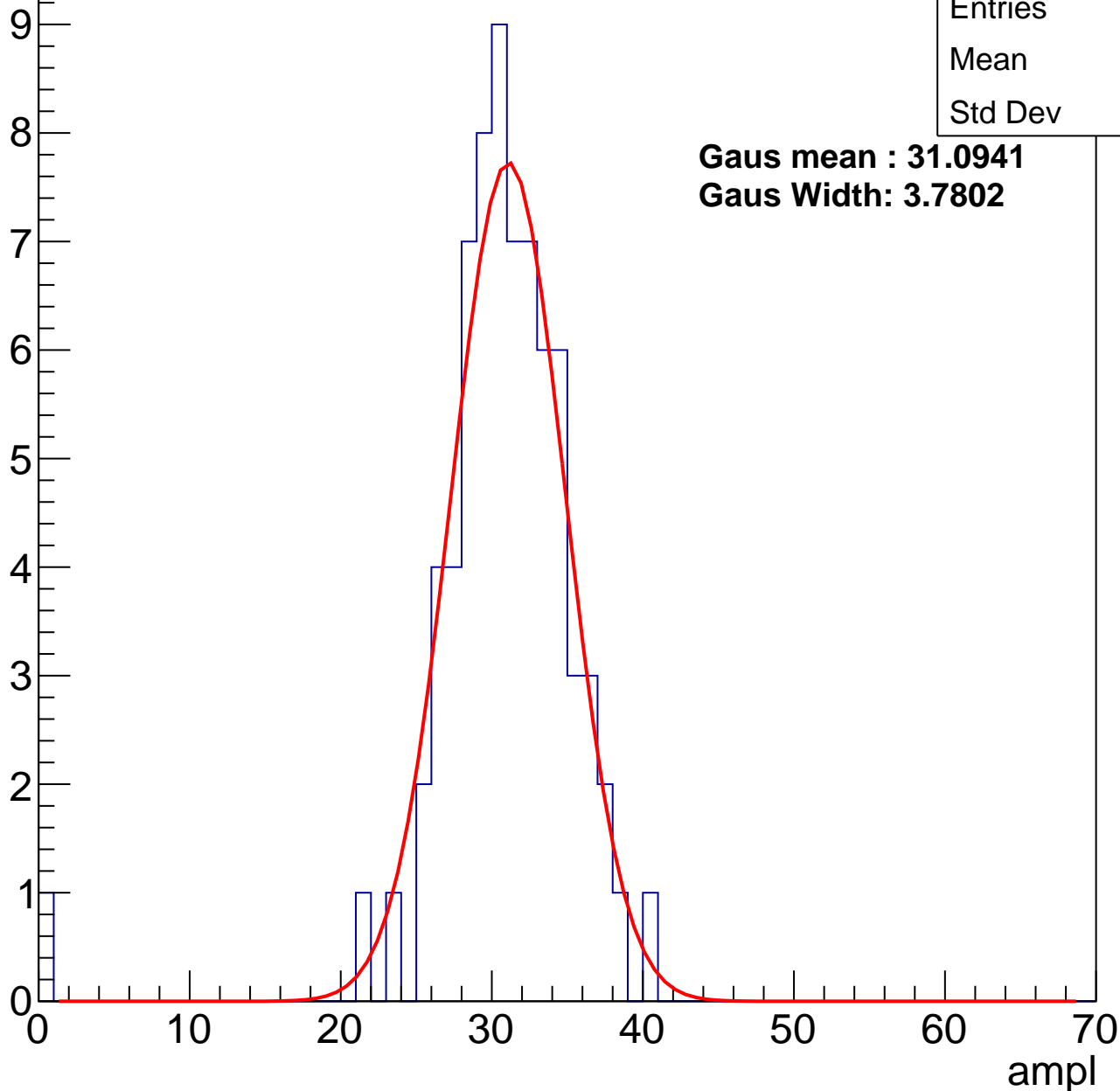
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	30.3
Std Dev	5.02

Gaus mean : 31.0941

Gaus Width: 3.7802



B1L101S, U21-ch100, adc1

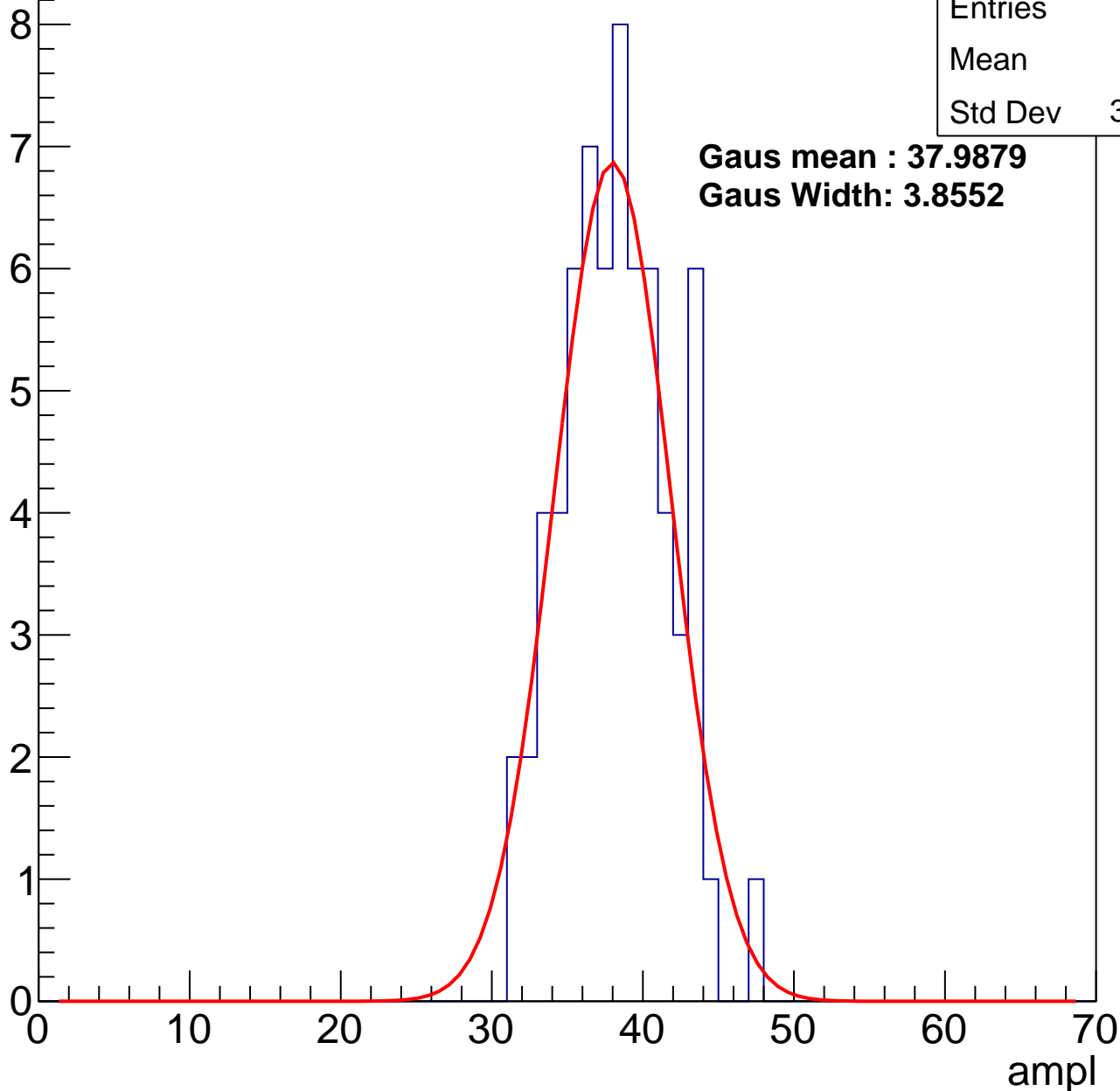
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	37.8
Std Dev	3.483

Gaus mean : 37.9879

Gaus Width: 3.8552



B1L101S, U21-ch100, adc2

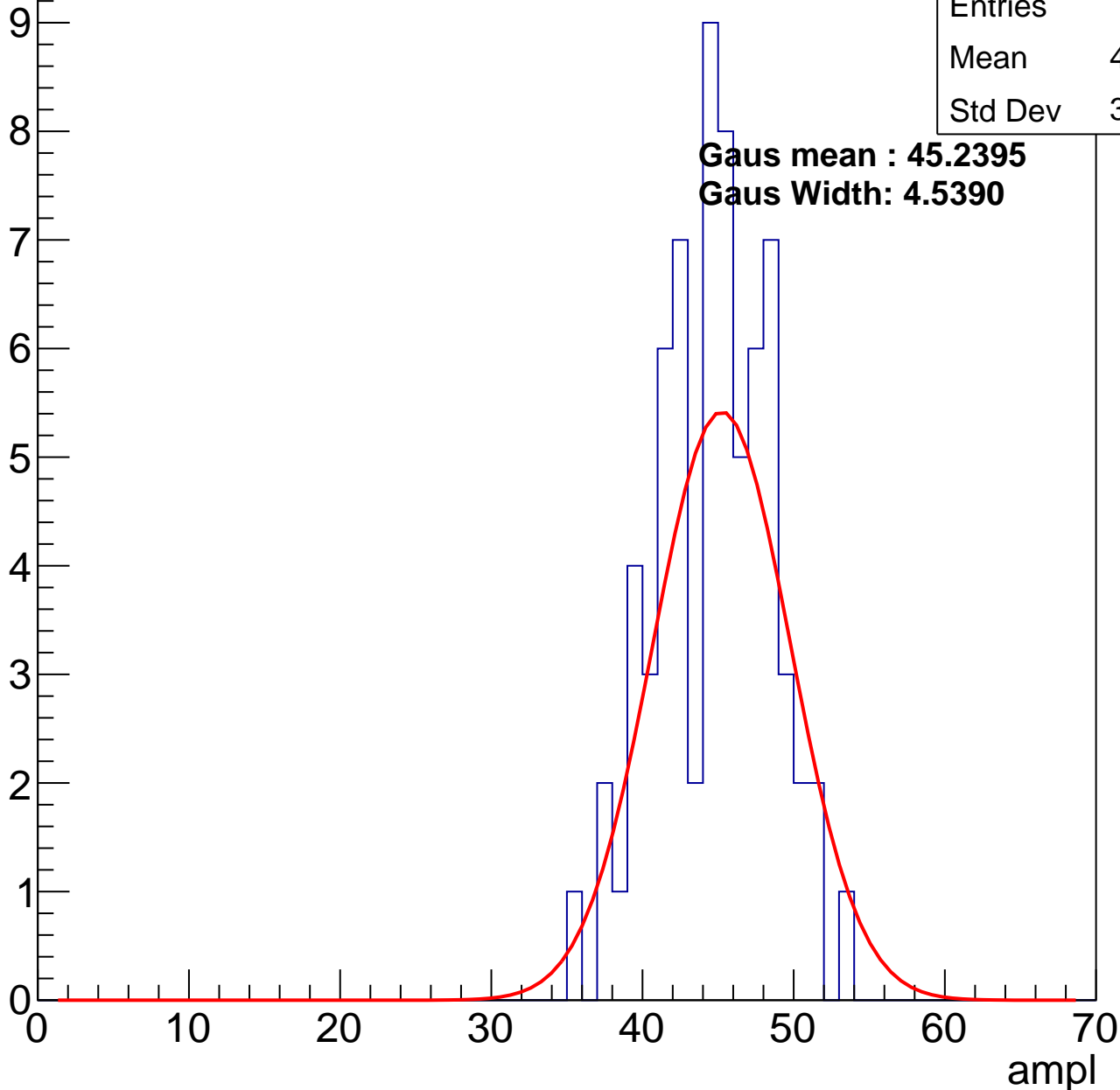
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	44.28
Std Dev	3.733

Gaus mean : 45.2395

Gaus Width: 4.5390

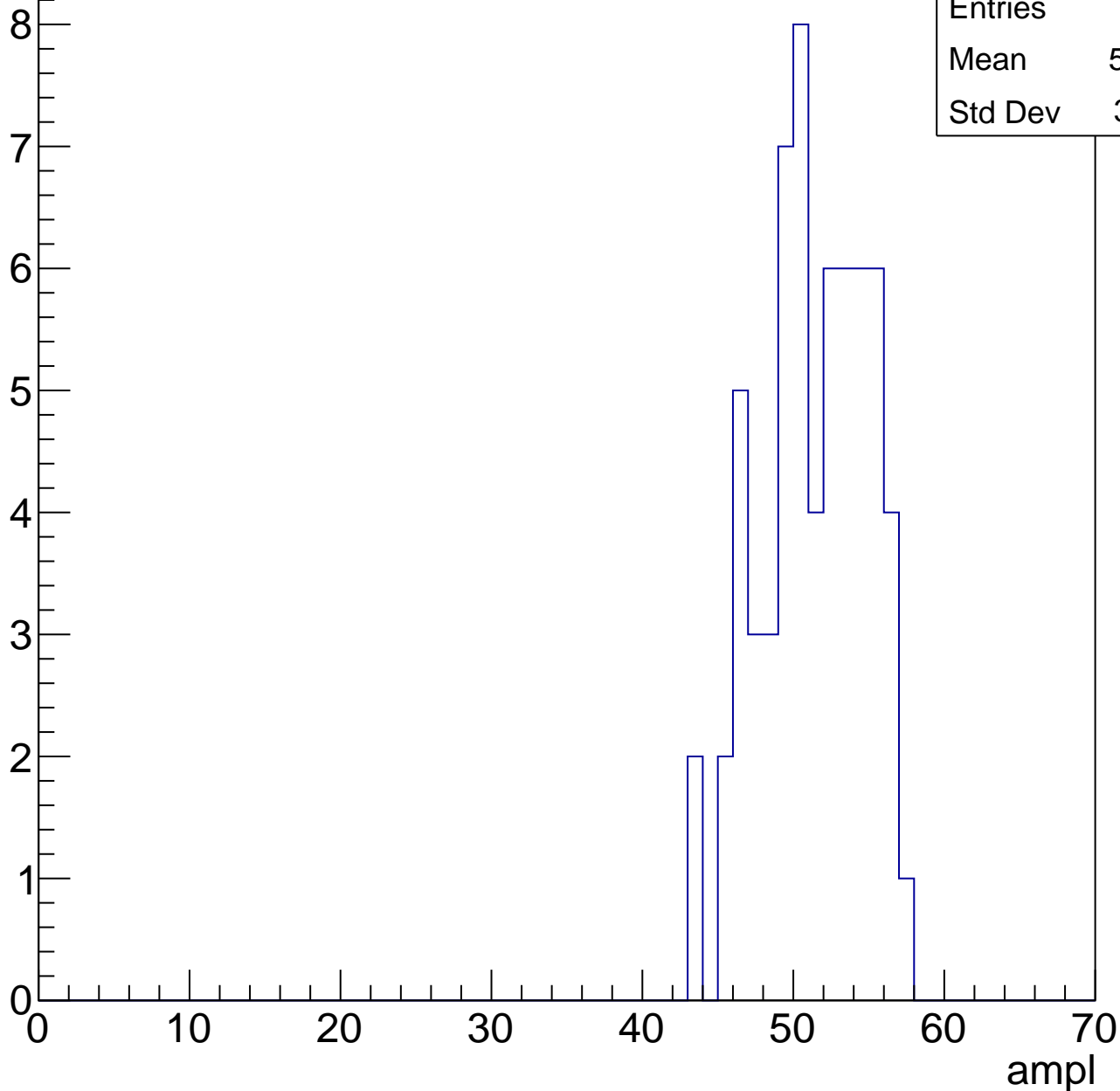


B1L101S, U21-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	50.84
Std Dev	3.451

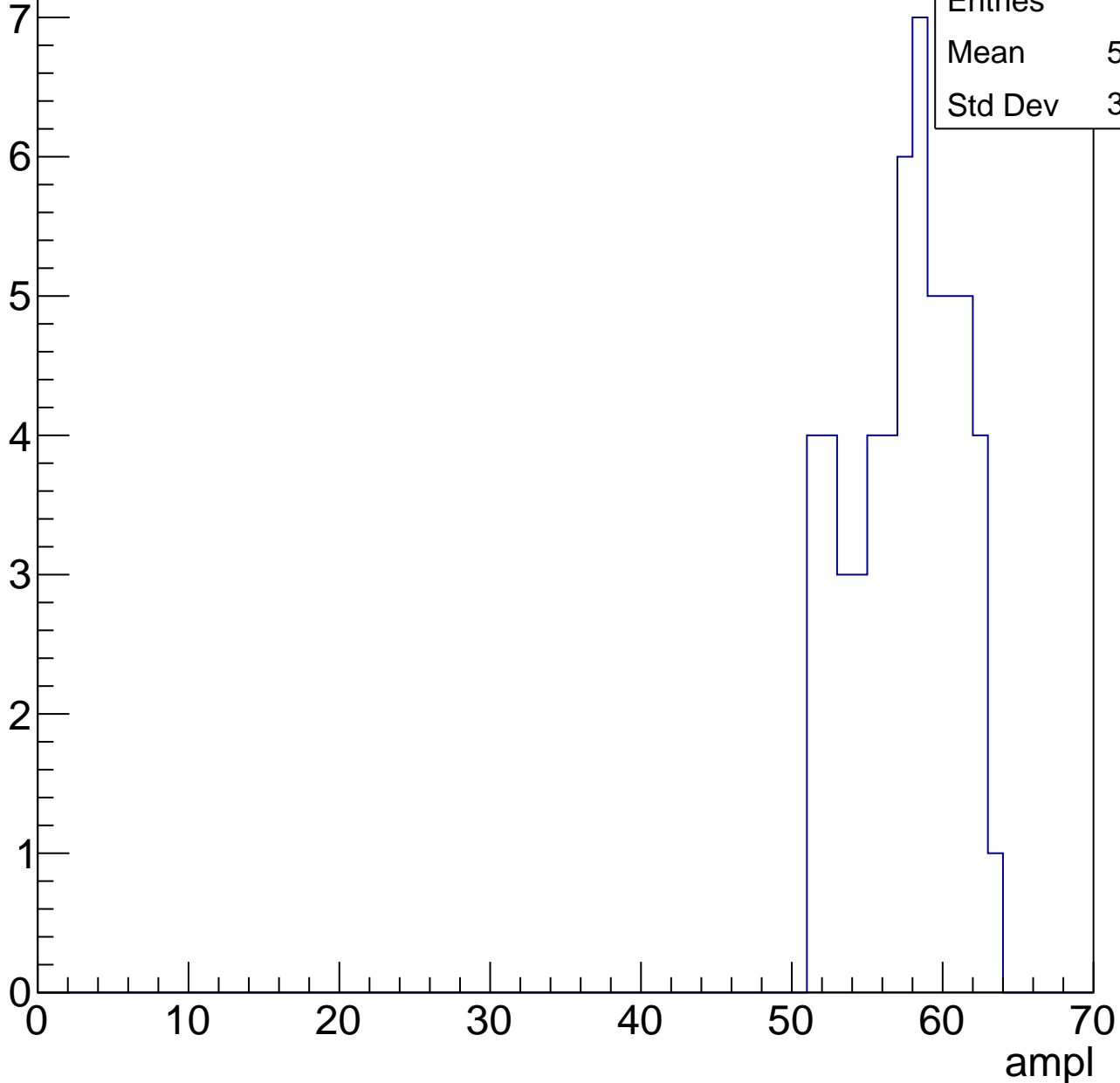


B1L101S, U21-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	57.02
Std Dev	3.376

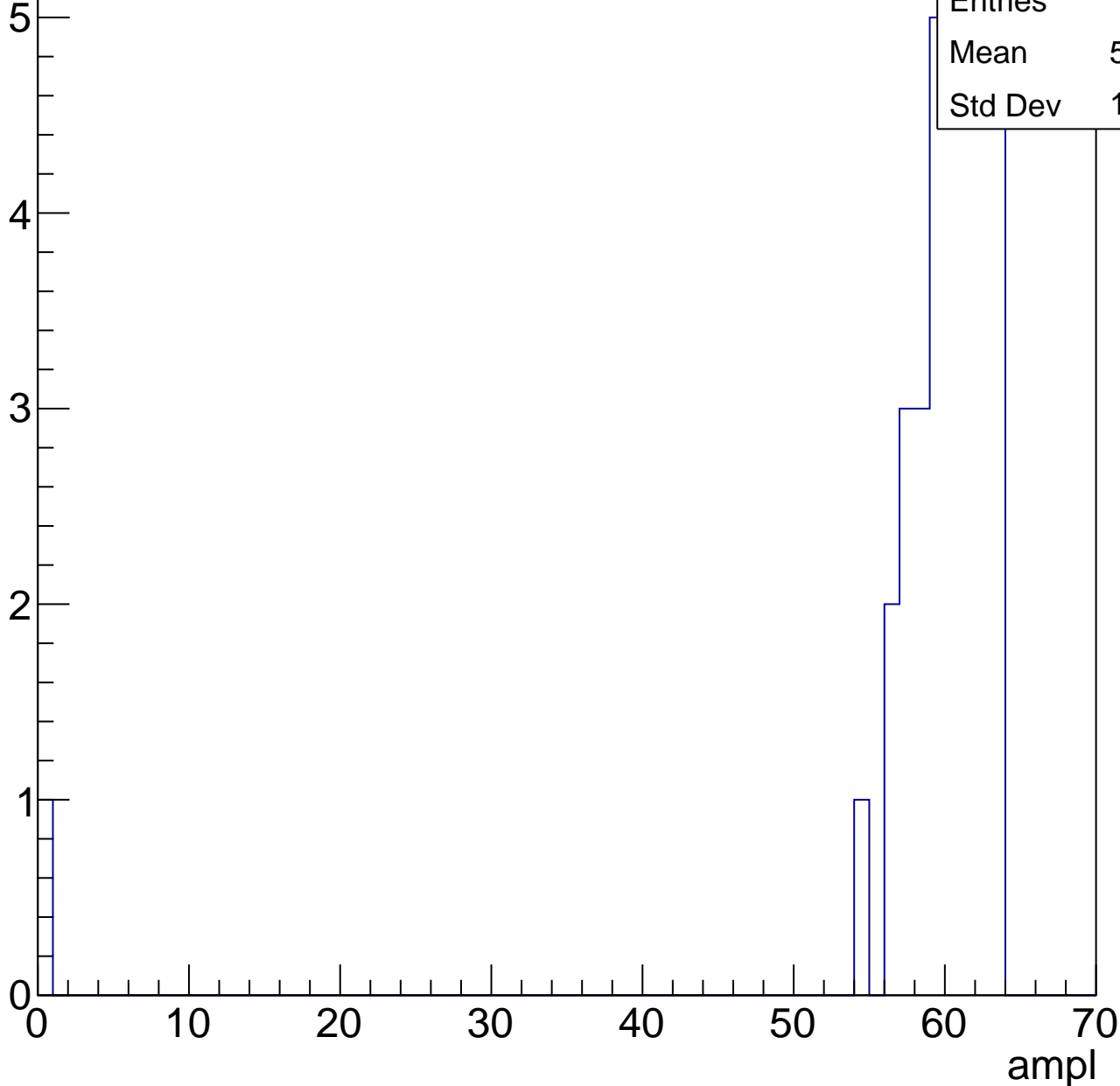


B1L101S, U21-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

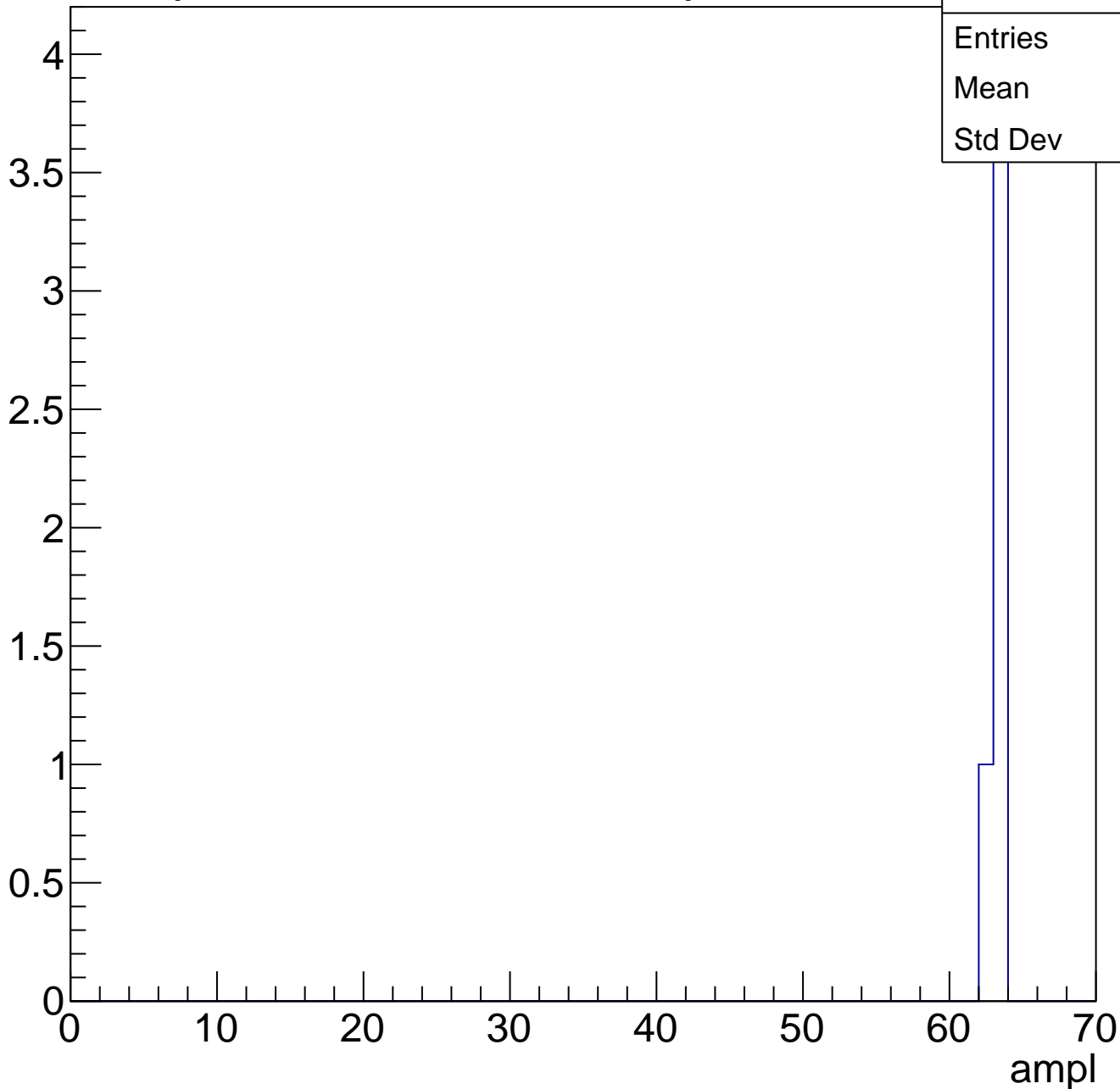
Entries	35
Mean	58.17
Std Dev	10.23



B1L101S, U21-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch101, adc0

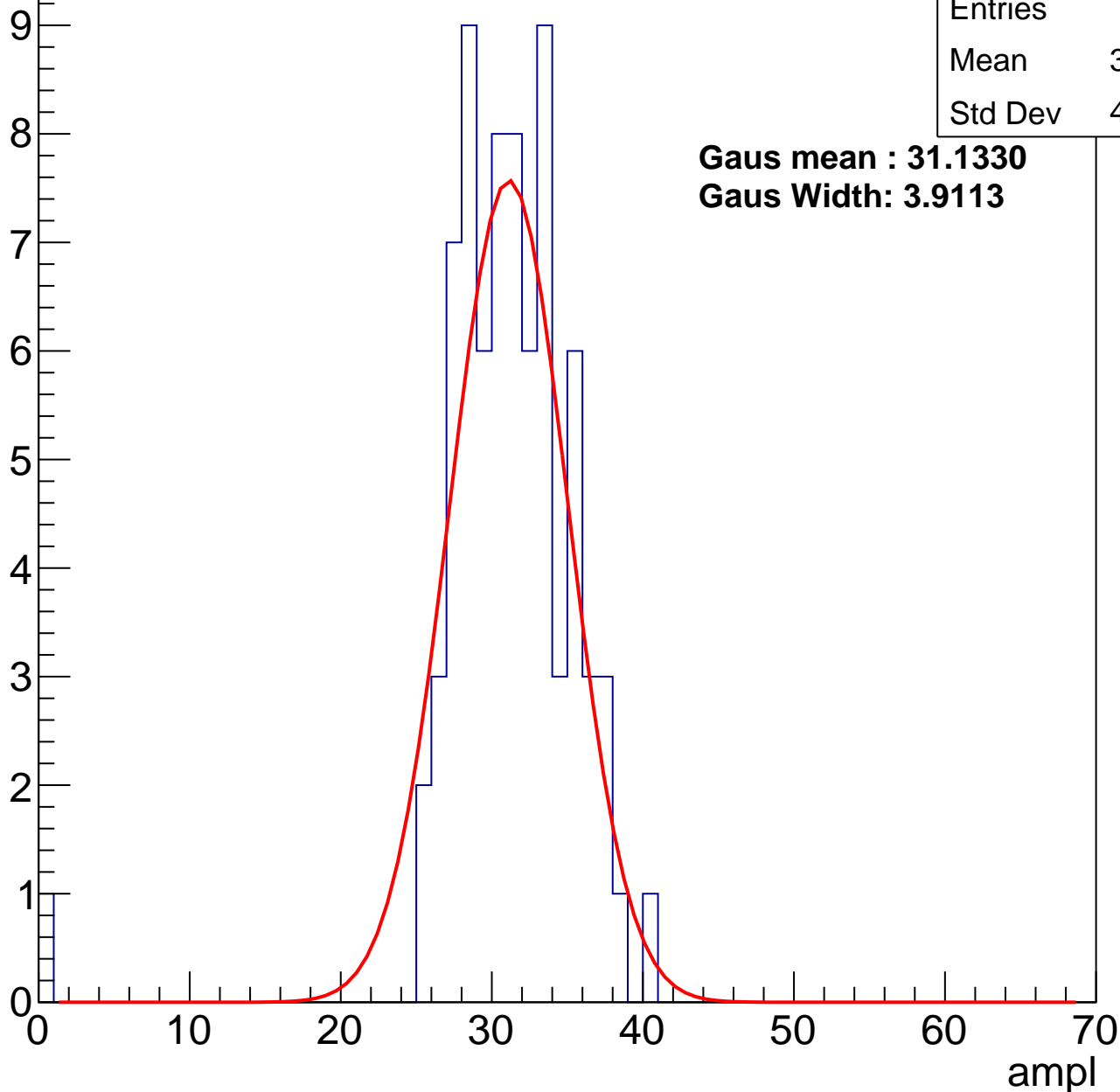
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	30.64
Std Dev	4.869

Gaus mean : 31.1330

Gaus Width: 3.9113



B1L101S, U21-ch101, adc1

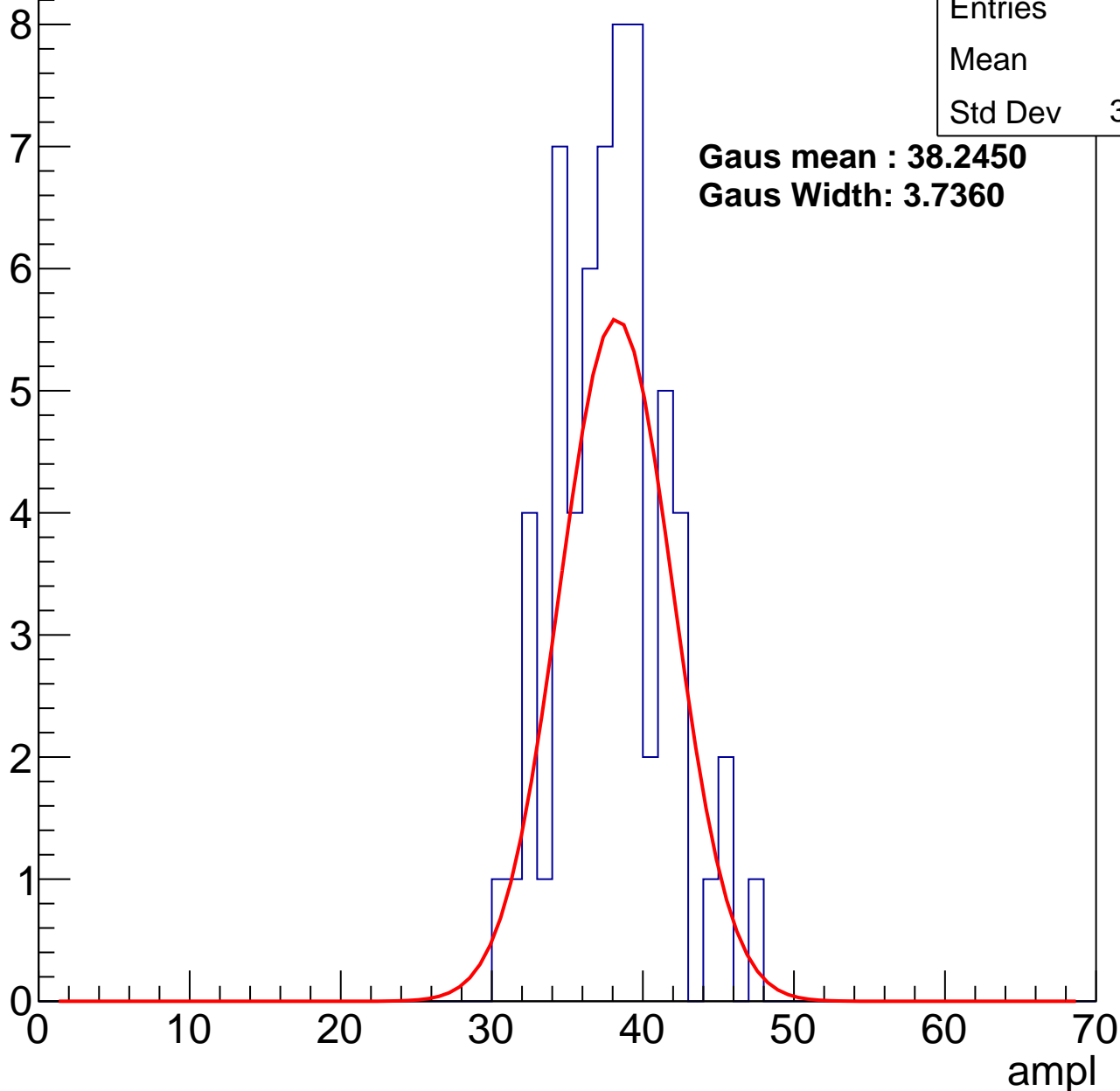
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.5
Std Dev	3.559

Gaus mean : 38.2450

Gaus Width: 3.7360



B1L101S, U21-ch101, adc2

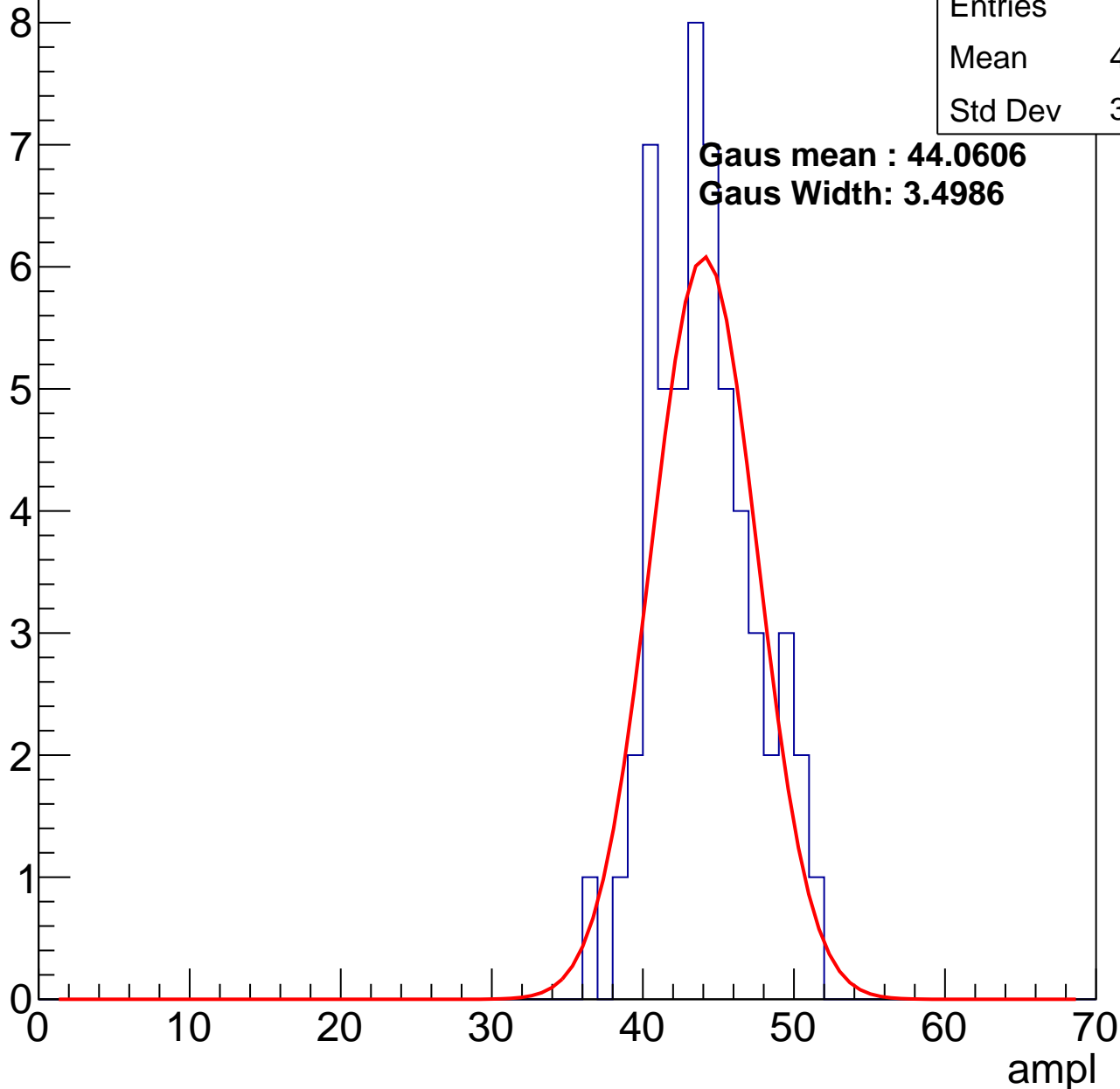
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	43.62
Std Dev	3.282

Gaus mean : 44.0606

Gaus Width: 3.4986

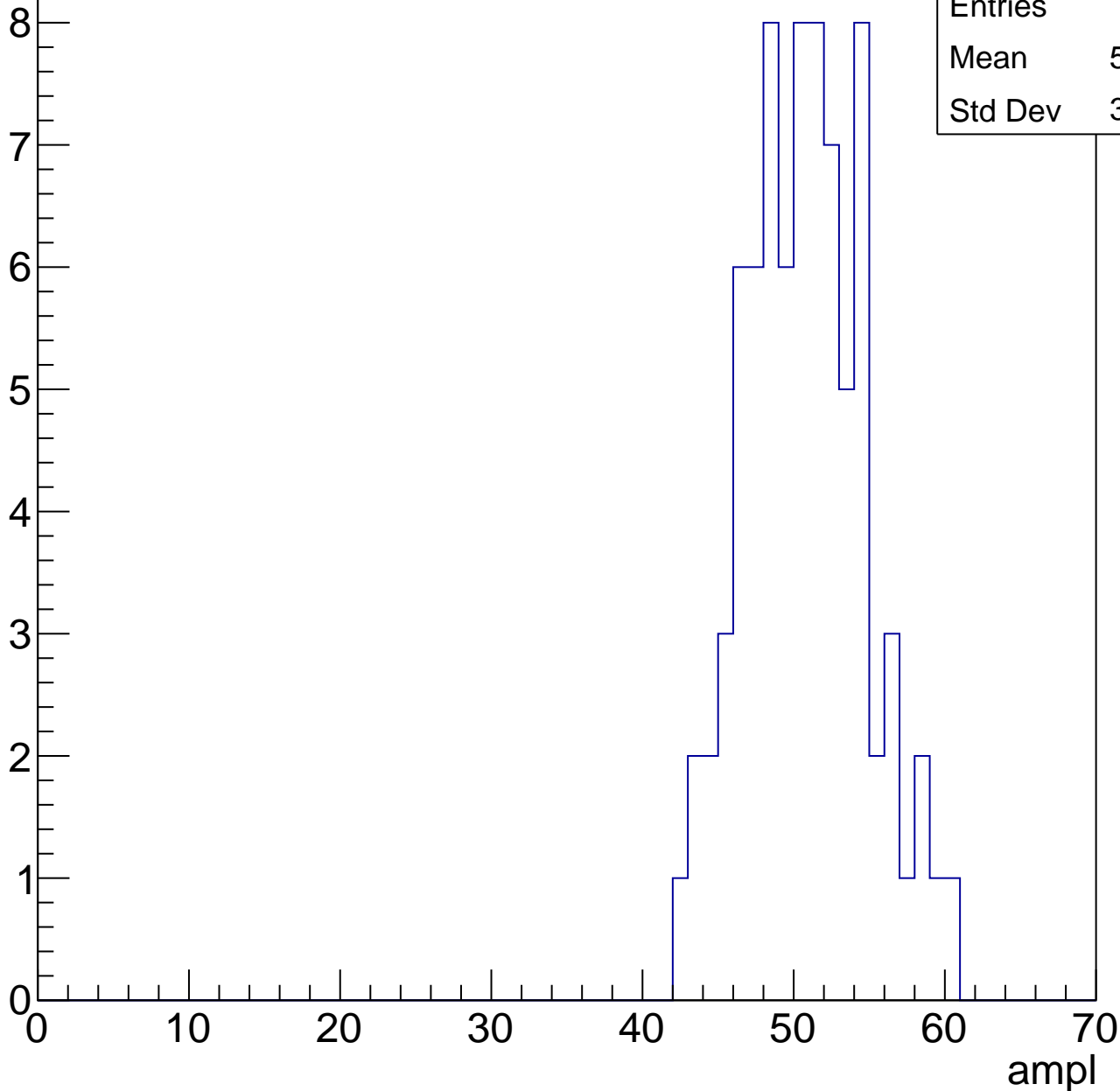


B1L101S, U21-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

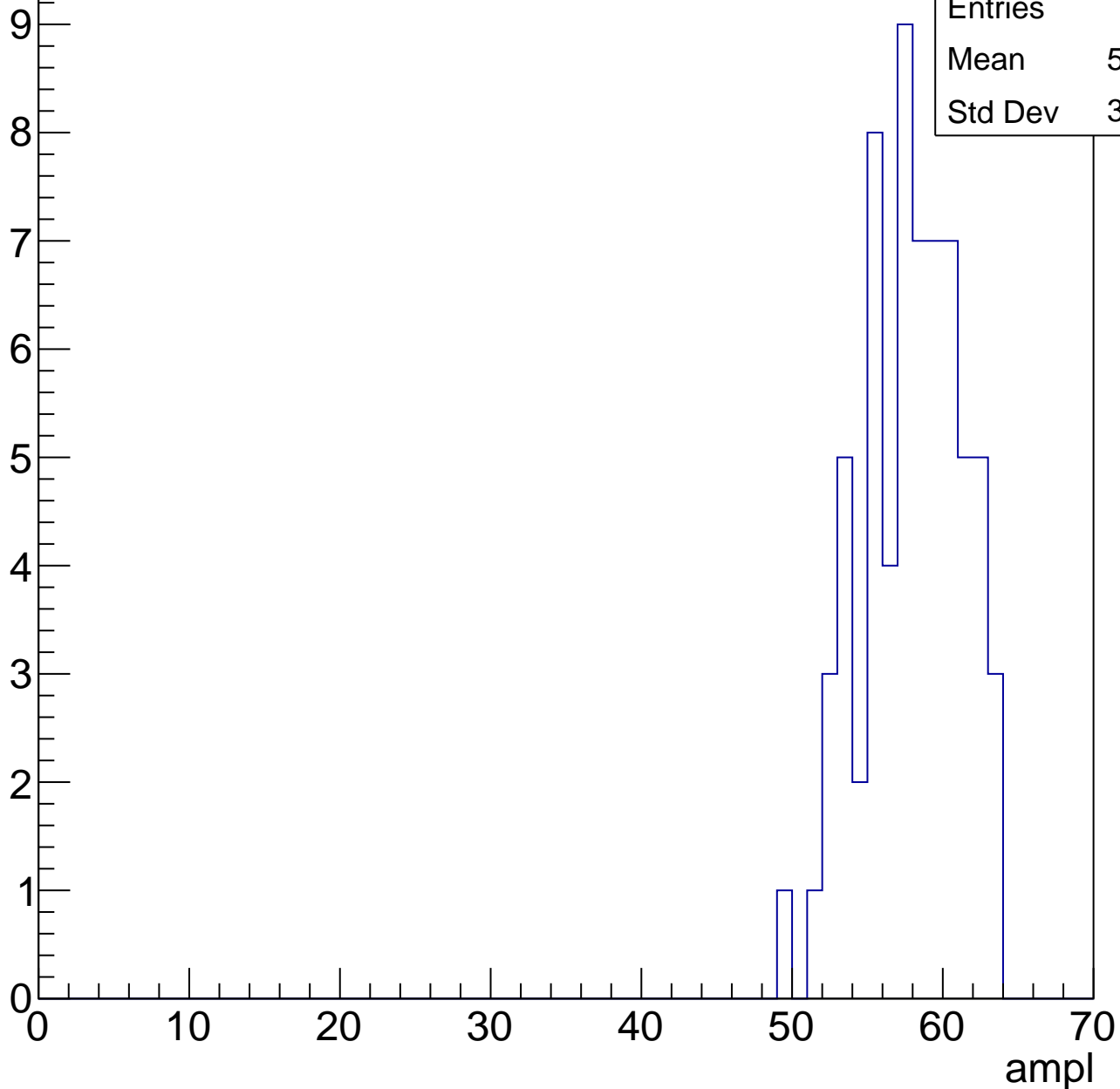
Entries	80
Mean	50.33
Std Dev	3.895



B1L101S, U21-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



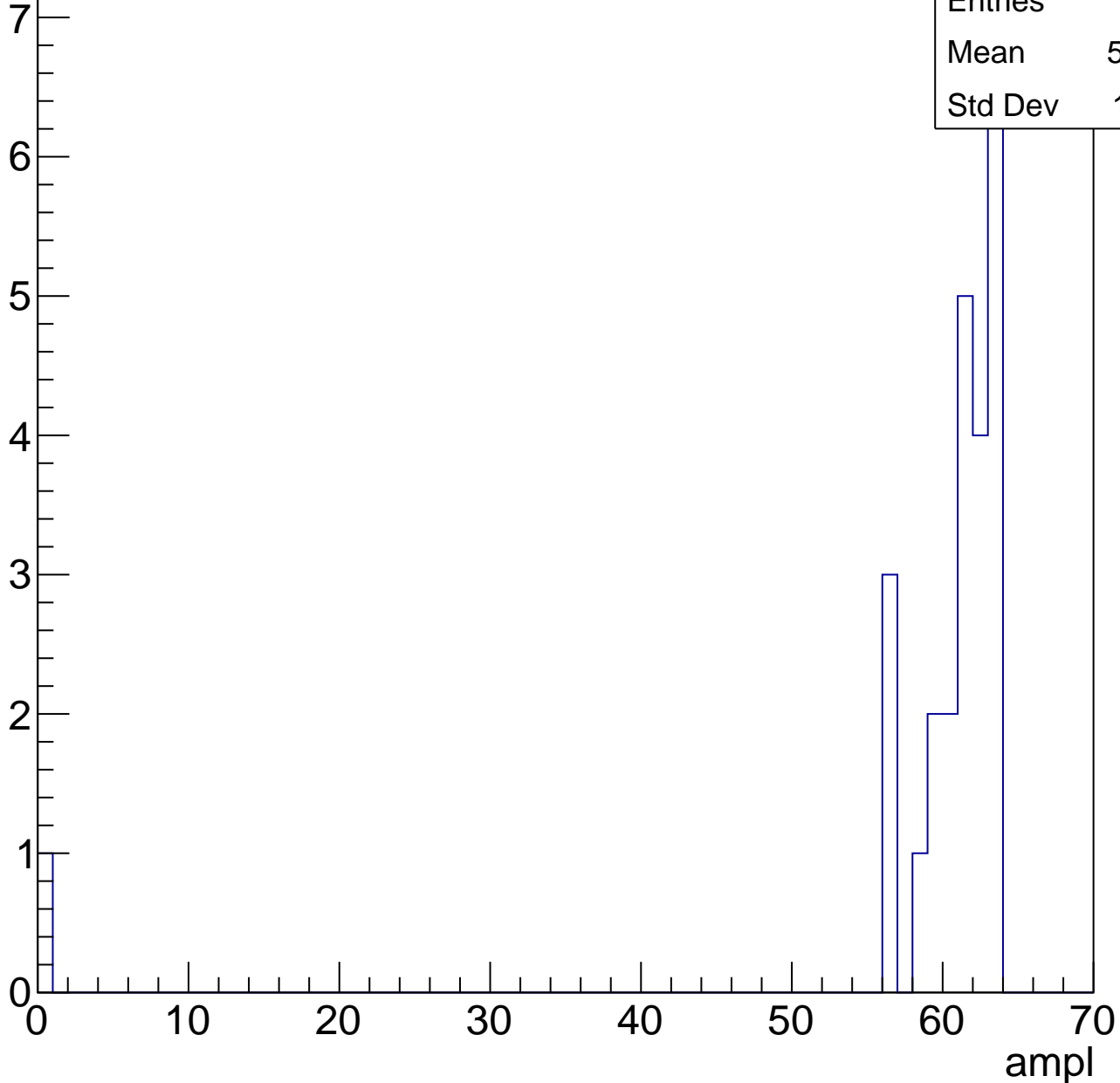
Entries	67
Mean	57.45
Std Dev	3.247

B1L101S, U21-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.32
Std Dev	12.11



B1L101S, U21-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch102, adc0

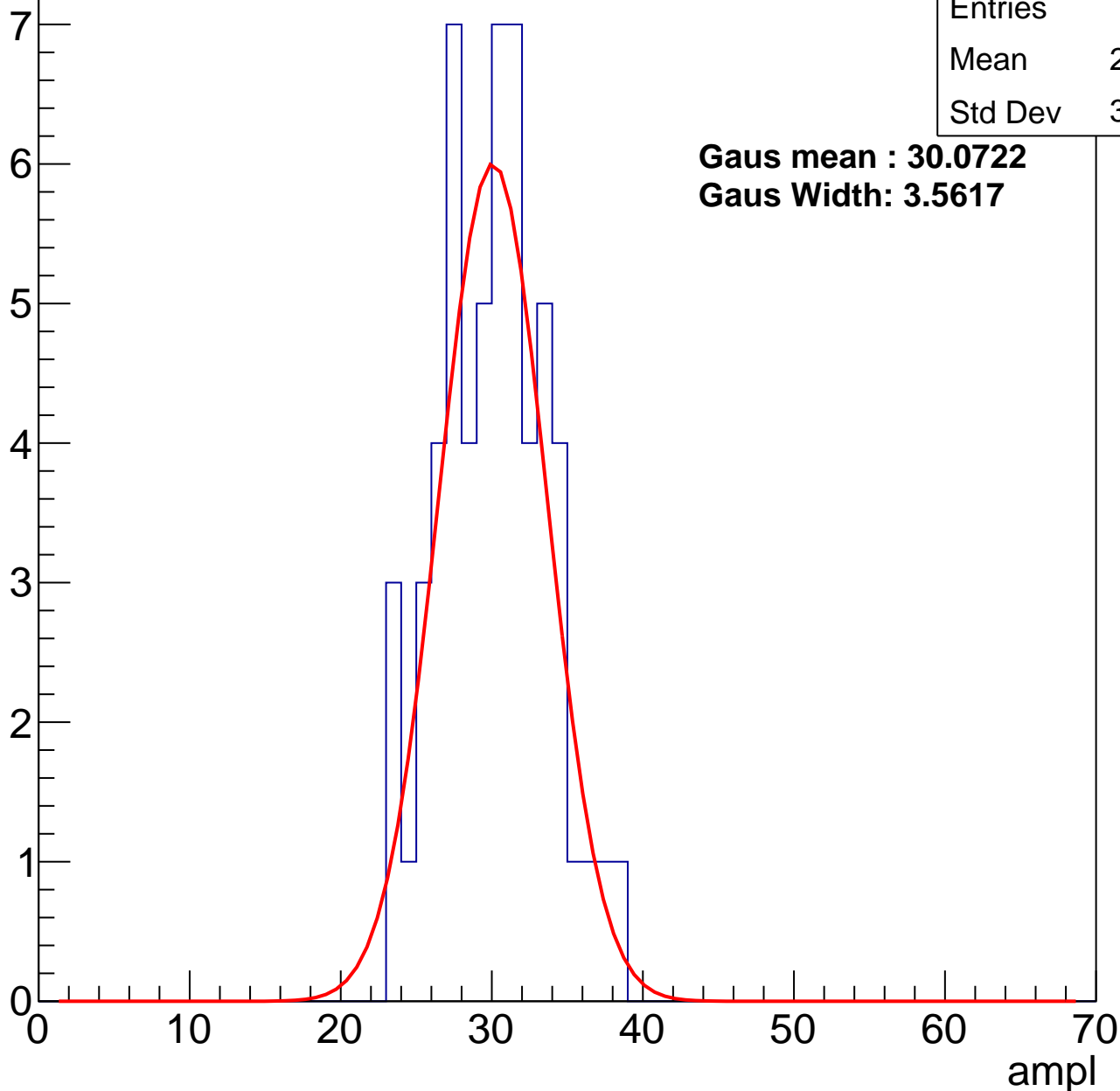
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.66
Std Dev	3.487

Gaus mean : 30.0722

Gaus Width: 3.5617



B1L101S, U21-ch102, adc1

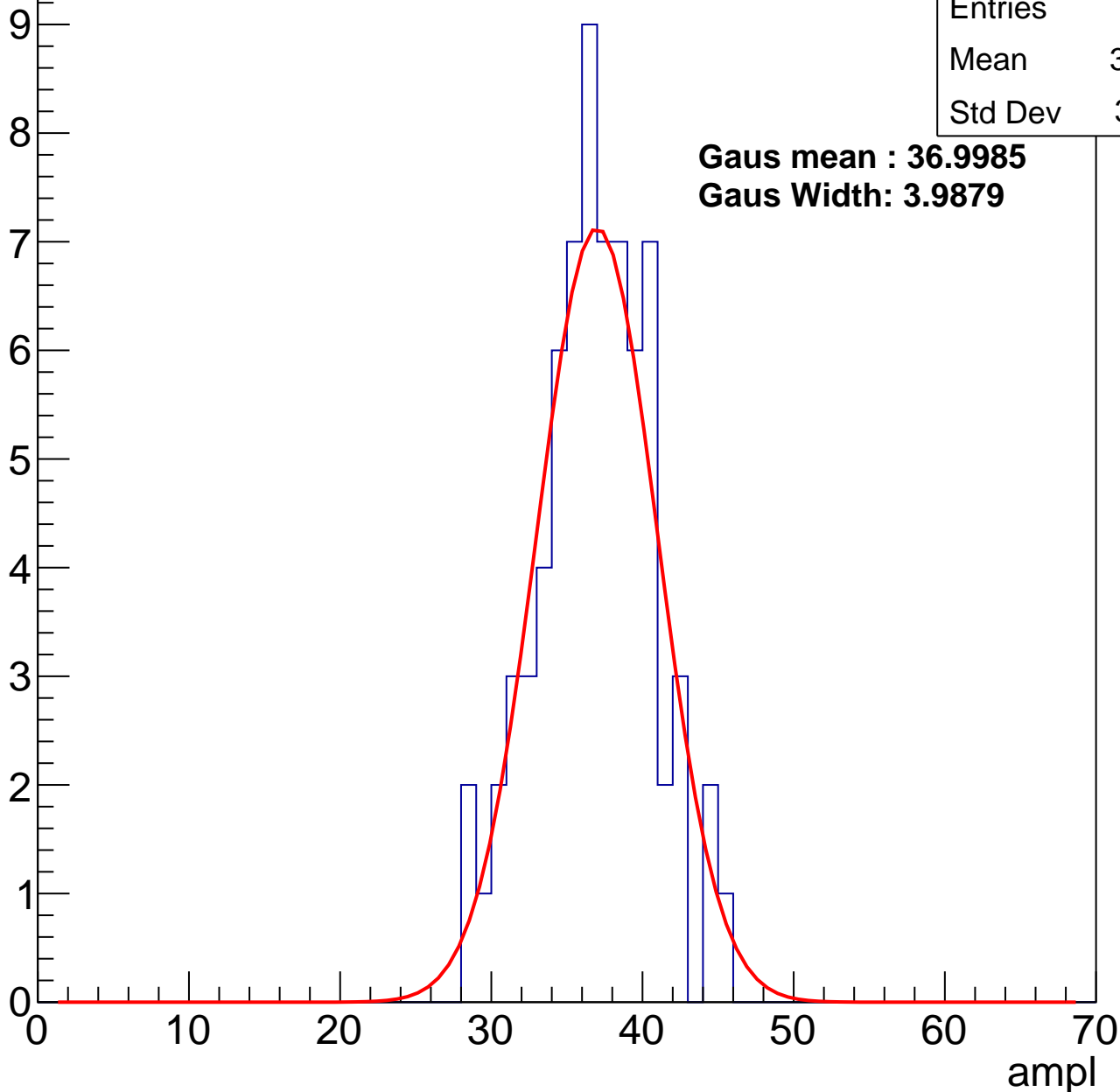
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.38
Std Dev	3.721

Gaus mean : 36.9985

Gaus Width: 3.9879



B1L101S, U21-ch102, adc2

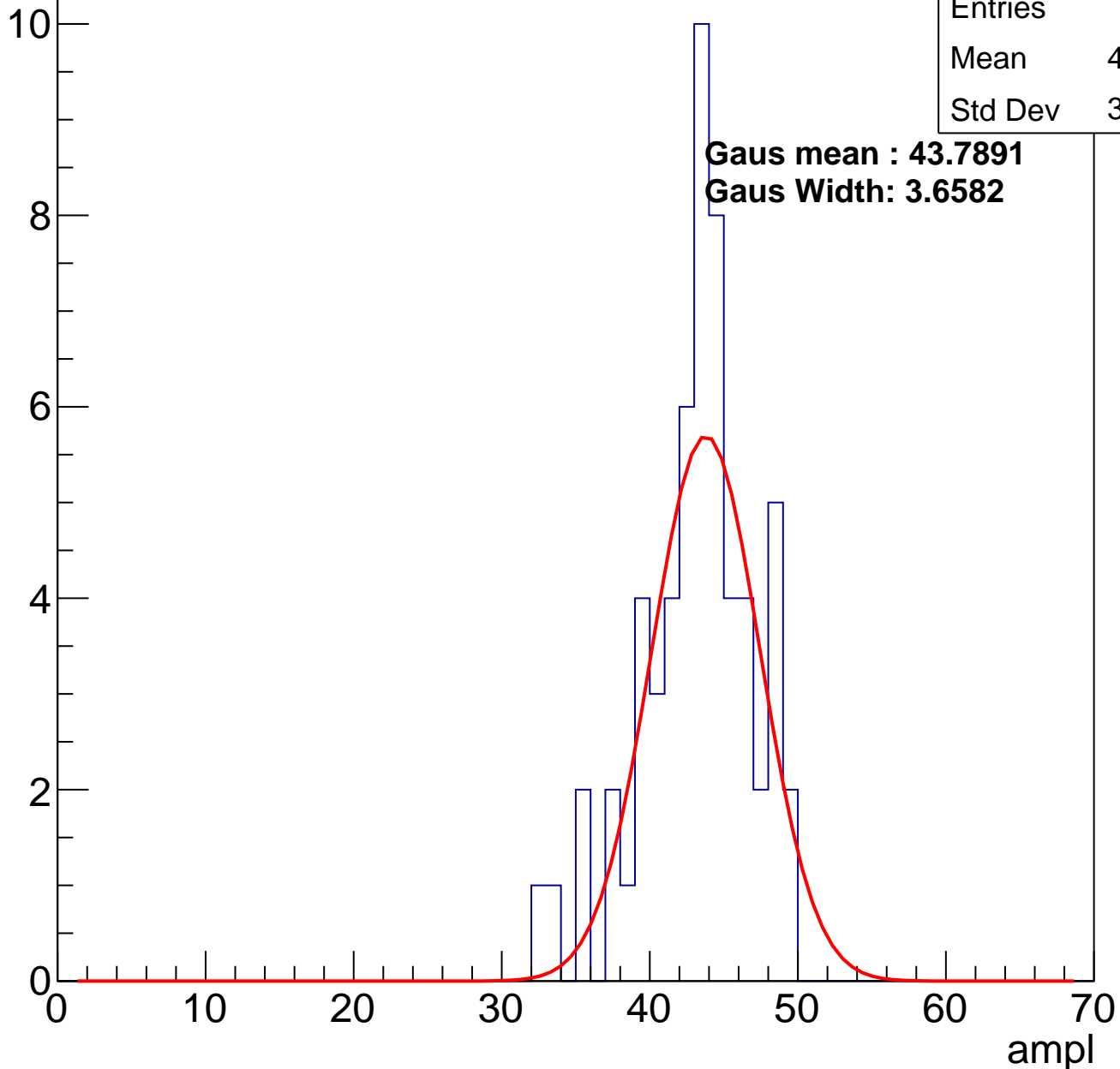
calib_packv5_042523_0143.root, FC#0, port D2

Entries	59
Mean	42.66
Std Dev	3.776

Gaus mean : 43.7891

Gaus Width: 3.6582

Entry



B1L101S, U21-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

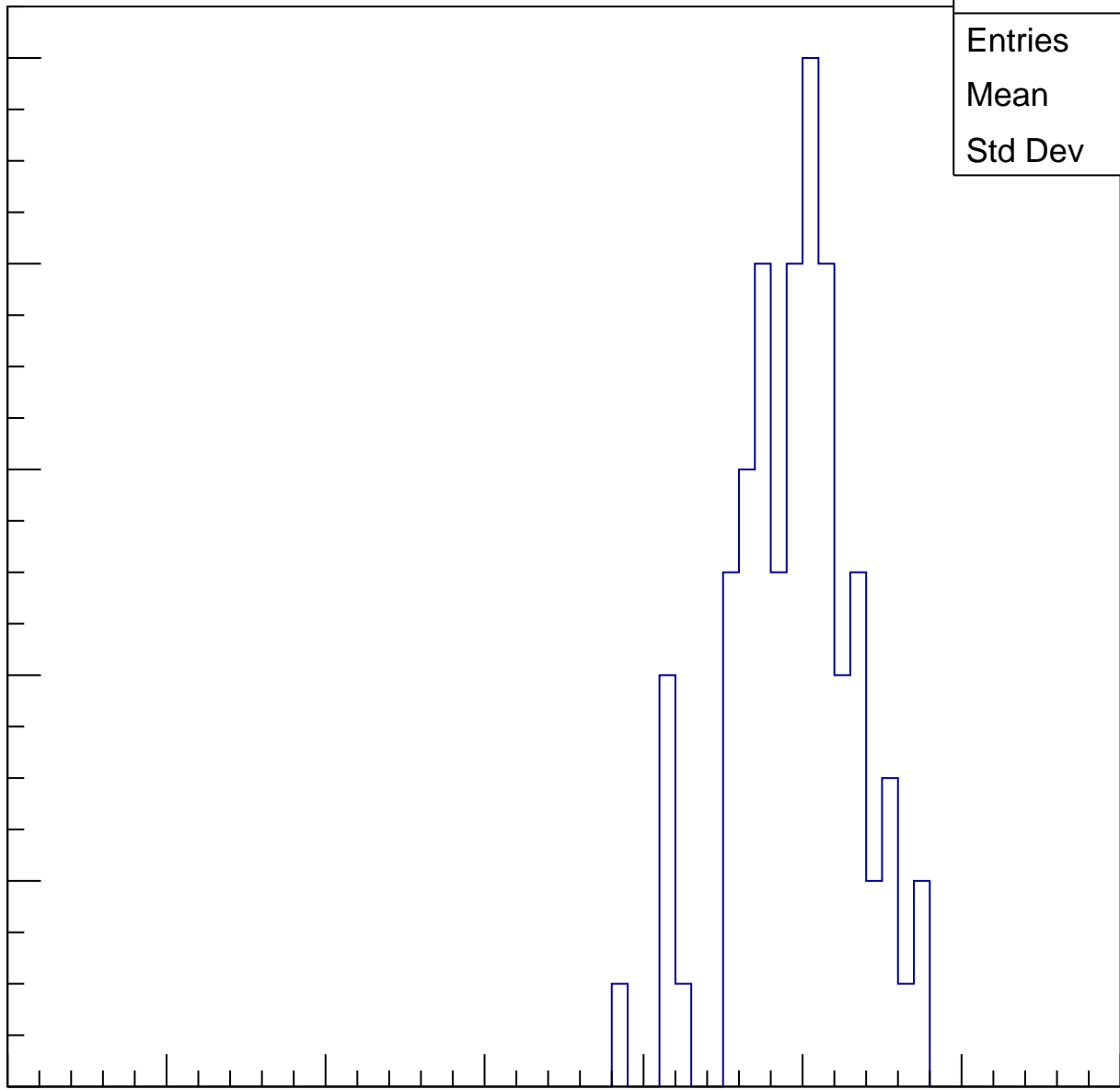
Entries	73
Mean	49
Std Dev	3.864

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

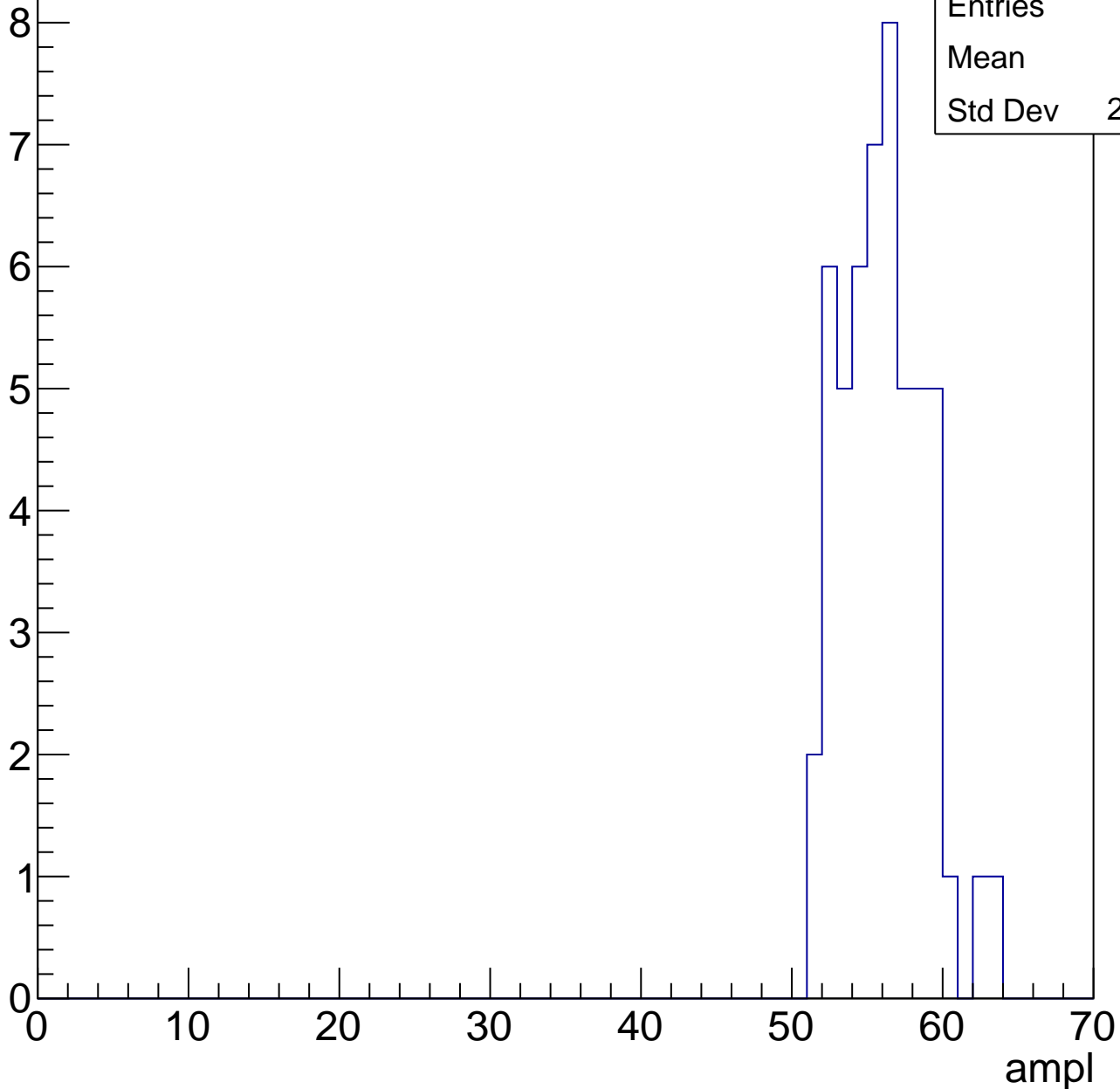


B1L101S, U21-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	55.6
Std Dev	2.719

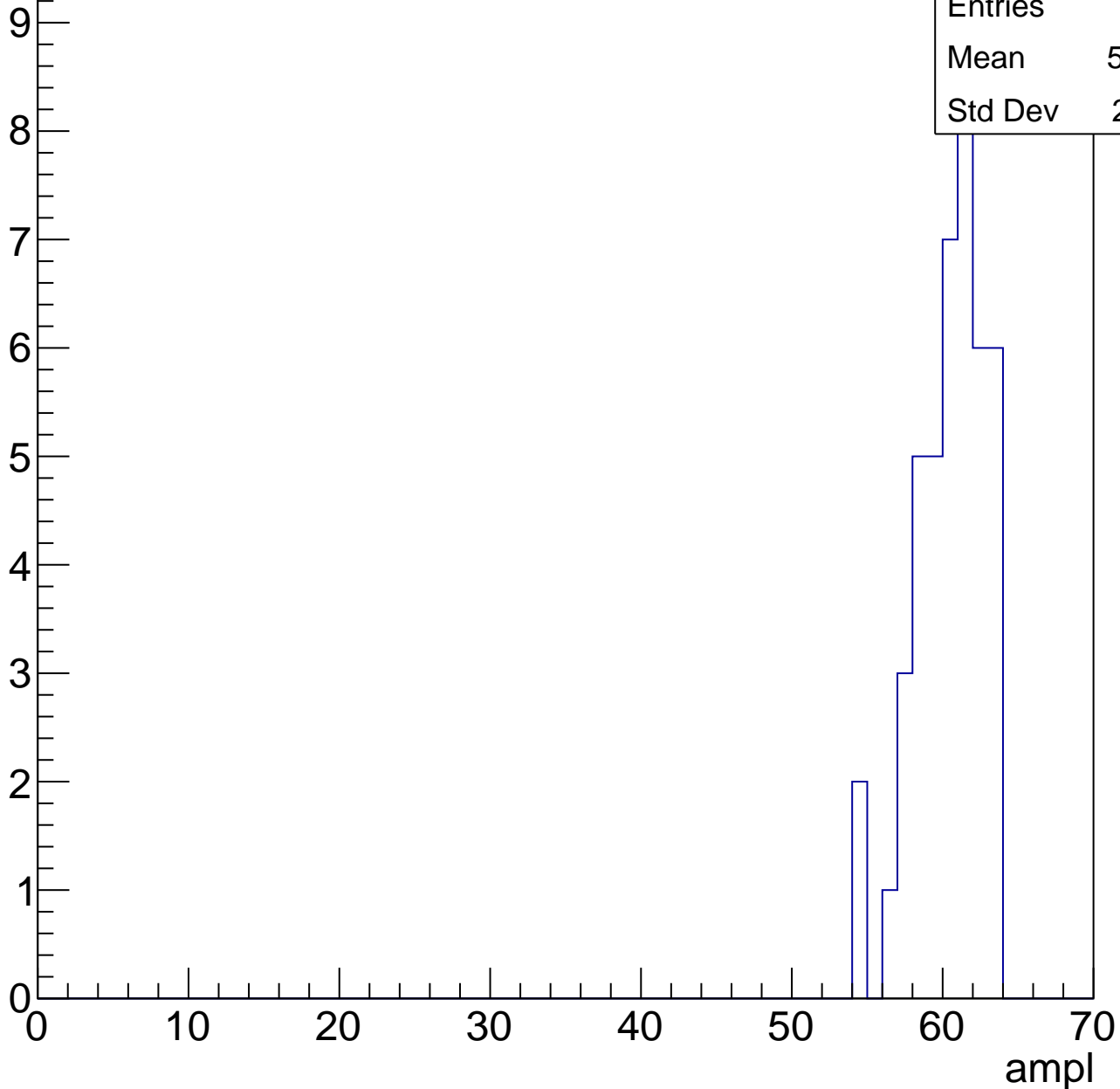


B1L101S, U21-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

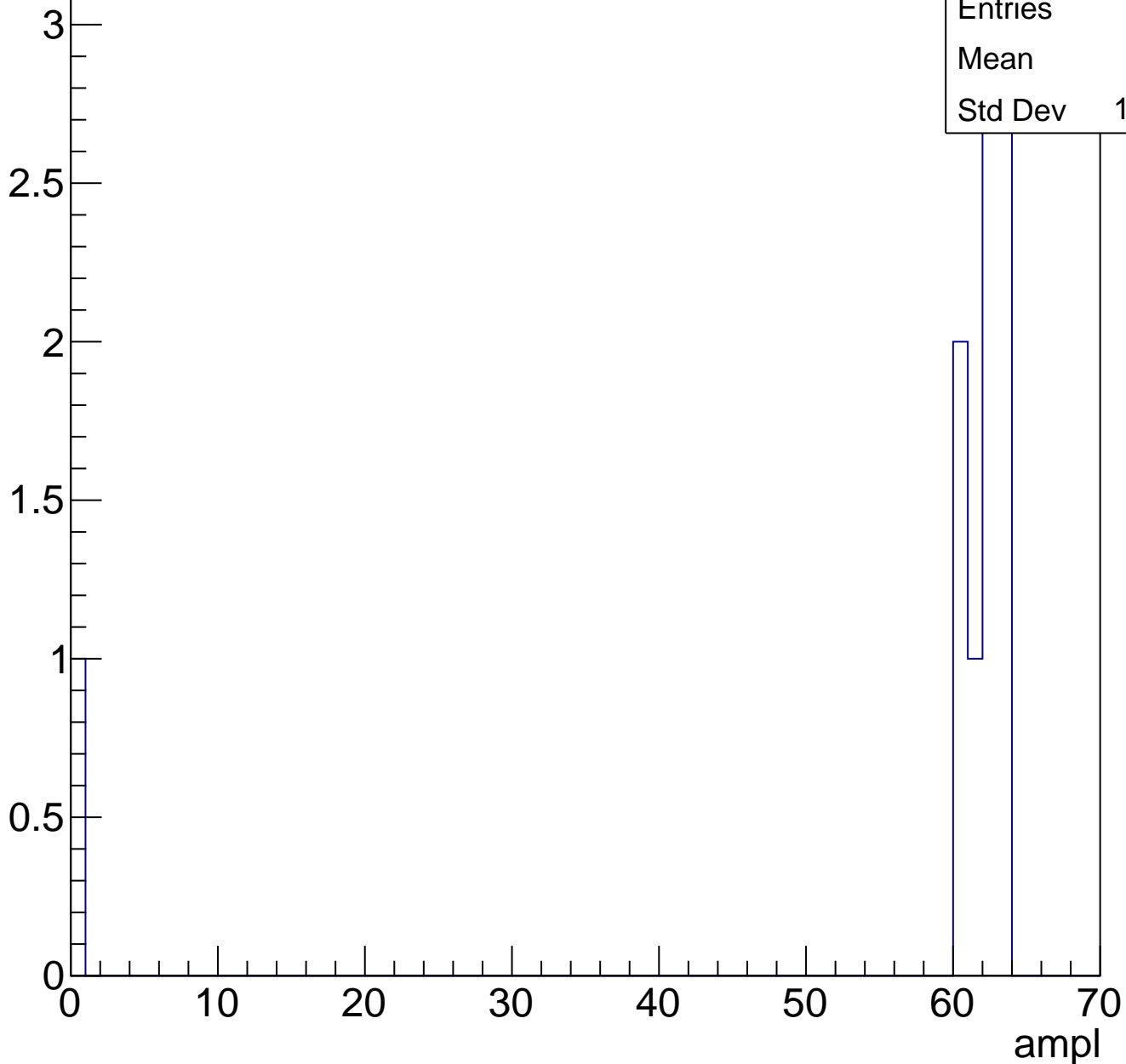
Entries	44
Mean	59.98
Std Dev	2.271



B1L101S, U21-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch103, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	29.83
Std Dev	3.126

Gaus mean : 29.8700

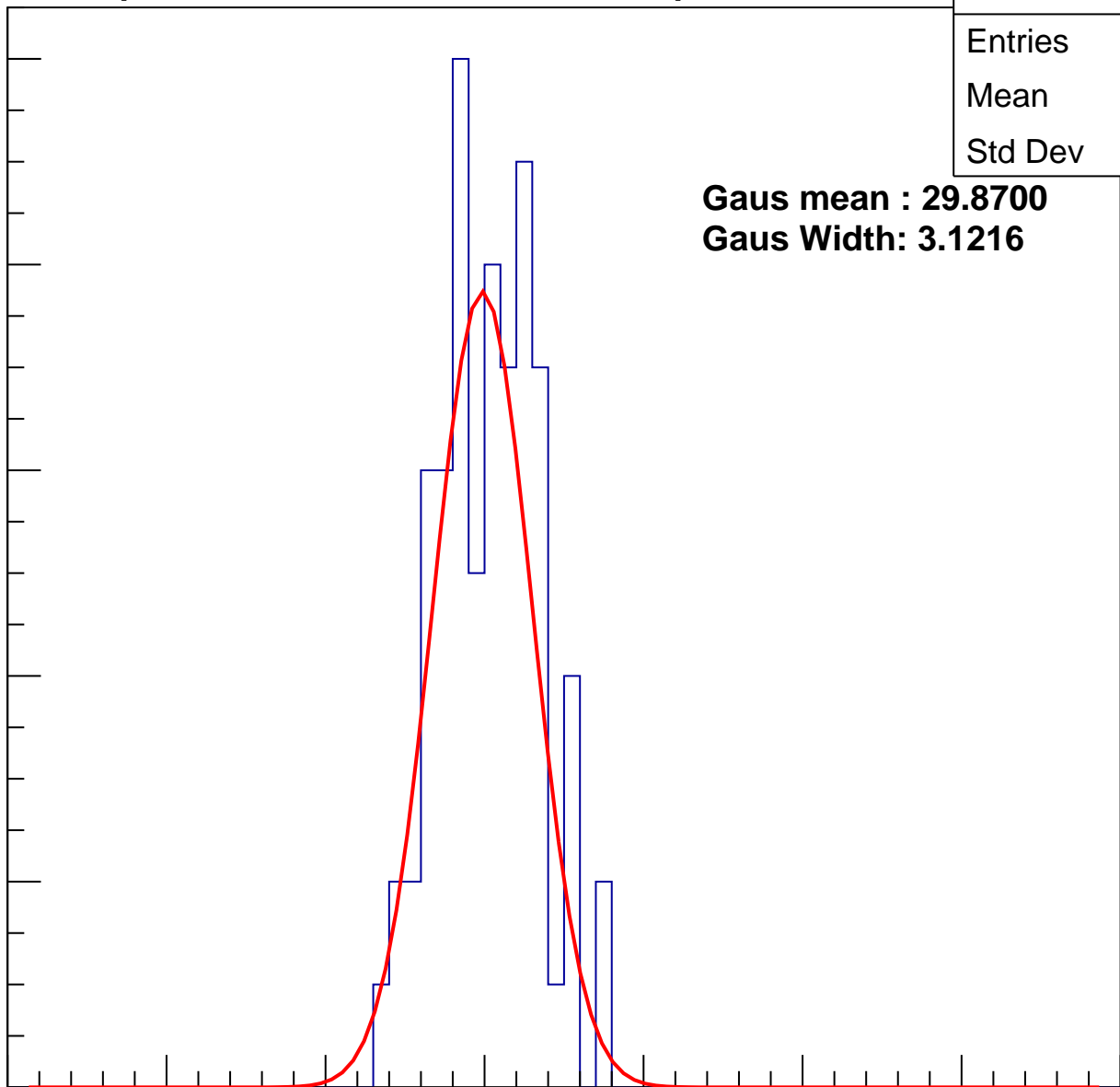
Gaus Width: 3.1216

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

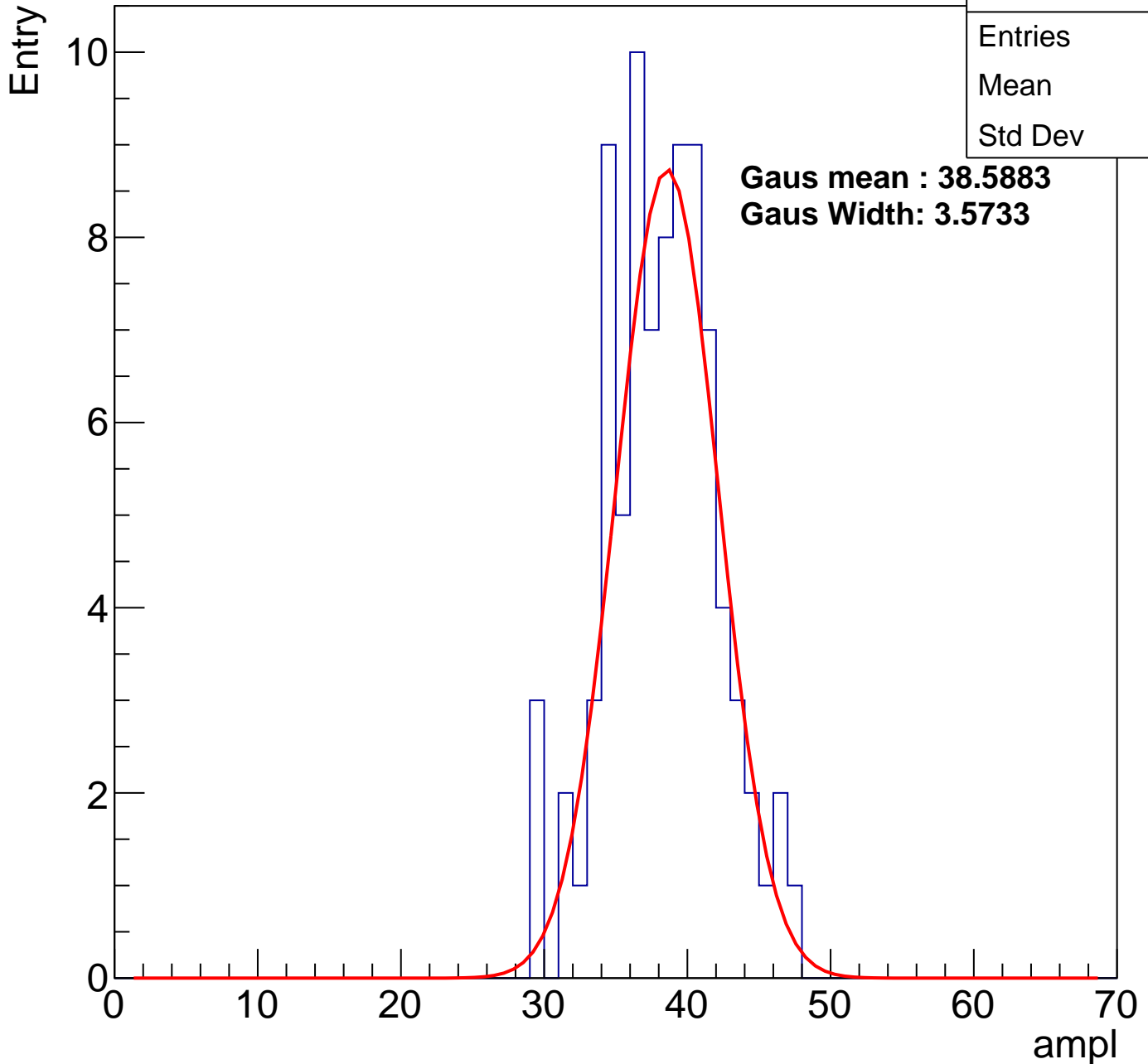


B1L101S, U21-ch103, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	86
Mean	37.8
Std Dev	3.83

Gaus mean : 38.5883
Gaus Width: 3.5733



B1L101S, U21-ch103, adc2

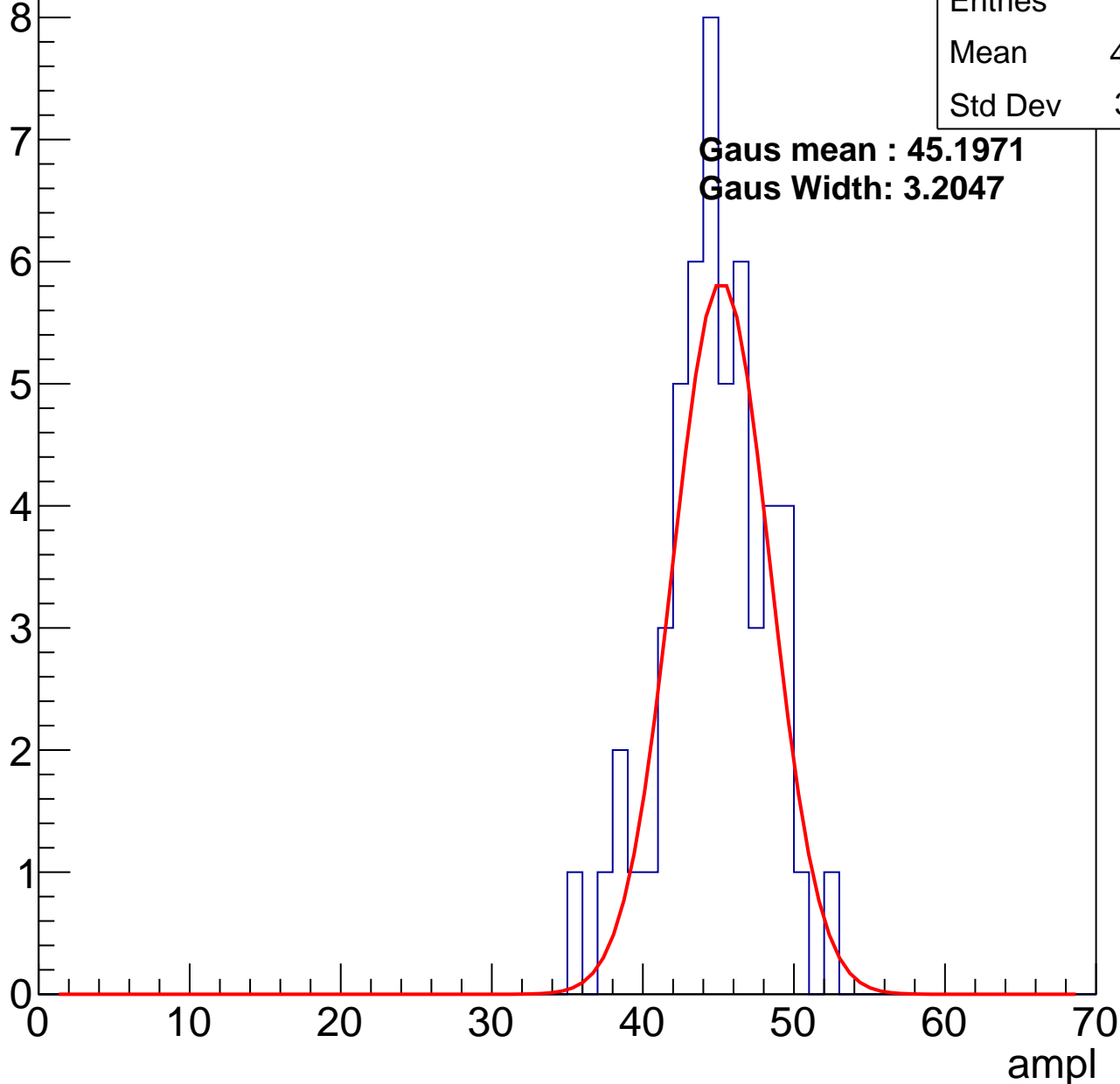
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.27
Std Dev	3.431

Gaus mean : 45.1971

Gaus Width: 3.2047

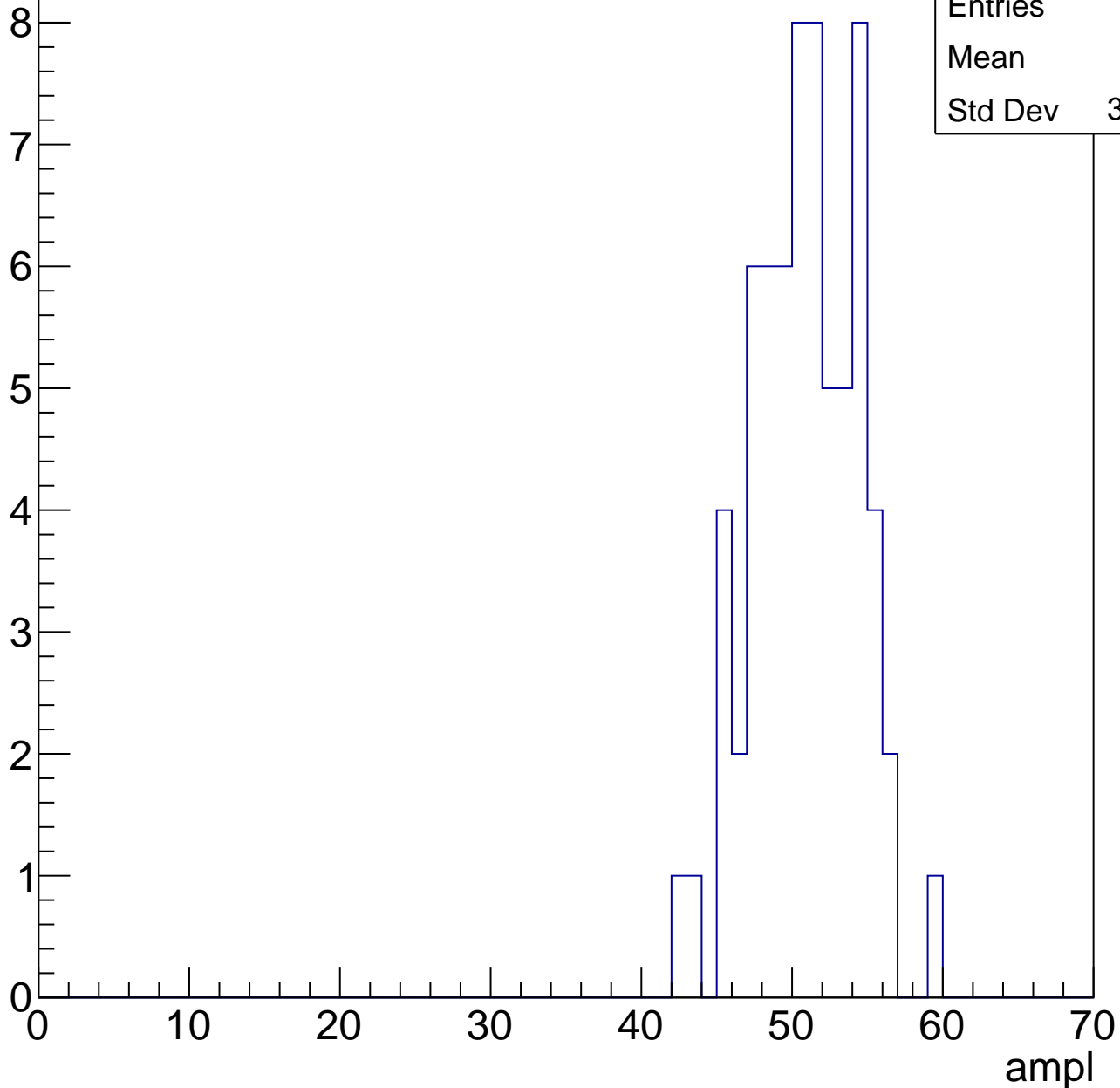


B1L101S, U21-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.4
Std Dev	3.395

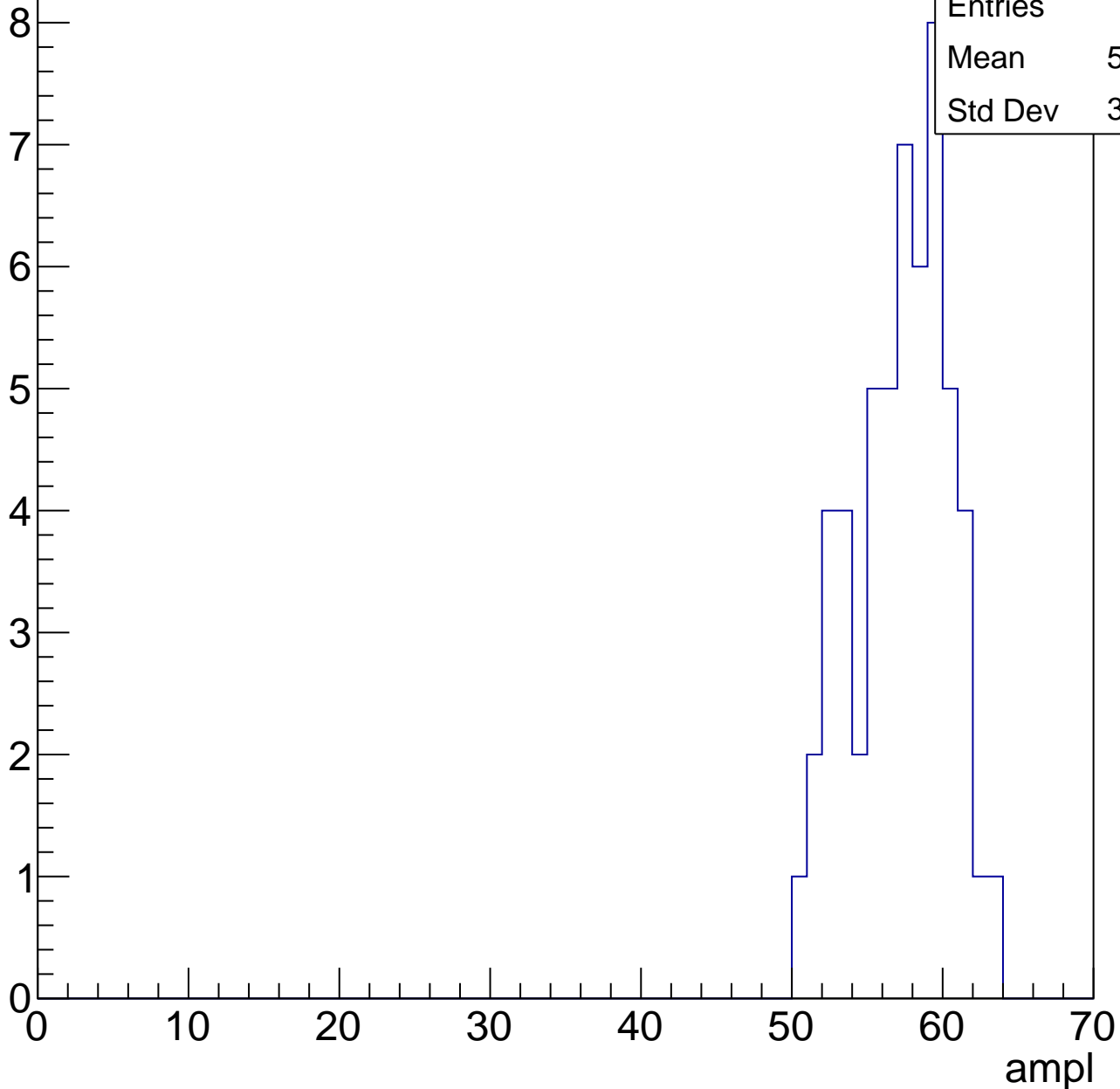


B1L101S, U21-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.78
Std Dev	3.114

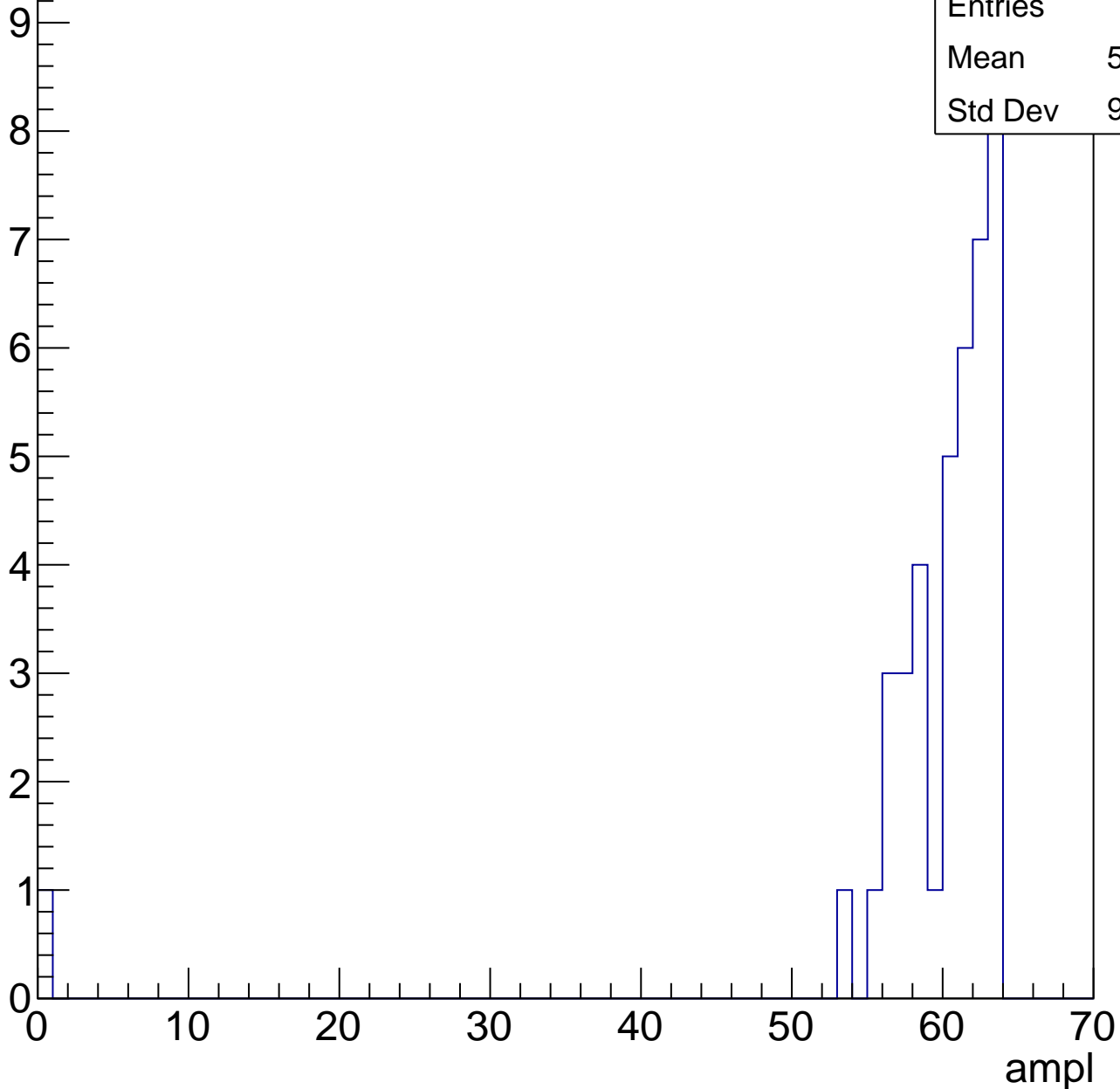


B1L101S, U21-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.66
Std Dev	9.636



B1L101S, U21-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch104, adc0

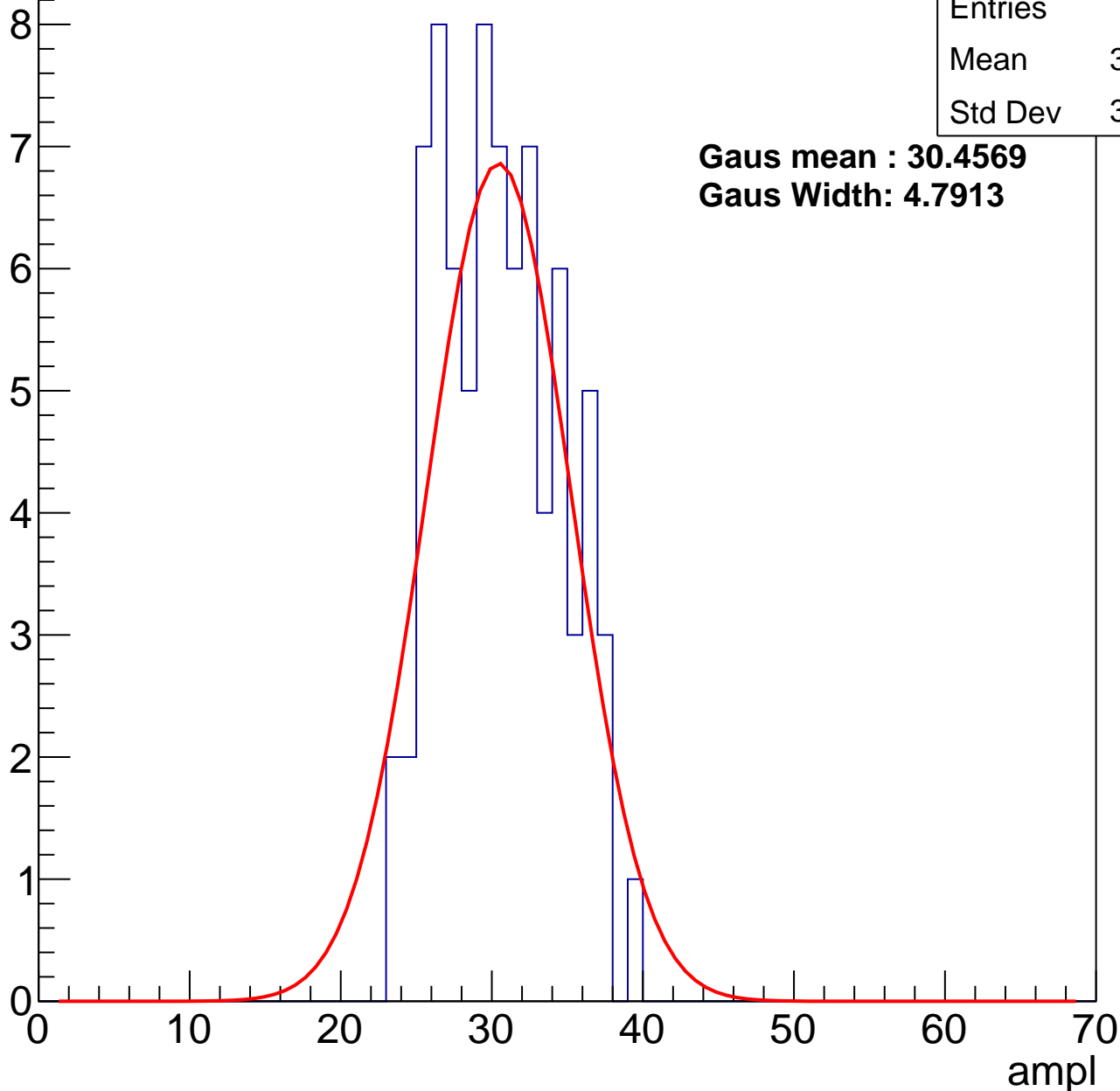
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	30.02
Std Dev	3.879

Gaus mean : 30.4569

Gaus Width: 4.7913



B1L101S, U21-ch104, adc1

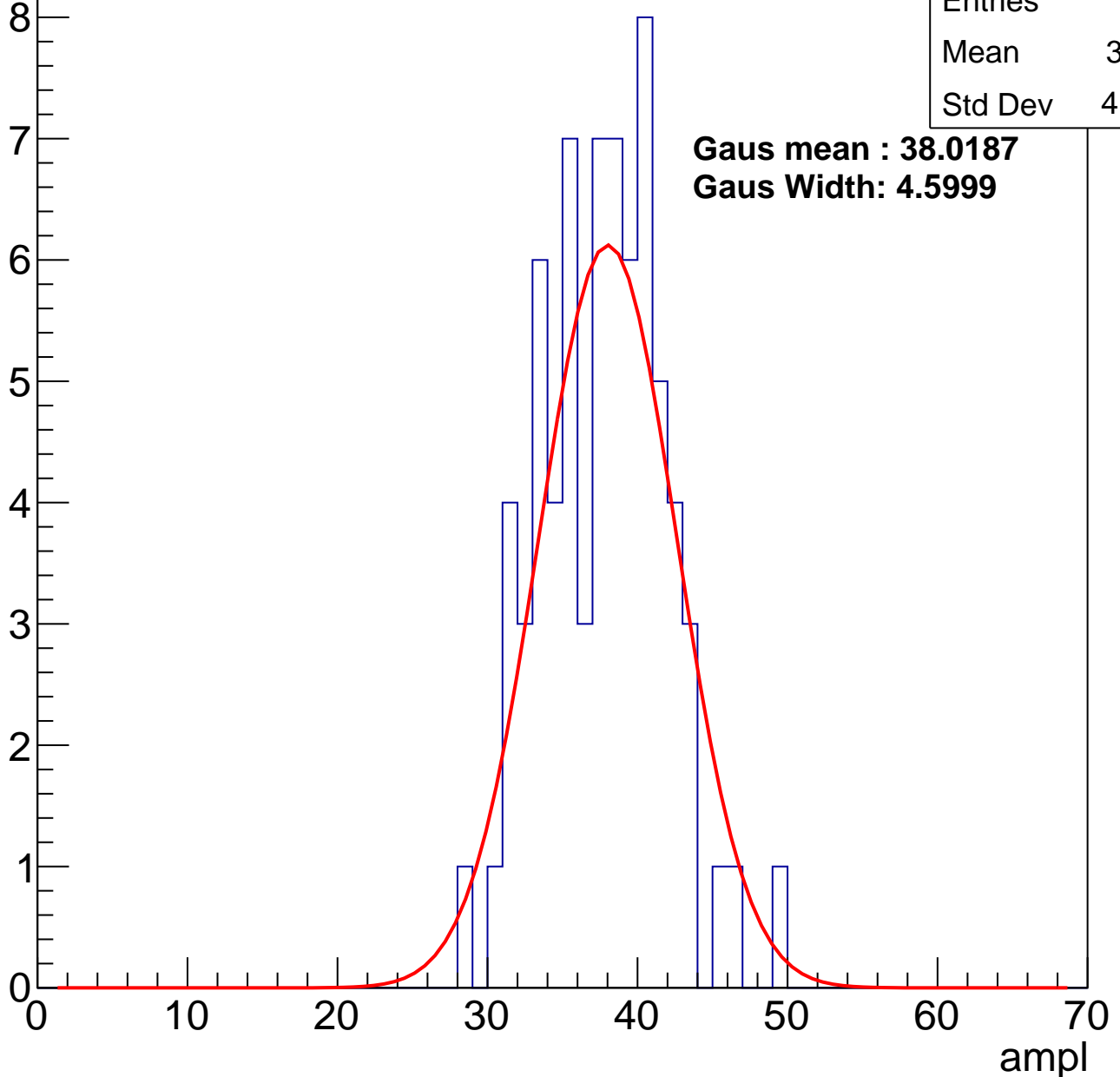
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.31
Std Dev	4.057

Gaus mean : 38.0187

Gaus Width: 4.5999



B1L101S, U21-ch104, adc2

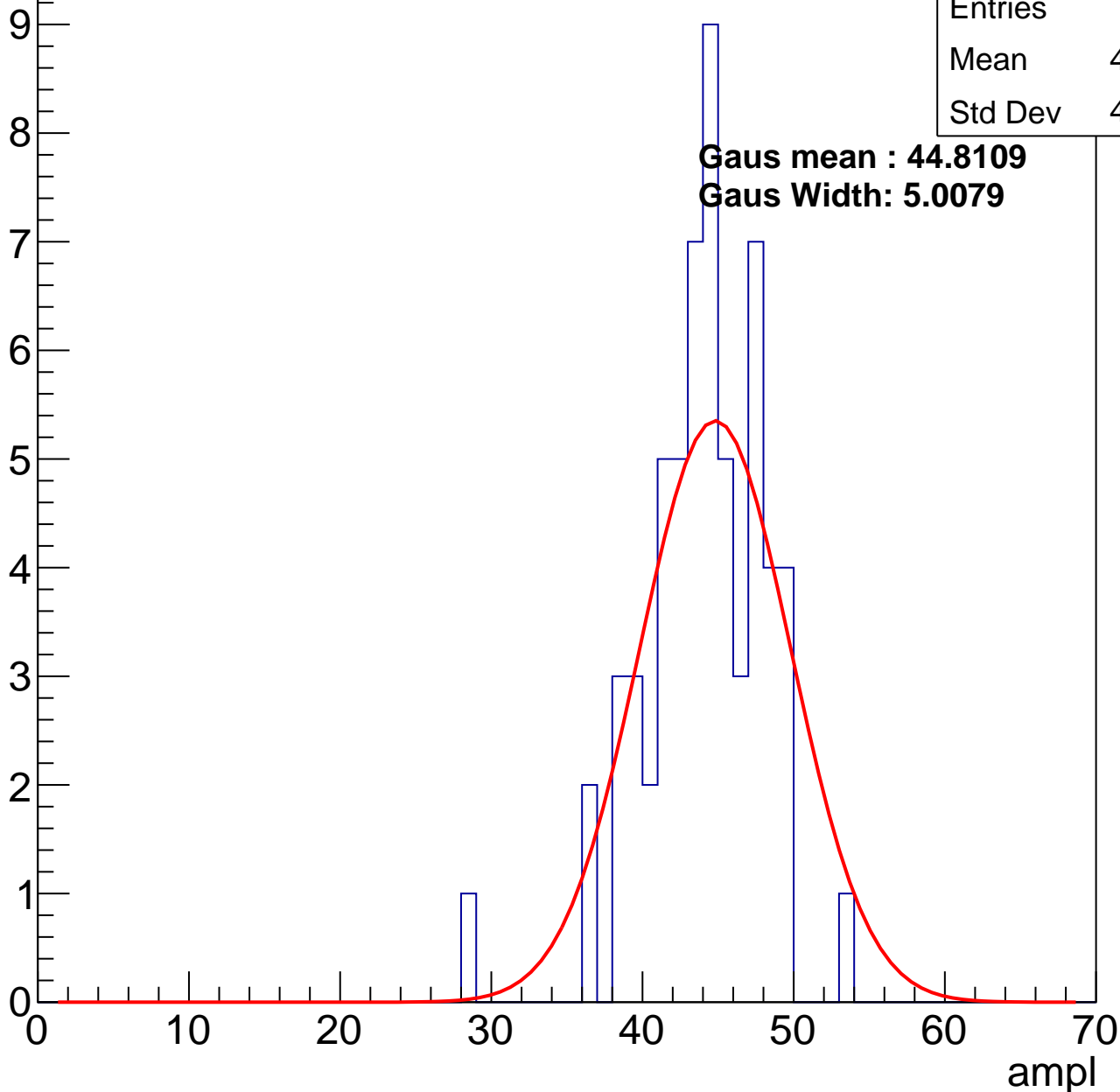
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.54
Std Dev	4.015

Gaus mean : 44.8109

Gaus Width: 5.0079

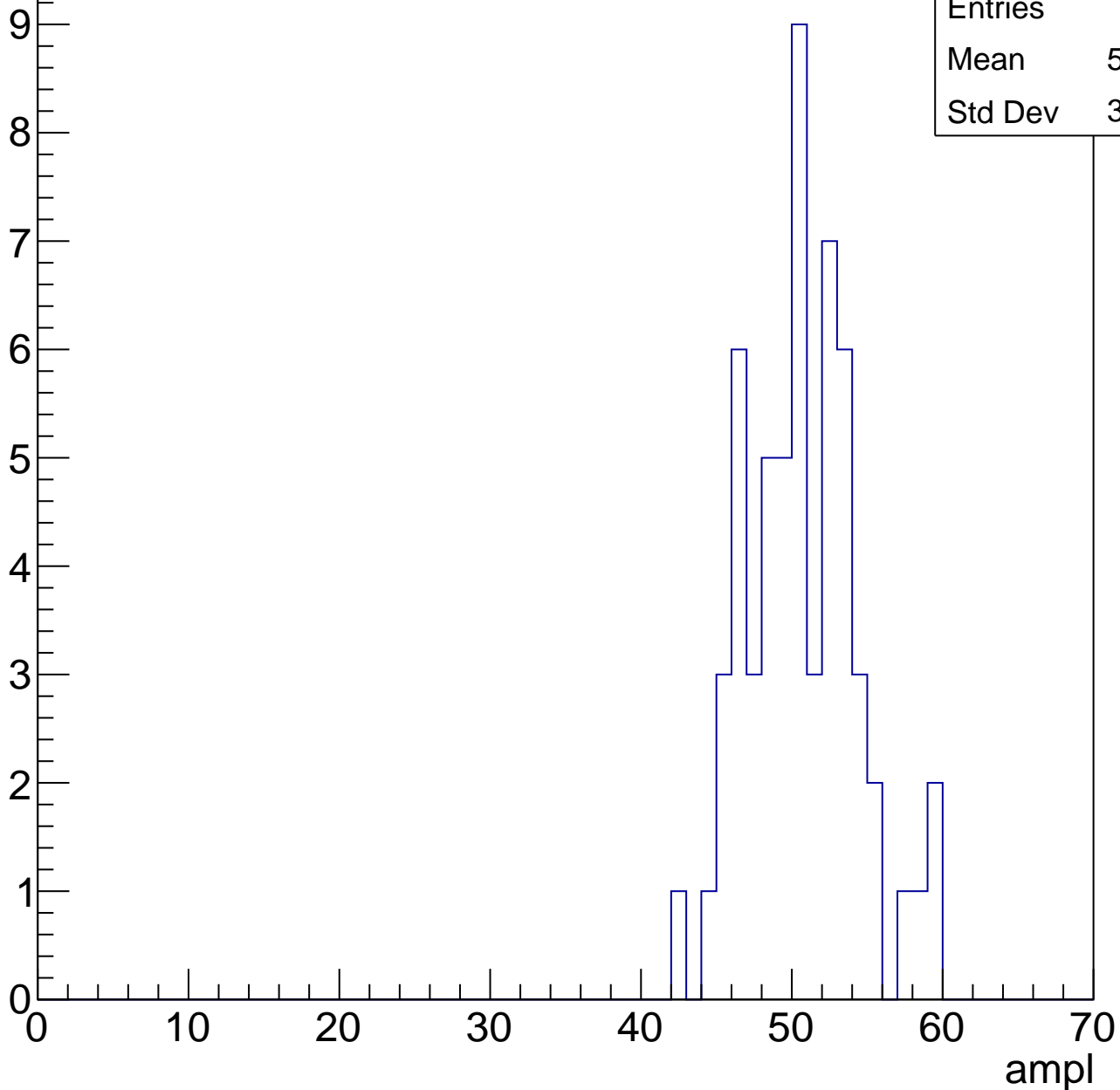


B1L101S, U21-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.22
Std Dev	3.663

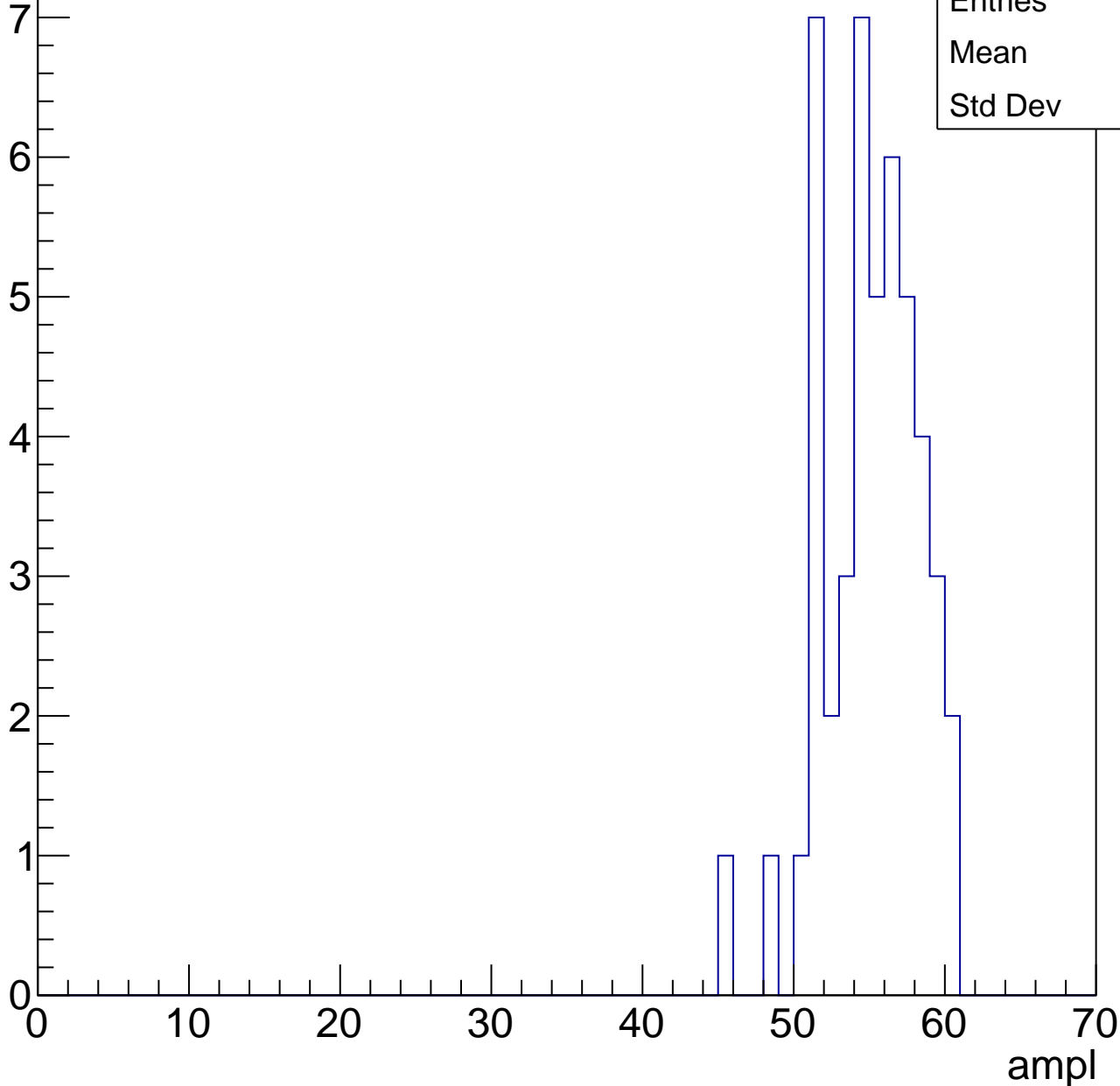


B1L101S, U21-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	54.6
Std Dev	3.18

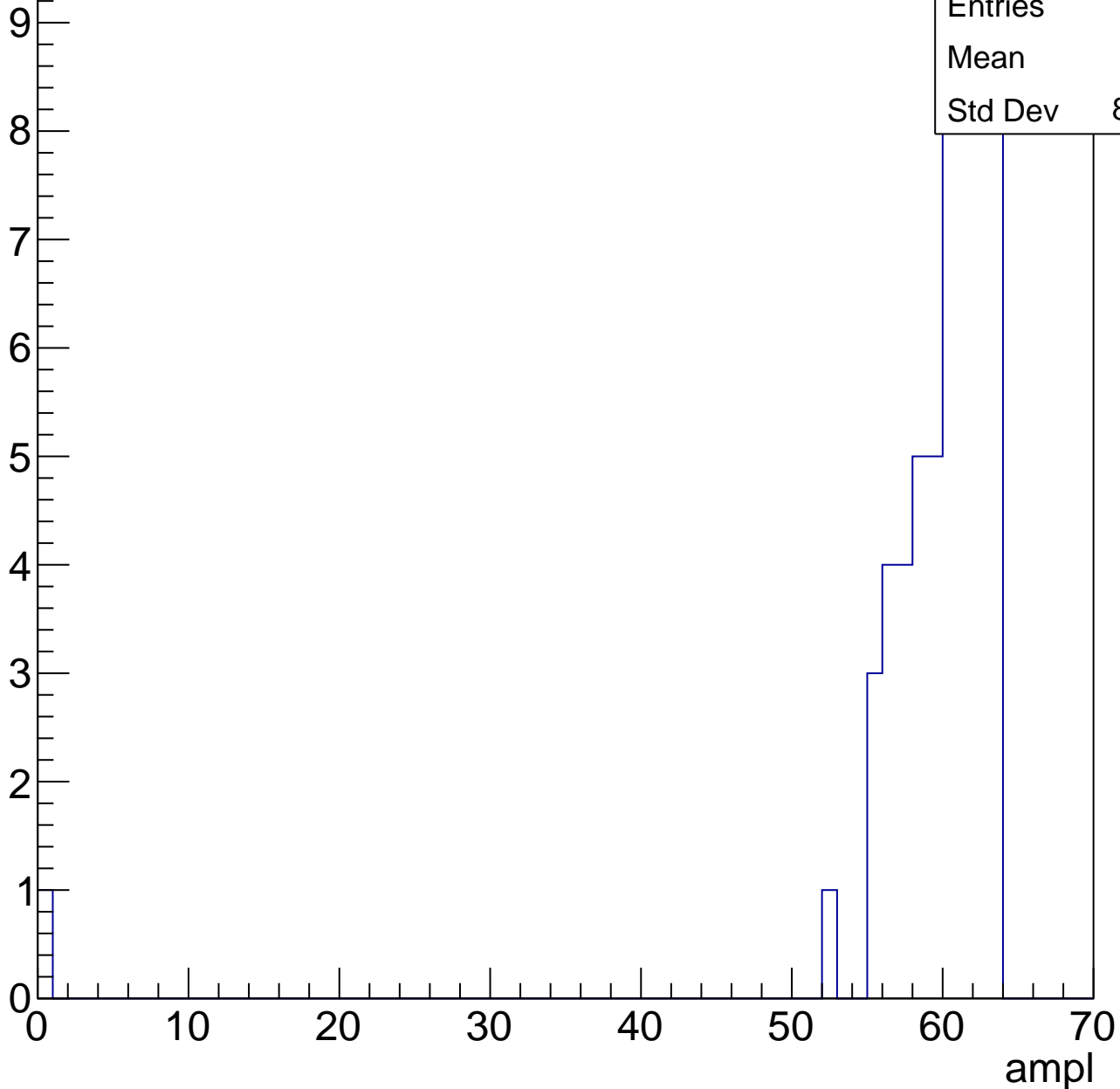


B1L101S, U21-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	58.7
Std Dev	8.261



B1L101S, U21-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch105, adc0

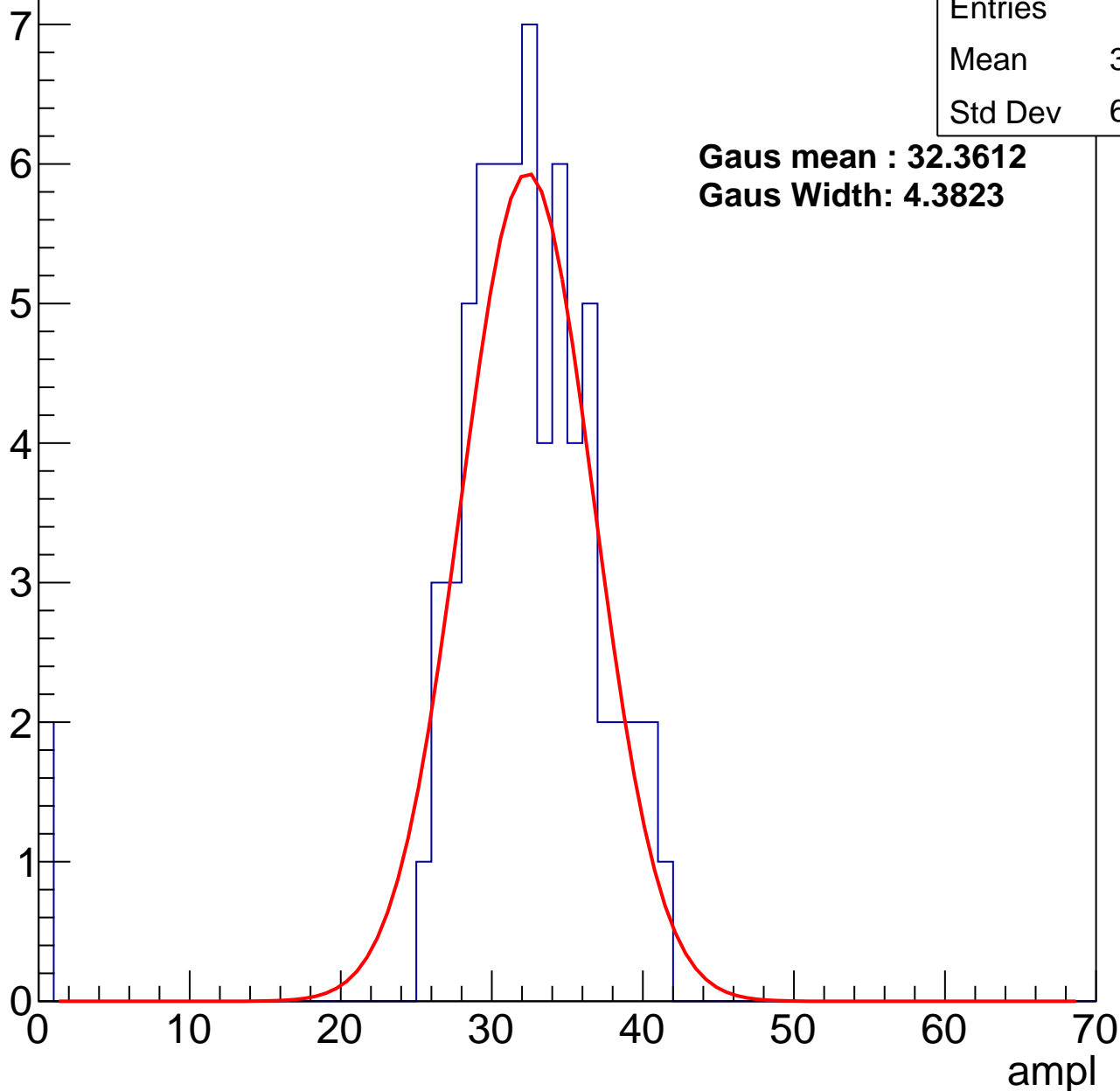
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	31.24
Std Dev	6.667

Gaus mean : 32.3612

Gaus Width: 4.3823



B1L101S, U21-ch105, adc1

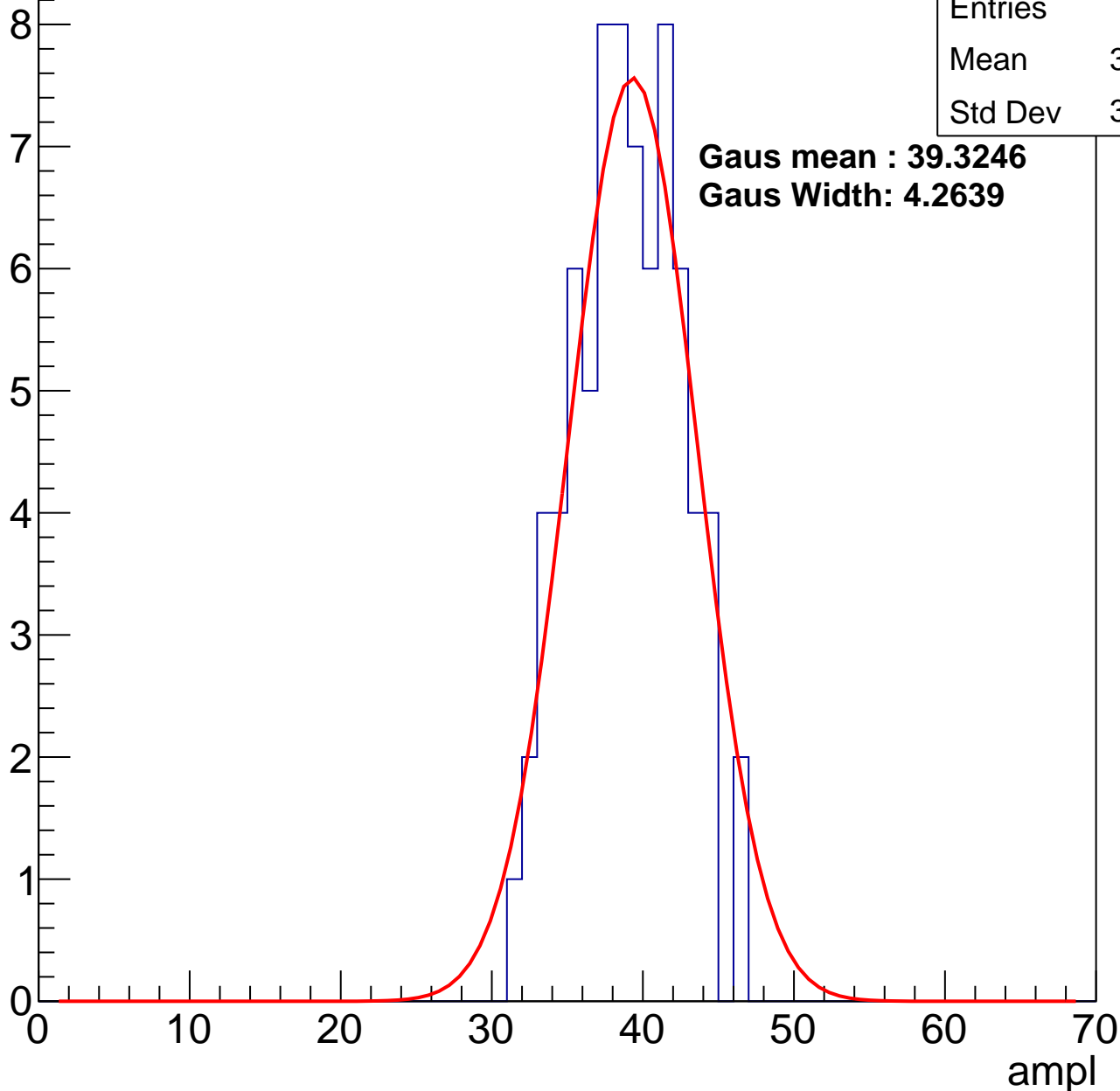
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	38.48
Std Dev	3.504

Gaus mean : 39.3246

Gaus Width: 4.2639



B1L101S, U21-ch105, adc2

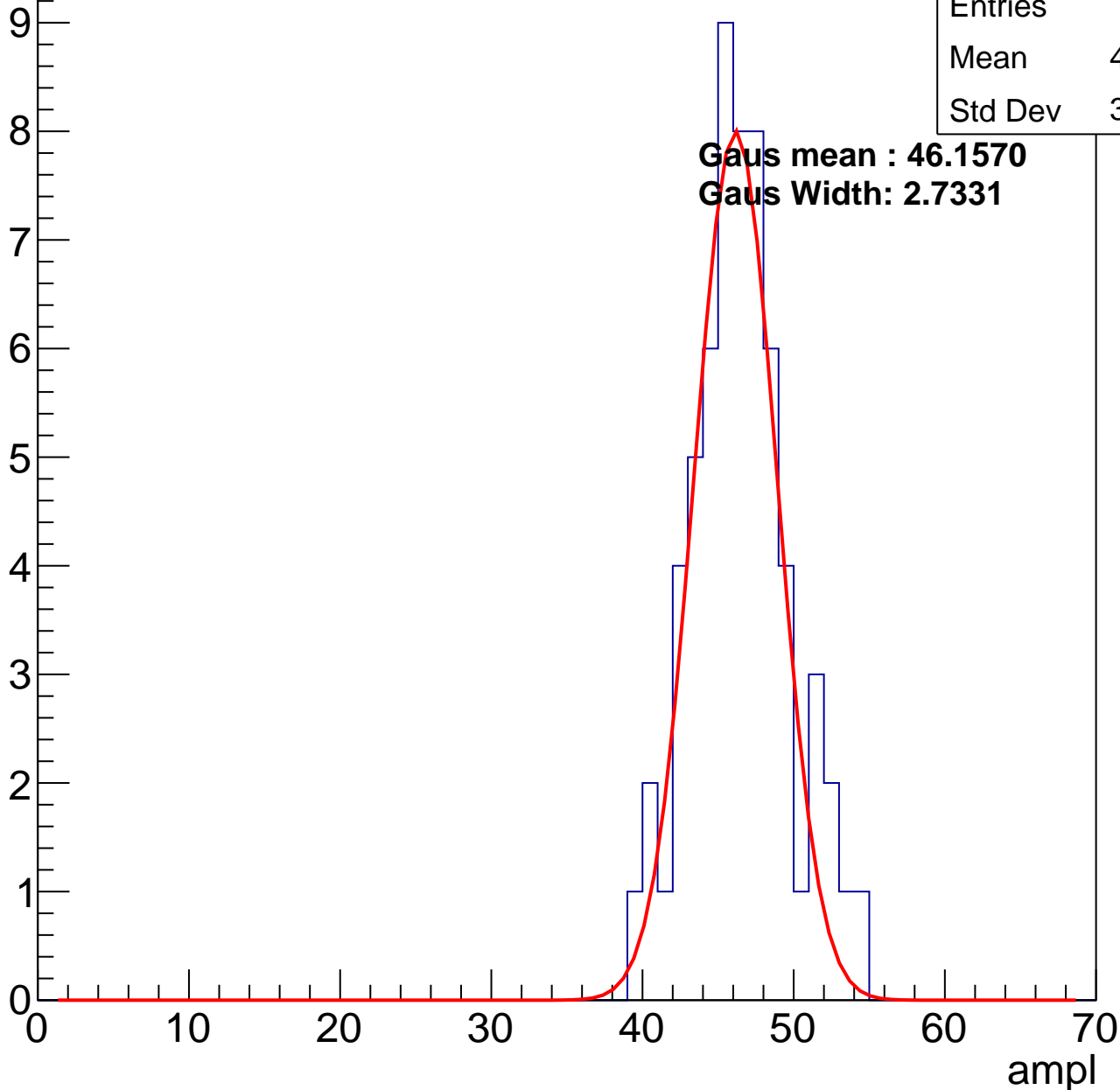
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	46.03
Std Dev	3.193

Gaus mean : 46.1570

Gaus Width: 2.7331

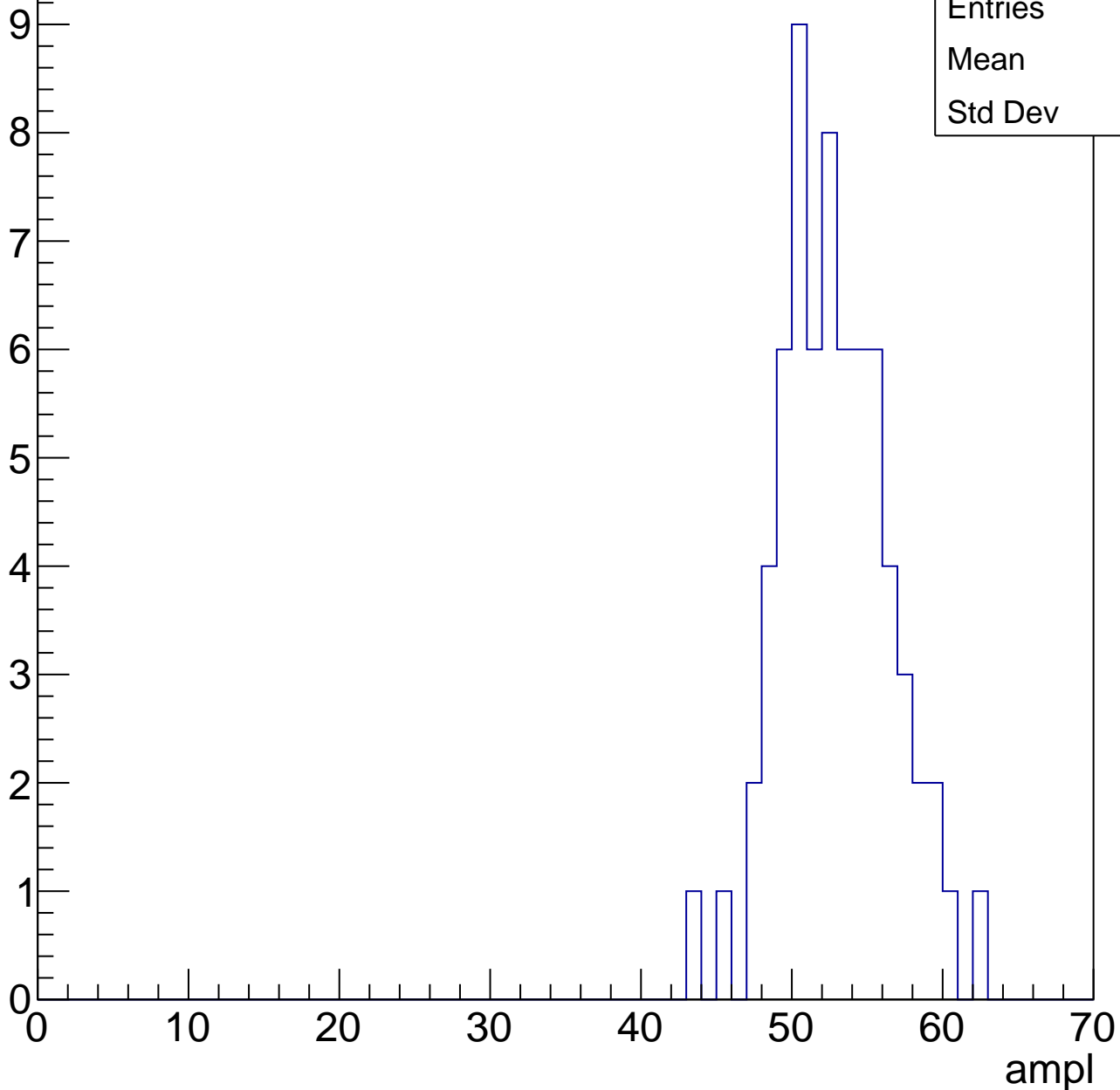


B1L101S, U21-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	52.4
Std Dev	3.61

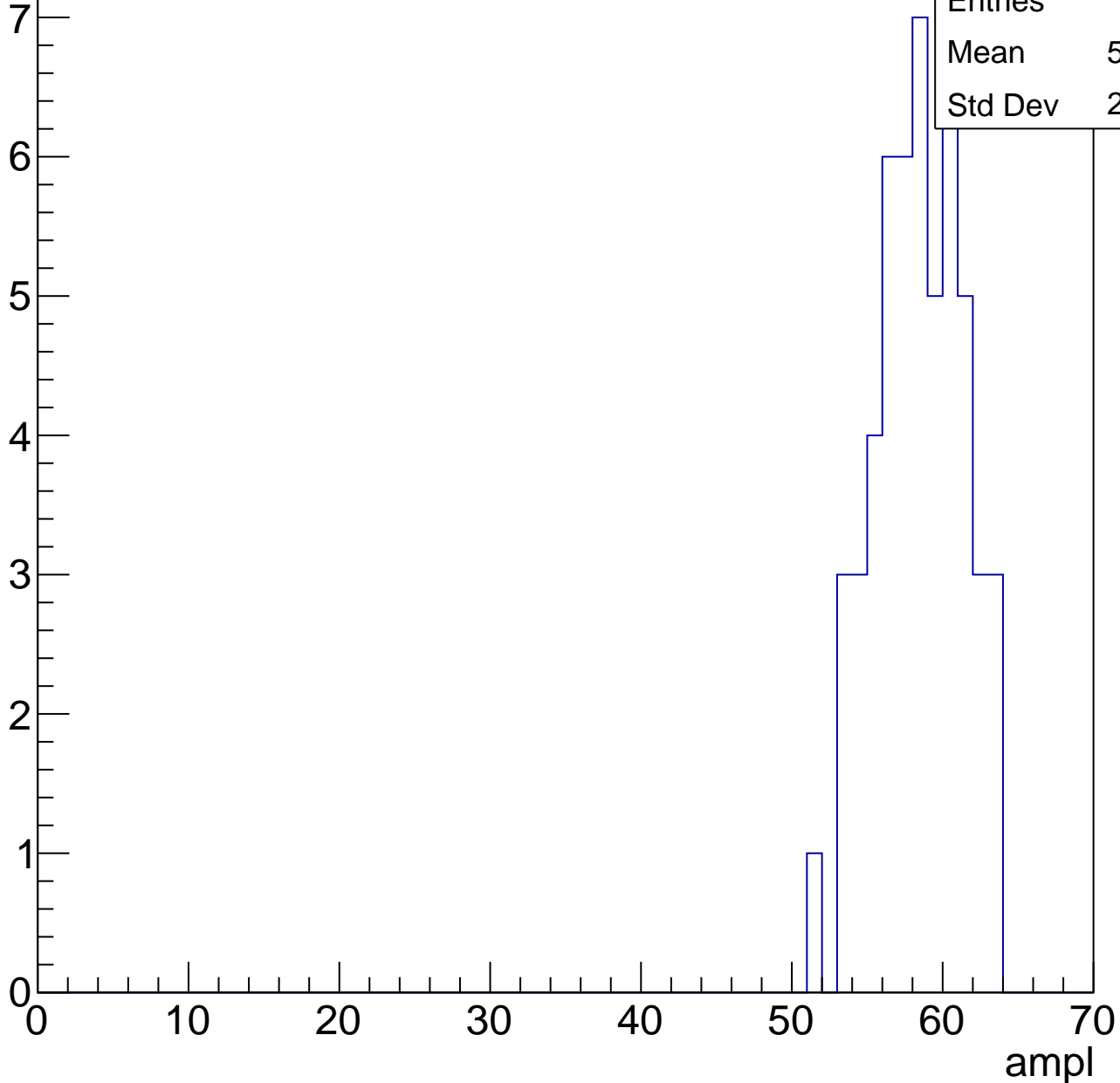


B1L101S, U21-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.94
Std Dev	2.877

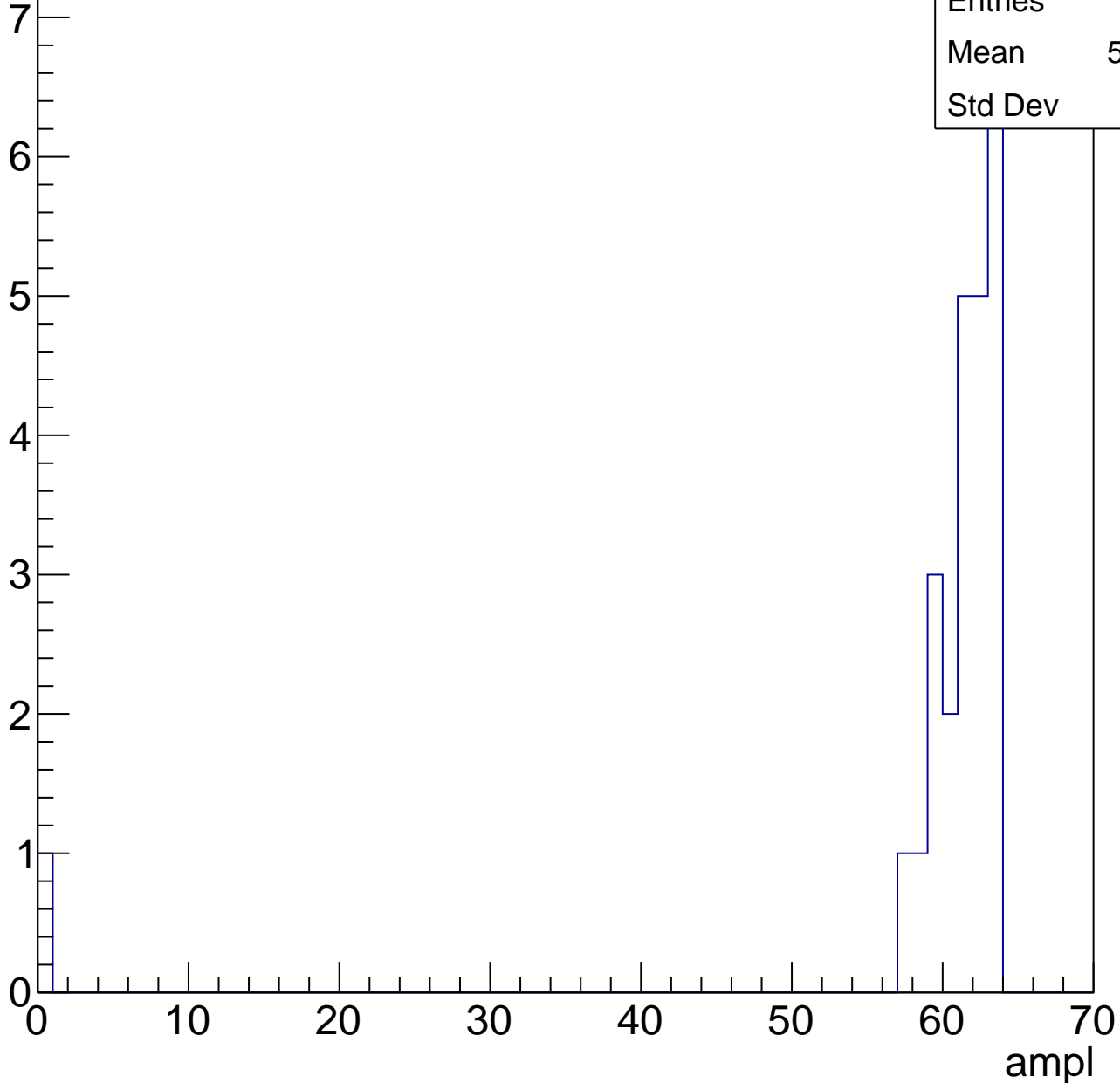


B1L101S, U21-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.72
Std Dev	12.1



B1L101S, U21-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U21-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch106, adc0

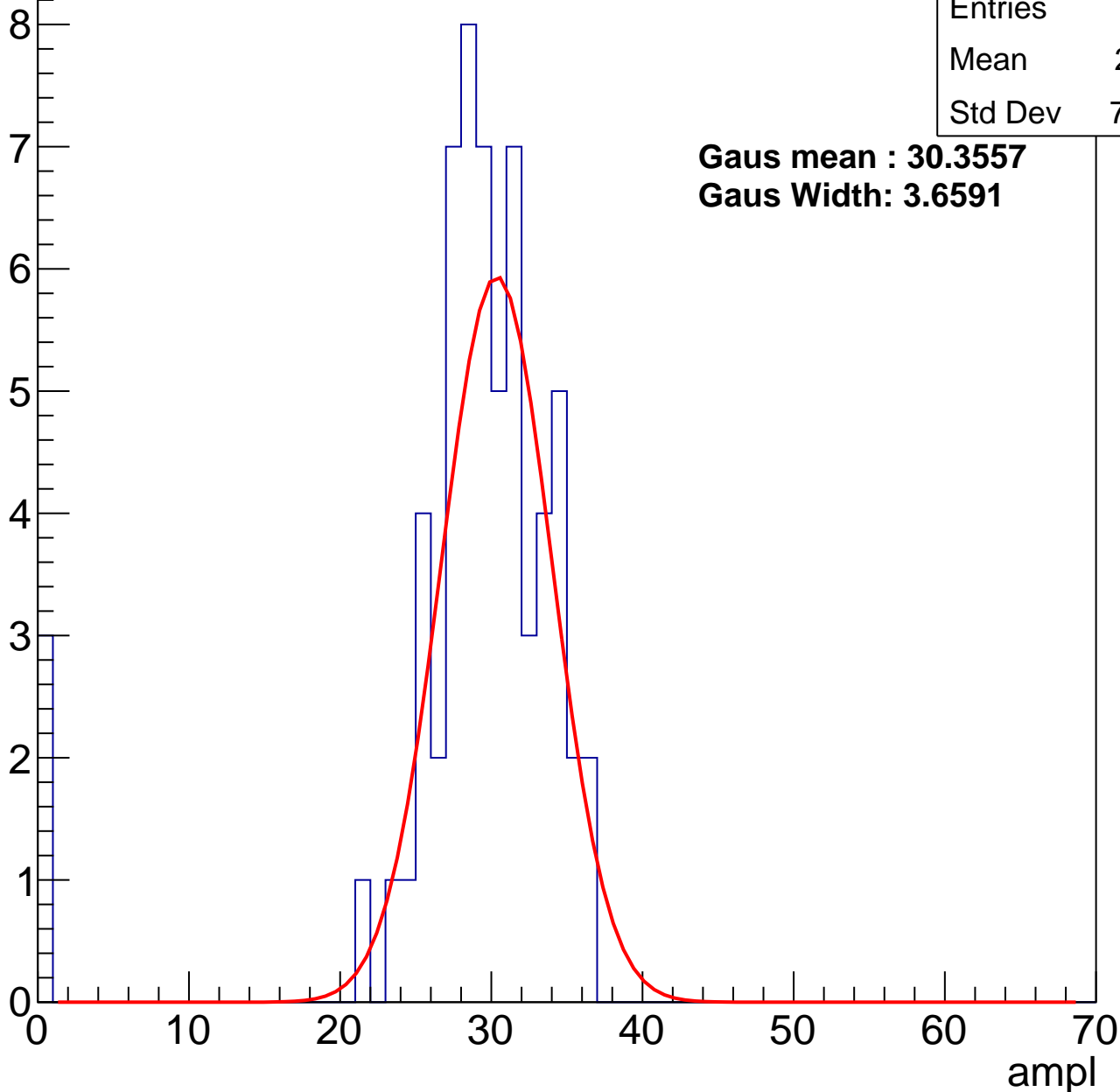
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.11
Std Dev	7.112

Gaus mean : 30.3557

Gaus Width: 3.6591



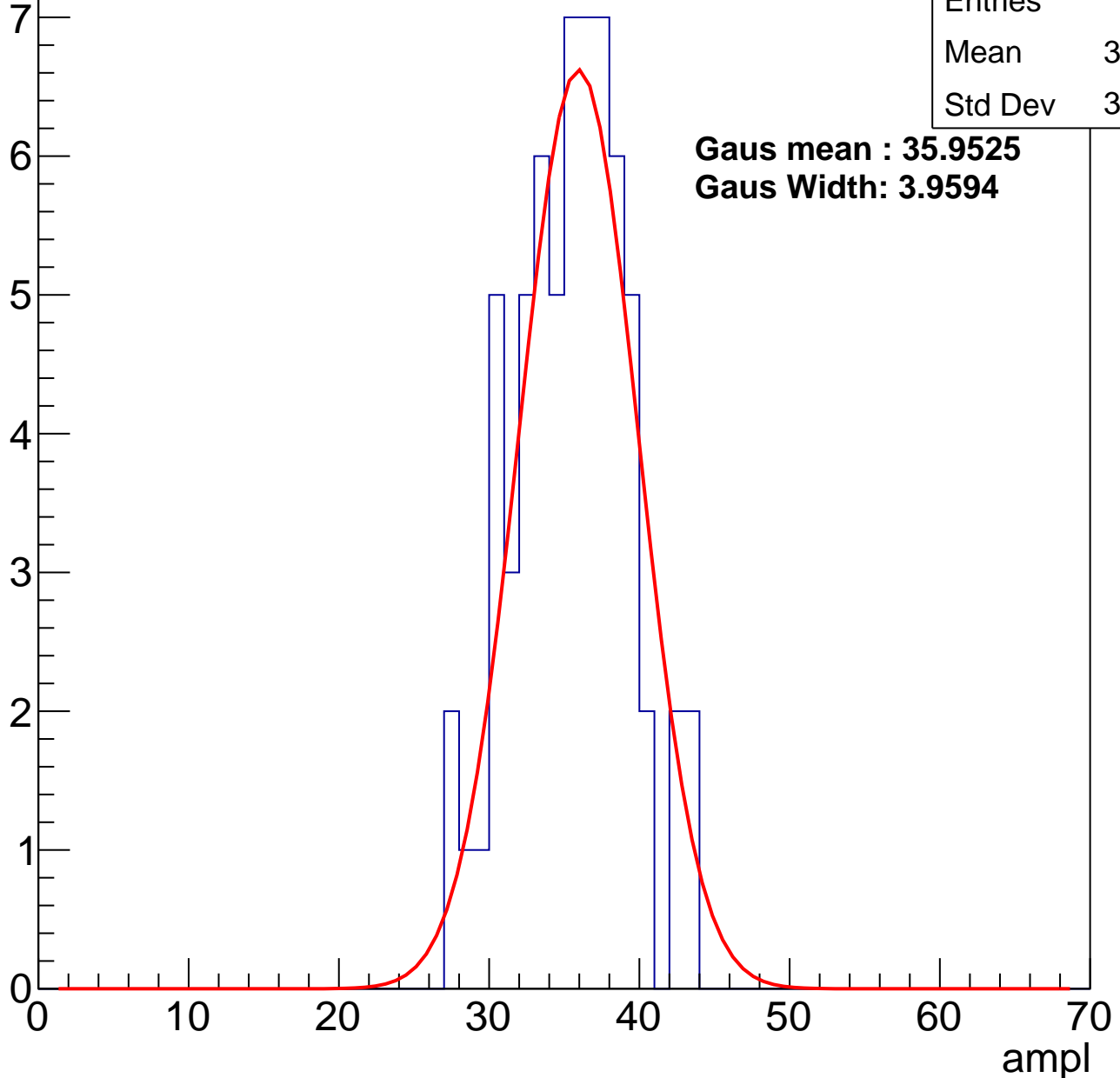
B1L101S, U21-ch106, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	35.02
Std Dev	3.703

Gaus mean : 35.9525
Gaus Width: 3.9594



B1L101S, U21-ch106, adc2

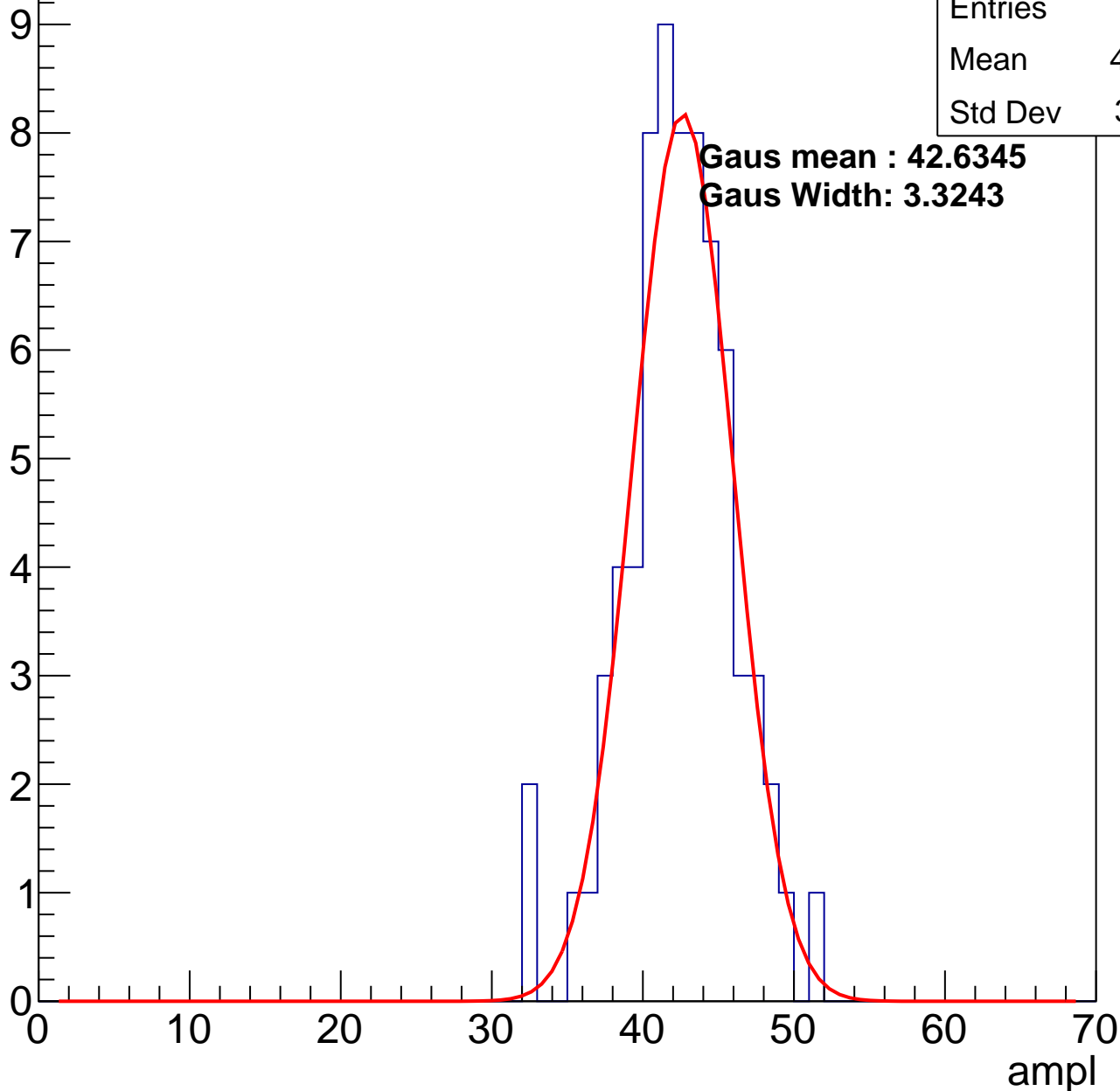
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	41.92
Std Dev	3.591

Gaus mean : 42.6345

Gaus Width: 3.3243

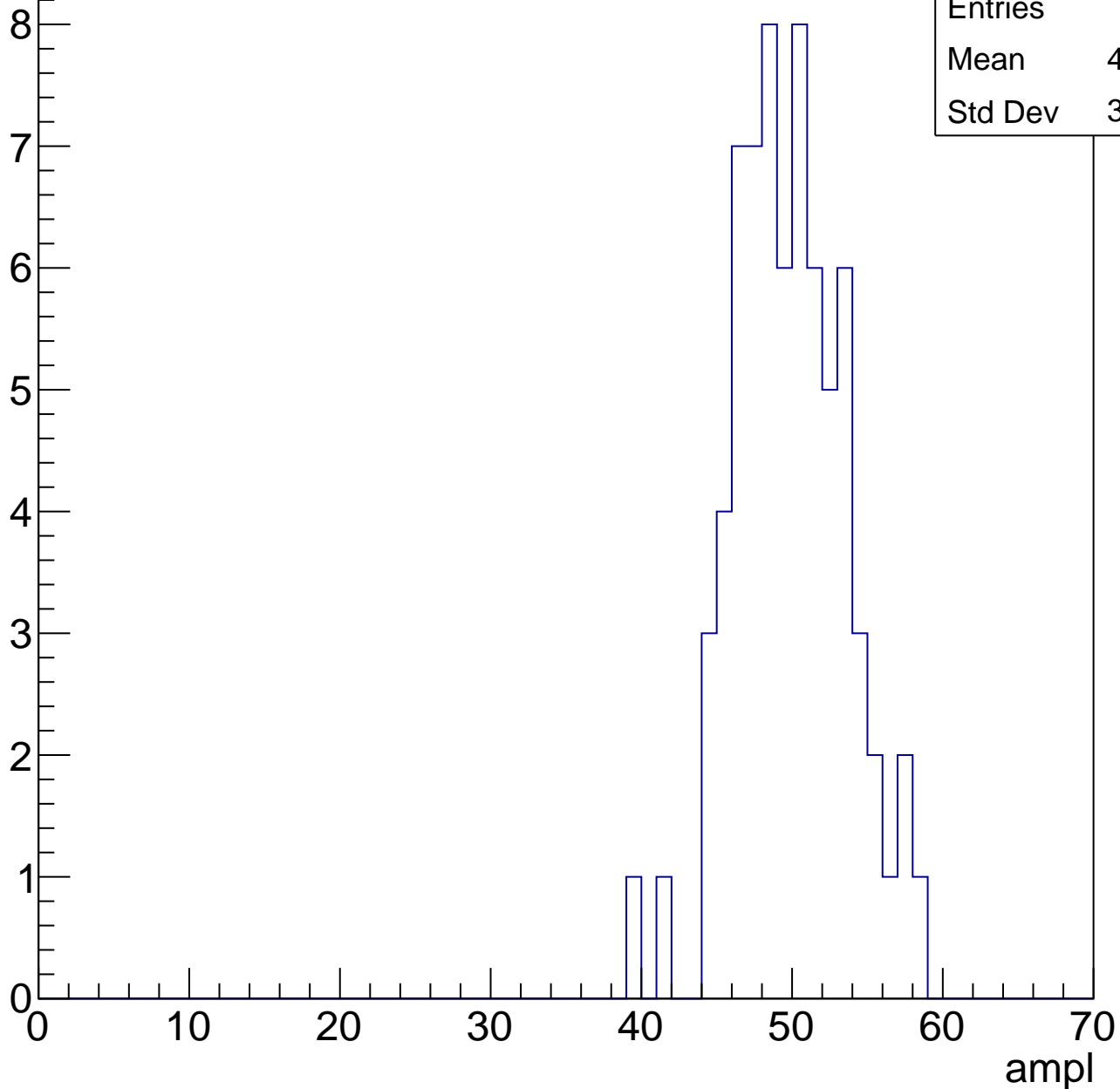


B1L101S, U21-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.37
Std Dev	3.697

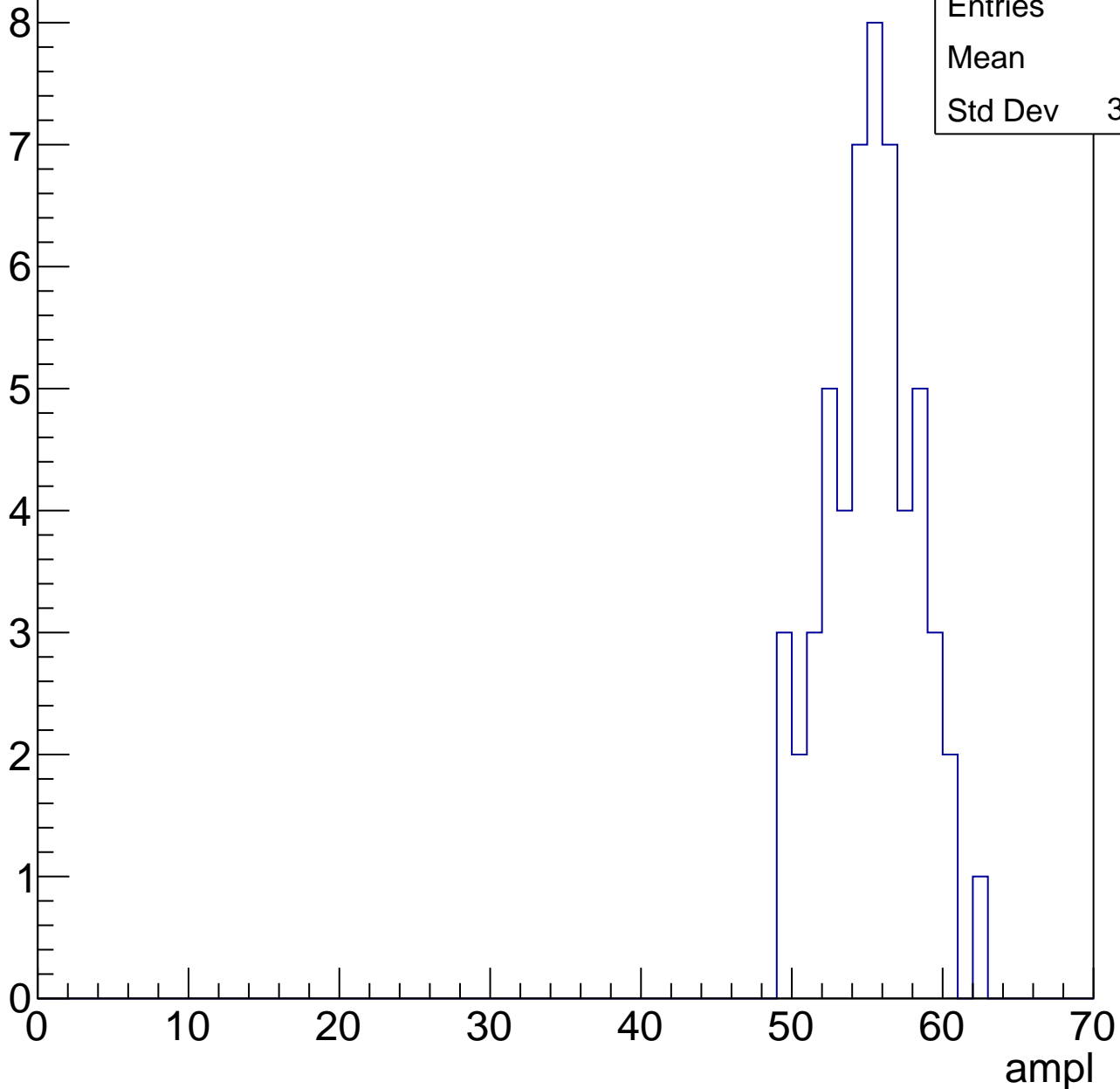


B1L101S, U21-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	54.8
Std Dev	3.002

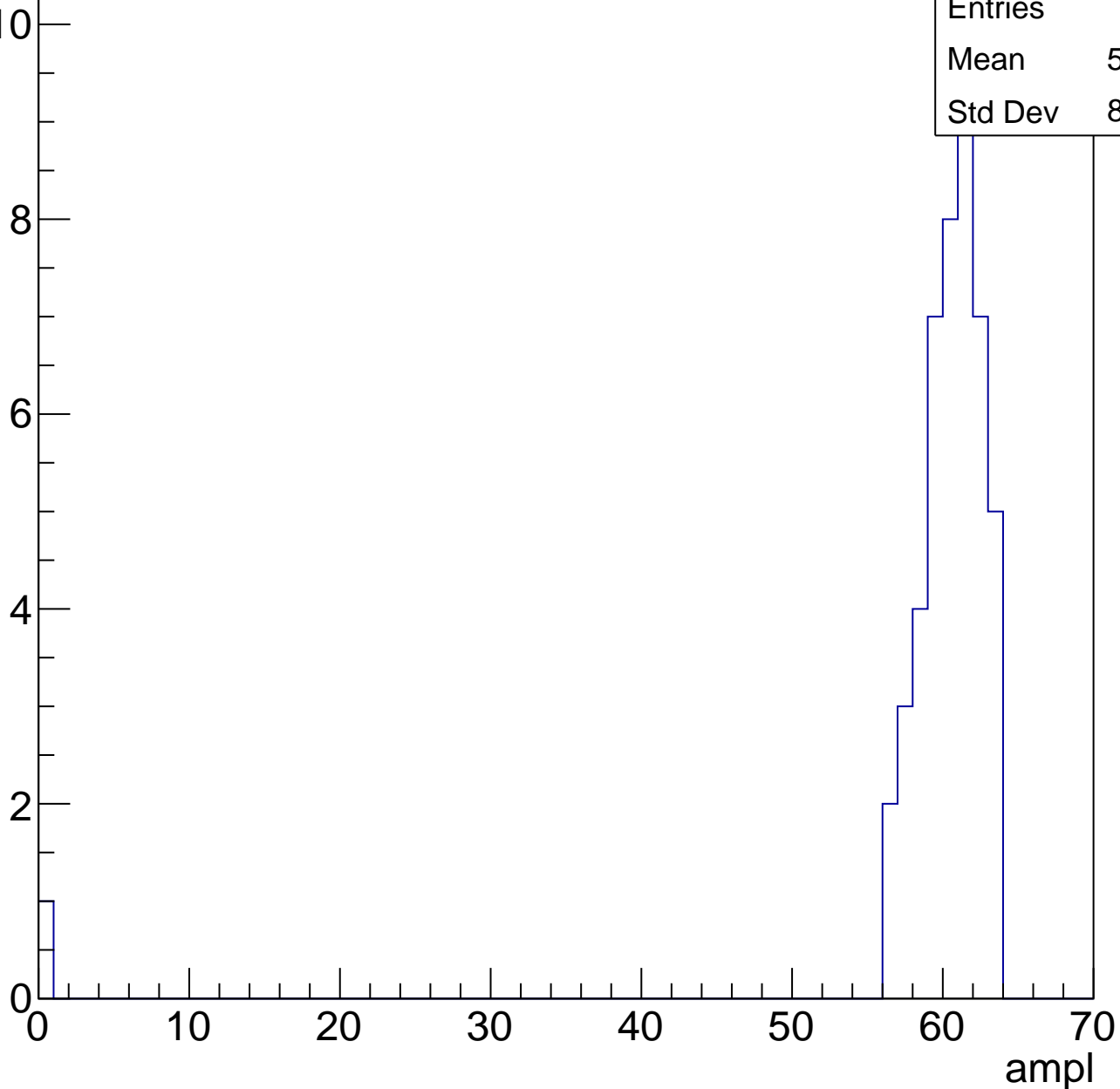


B1L101S, U21-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.87
Std Dev	8.879

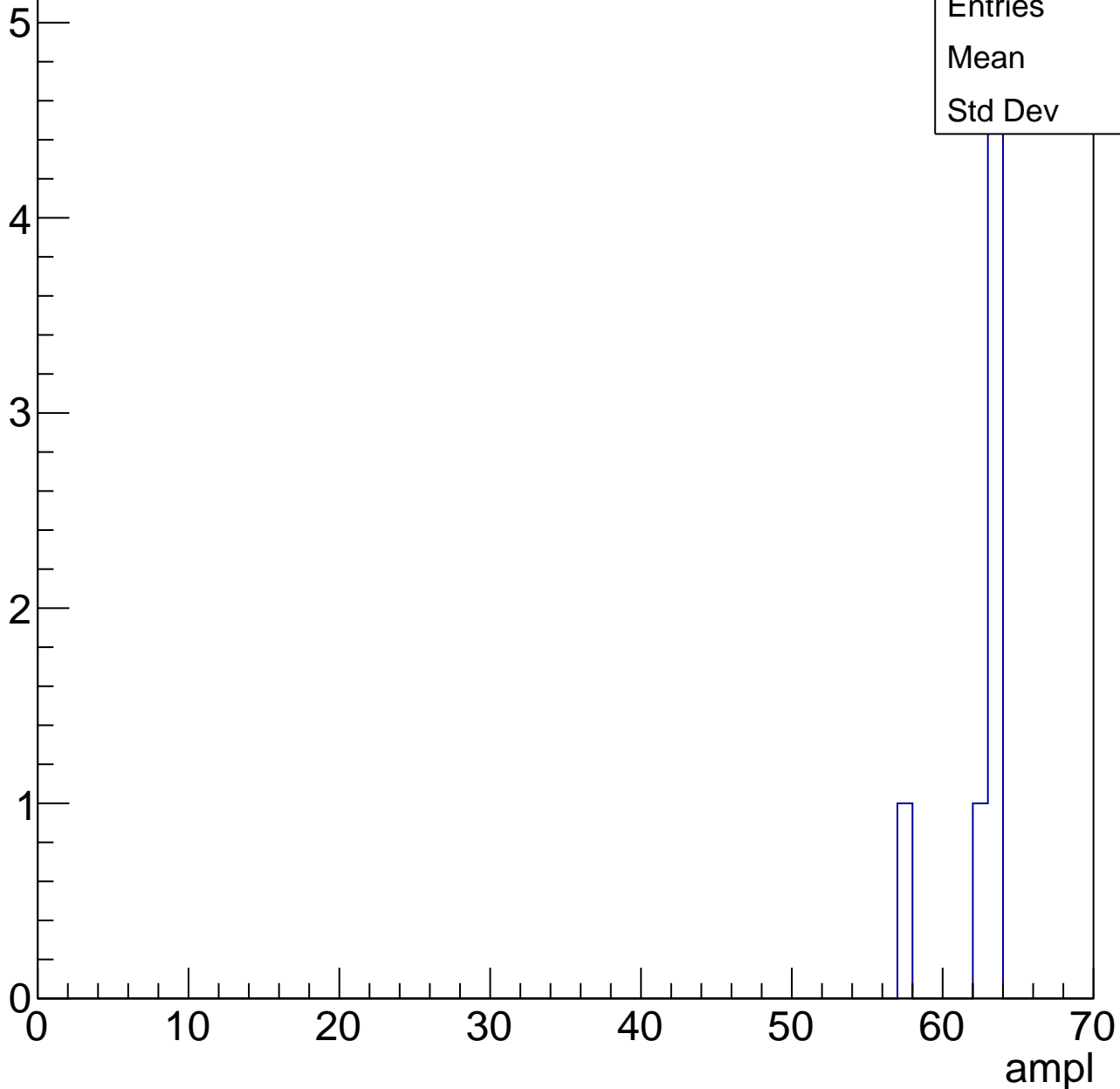


B1L101S, U21-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	7
Mean	62
Std Dev	2.07



B1L101S, U21-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch107, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	79
Mean	31.03
Std Dev	4.894

Gaus mean : 32.2000

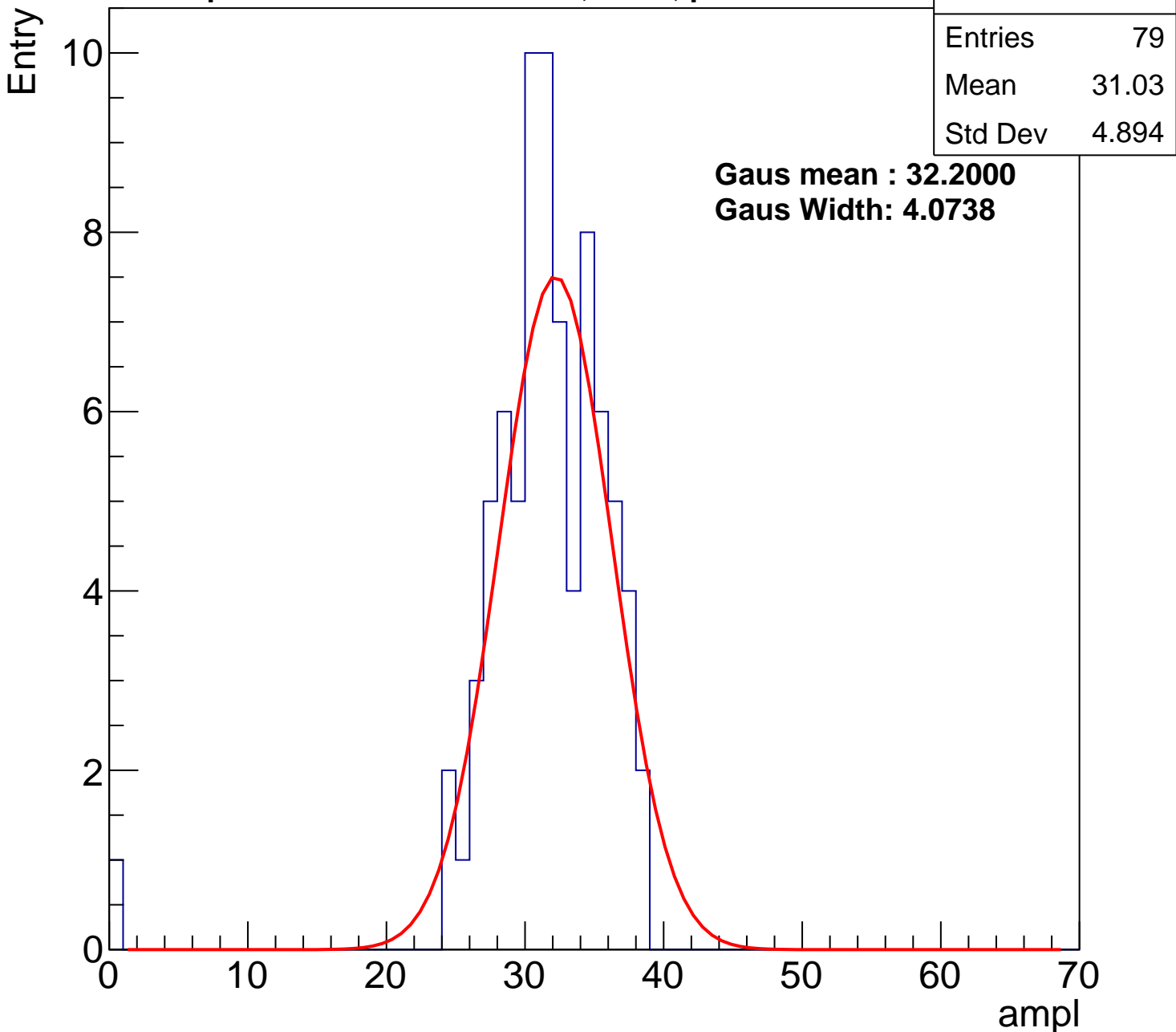
Gaus Width: 4.0738

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch107, adc1

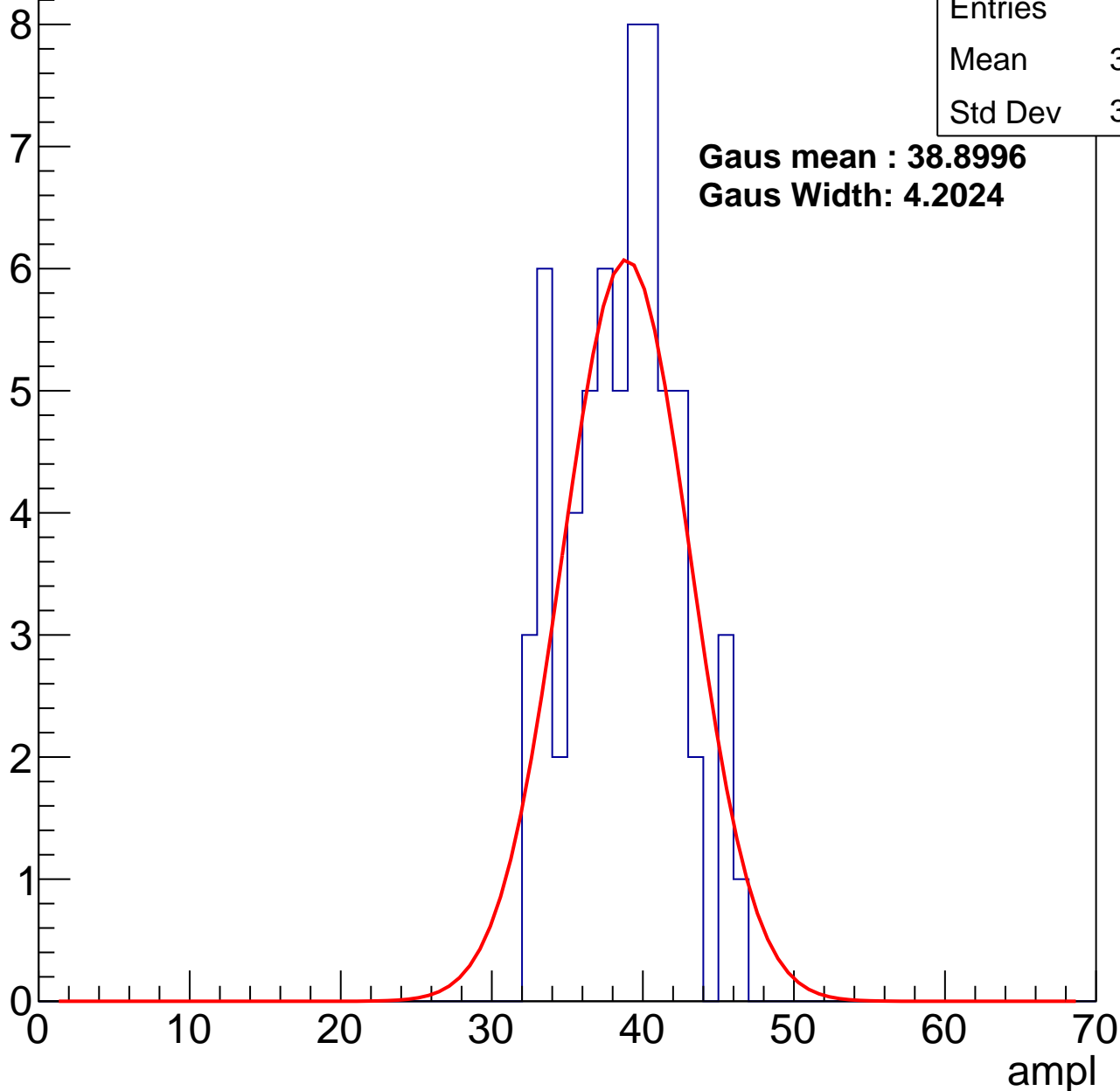
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	38.22
Std Dev	3.507

Gaus mean : 38.8996

Gaus Width: 4.2024



B1L101S, U21-ch107, adc2

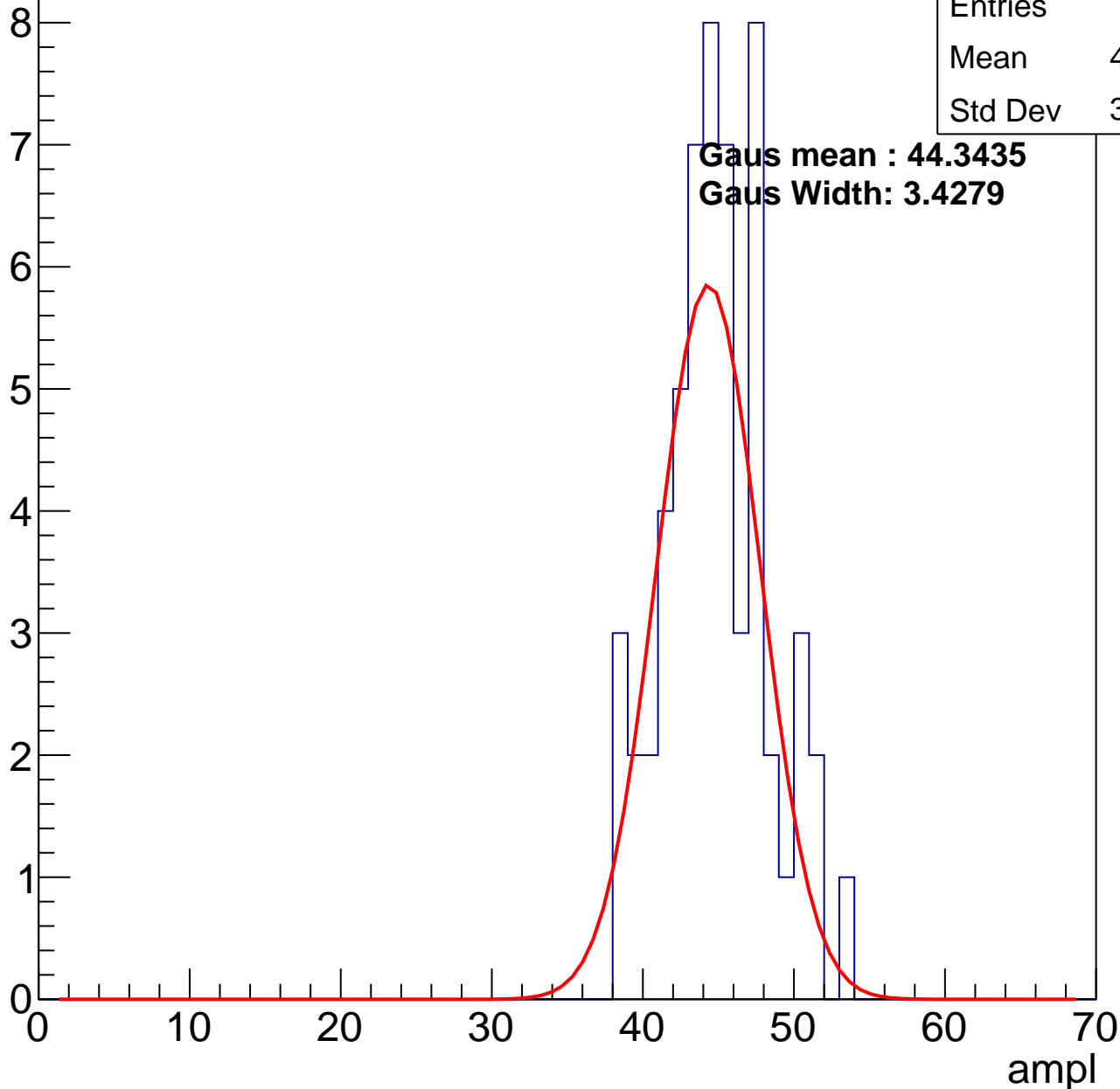
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.45
Std Dev	3.415

Gaus mean : 44.3435

Gaus Width: 3.4279

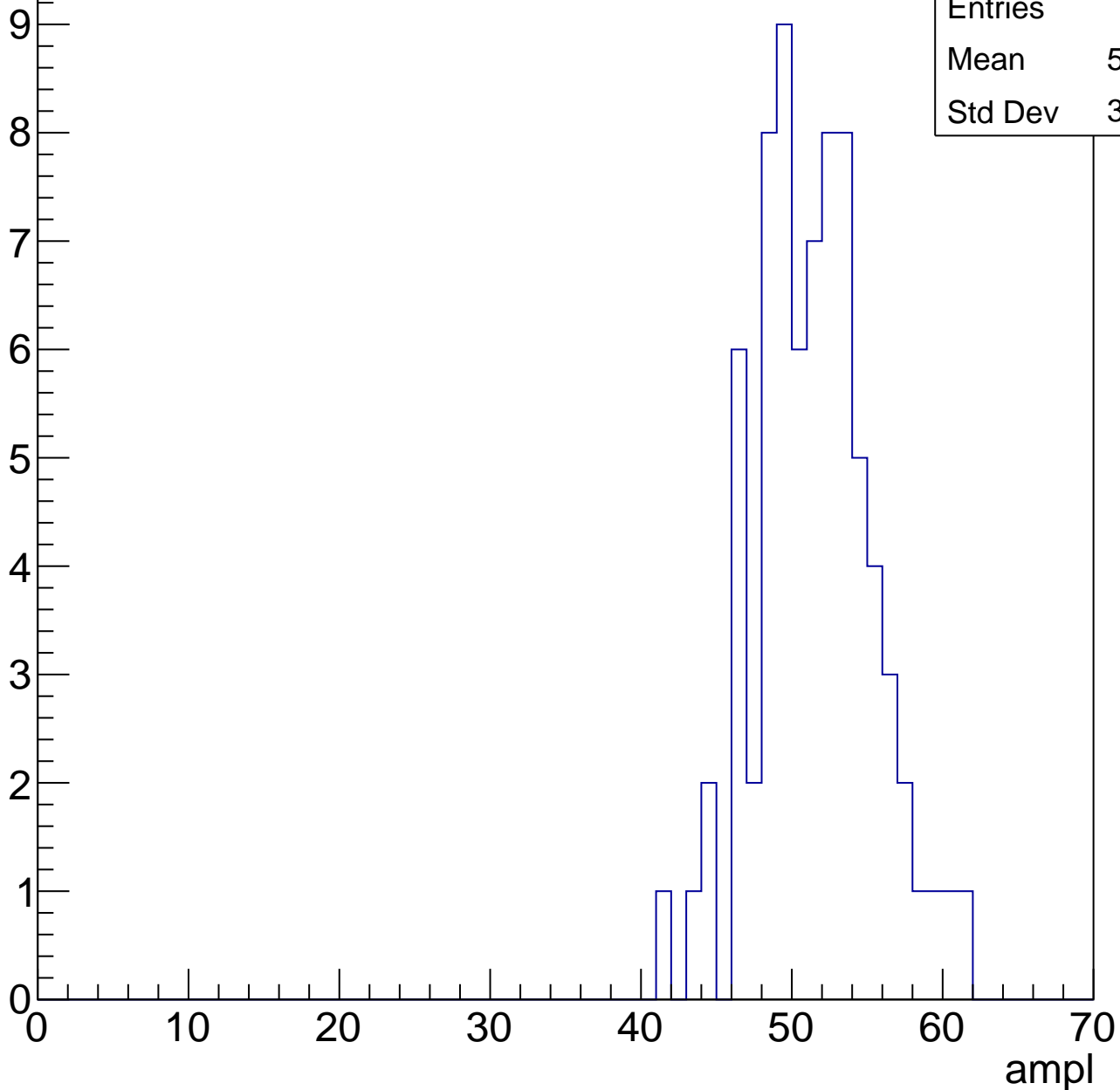


B1L101S, U21-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	50.97
Std Dev	3.886

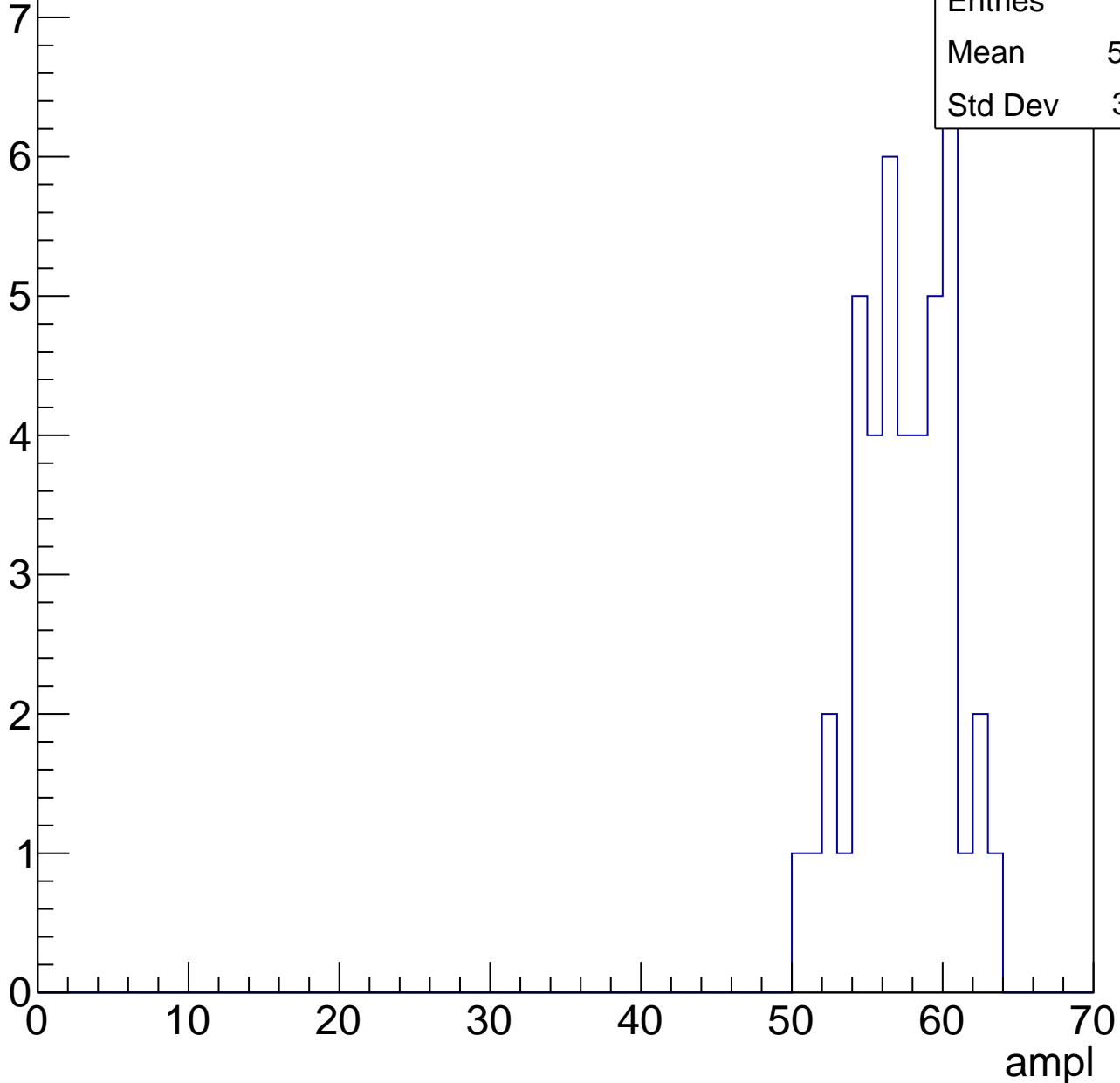


B1L101S, U21-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	56.98
Std Dev	3.041

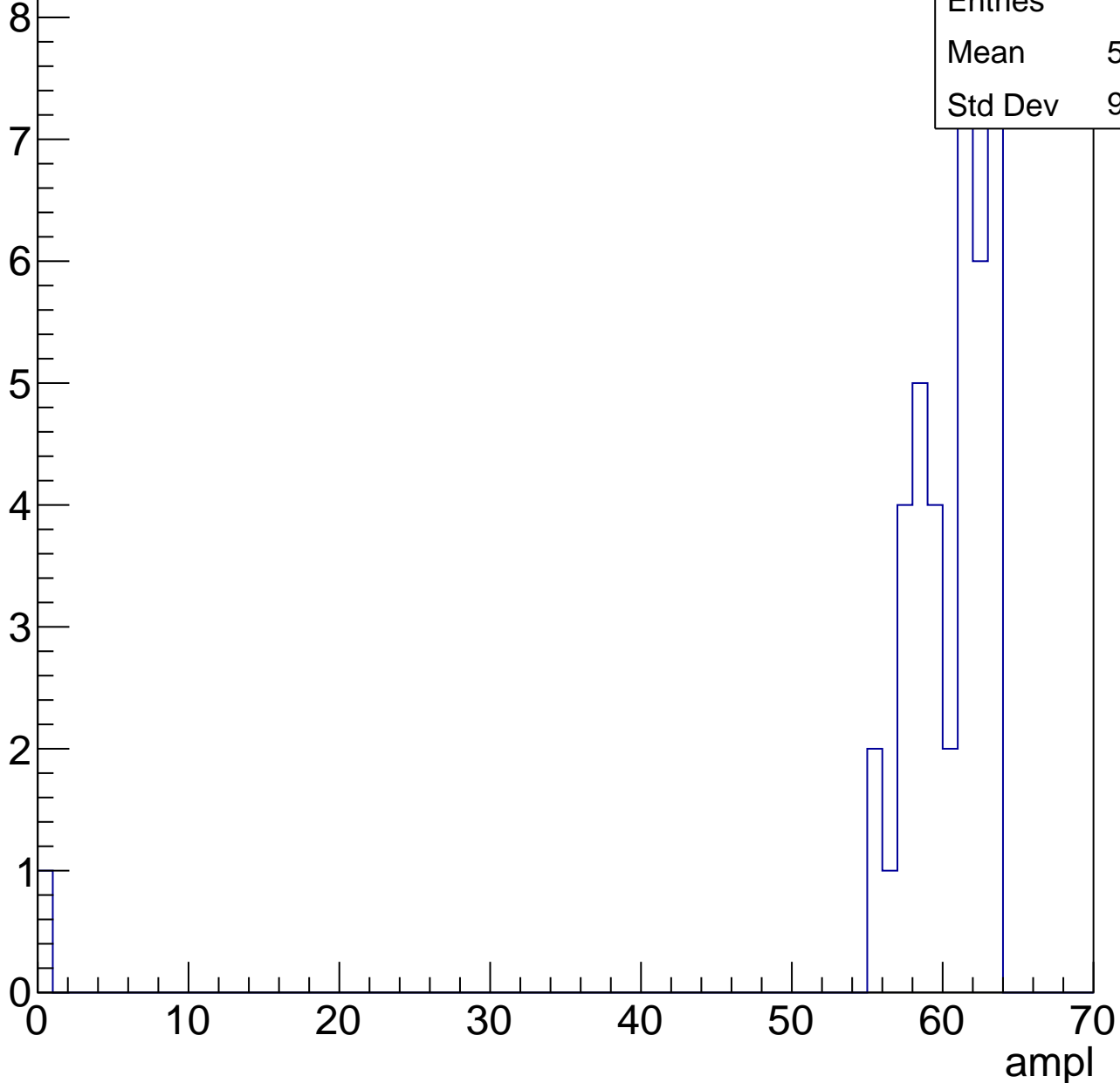


B1L101S, U21-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.63
Std Dev	9.568



B1L101S, U21-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B1L101S, U21-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch108, adc0

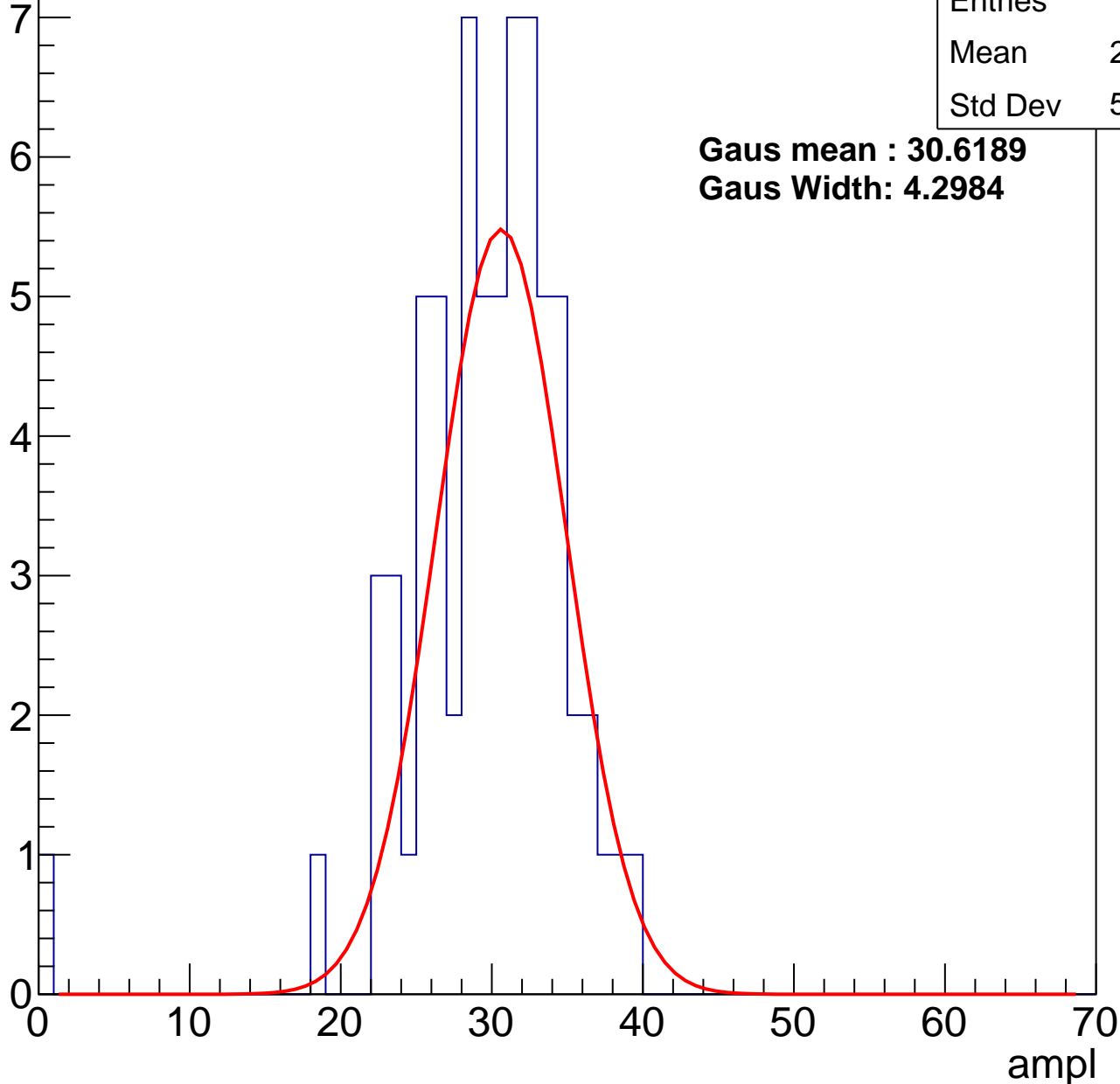
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.12
Std Dev	5.513

Gaus mean : 30.6189

Gaus Width: 4.2984



B1L101S, U21-ch108, adc1

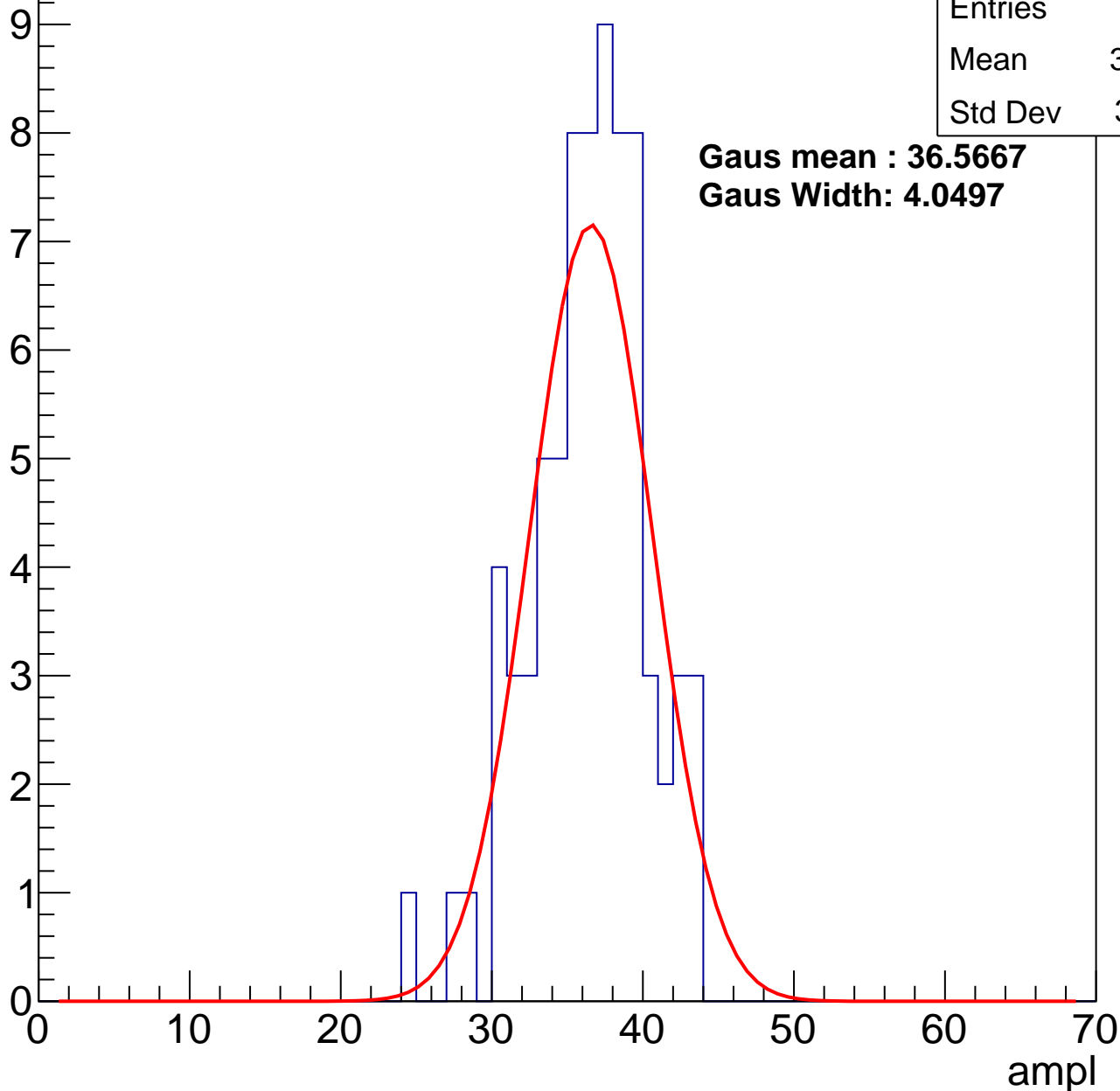
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	35.96
Std Dev	3.821

Gaus mean : 36.5667

Gaus Width: 4.0497



B1L101S, U21-ch108, adc2

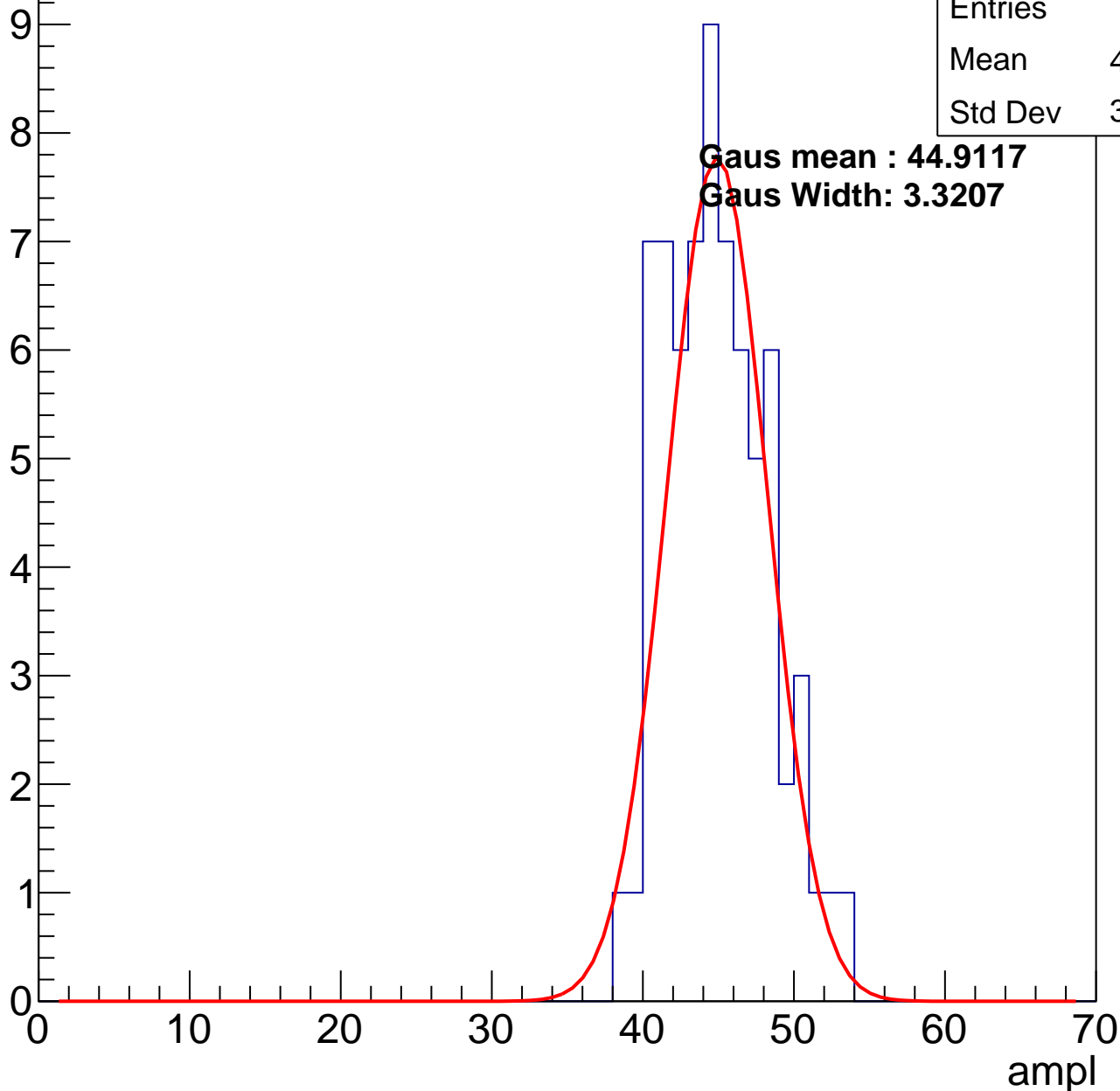
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	44.44
Std Dev	3.332

Gaus mean : 44.9117

Gaus Width: 3.3207

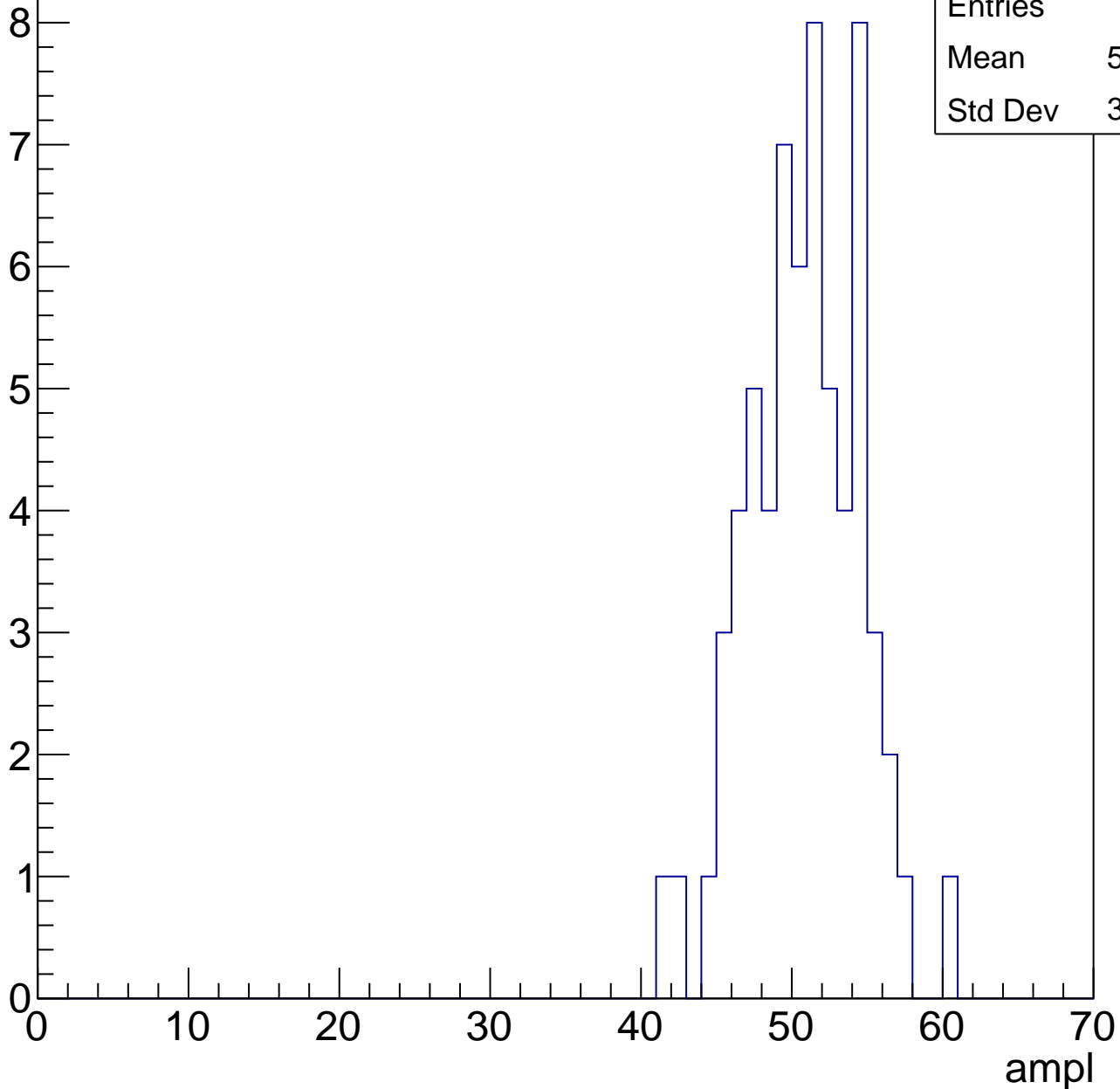


B1L101S, U21-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.34
Std Dev	3.684

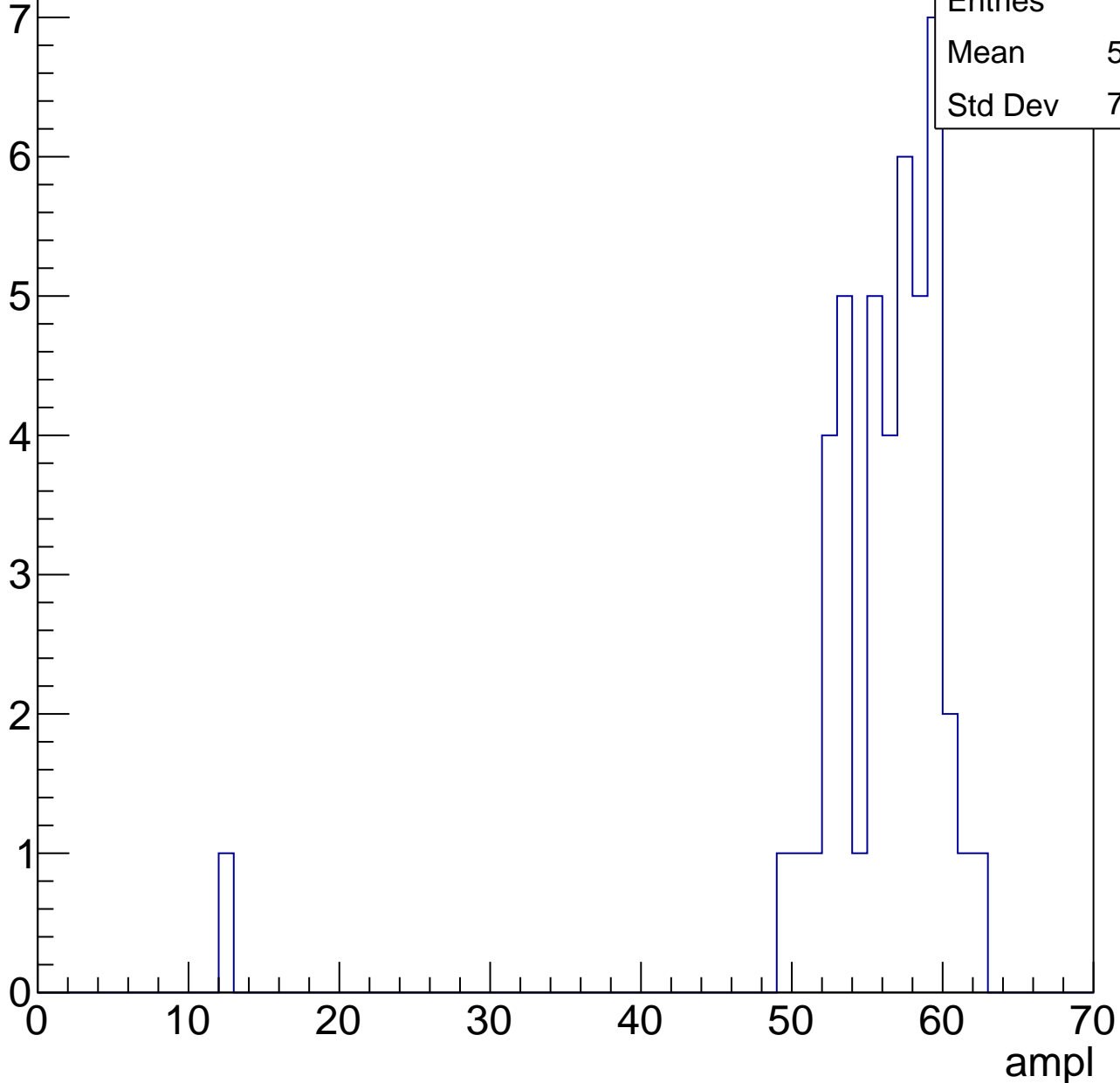


B1L101S, U21-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	55.02
Std Dev	7.154

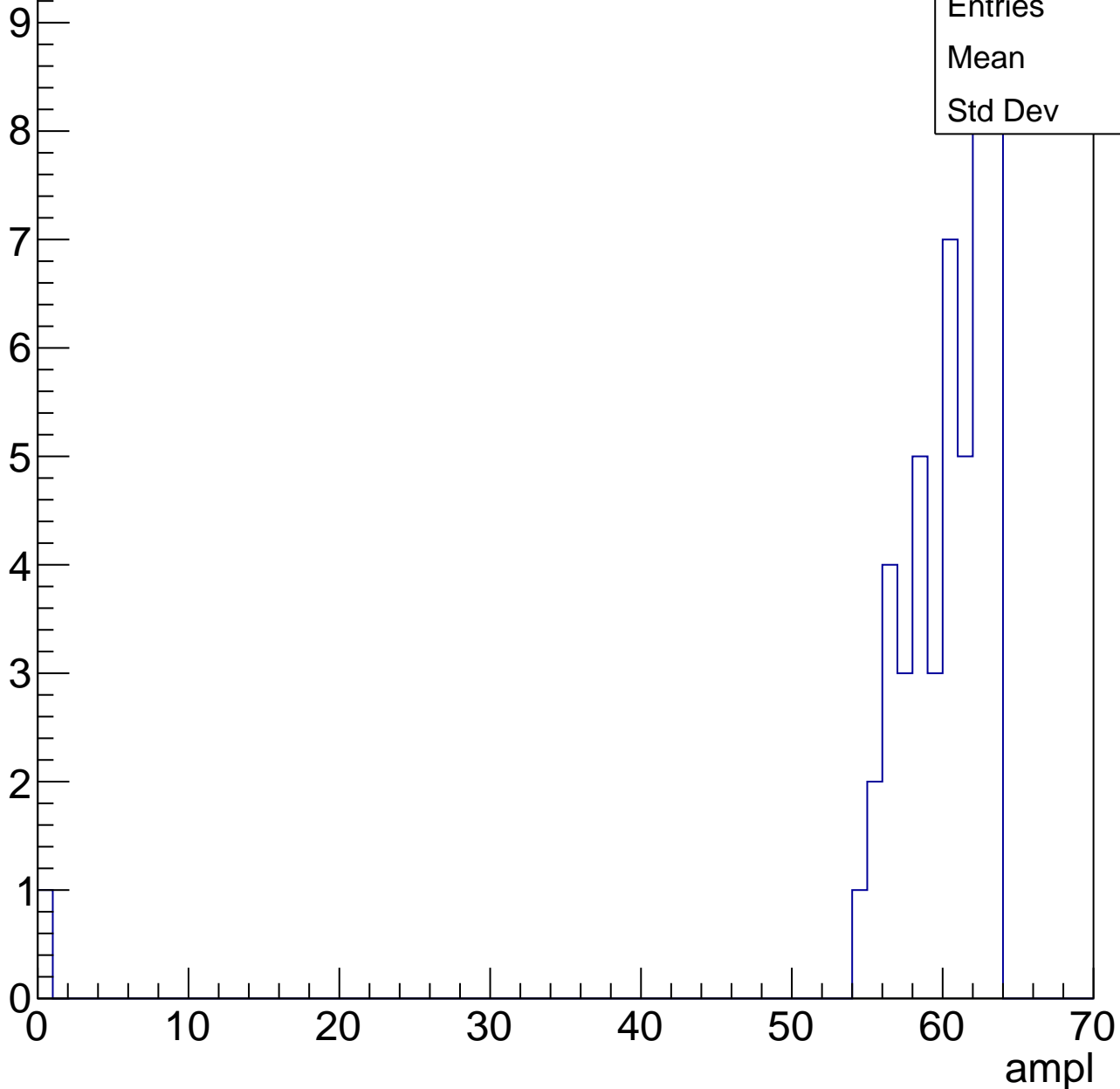


B1L101S, U21-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

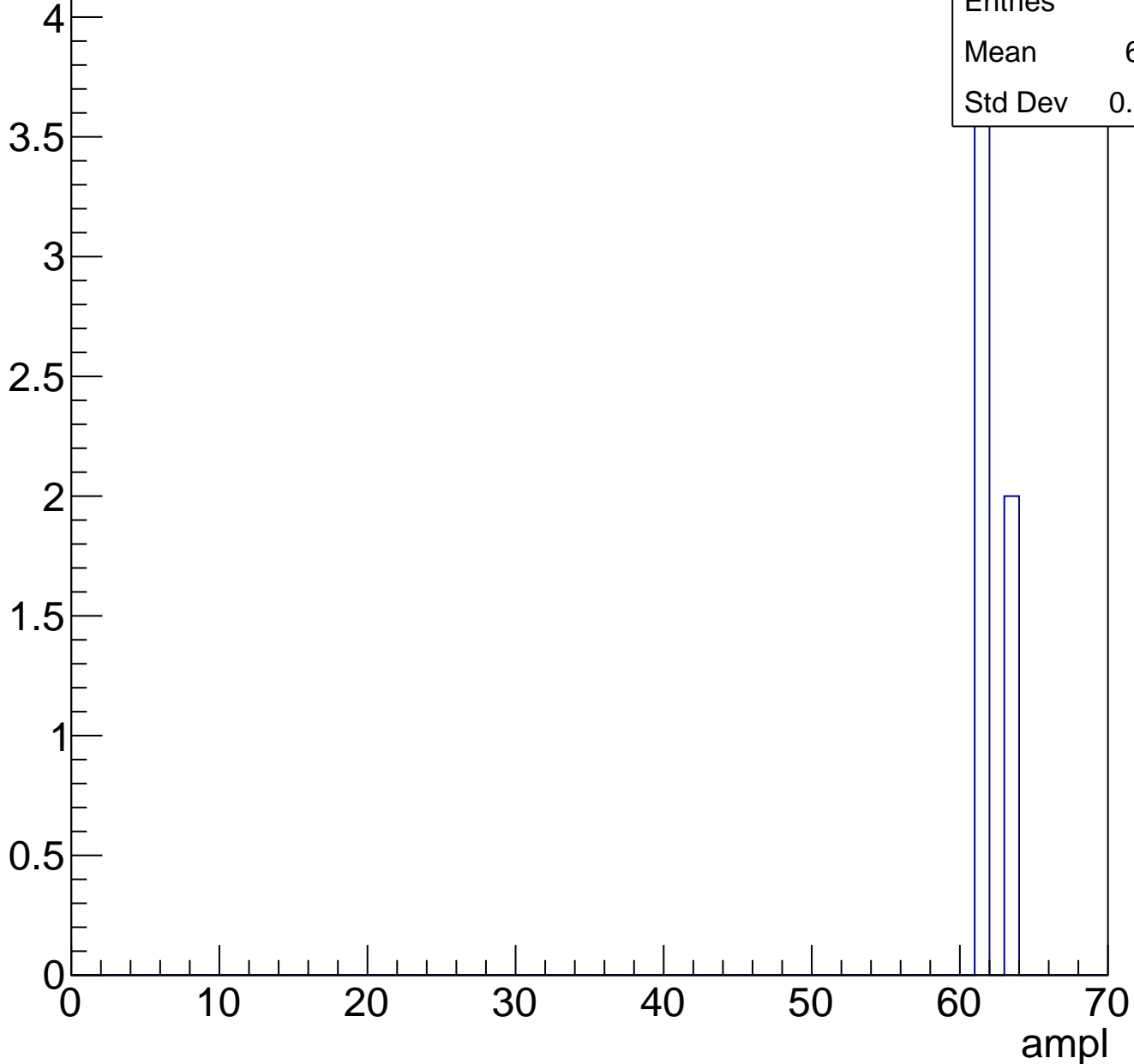
Entries	48
Mean	58.6
Std Dev	8.92



B1L101S, U21-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch109, adc0

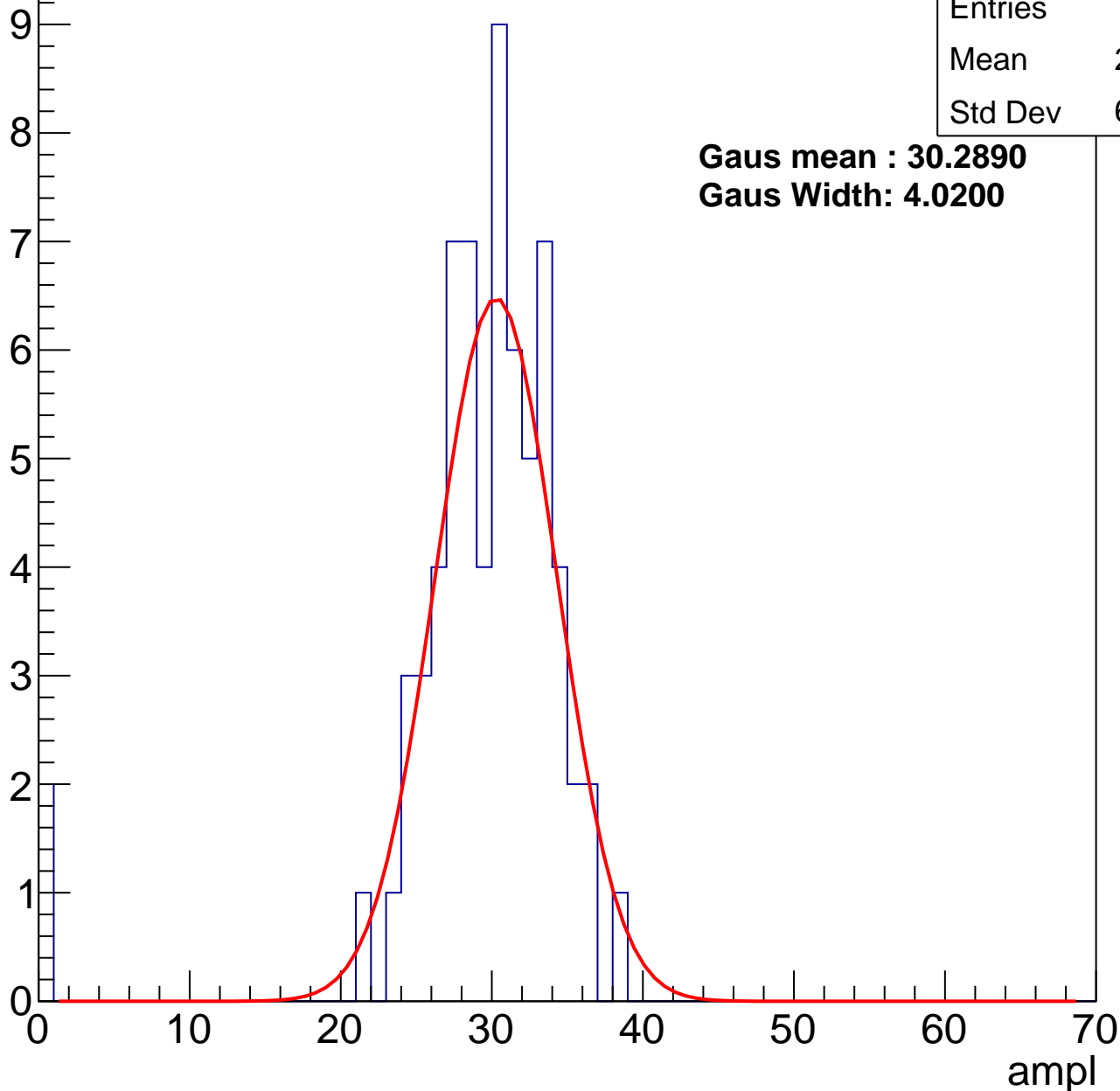
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.81
Std Dev	6.081

Gaus mean : 30.2890

Gaus Width: 4.0200



B1L101S, U21-ch109, adc1

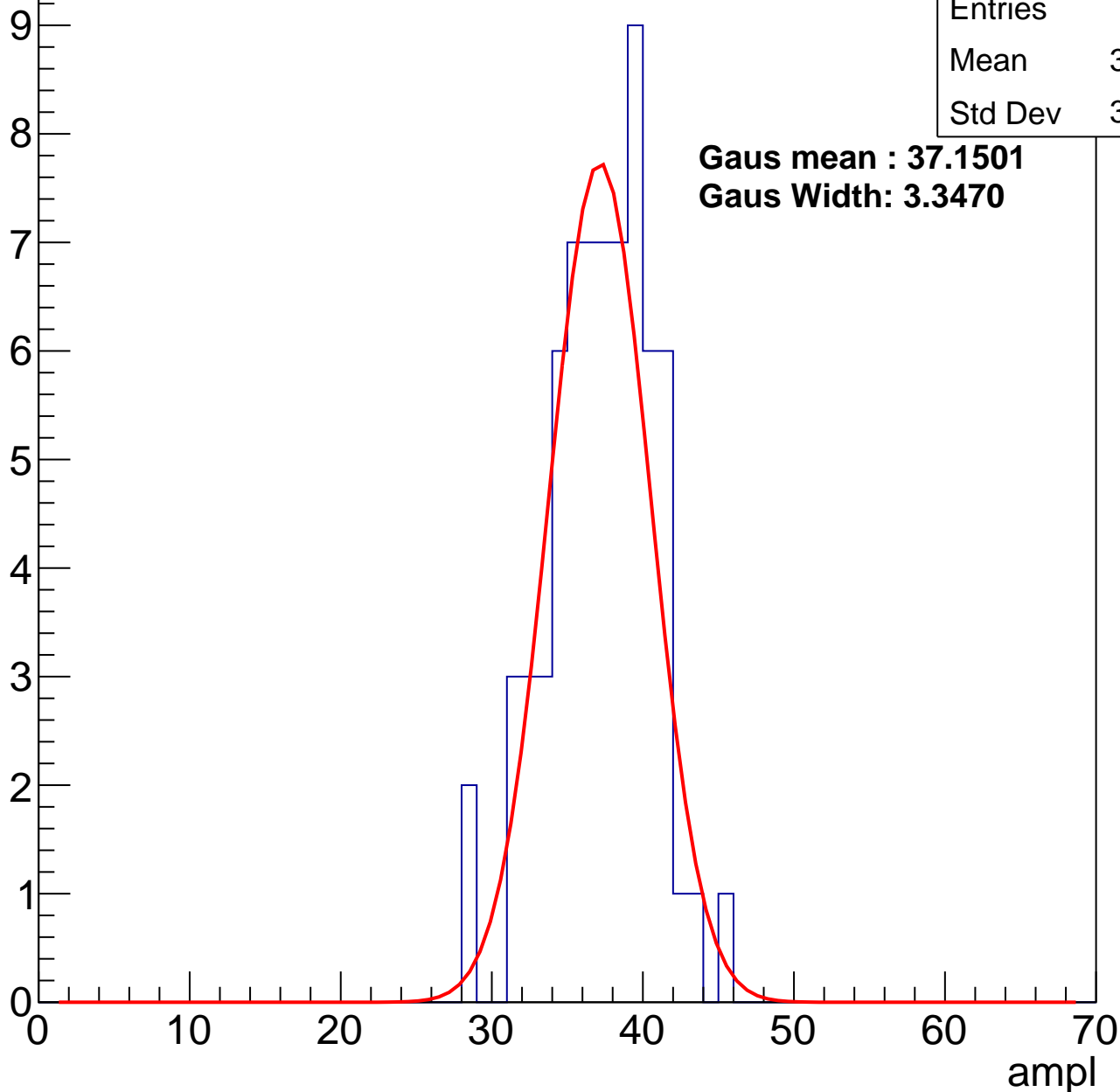
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.77
Std Dev	3.397

Gaus mean : 37.1501

Gaus Width: 3.3470



B1L101S, U21-ch109, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	43.31
Std Dev	3.335

Gaus mean : 43.7215

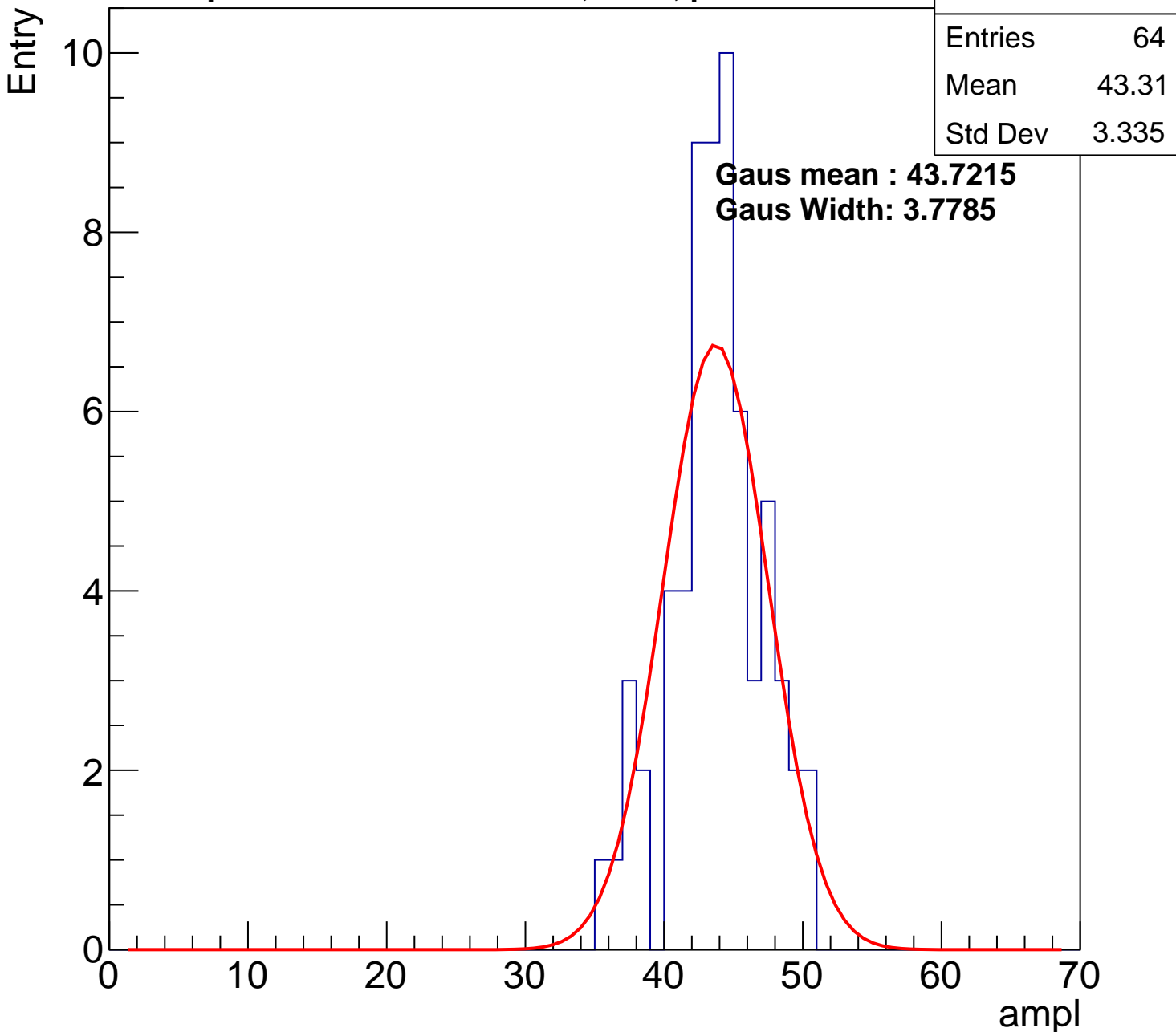
Gaus Width: 3.7785

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

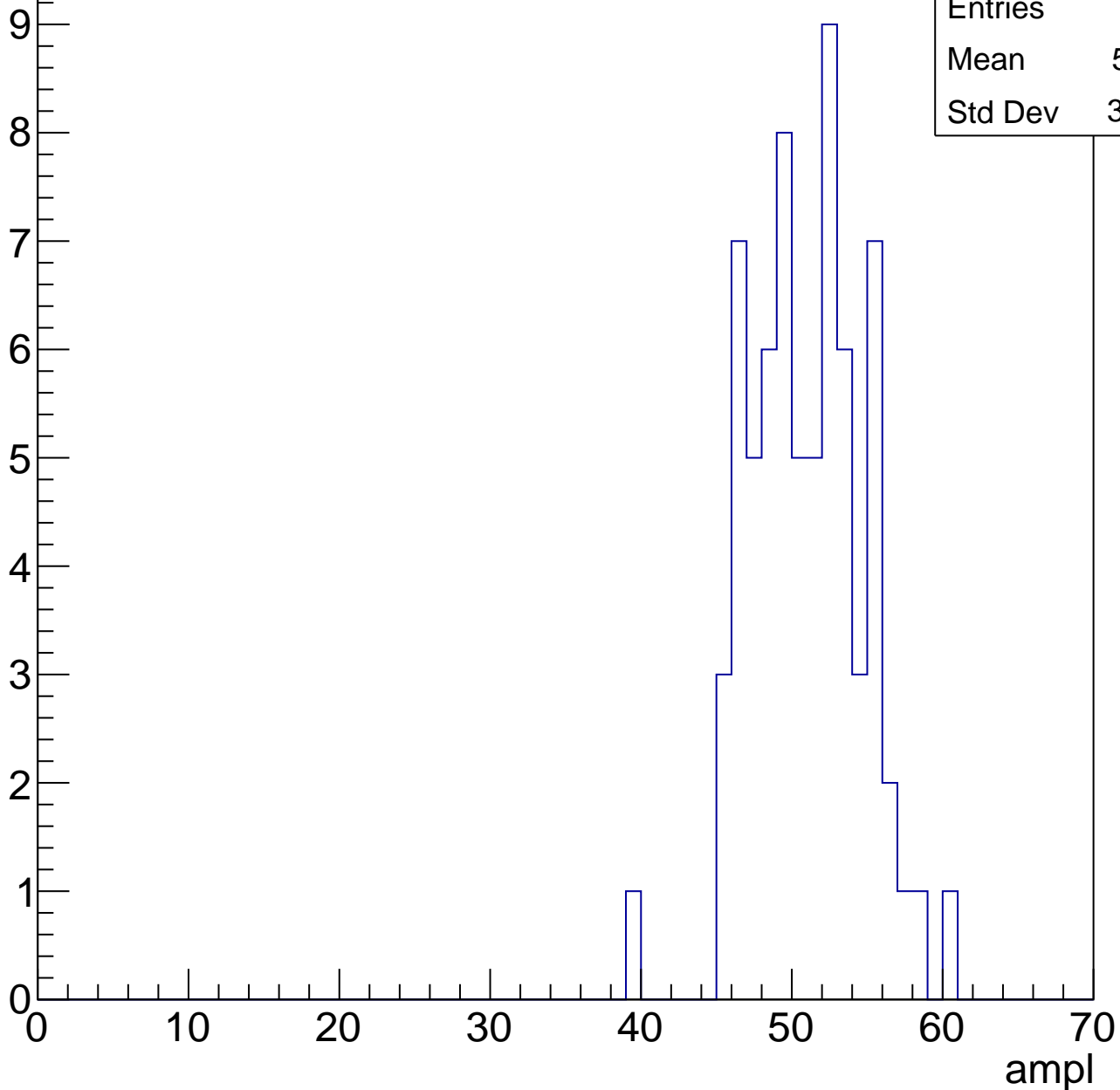


B1L101S, U21-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	50.51
Std Dev	3.722

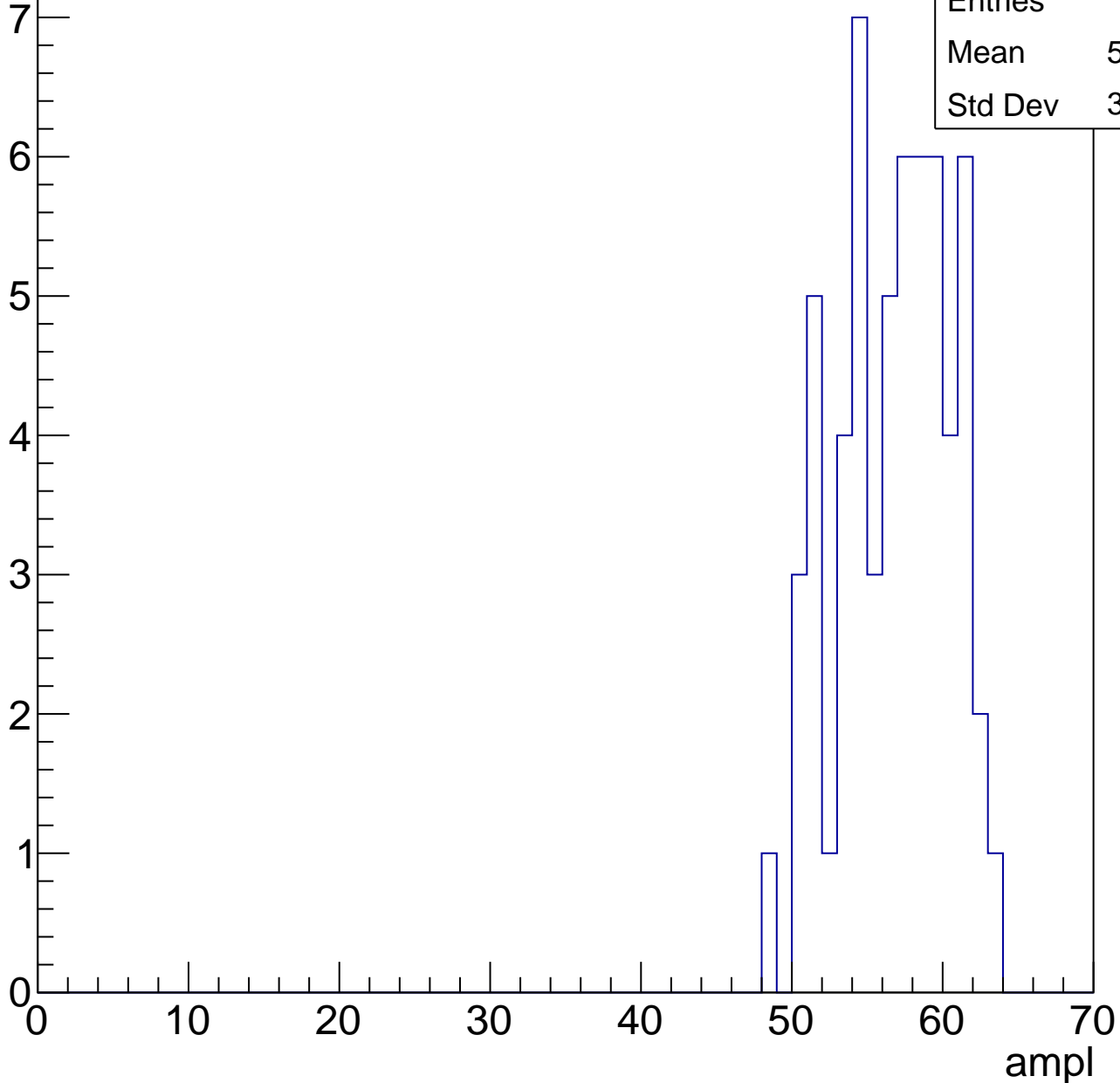


B1L101S, U21-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	56.28
Std Dev	3.638

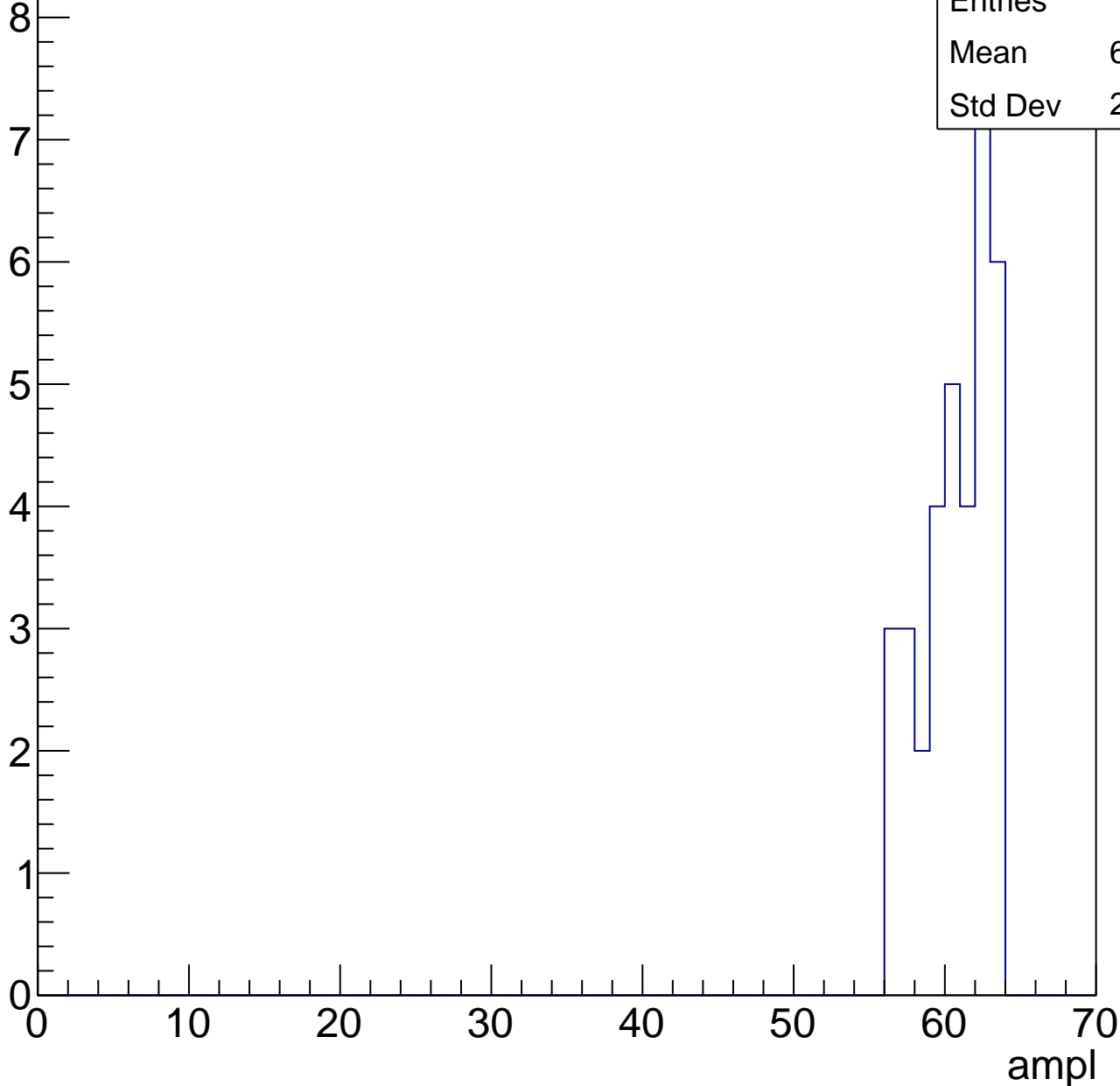


B1L101S, U21-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

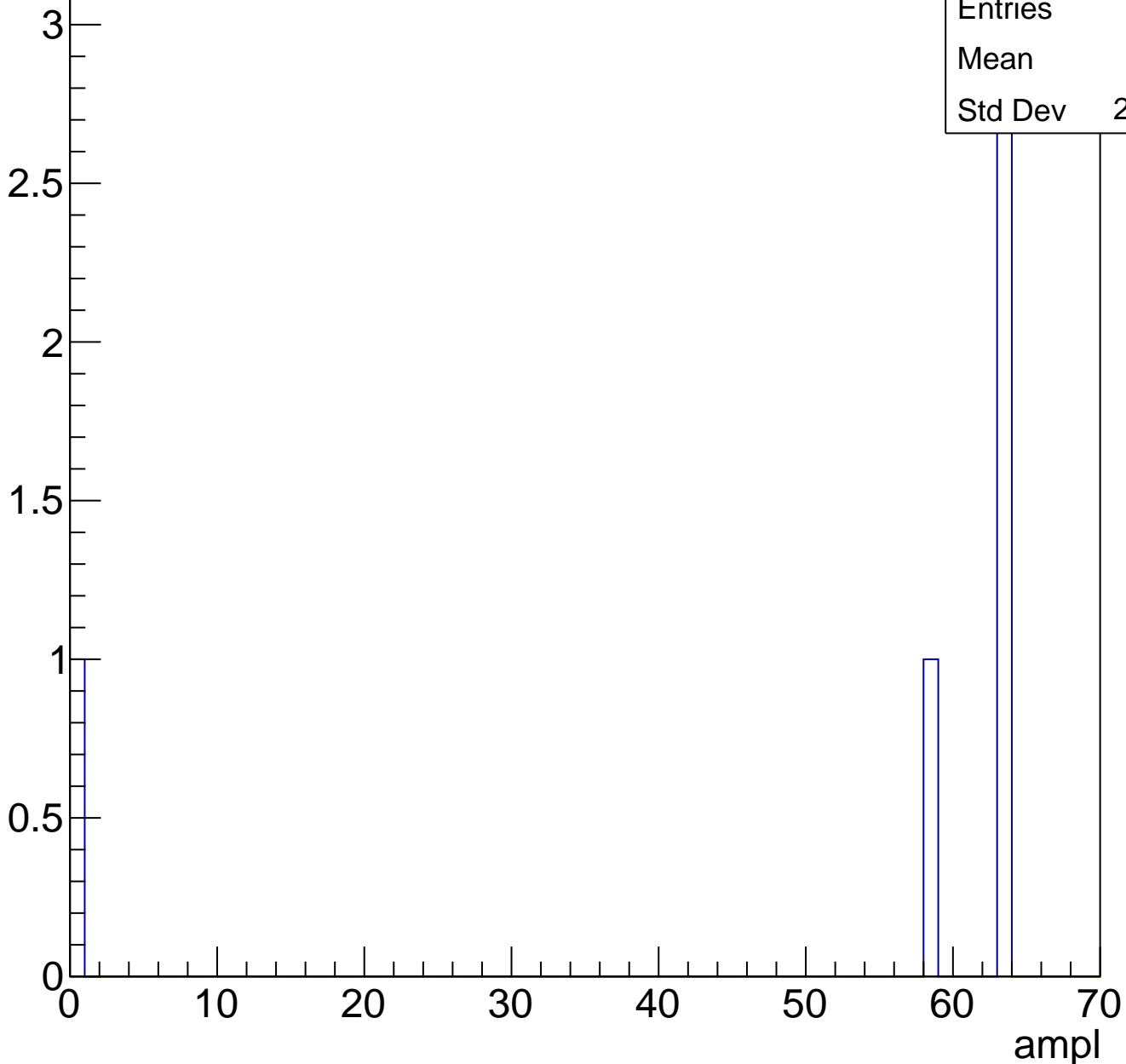
Entries	35
Mean	60.26
Std Dev	2.234



B1L101S, U21-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U21-ch110, adc0

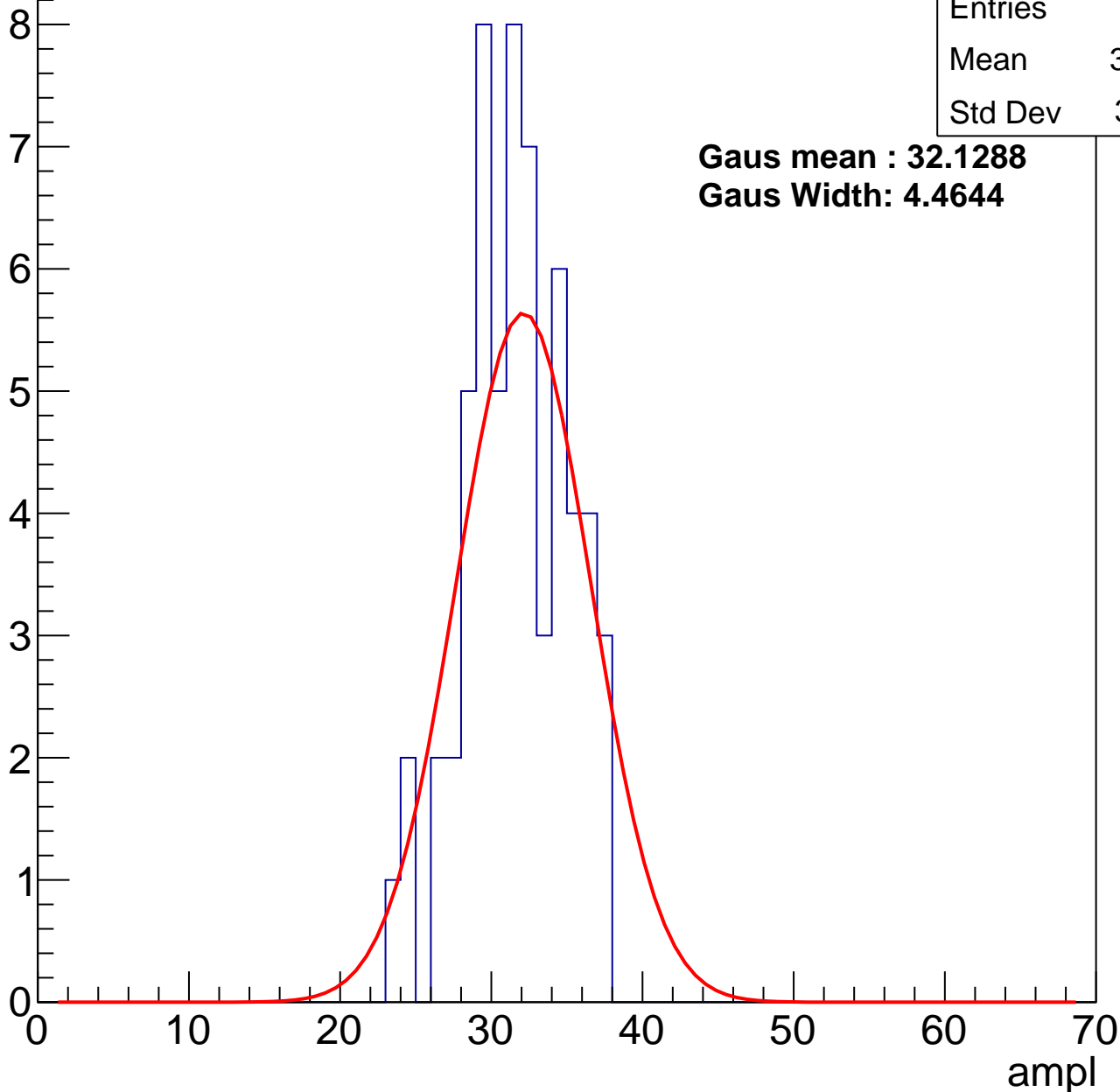
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	31.15
Std Dev	3.341

Gaus mean : 32.1288

Gaus Width: 4.4644



B1L101S, U21-ch110, adc1

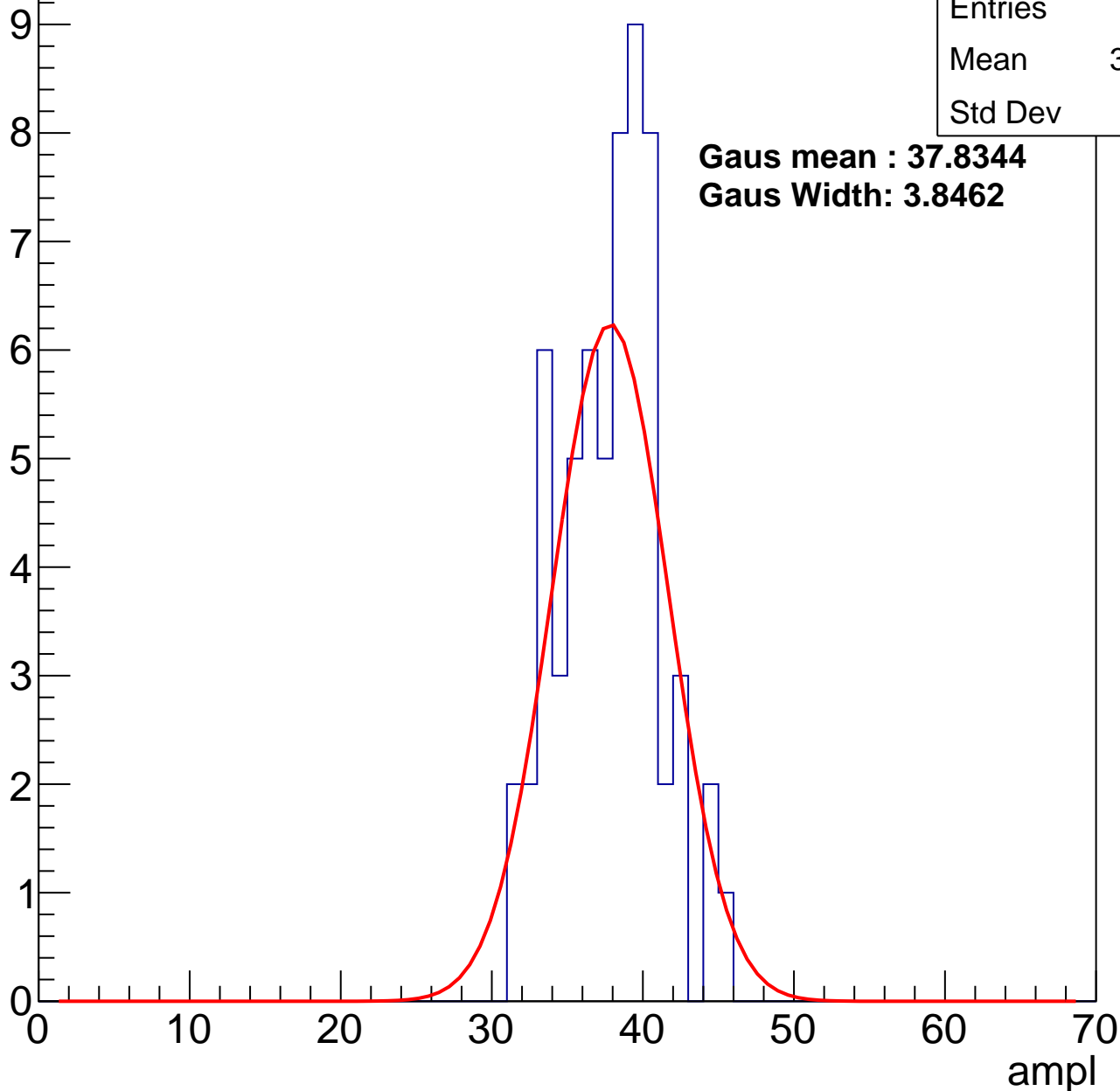
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	37.39
Std Dev	3.23

Gaus mean : 37.8344

Gaus Width: 3.8462



B1L101S, U21-ch110, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	44.33
Std Dev	3.57

Gaus mean : 44.9107

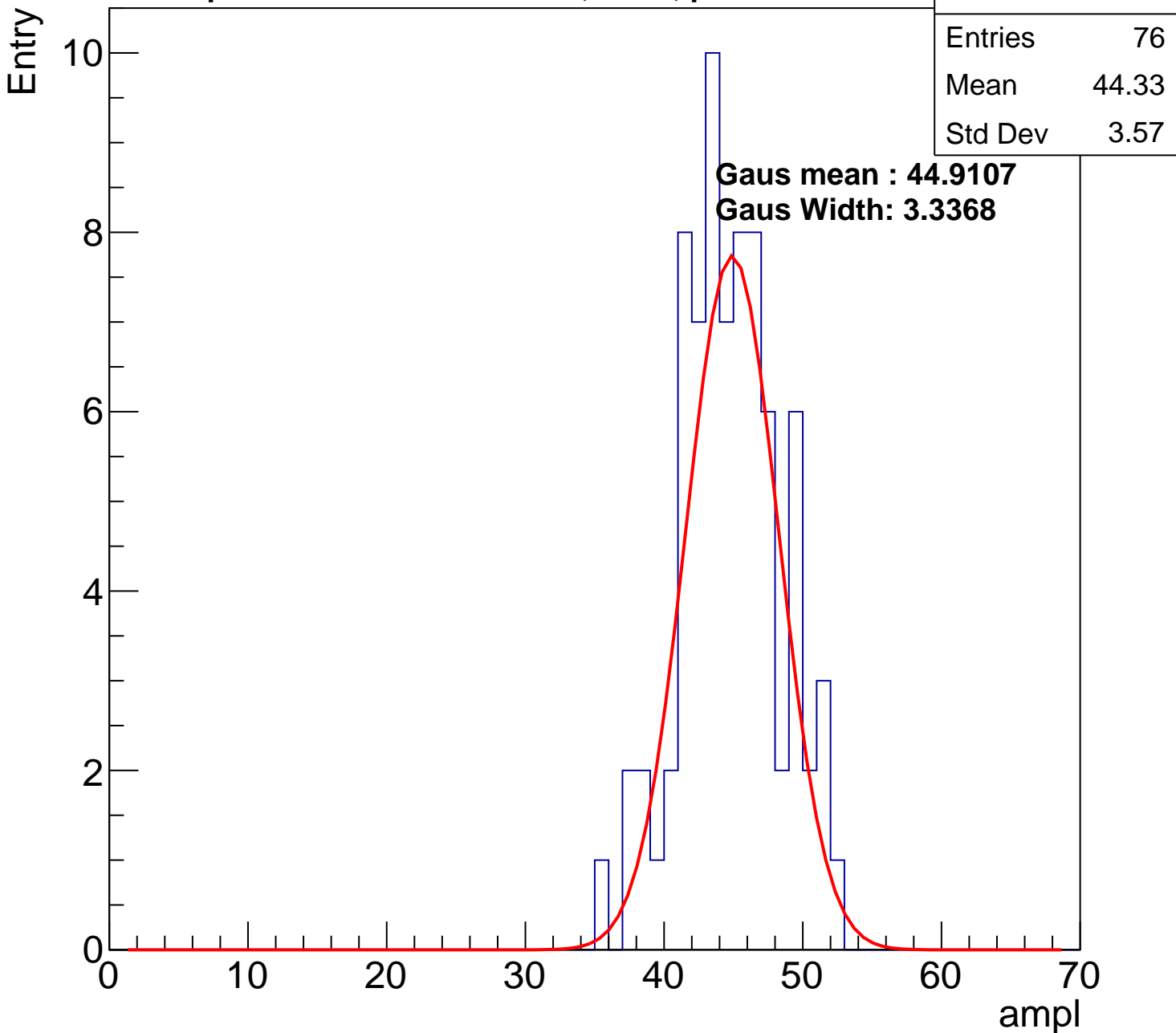
Gaus Width: 3.3368

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

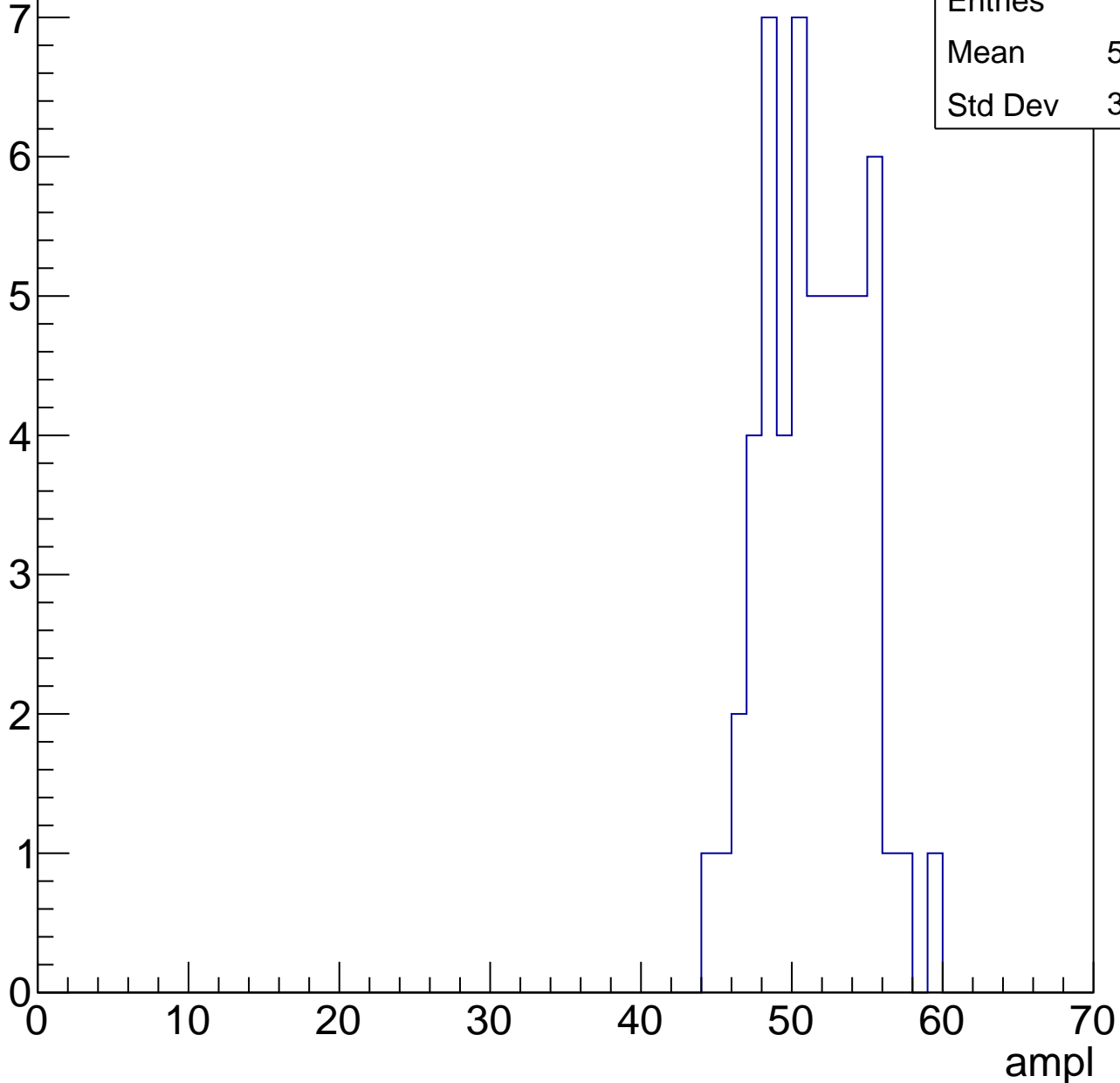


B1L101S, U21-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	50.96
Std Dev	3.236

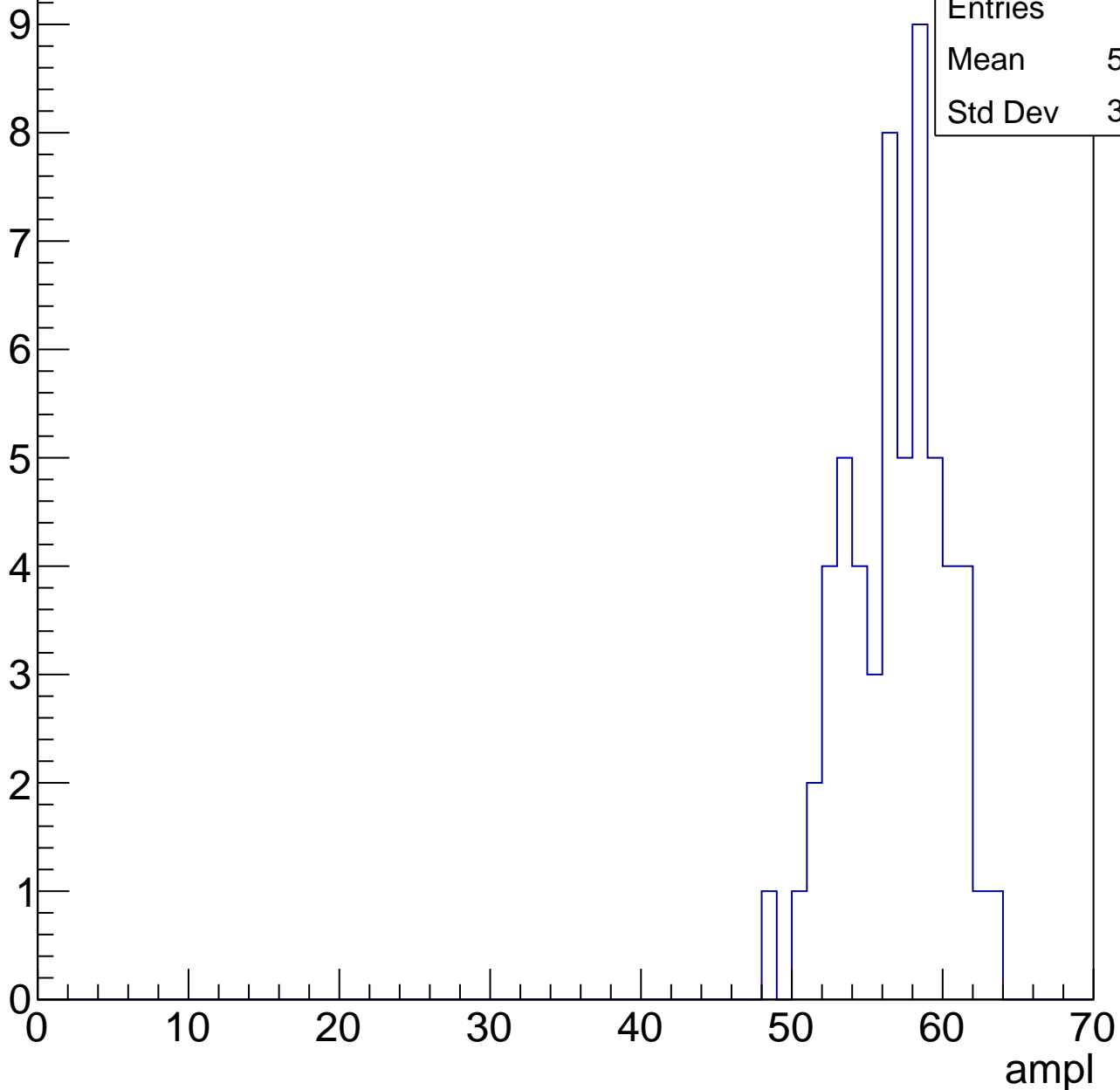


B1L101S, U21-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.37
Std Dev	3.259

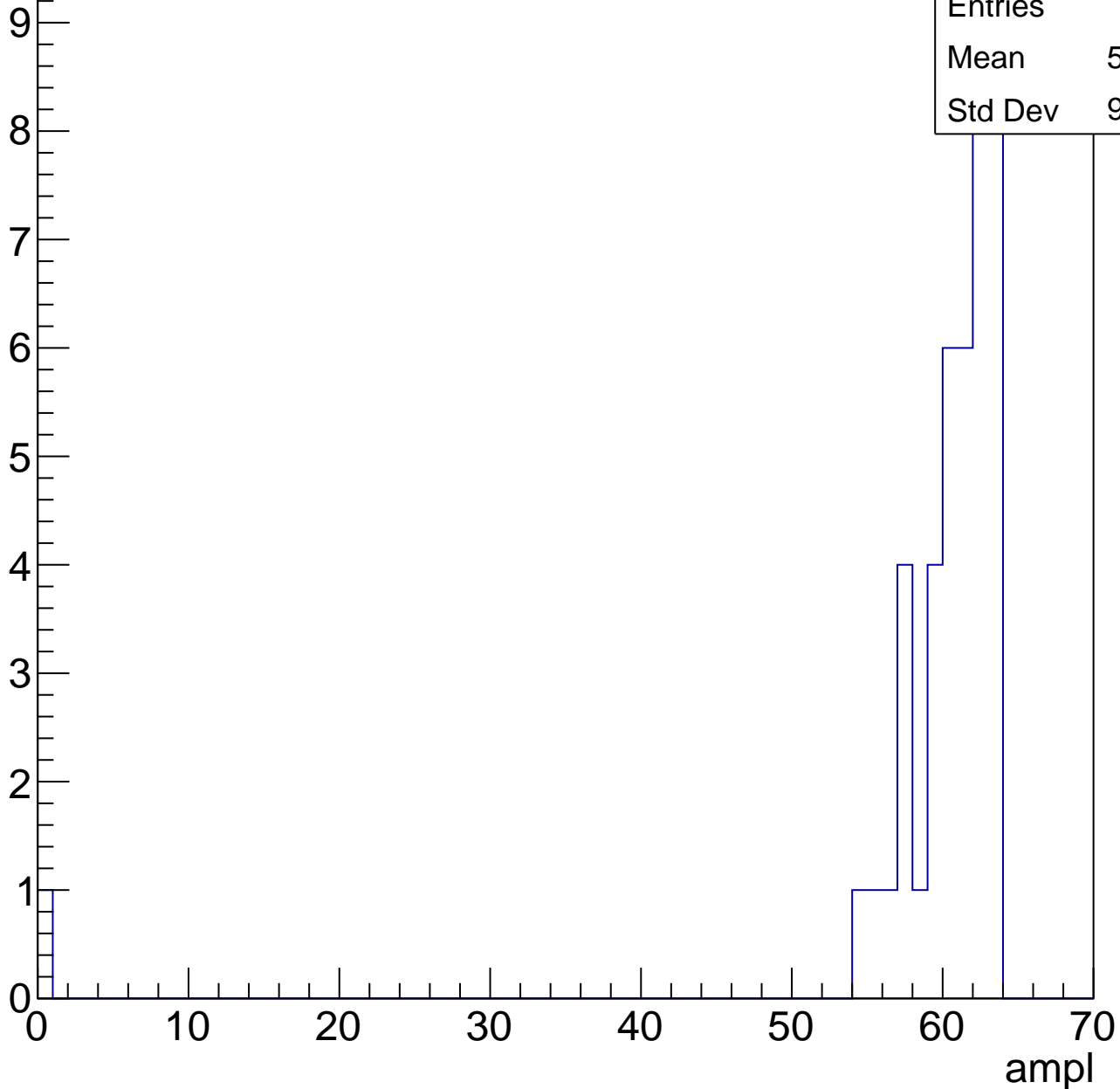


B1L101S, U21-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	59.02
Std Dev	9.404



B1L101S, U21-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch111, adc0

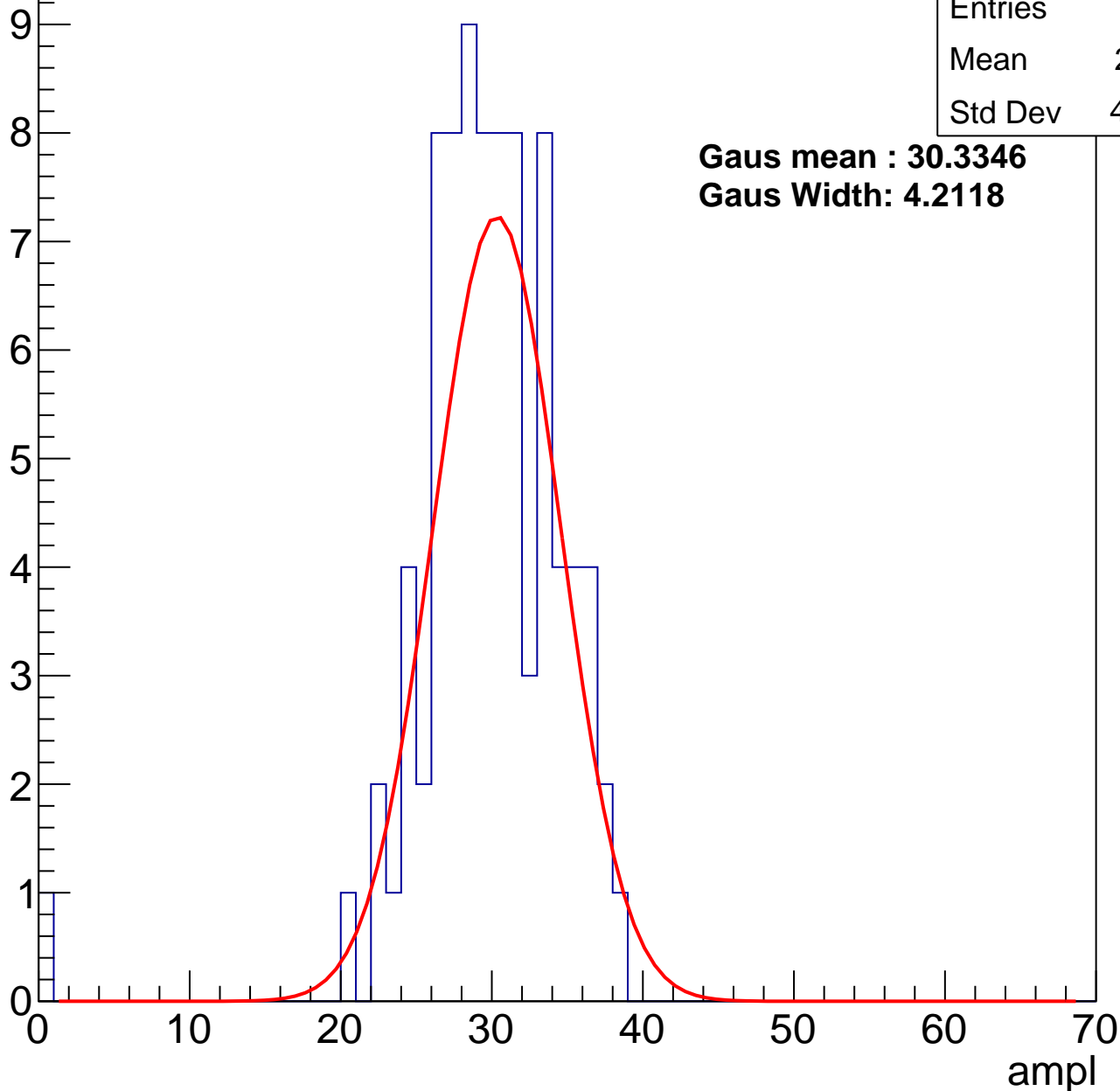
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	29.31
Std Dev	4.984

Gaus mean : 30.3346

Gaus Width: 4.2118



B1L101S, U21-ch111, adc1

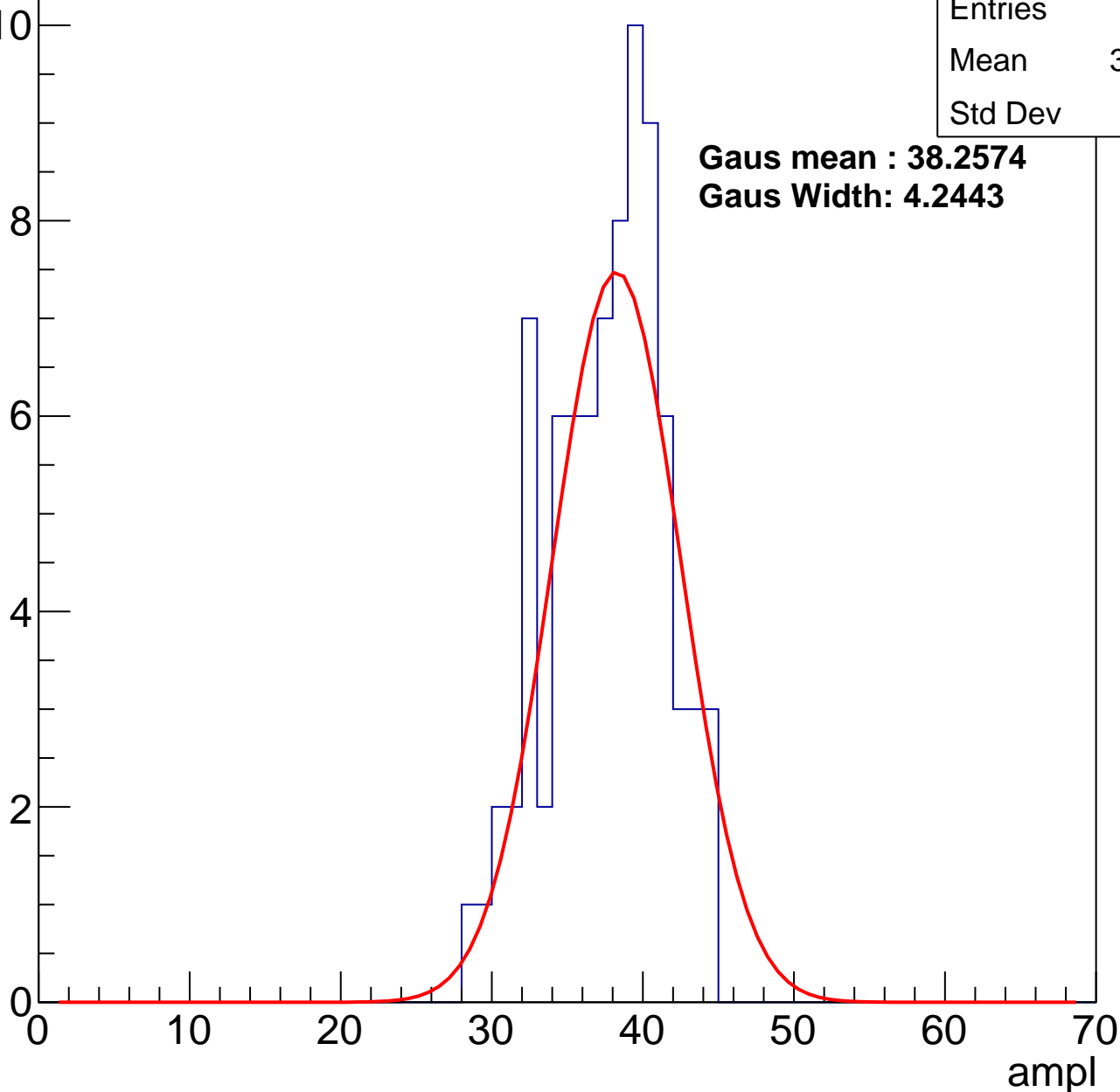
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	37.13
Std Dev	3.76

Gaus mean : 38.2574

Gaus Width: 4.2443



B1L101S, U21-ch111, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	44.25
Std Dev	2.825

7

6

5

4

3

2

1

0

0

1

1

4

6

7

7

5

2

1

1

0

0

0

0

0

0

0

0

0

0

0

Gaus mean : 44.7765

Gaus Width: 2.6951

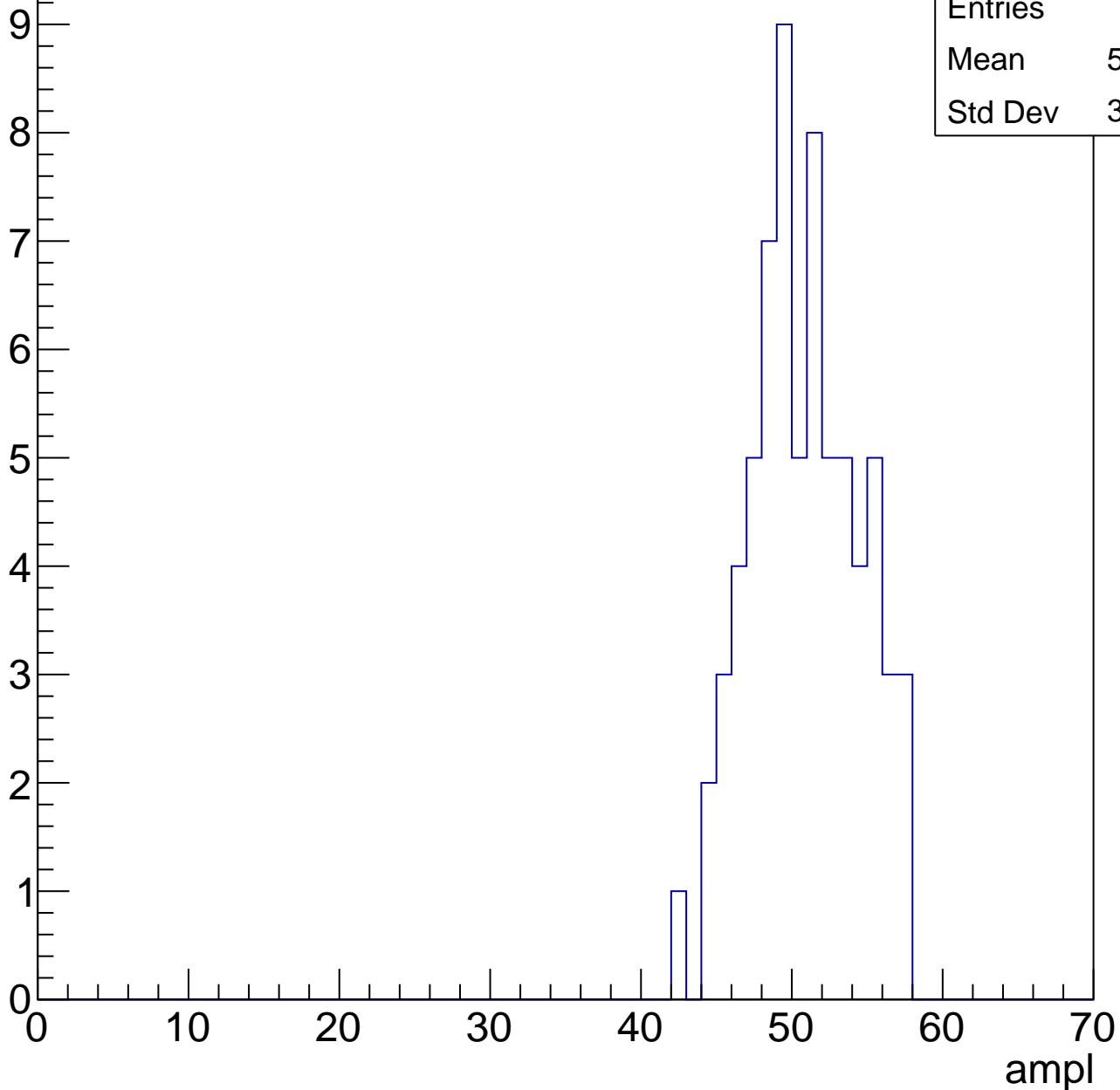
ampl

B1L101S, U21-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.35
Std Dev	3.542

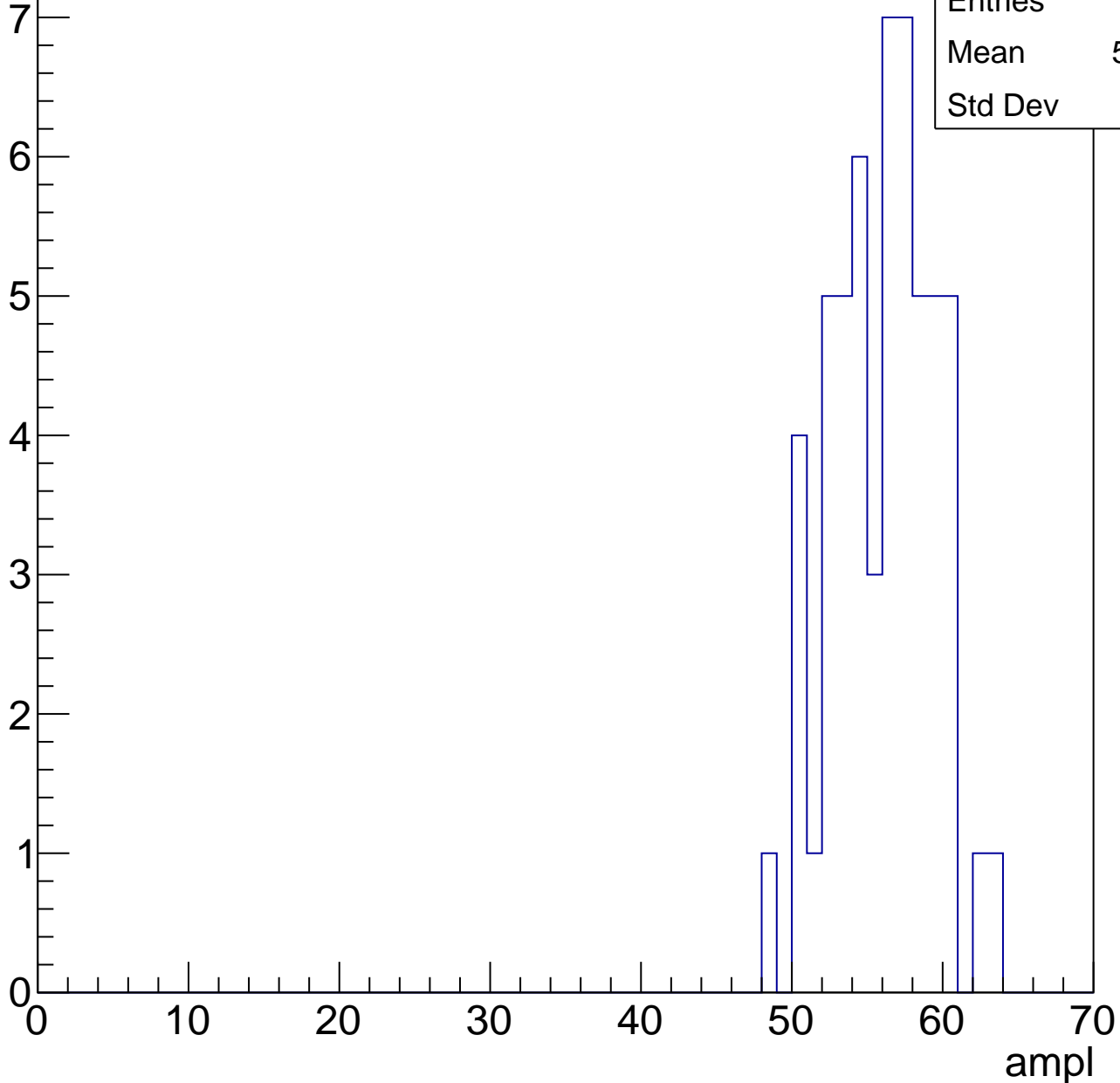


B1L101S, U21-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	55.61
Std Dev	3.31

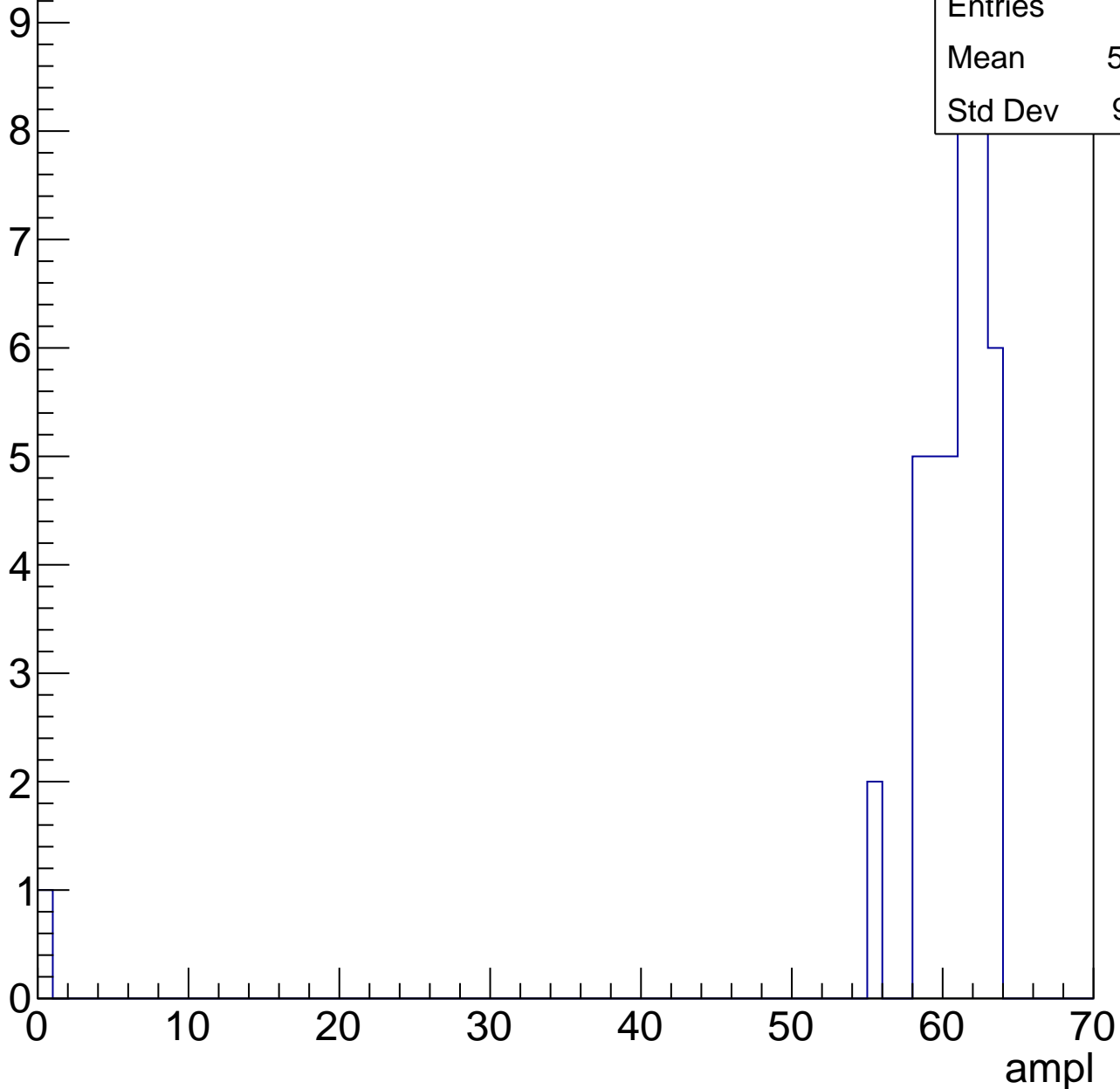


B1L101S, U21-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	59.05
Std Dev	9.431



B1L101S, U21-ch111, adc6

calib_packv5_042523_0143.root, FC#0, port D2

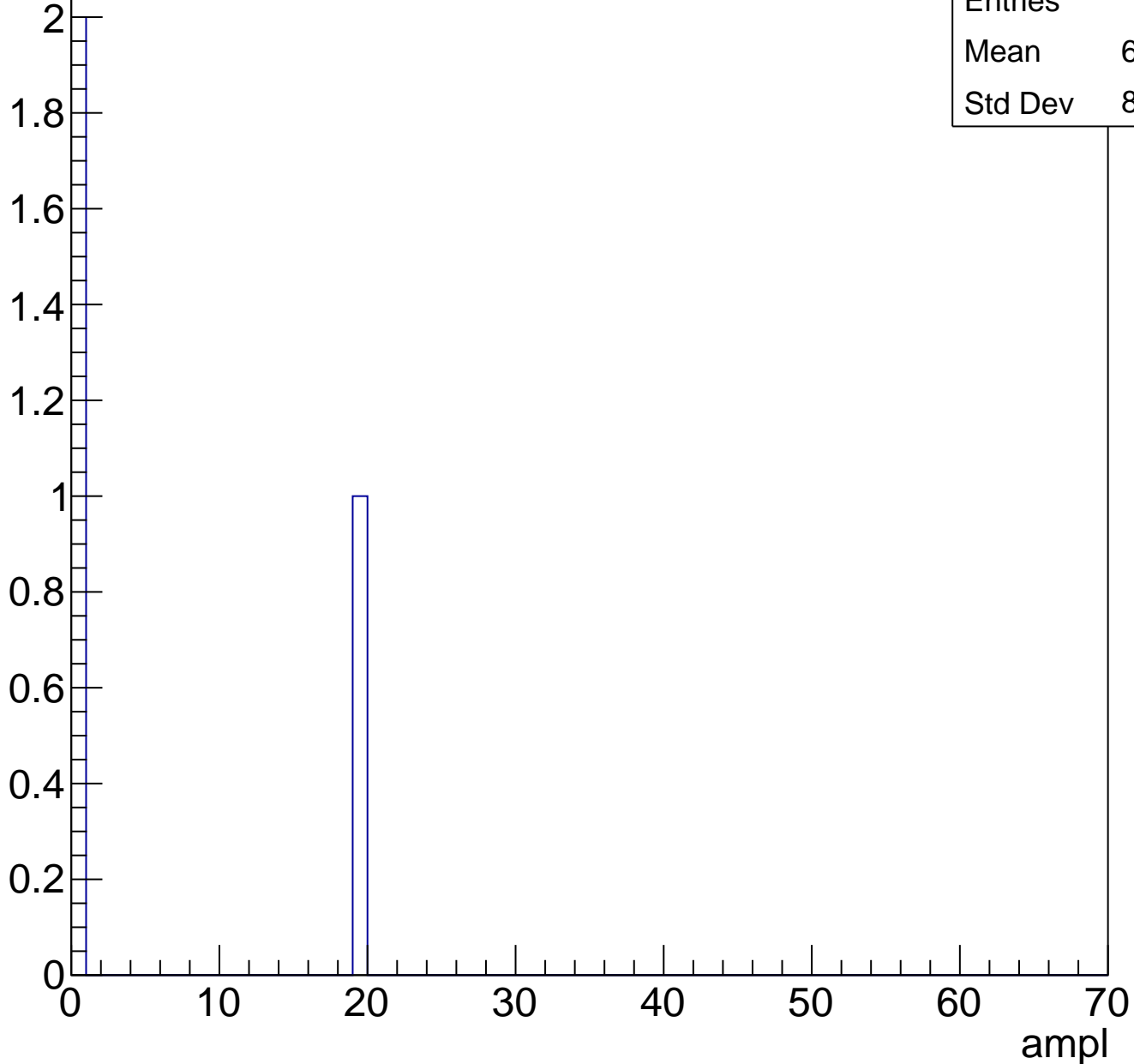
Entry



B1L101S, U21-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L101S, U21-ch112, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	29.75
Std Dev	3.749

Gaus mean : 29.9927

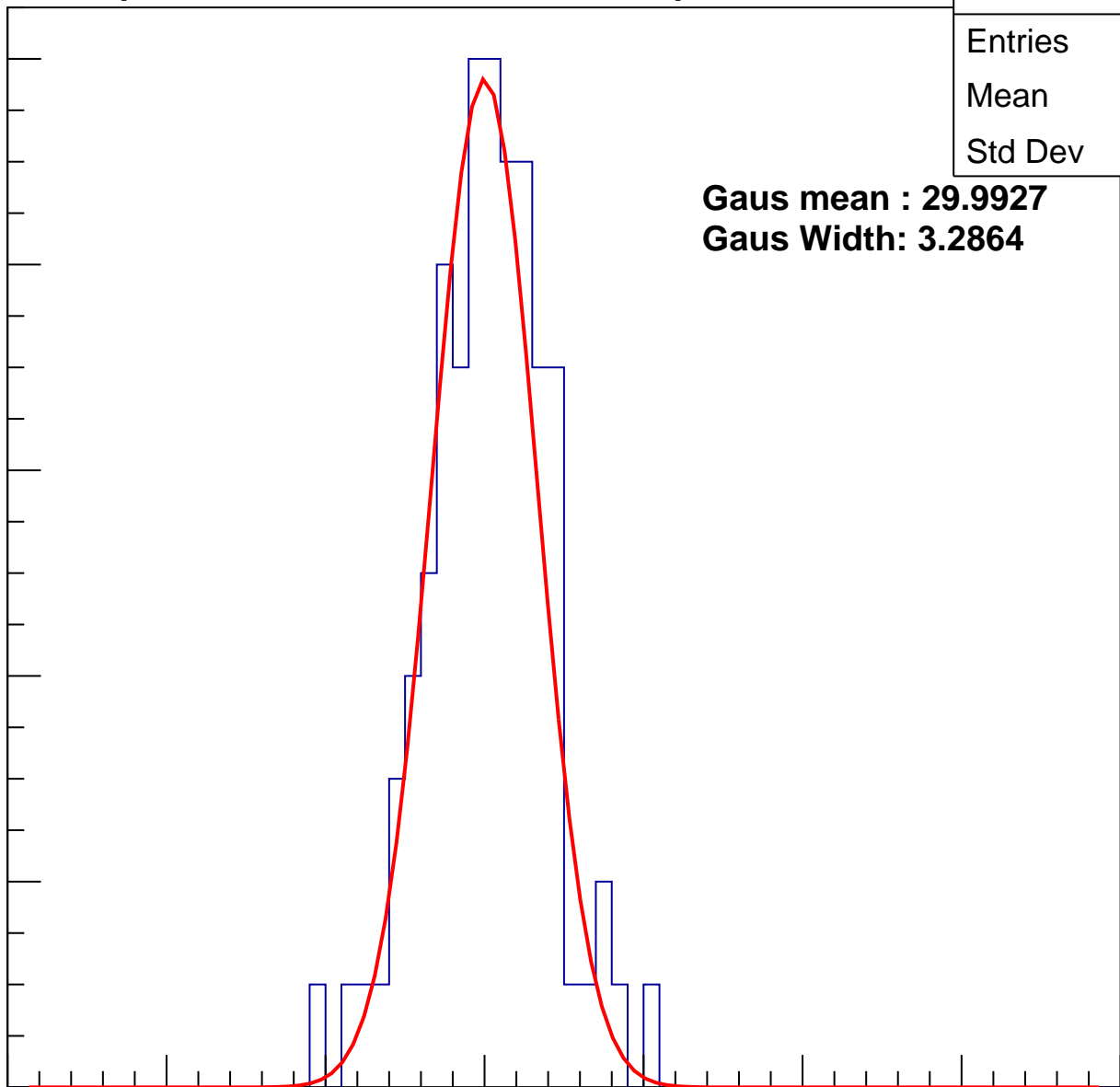
Gaus Width: 3.2864

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch112, adc1

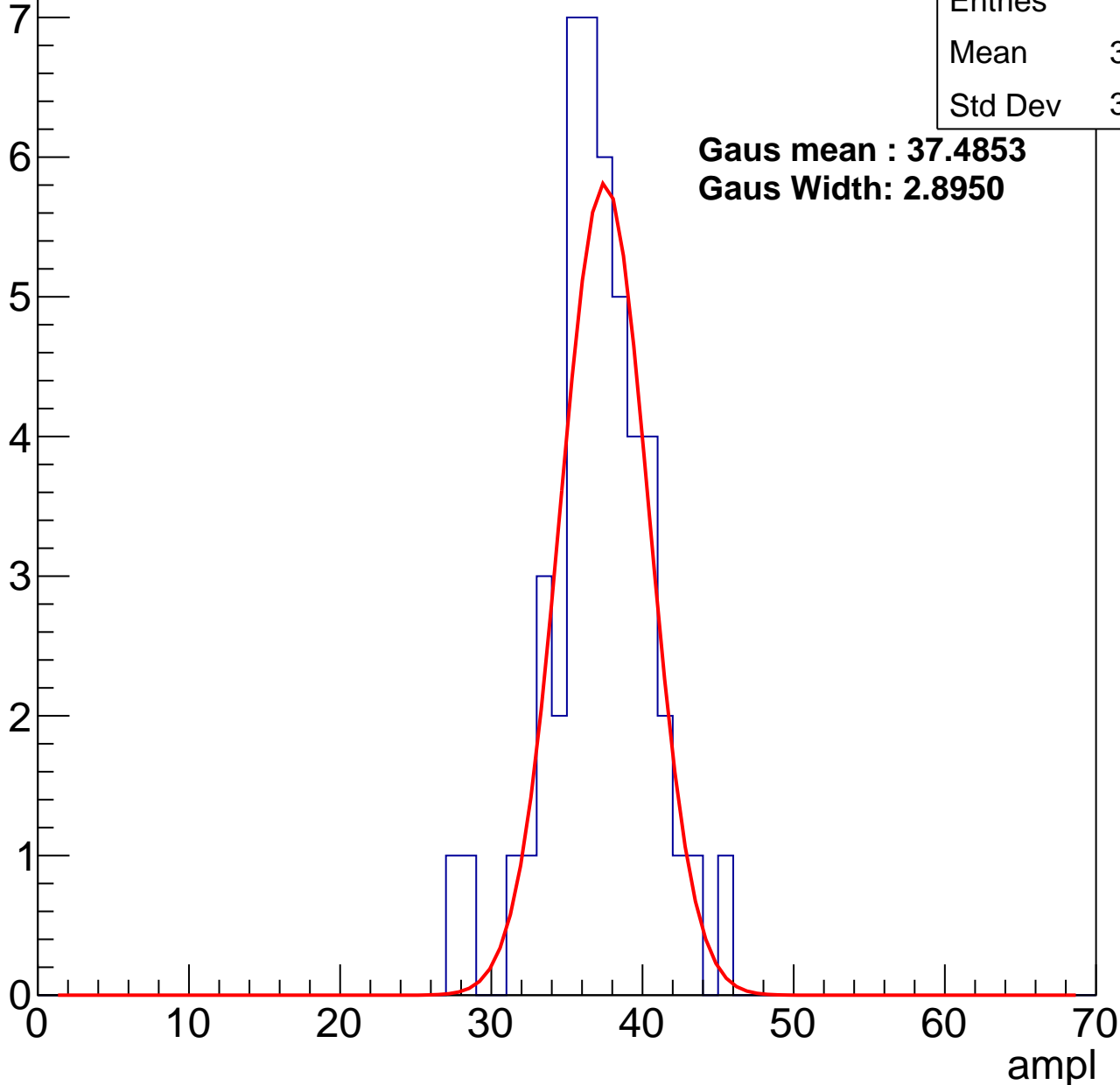
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	36.64
Std Dev	3.423

Gaus mean : 37.4853

Gaus Width: 2.8950



B1L101S, U21-ch112, adc2

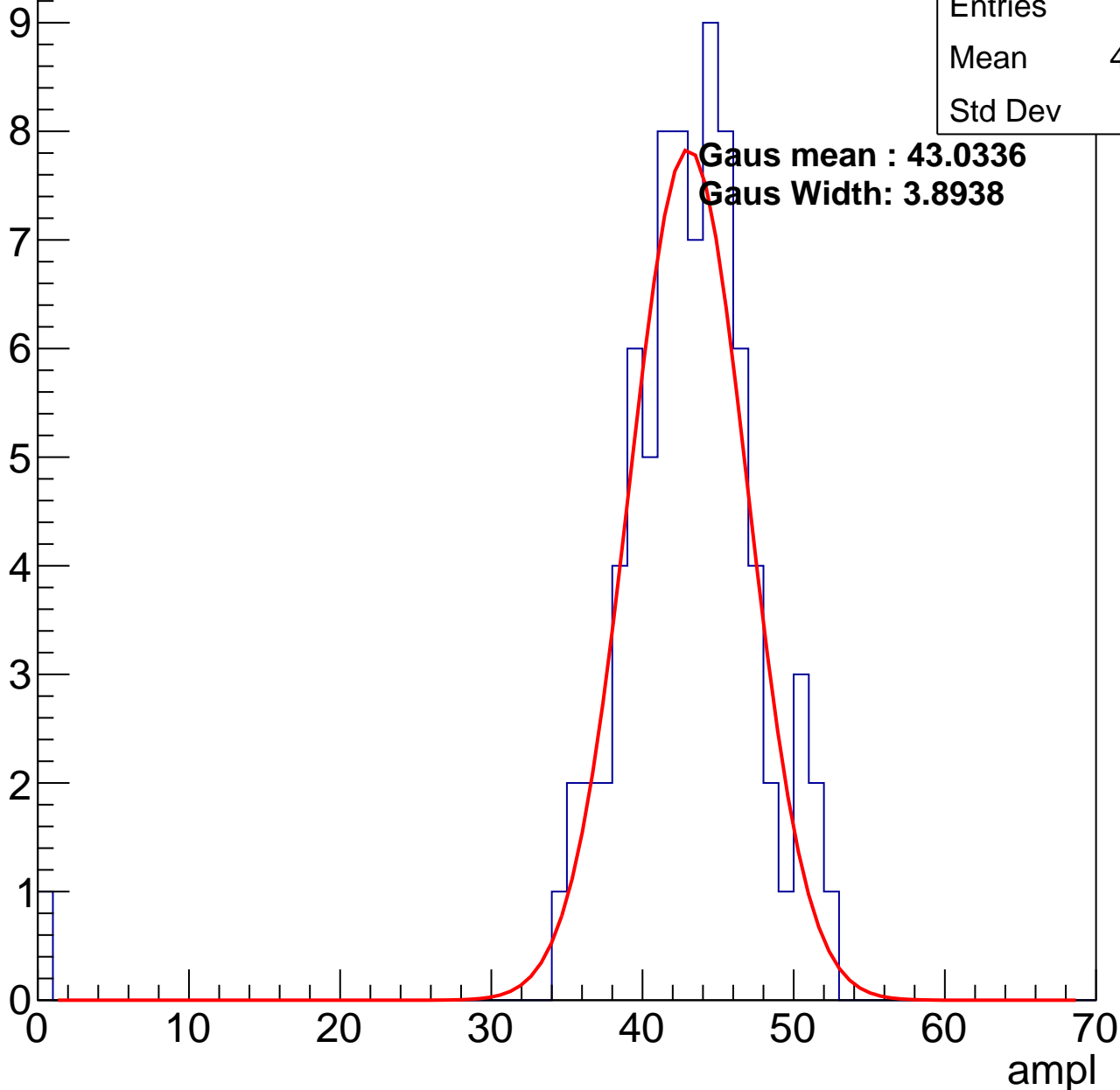
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	42.32
Std Dev	6.12

Gaus mean : 43.0336

Gaus Width: 3.8938

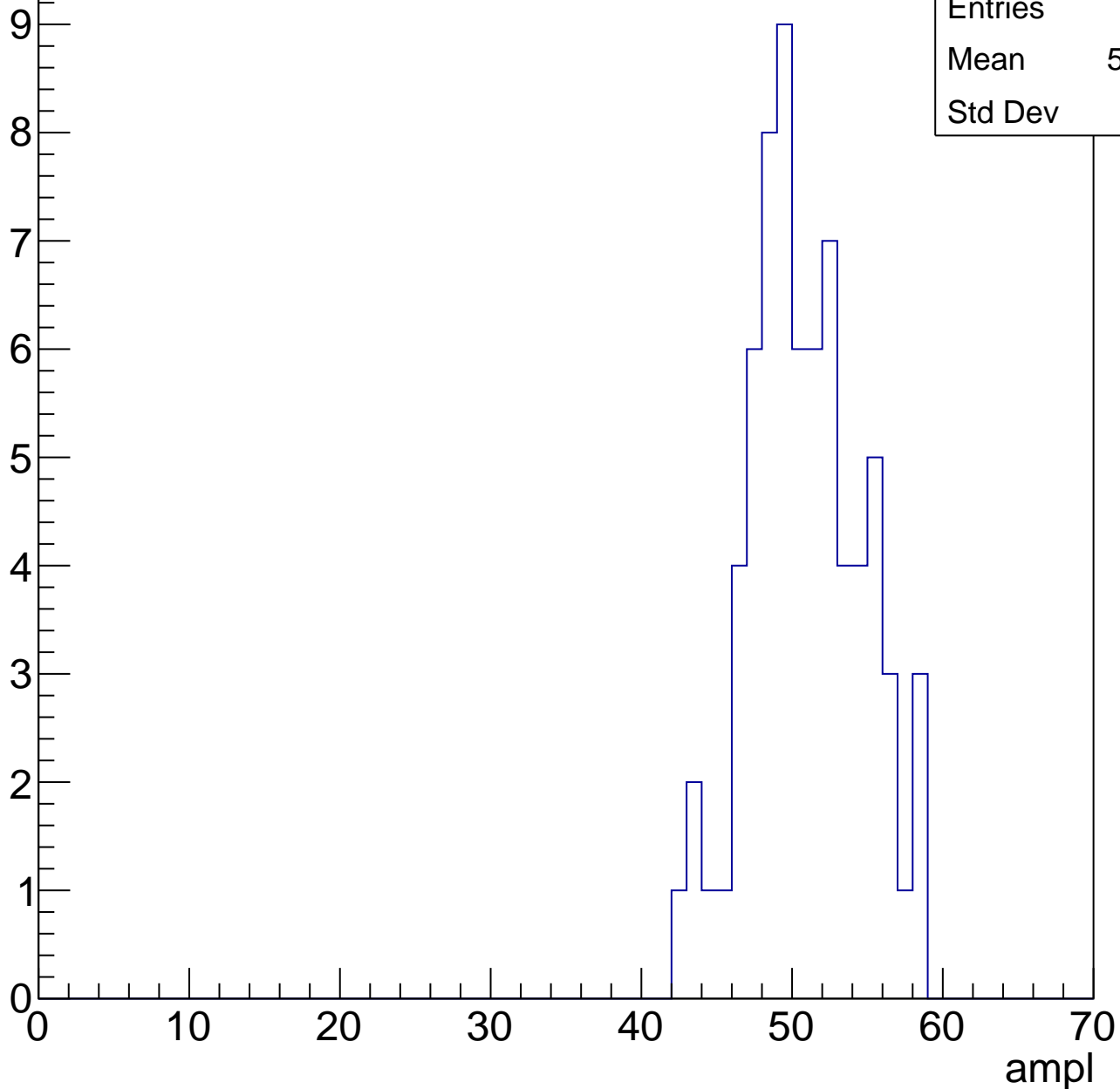


B1L101S, U21-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	50.42
Std Dev	3.71

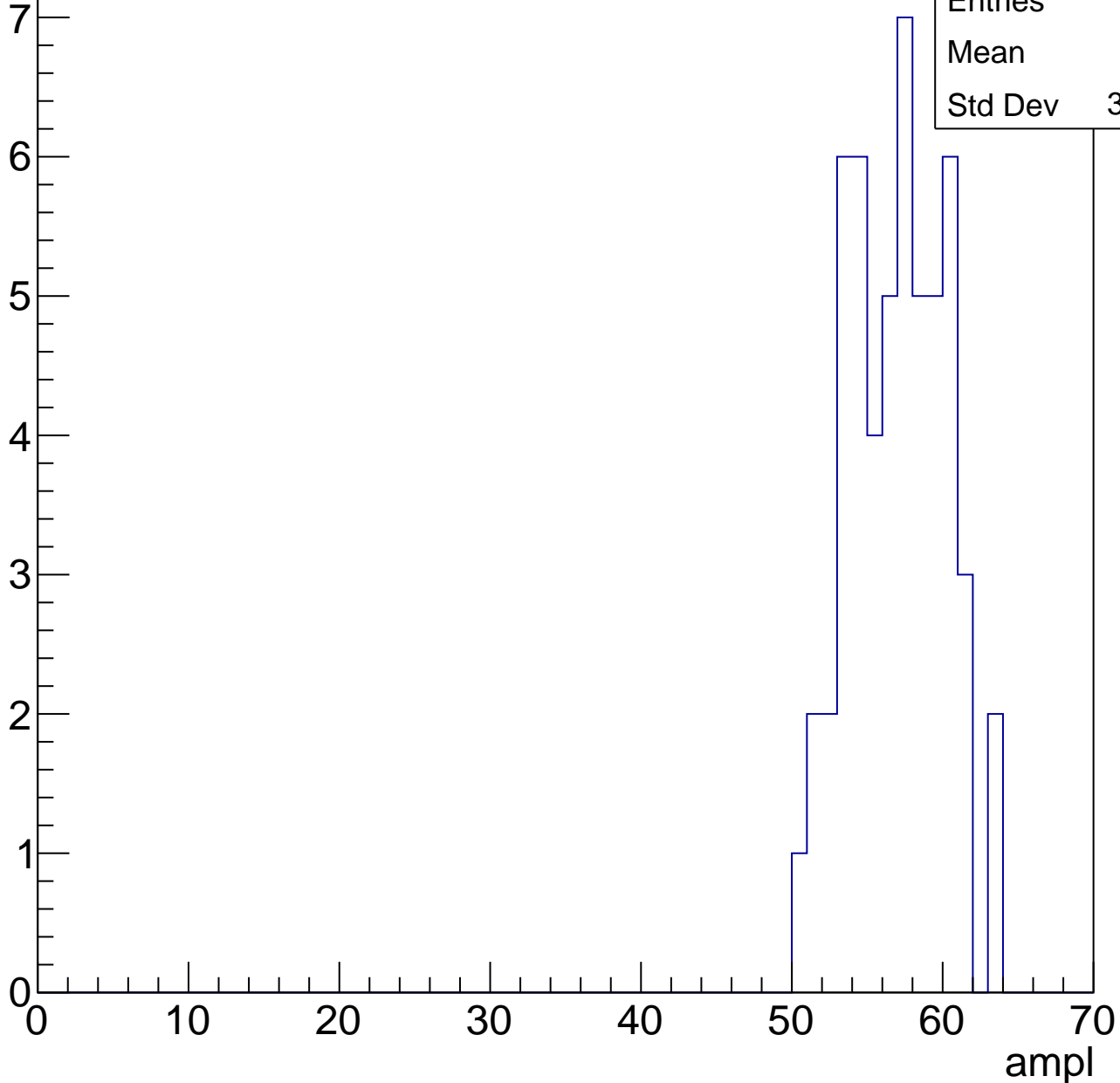


B1L101S, U21-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	56.5
Std Dev	3.125

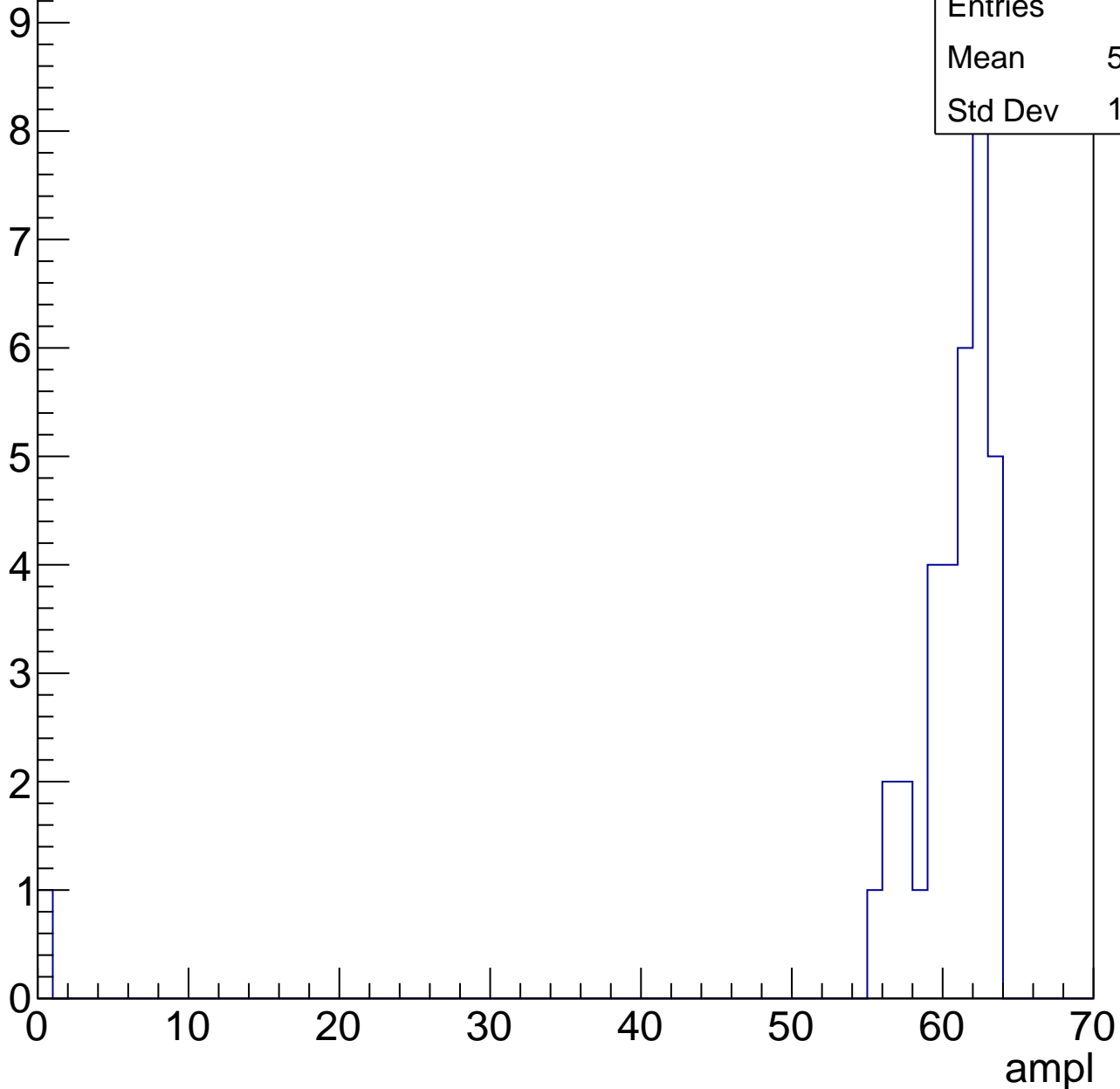


B1L101S, U21-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

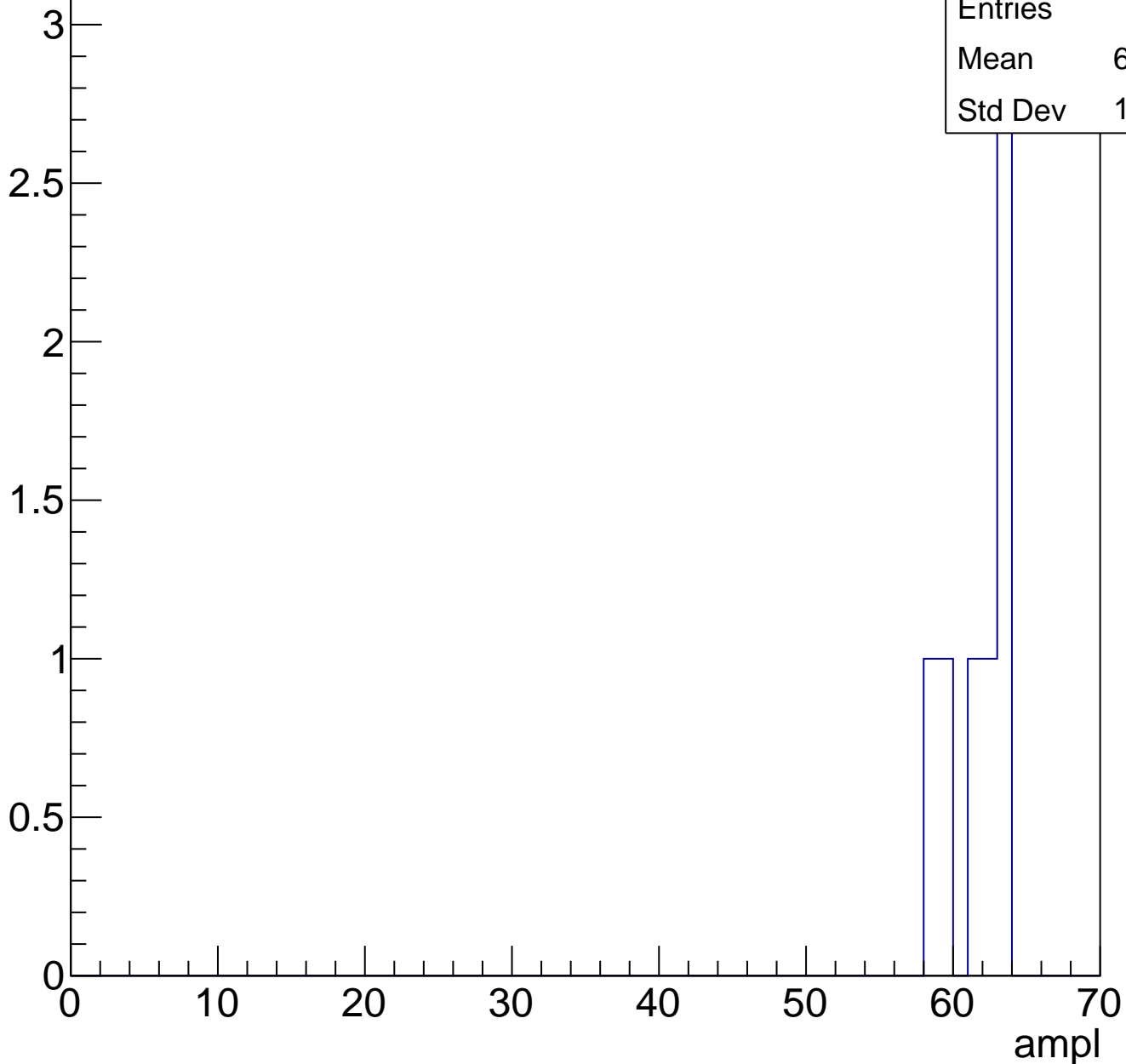
Entries	35
Mean	58.69
Std Dev	10.29



B1L101S, U21-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch113, adc0

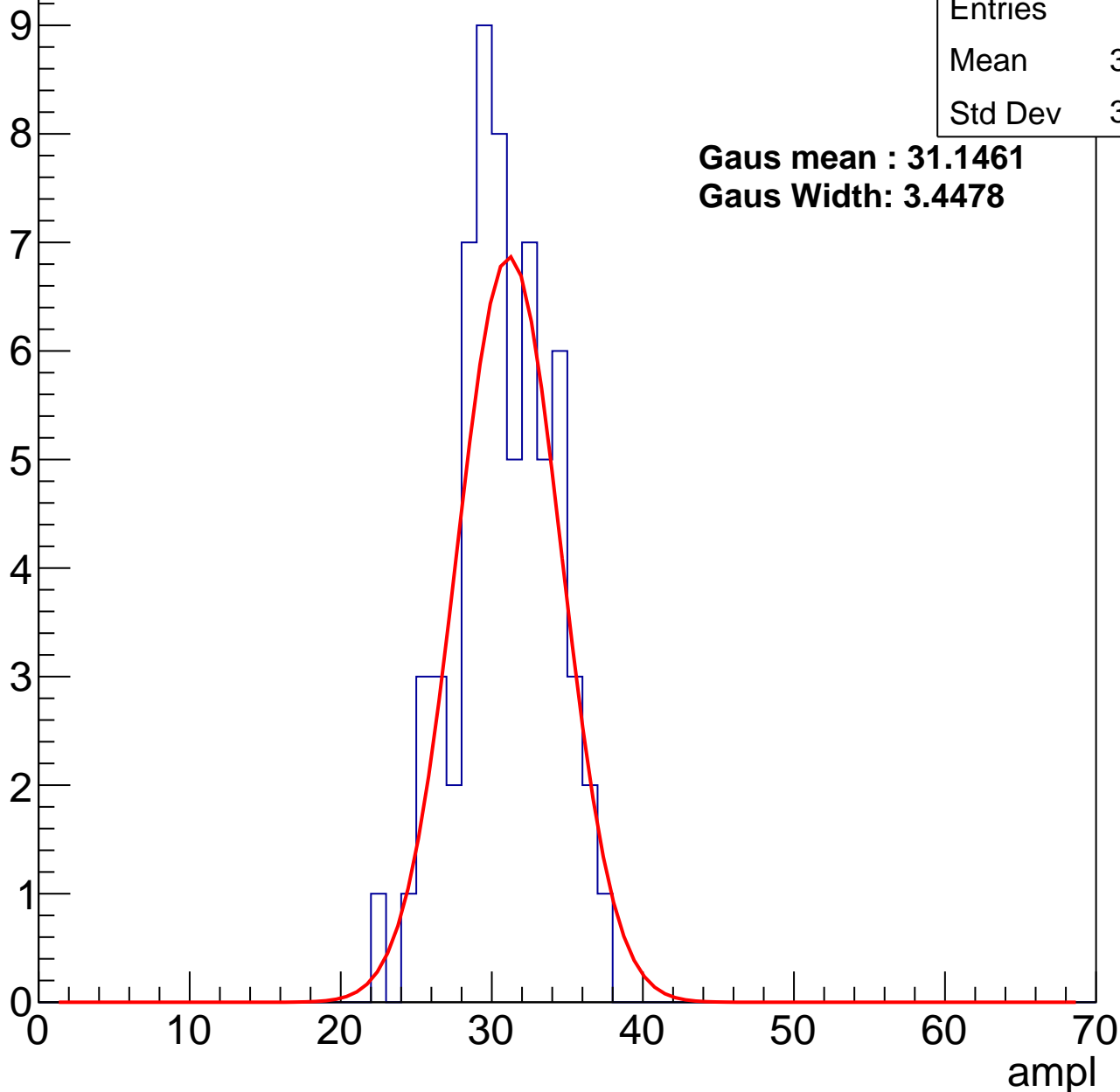
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.35
Std Dev	3.188

Gaus mean : 31.1461

Gaus Width: 3.4478



B1L101S, U21-ch113, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	37.26
Std Dev	3.496

Gaus mean : 37.8538

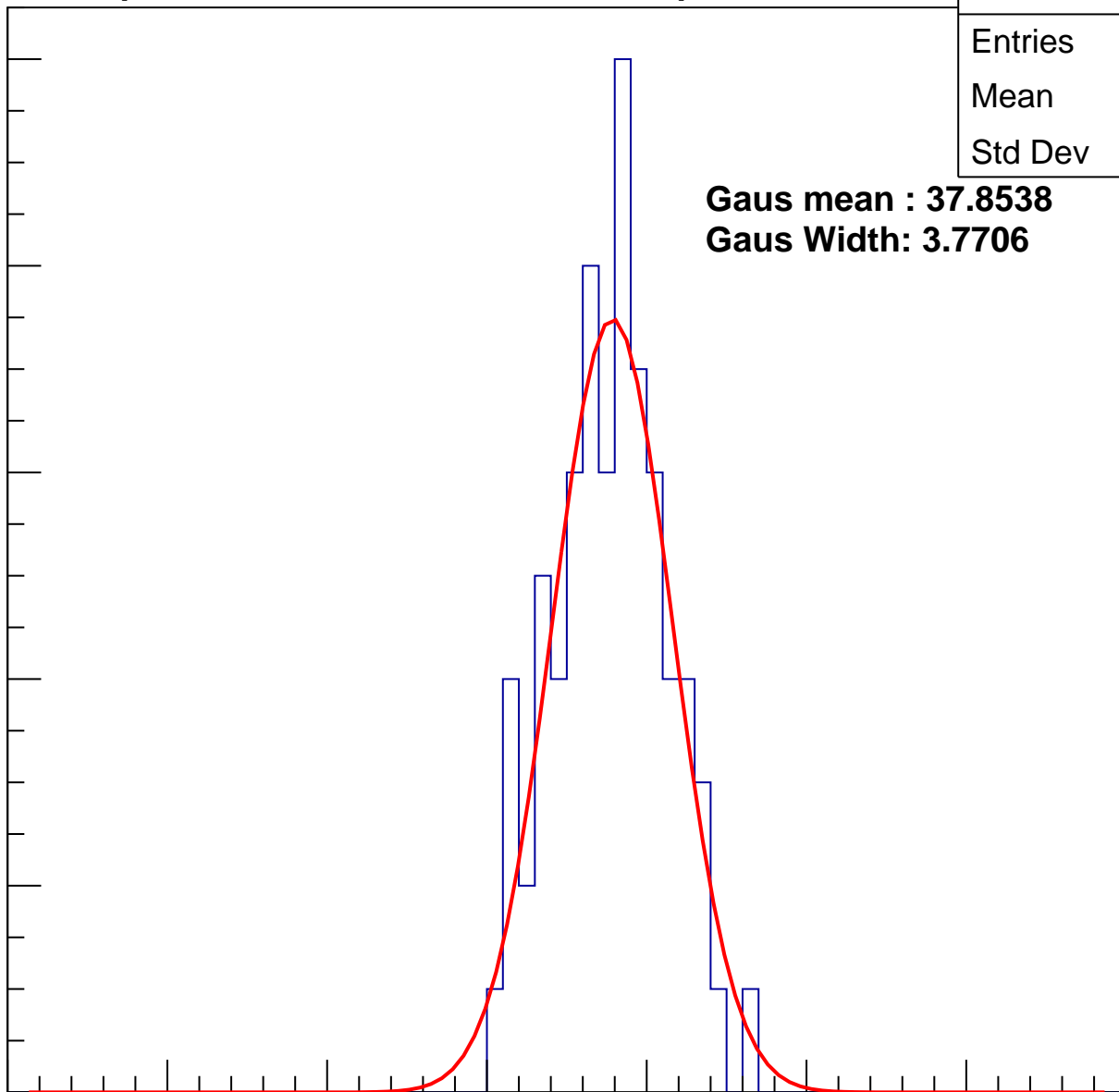
Gaus Width: 3.7706

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch113, adc2

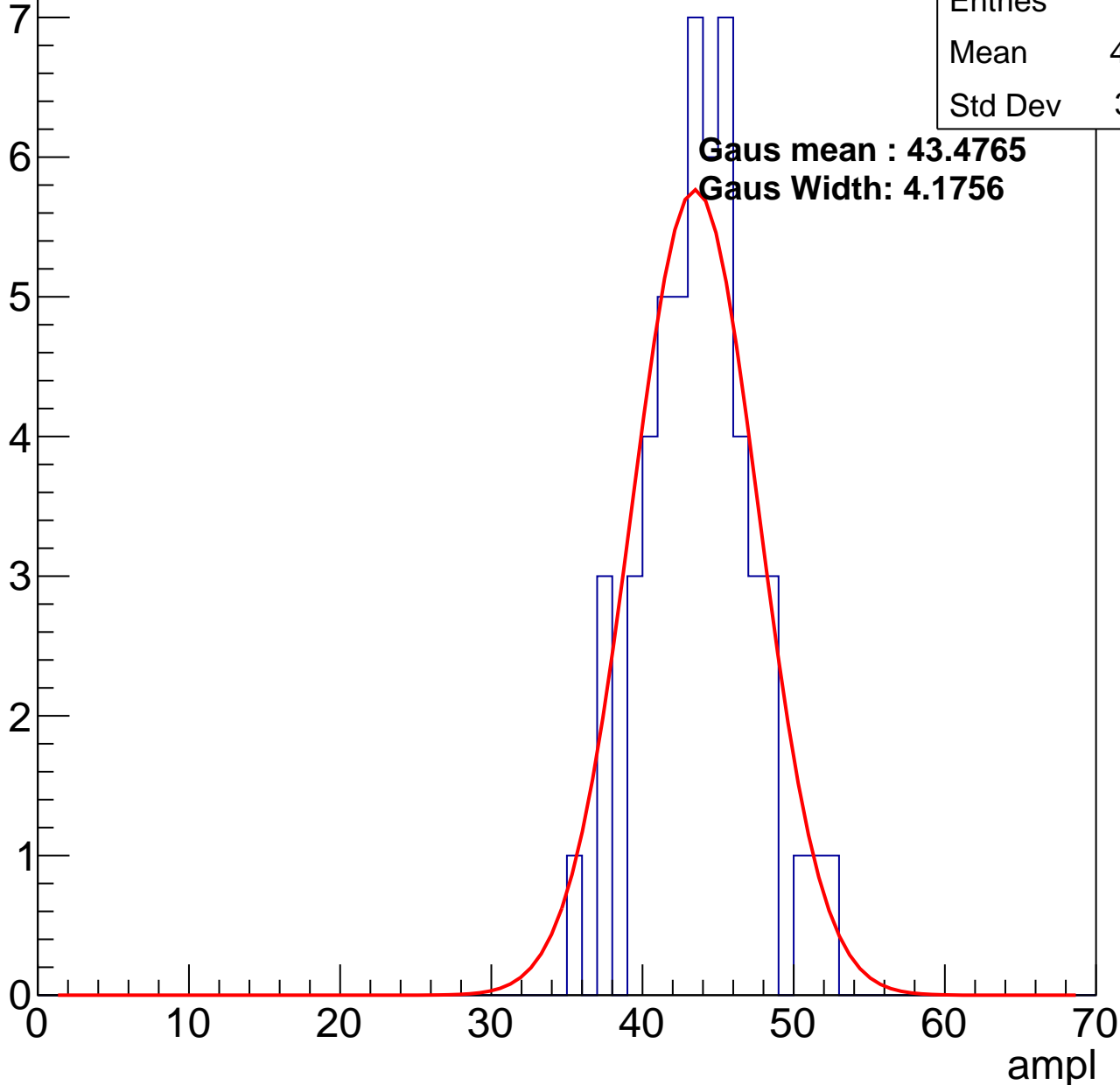
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.33
Std Dev	3.501

Gaus mean : 43.4765

Gaus Width: 4.1756



B1L101S, U21-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	50.22
Std Dev	3.775

Entry

10

8

6

4

2

0

0

10

20

30

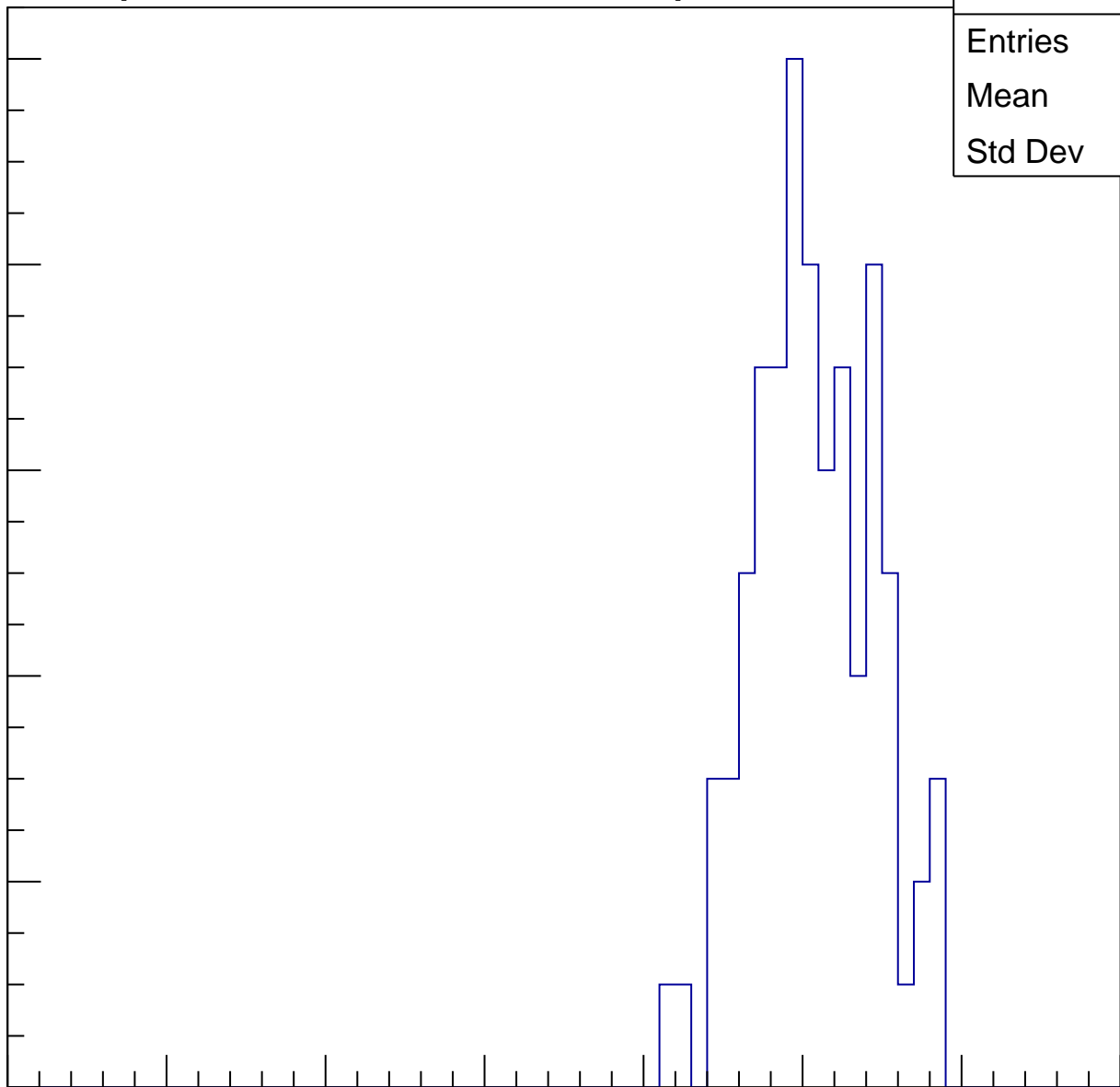
40

50

60

ampl

70

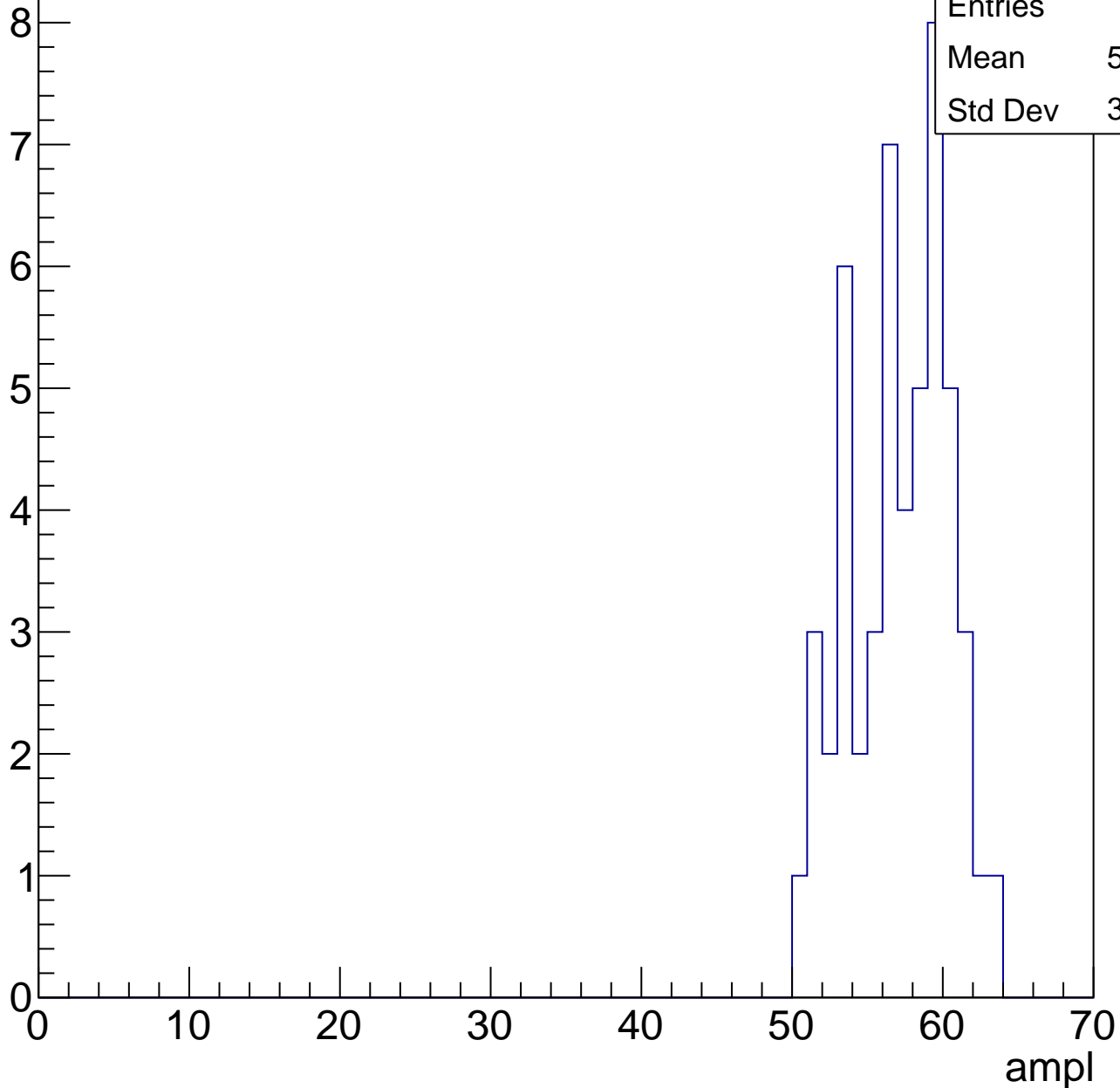


B1L101S, U21-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	56.63
Std Dev	3.205

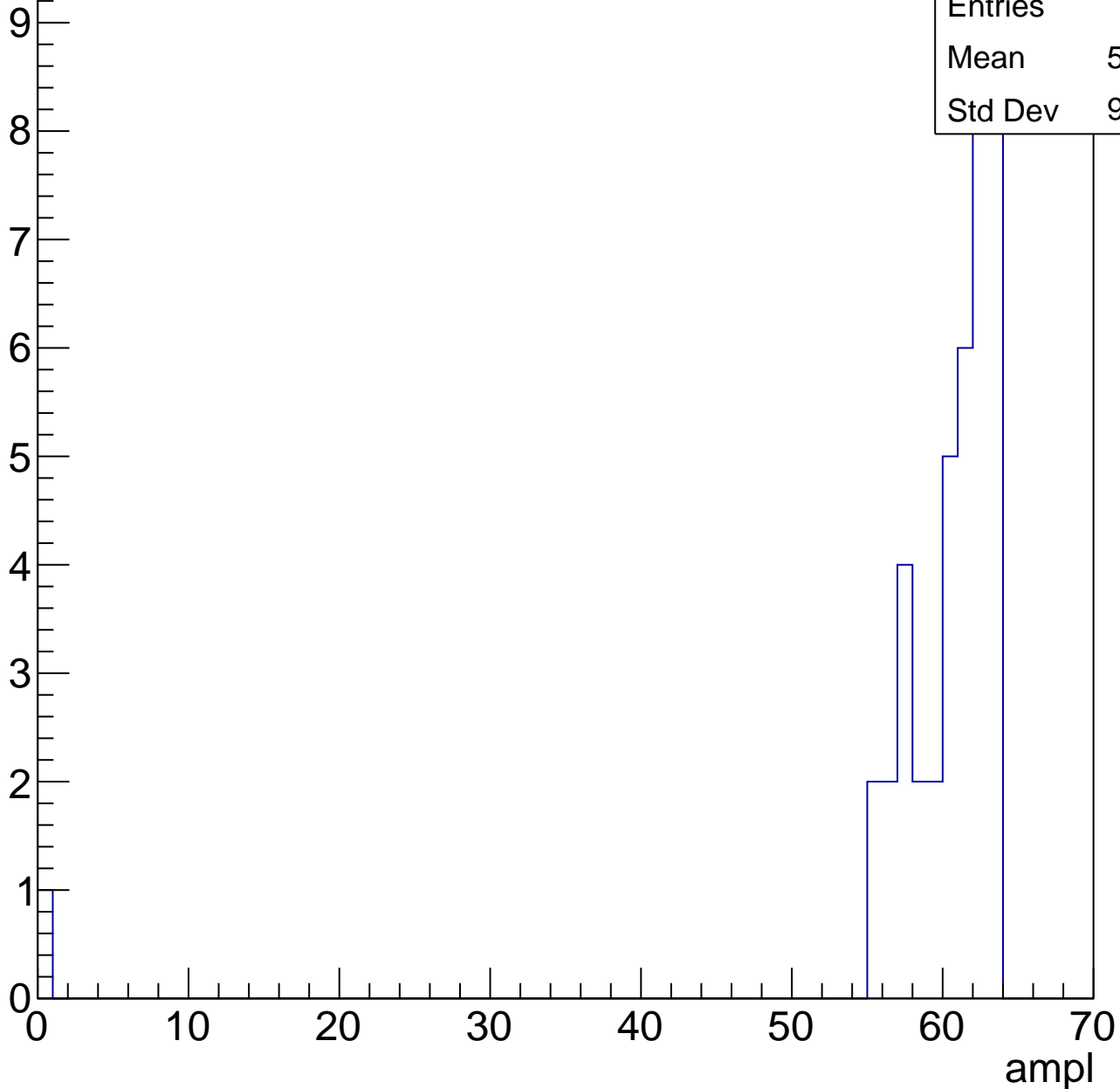


B1L101S, U21-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.93
Std Dev	9.515



B1L101S, U21-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch114, adc0

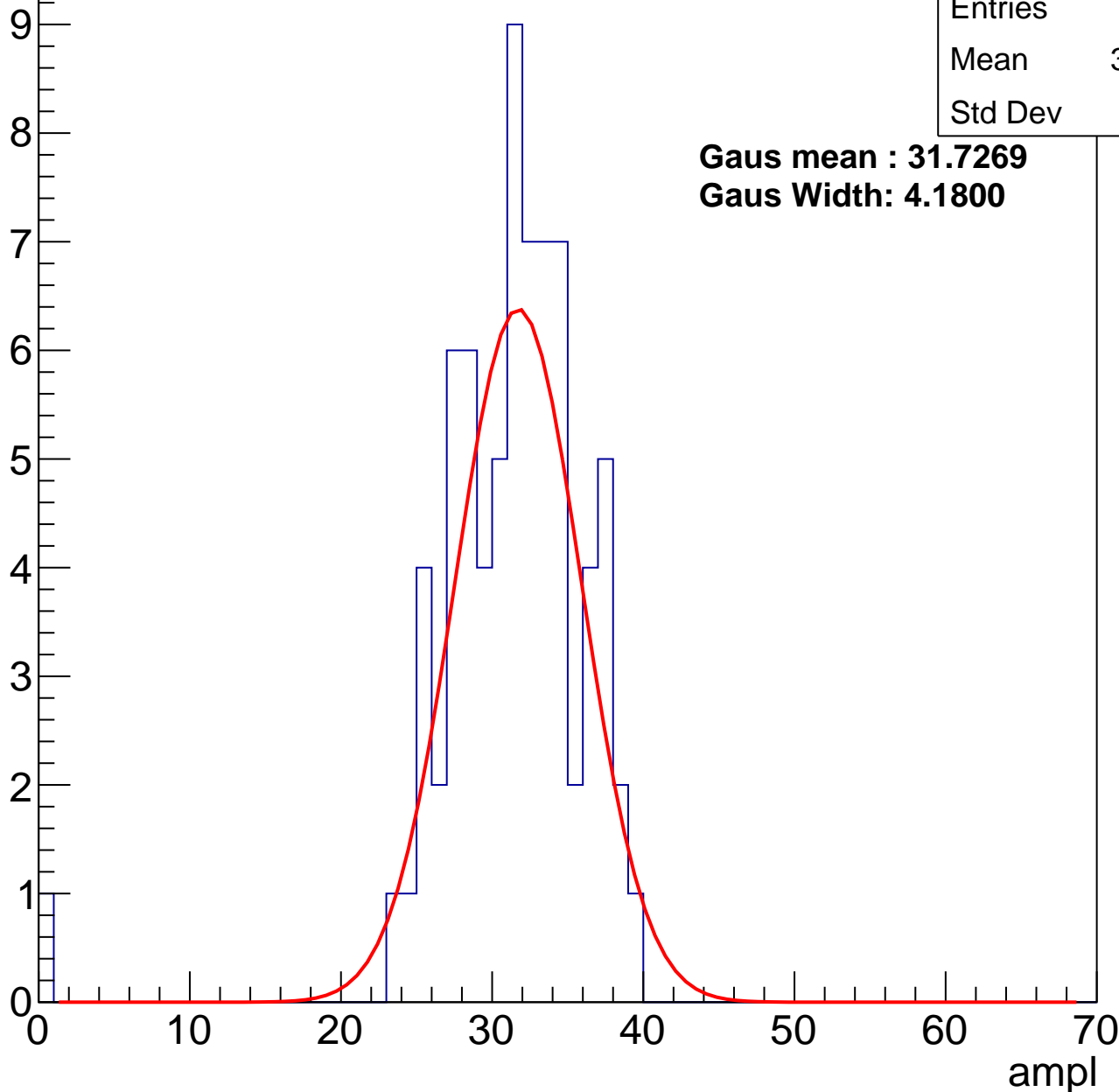
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.82
Std Dev	5.21

Gaus mean : 31.7269

Gaus Width: 4.1800



B1L101S, U21-ch114, adc1

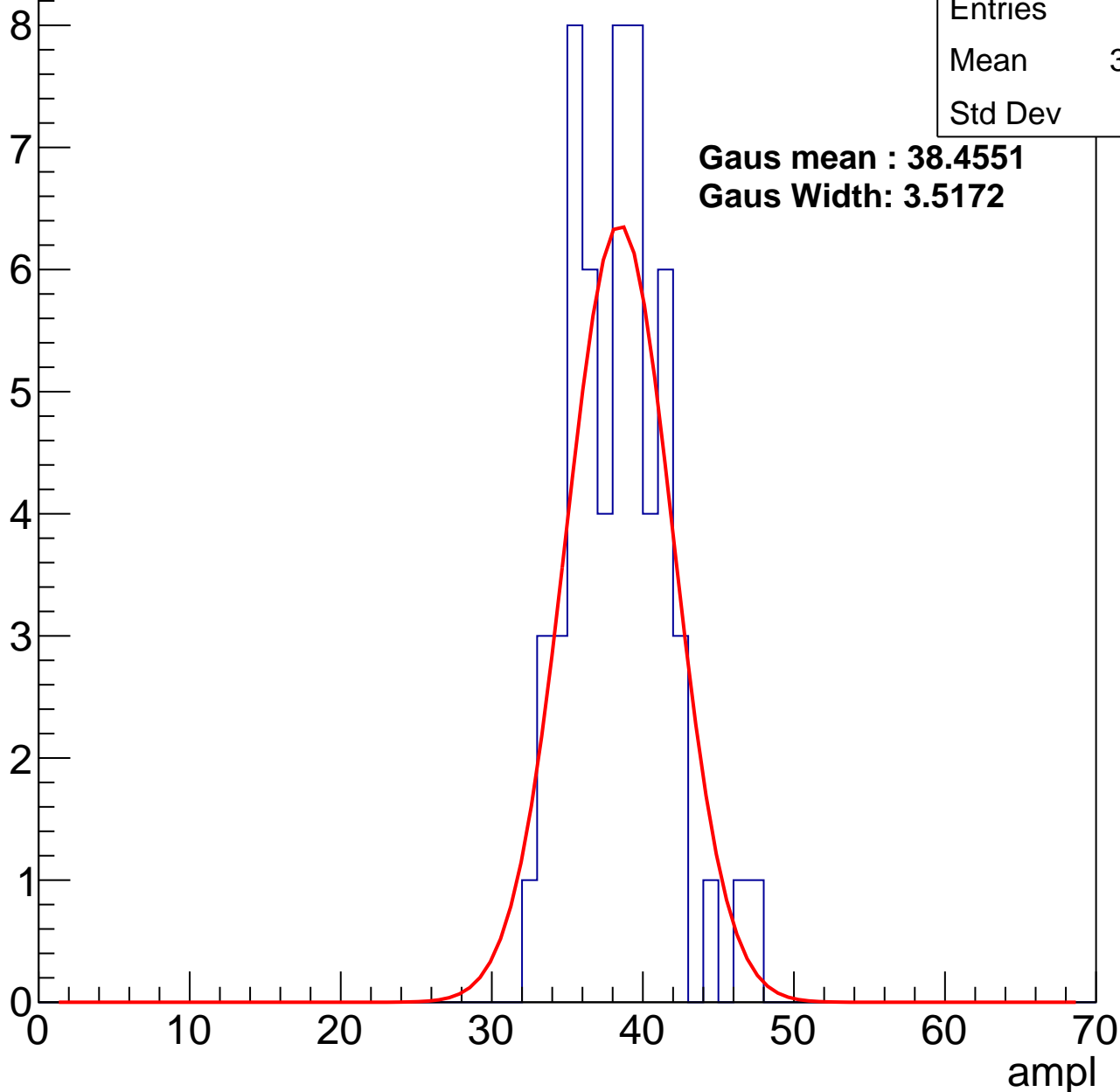
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	37.93
Std Dev	3.15

Gaus mean : 38.4551

Gaus Width: 3.5172



B1L101S, U21-ch114, adc2

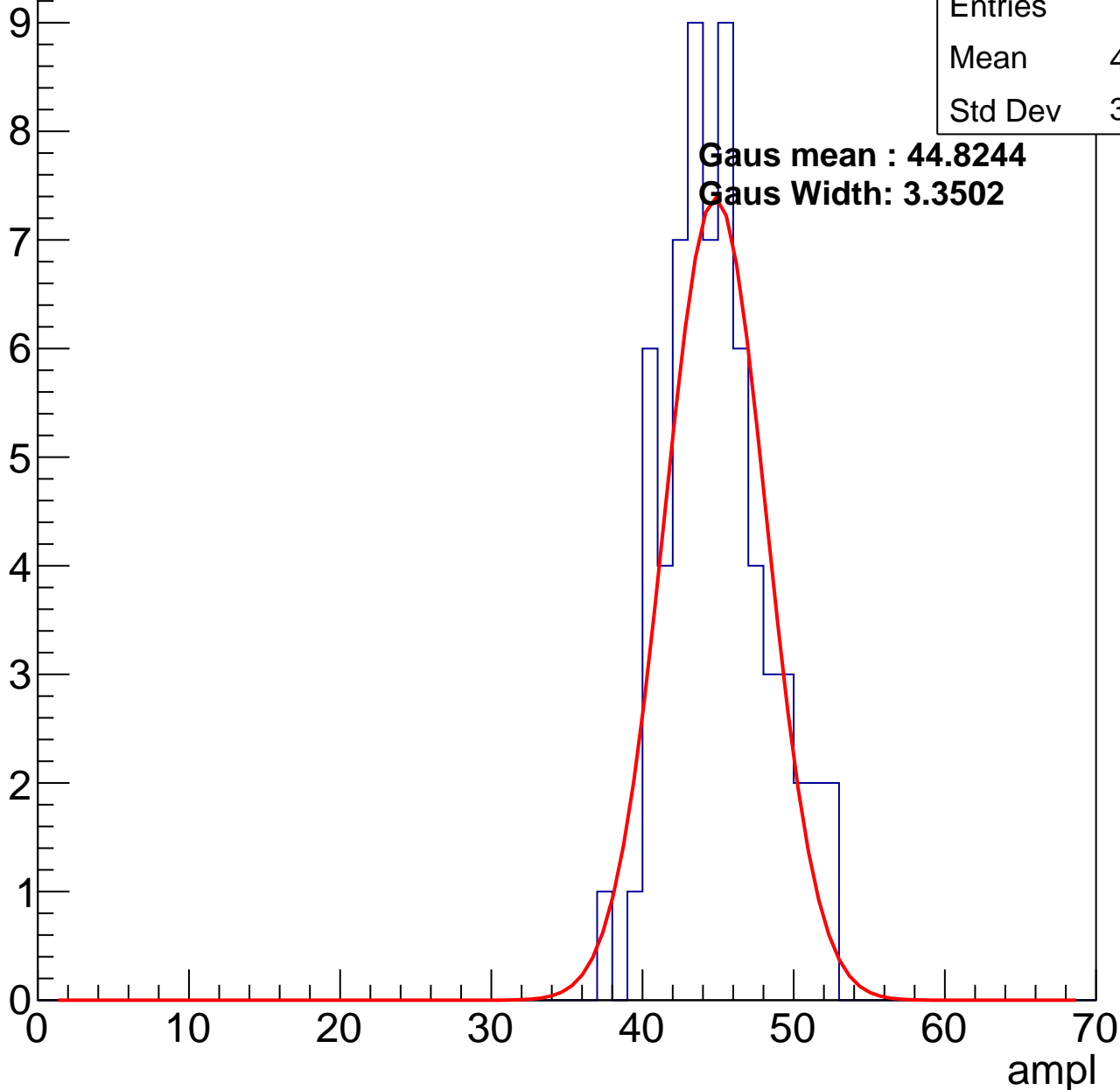
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.47
Std Dev	3.299

Gaus mean : 44.8244

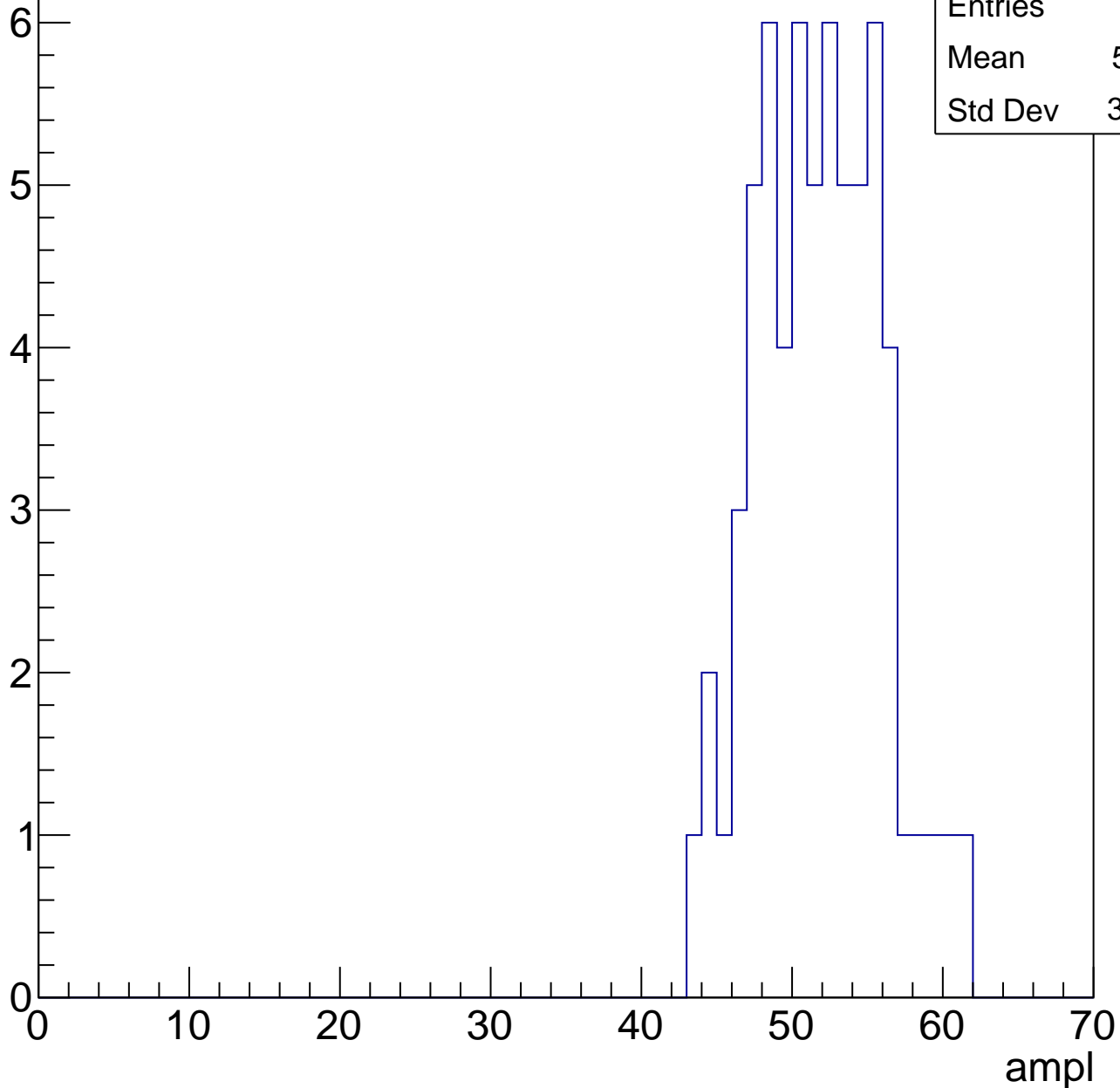
Gaus Width: 3.3502



B1L101S, U21-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



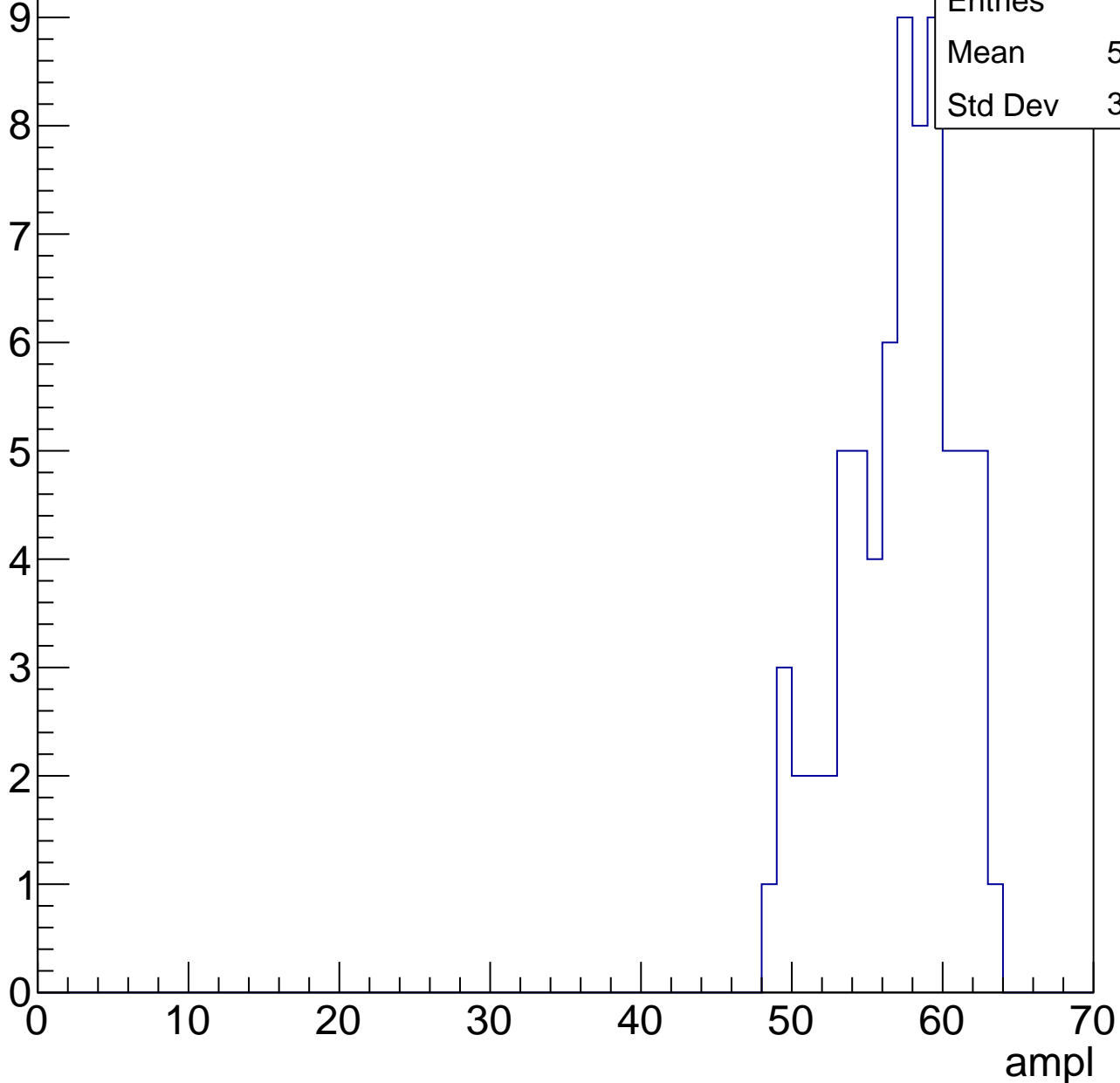
Entries	64
Mean	51.31
Std Dev	3.992

B1L101S, U21-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	56.64
Std Dev	3.653

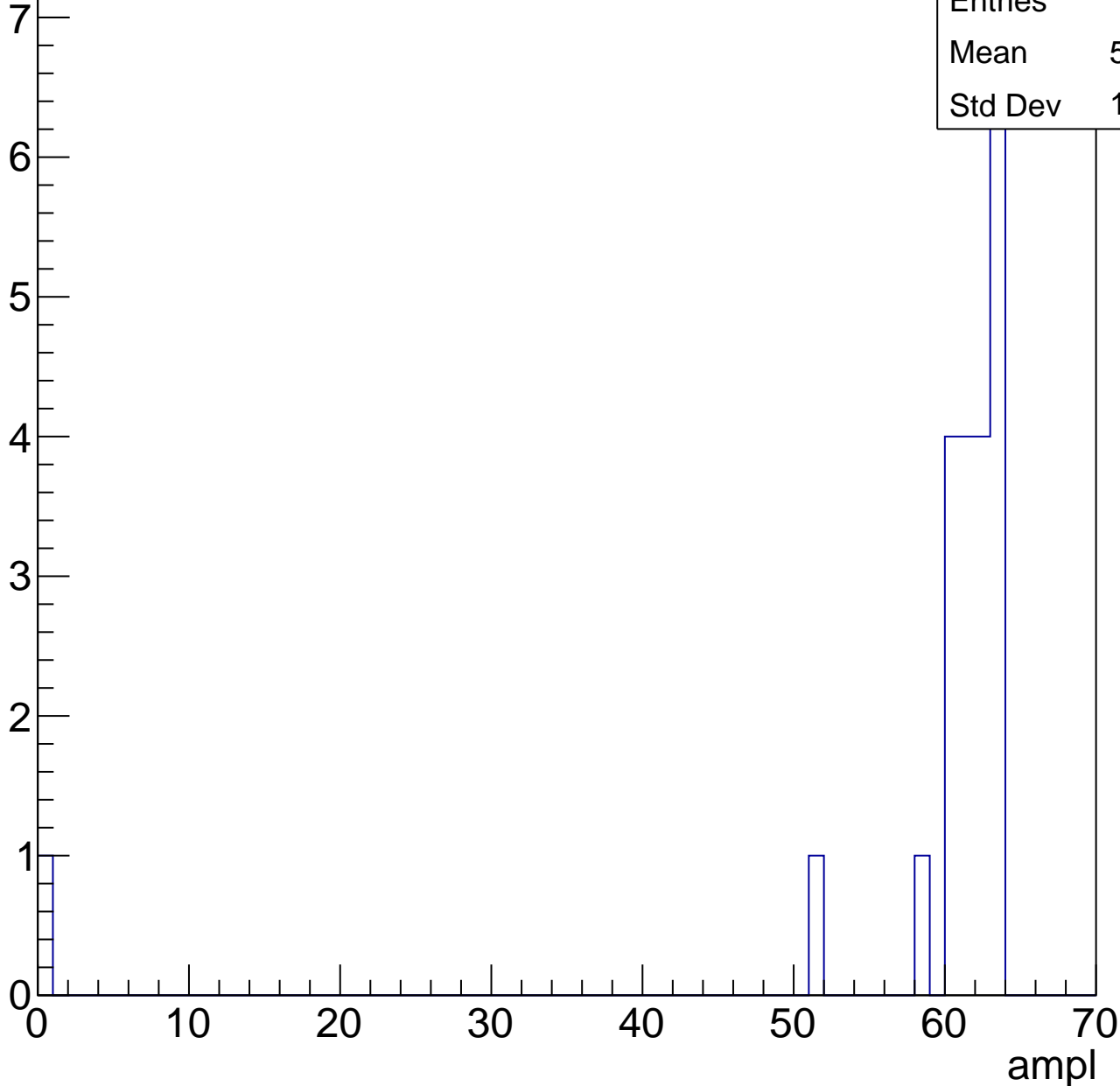


B1L101S, U21-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	22
Mean	58.27
Std Dev	12.97



B1L101S, U21-ch114, adc6

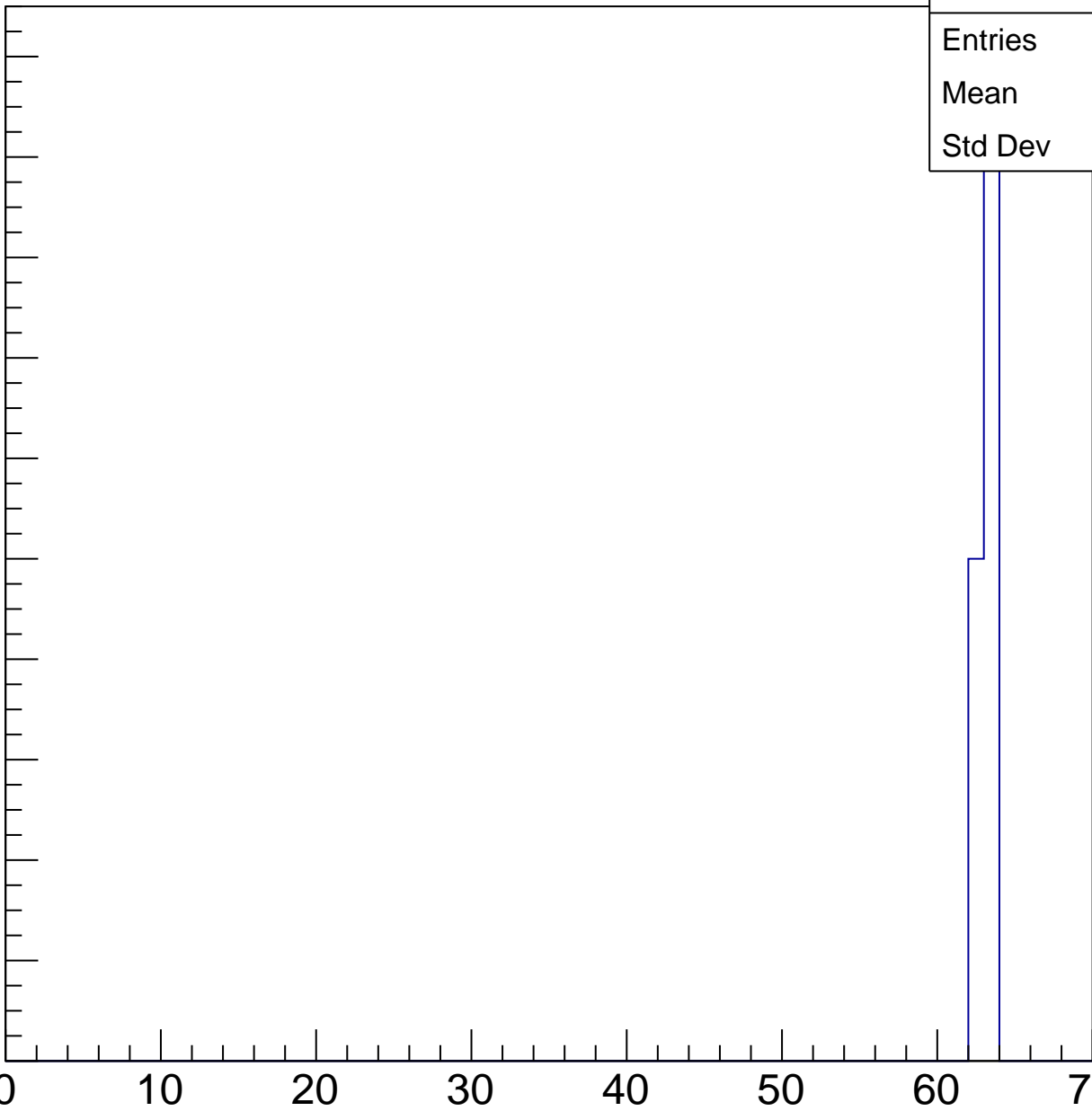
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L101S, U21-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch115, adc0

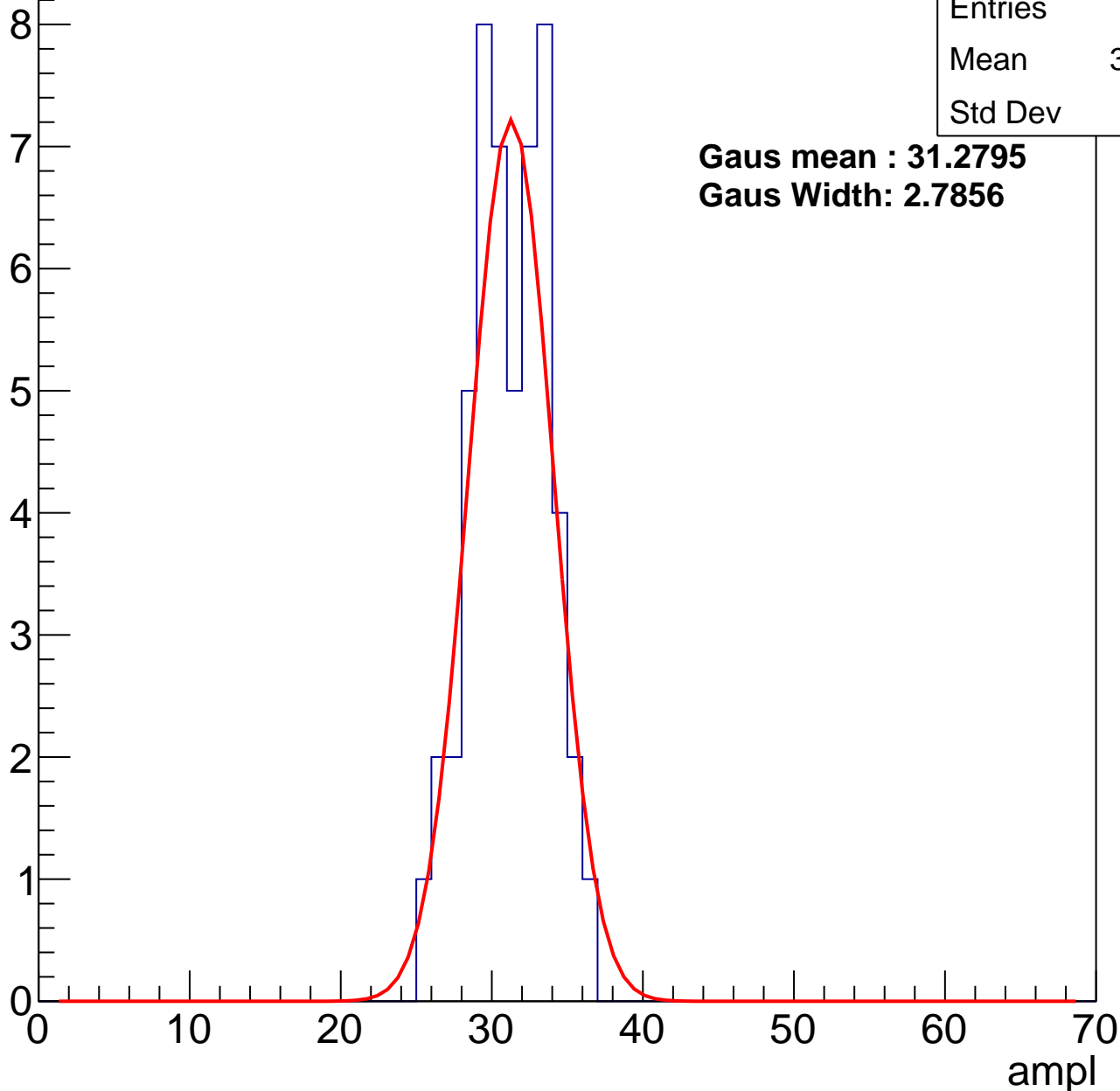
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	30.73
Std Dev	2.52

Gaus mean : 31.2795

Gaus Width: 2.7856



B1L101S, U21-ch115, adc1

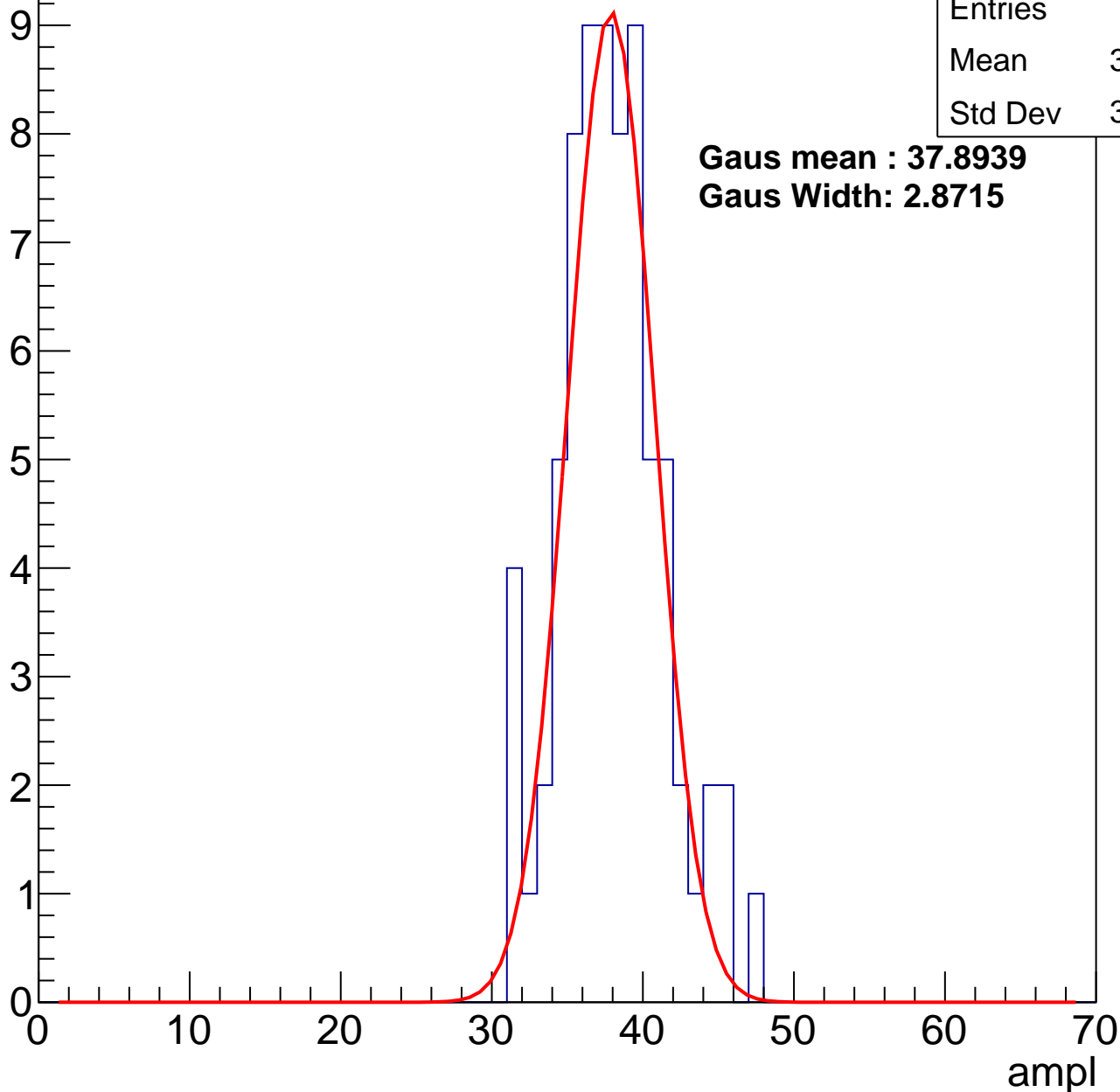
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	37.55
Std Dev	3.404

Gaus mean : 37.8939

Gaus Width: 2.8715



B1L101S, U21-ch115, adc2

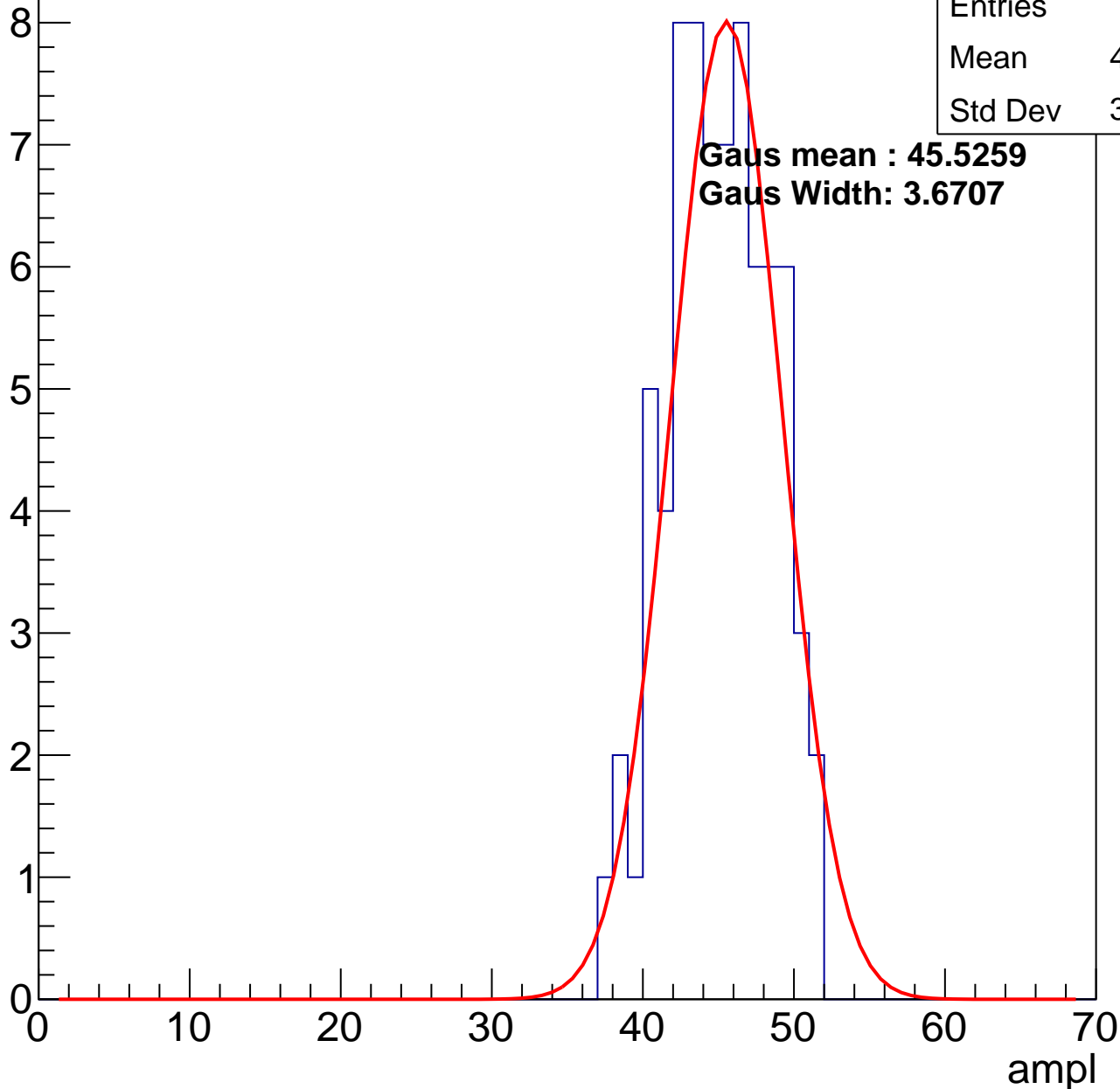
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	44.64
Std Dev	3.323

Gaus mean : 45.5259

Gaus Width: 3.6707

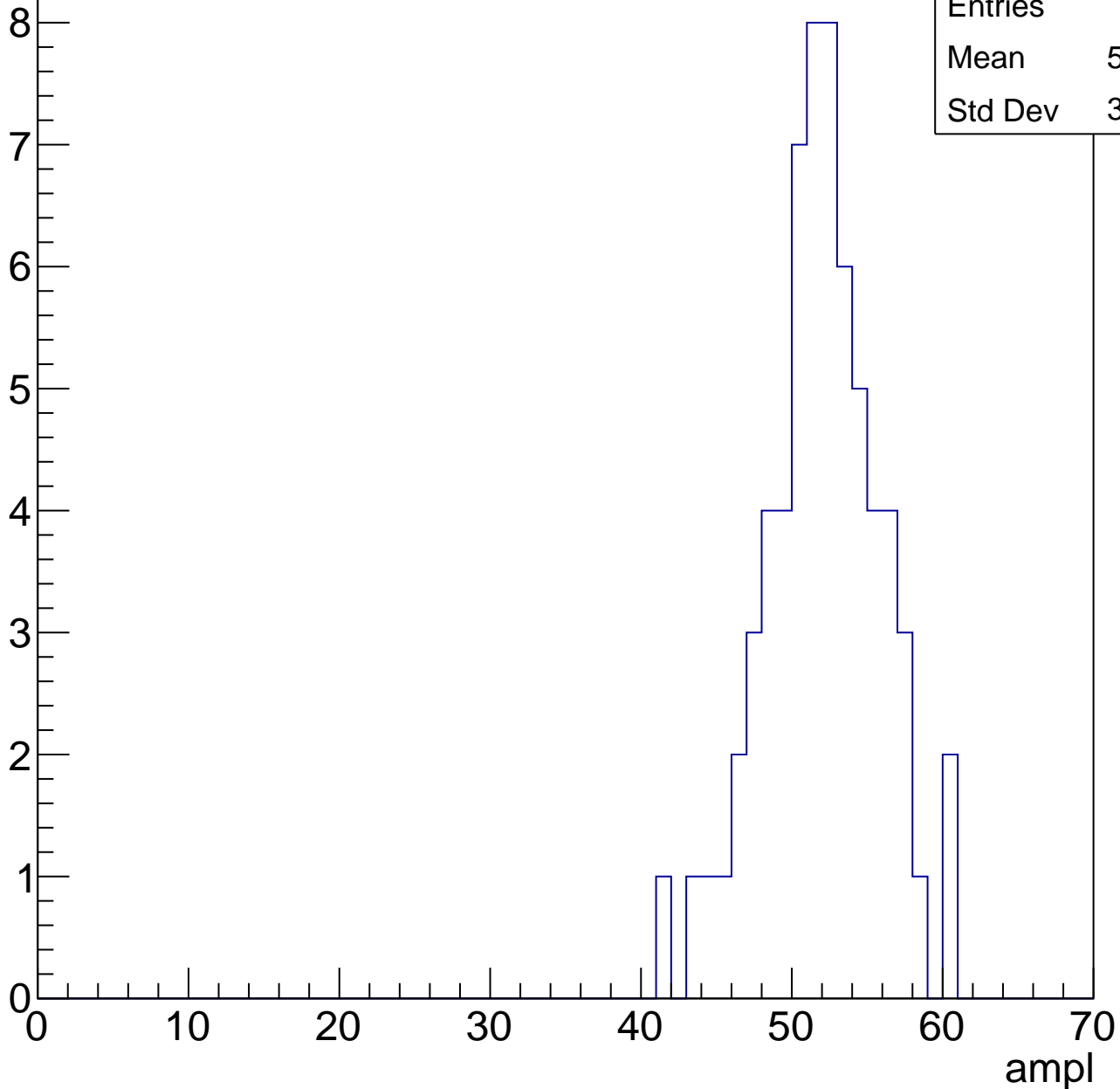


B1L101S, U21-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	51.52
Std Dev	3.819

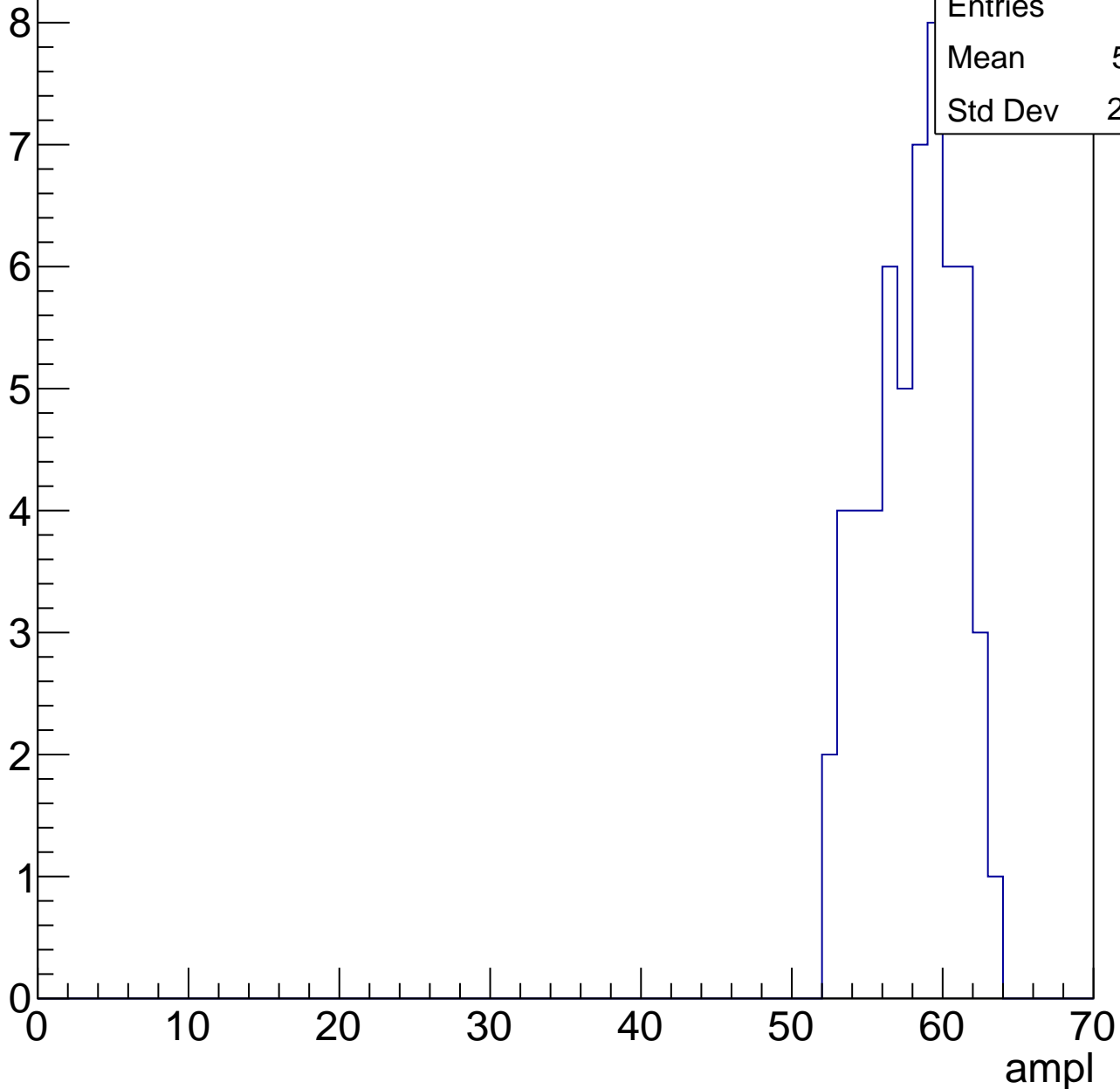


B1L101S, U21-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	57.61
Std Dev	2.839

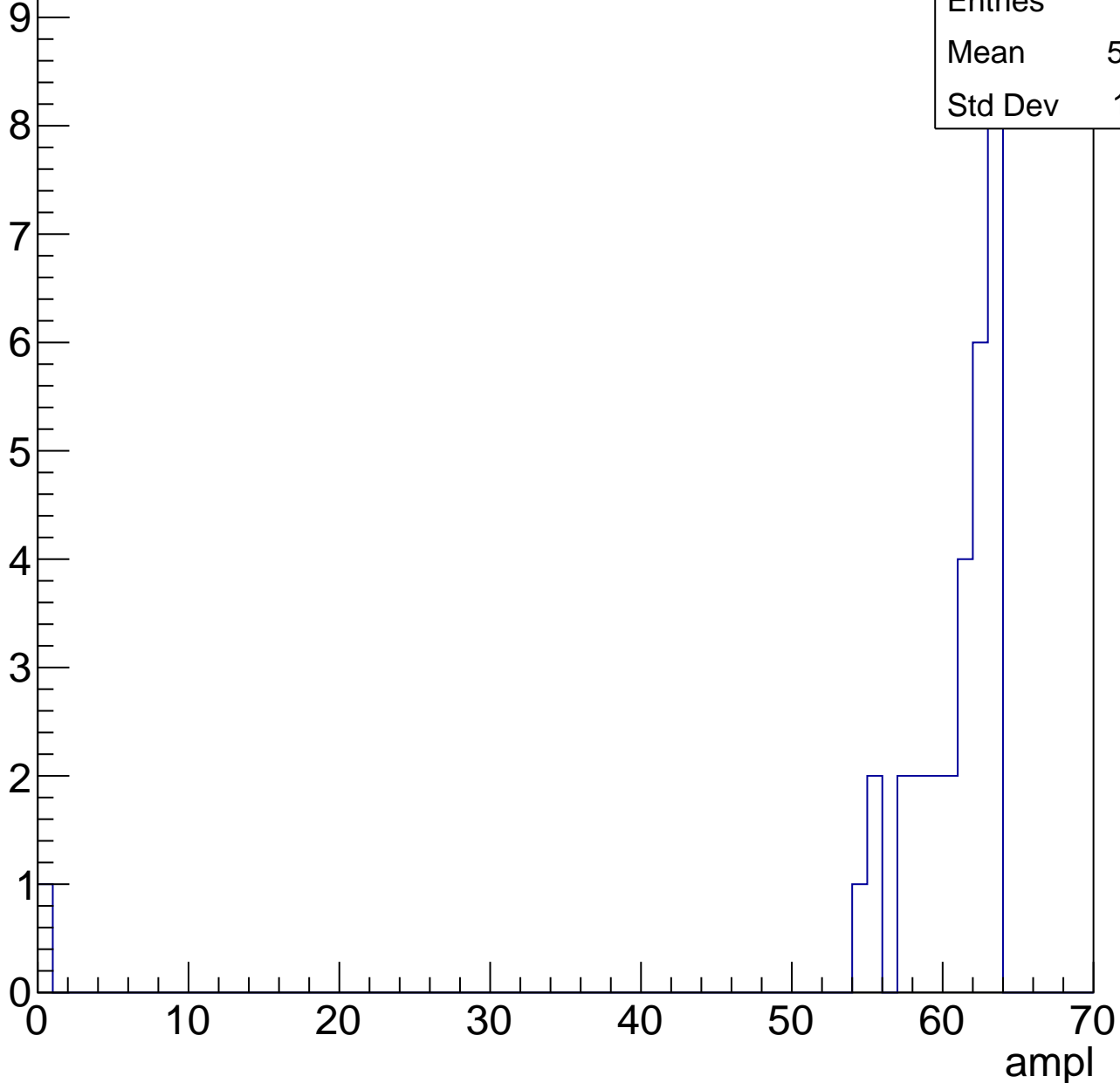


B1L101S, U21-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	58.55
Std Dev	11.01



B1L101S, U21-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch116, adc0

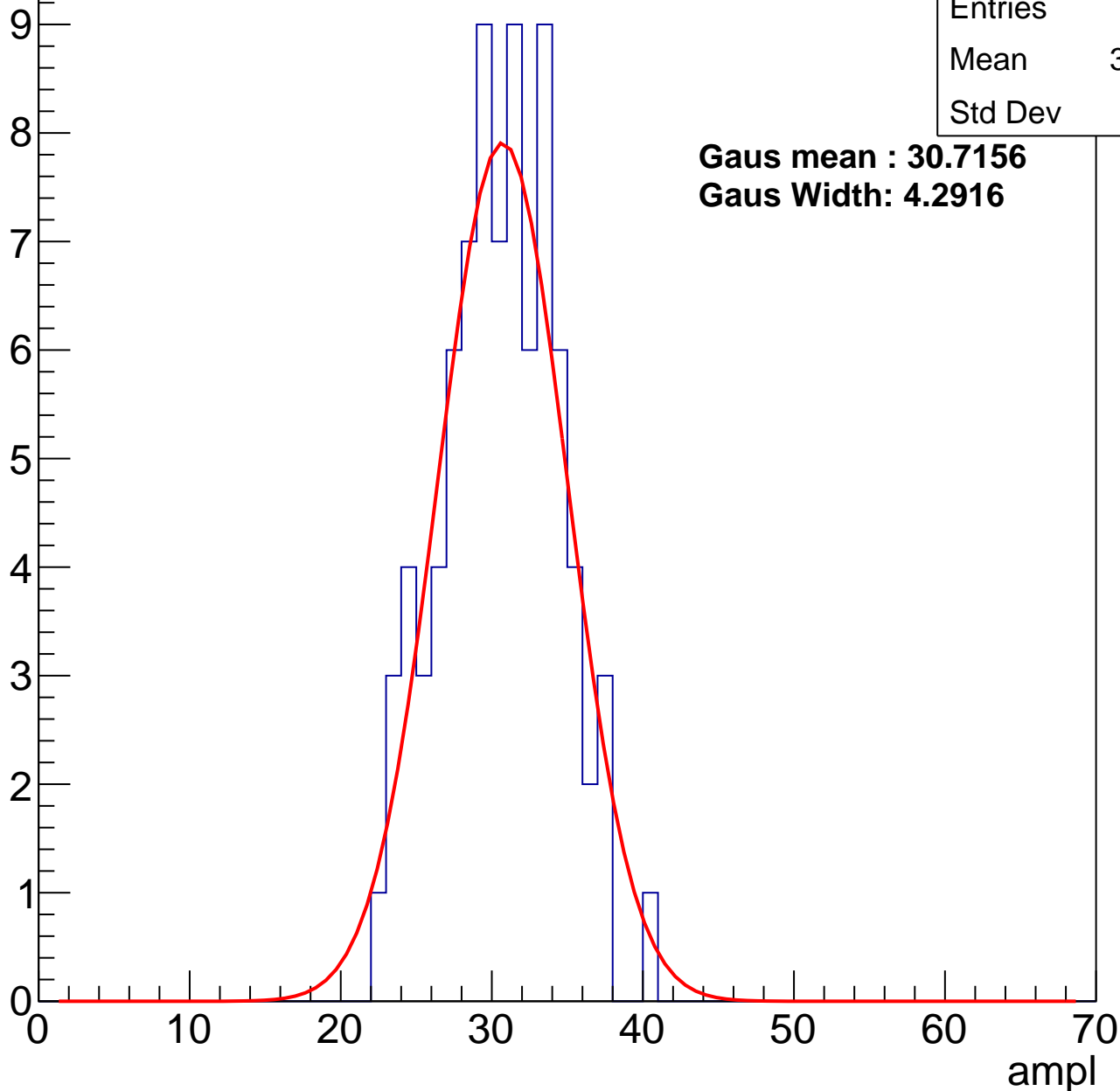
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	30.12
Std Dev	3.8

Gaus mean : 30.7156

Gaus Width: 4.2916



B1L101S, U21-ch116, adc1

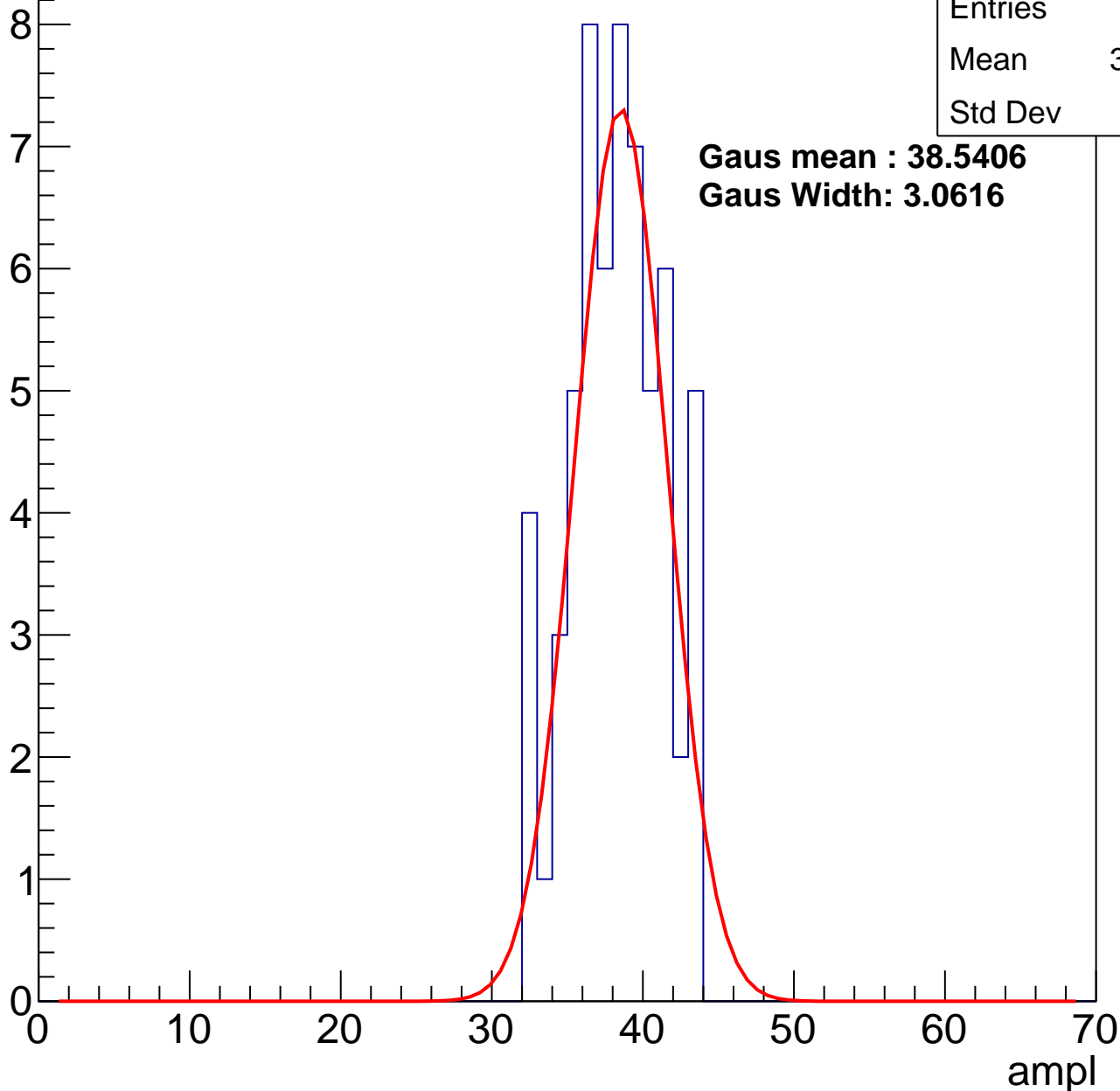
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.83
Std Dev	2.99

Gaus mean : 38.5406

Gaus Width: 3.0616



B1L101S, U21-ch116, adc2

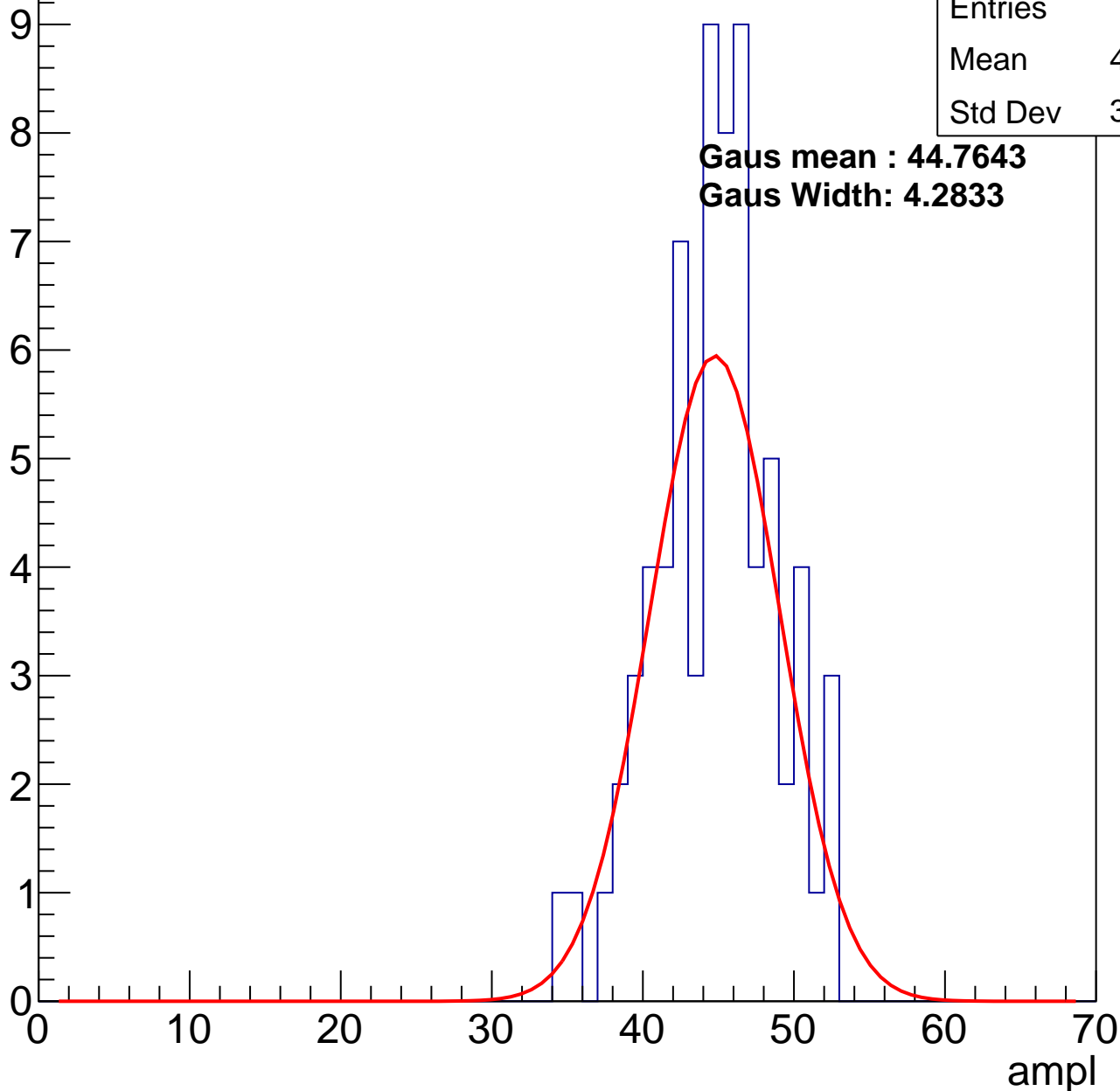
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	44.35
Std Dev	3.926

Gaus mean : 44.7643

Gaus Width: 4.2833

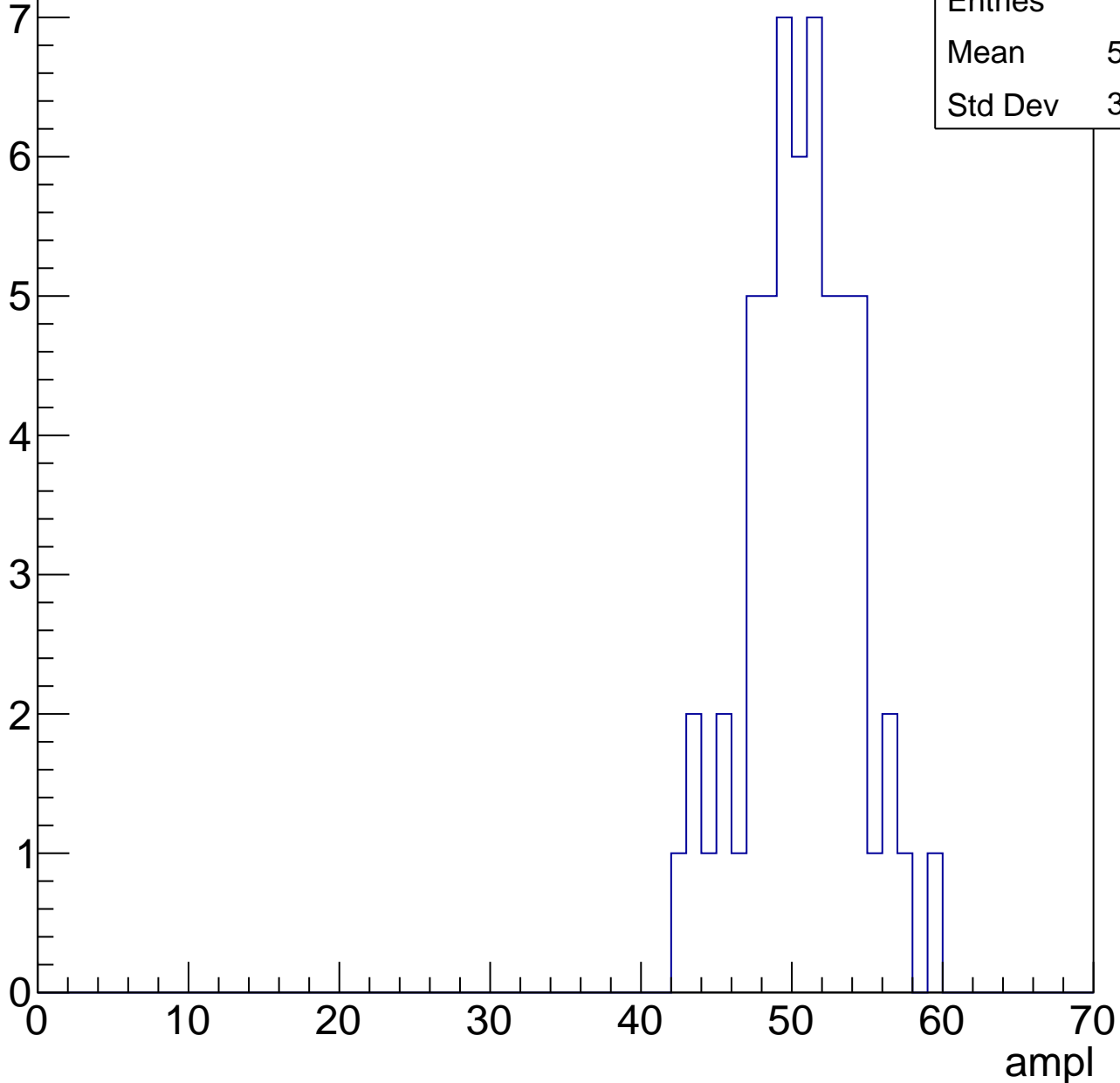


B1L101S, U21-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	50.19
Std Dev	3.536

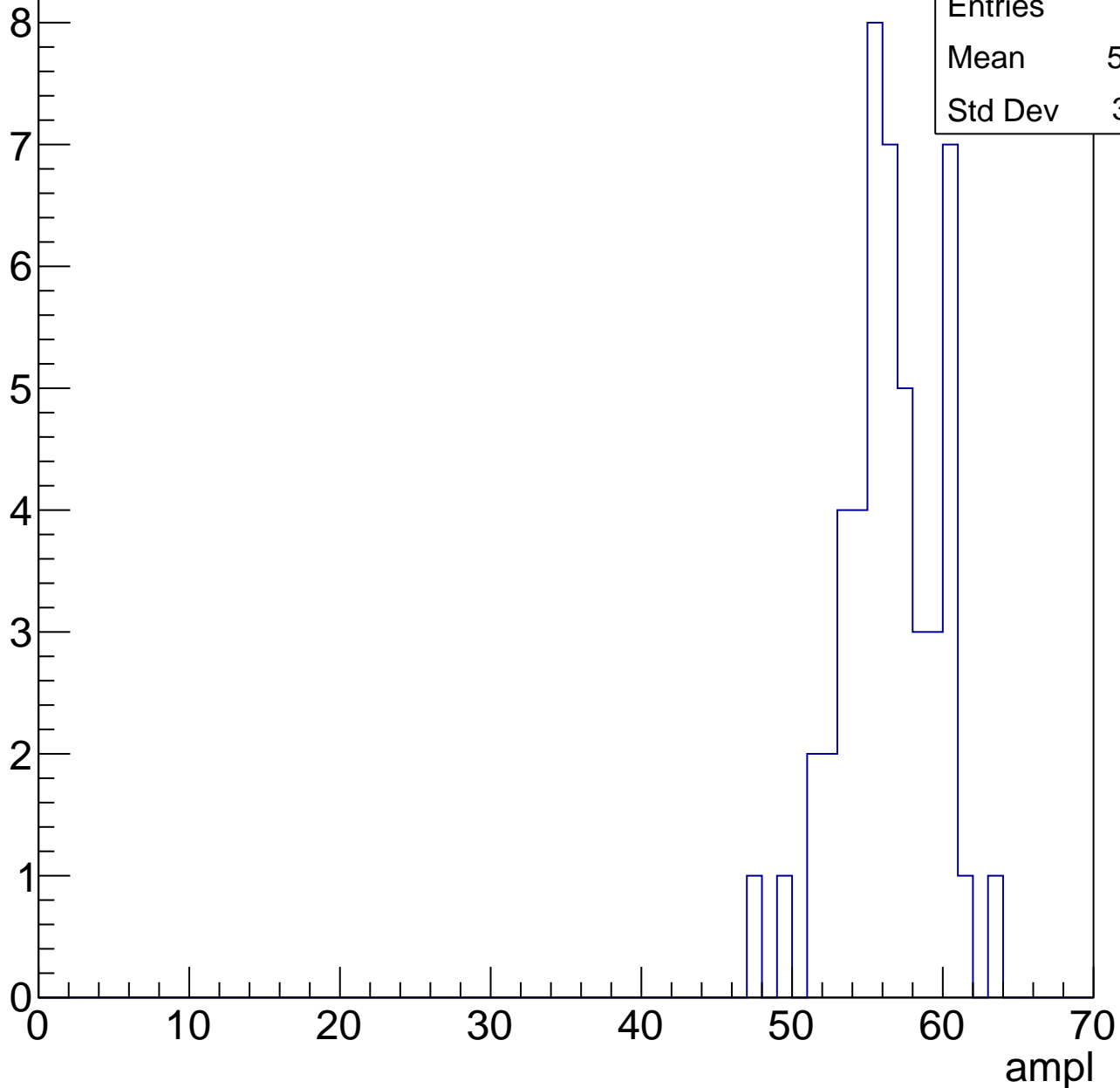


B1L101S, U21-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.96
Std Dev	3.201

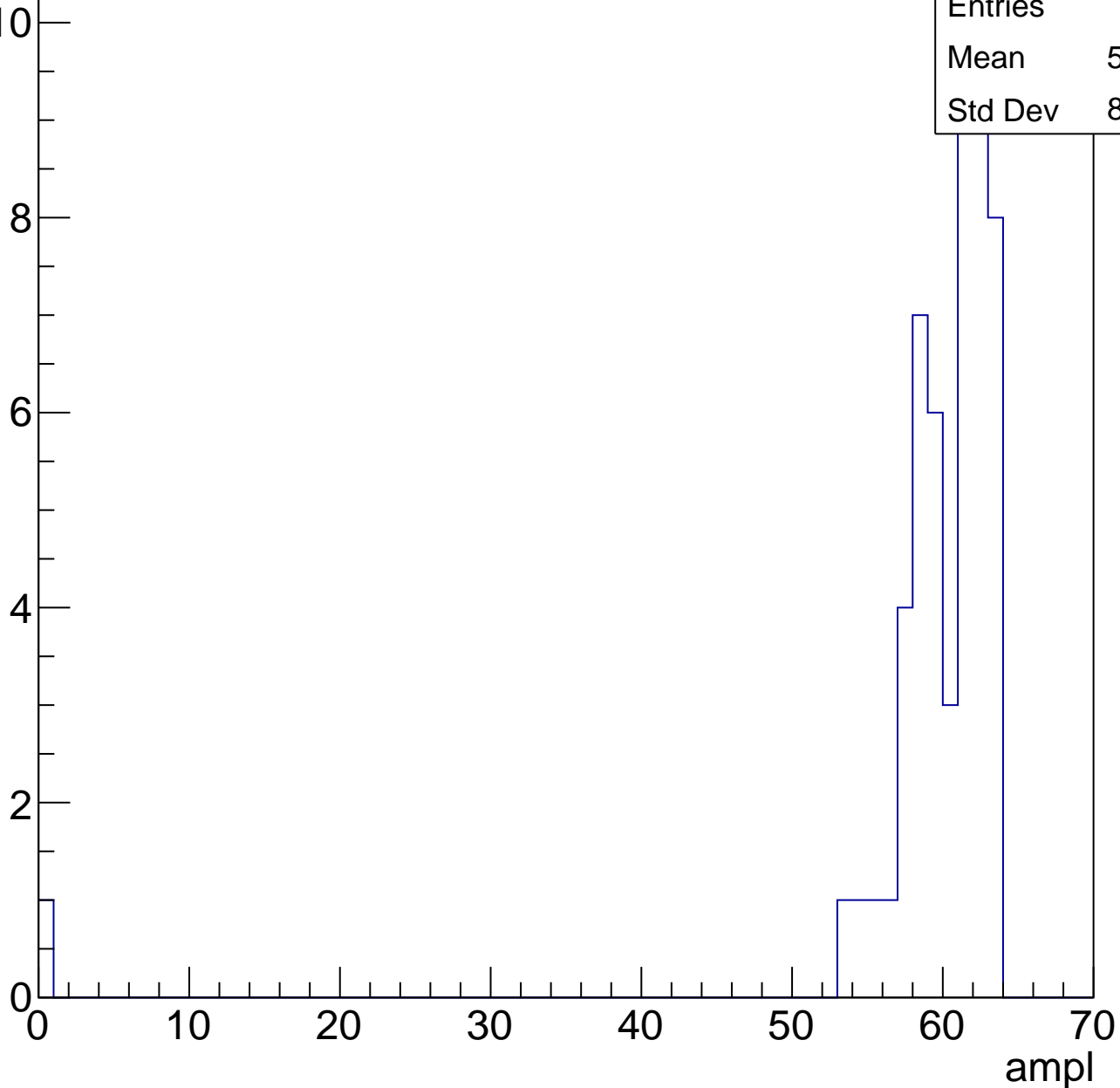


B1L101S, U21-ch116, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	58.83
Std Dev	8.599



B1L101S, U21-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U21-ch117, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	30.59
Std Dev	3.683

Gaus mean : 30.9031

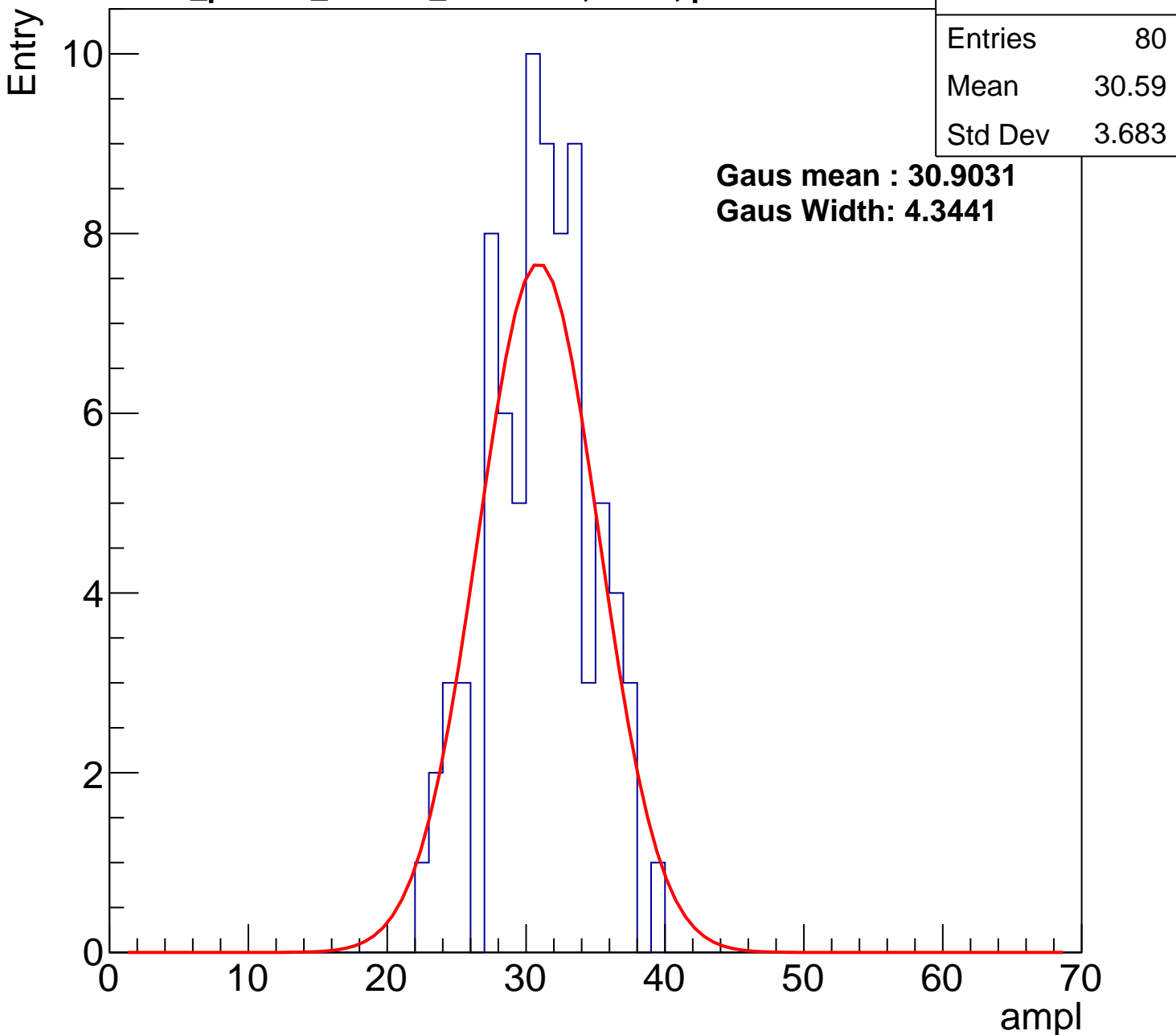
Gaus Width: 4.3441

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch117, adc1

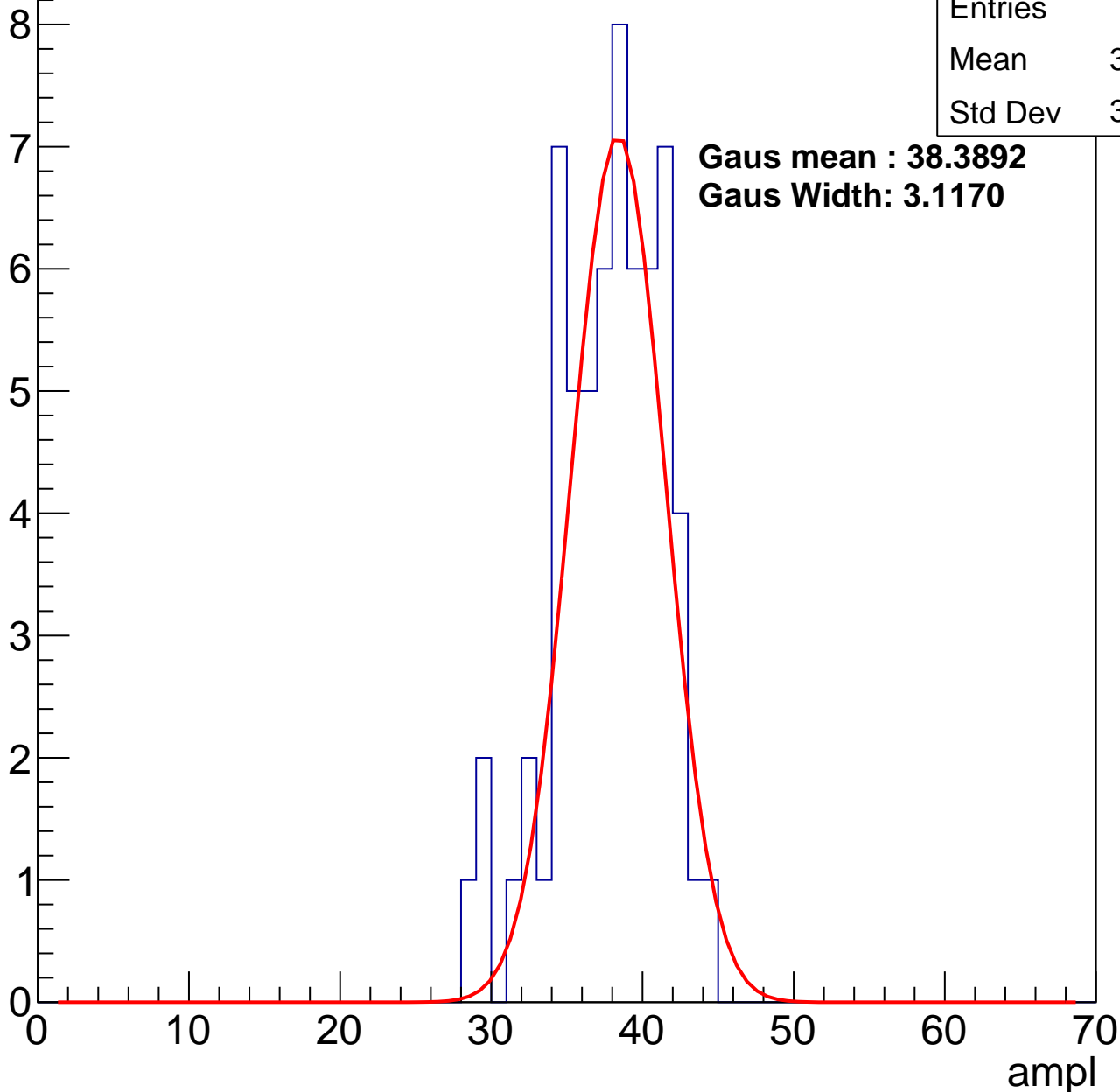
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	37.29
Std Dev	3.512

Gaus mean : 38.3892

Gaus Width: 3.1170



B1L101S, U21-ch117, adc2

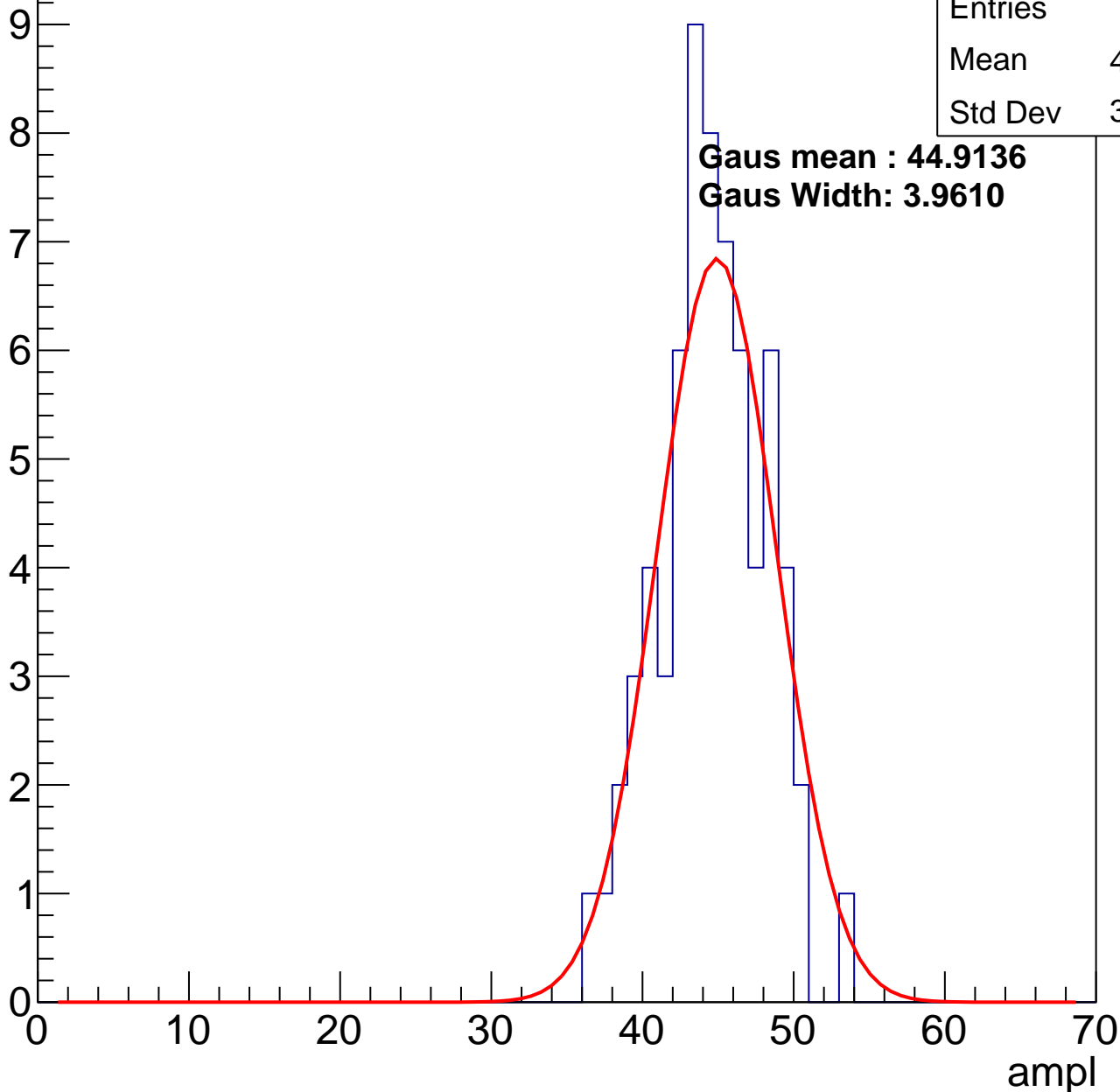
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	44.12
Std Dev	3.453

Gaus mean : 44.9136

Gaus Width: 3.9610



B1L101S, U21-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

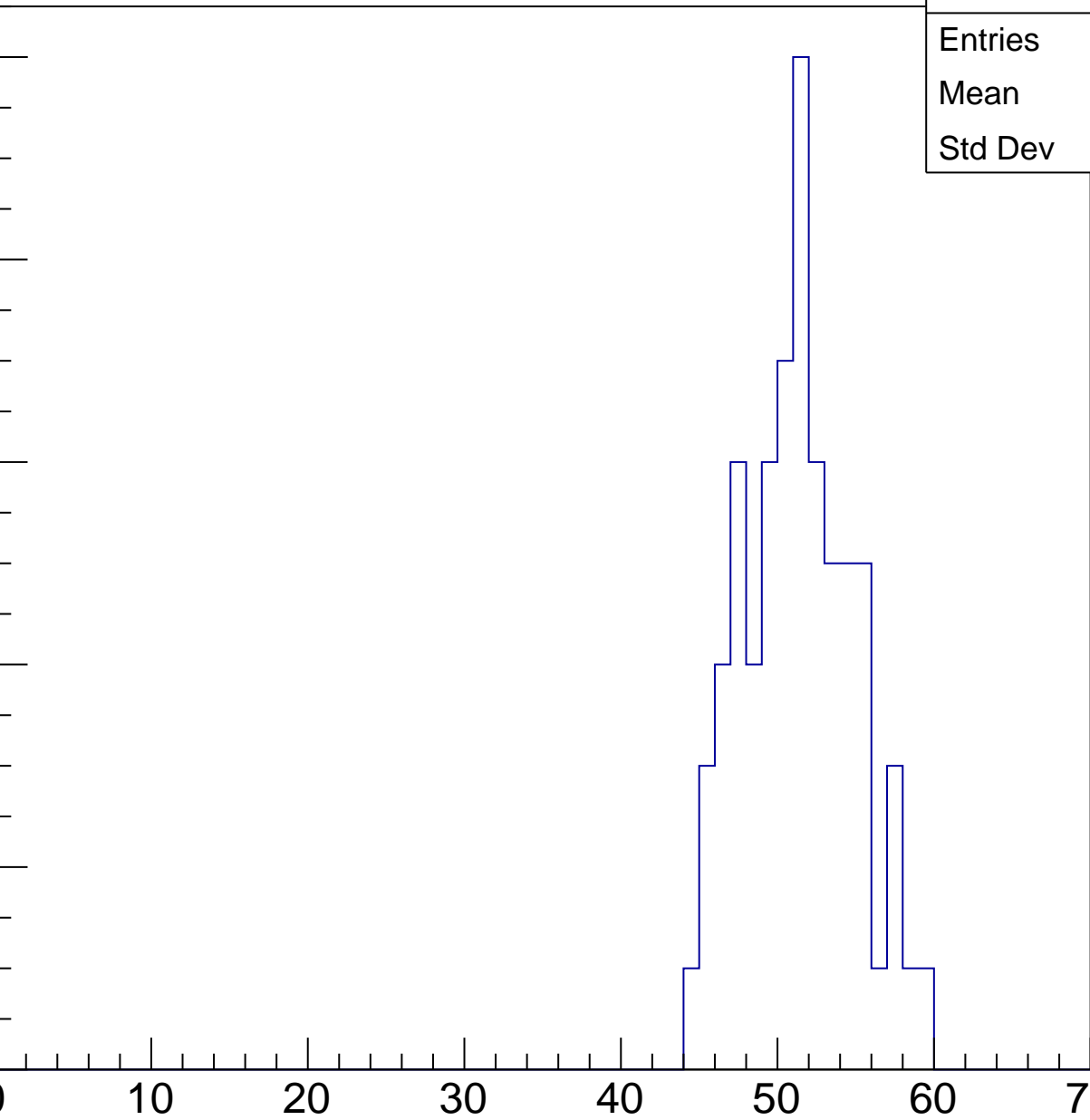
Entries	68
Mean	50.84
Std Dev	3.462

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

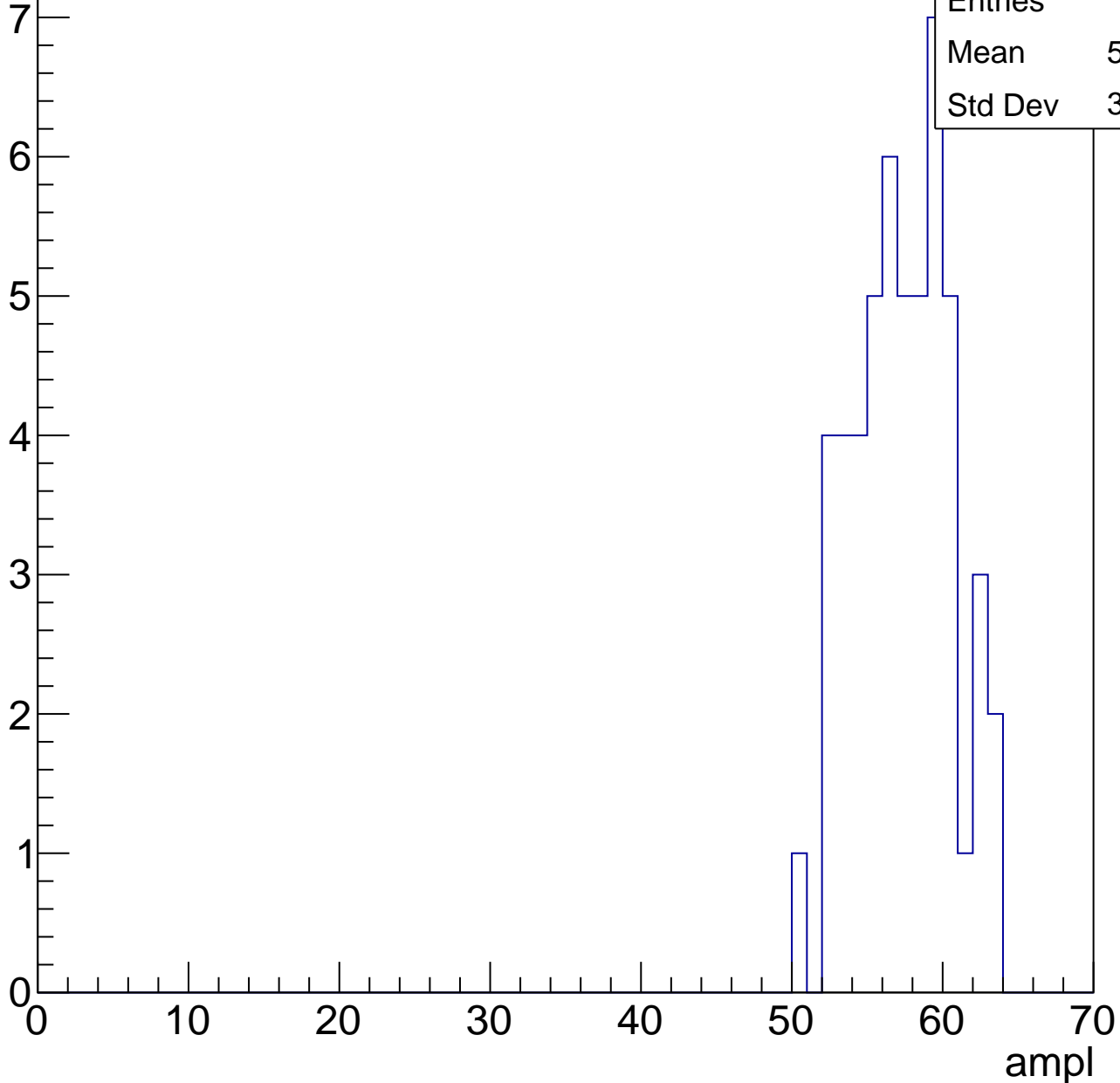


B1L101S, U21-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.88
Std Dev	3.148

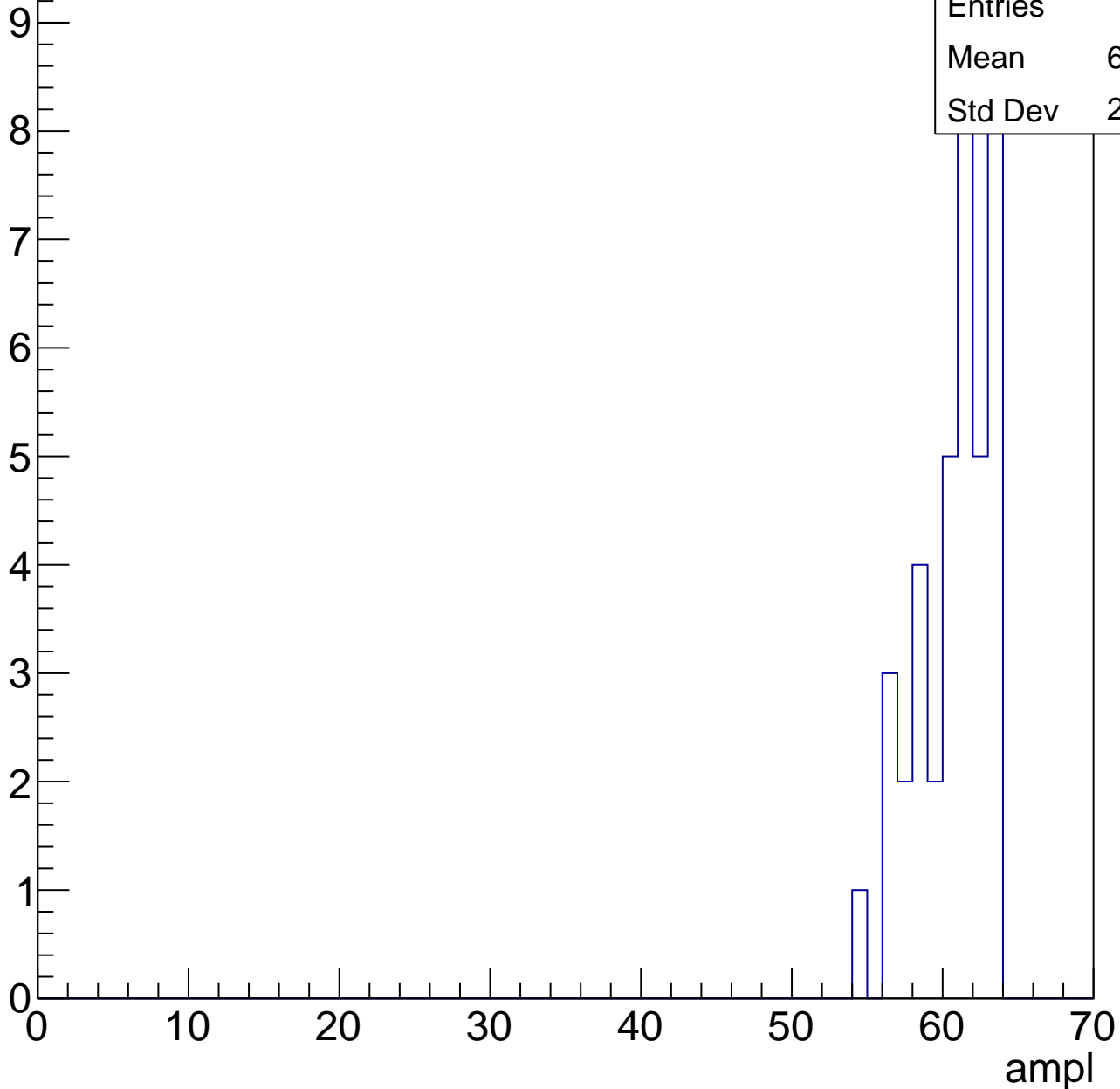


B1L101S, U21-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	60.23
Std Dev	2.369



B1L101S, U21-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

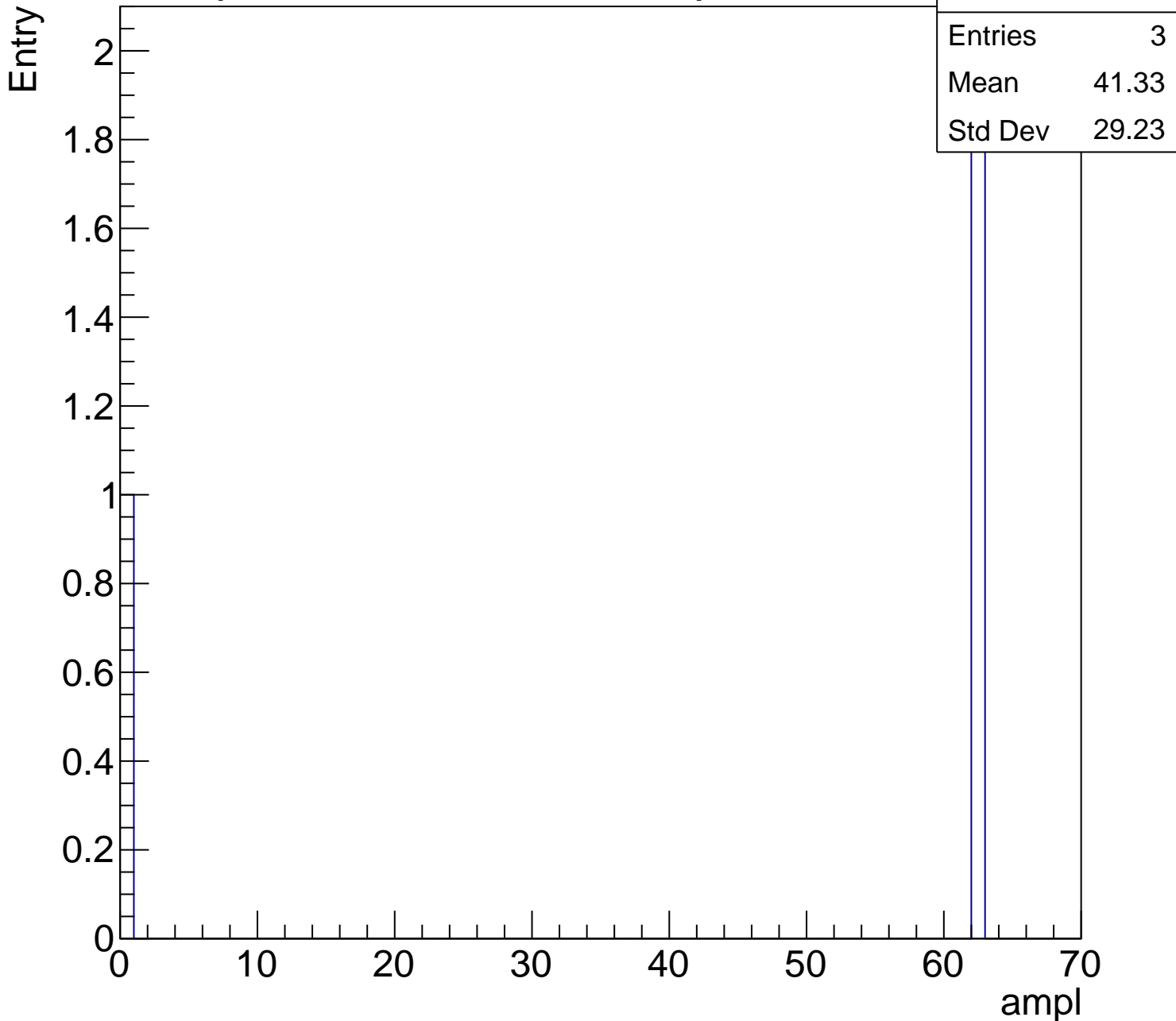
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	41.33
Std Dev	29.23

0 10 20 30 40 50 60 70

ampl



B1L101S, U21-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U21-ch118, adc0

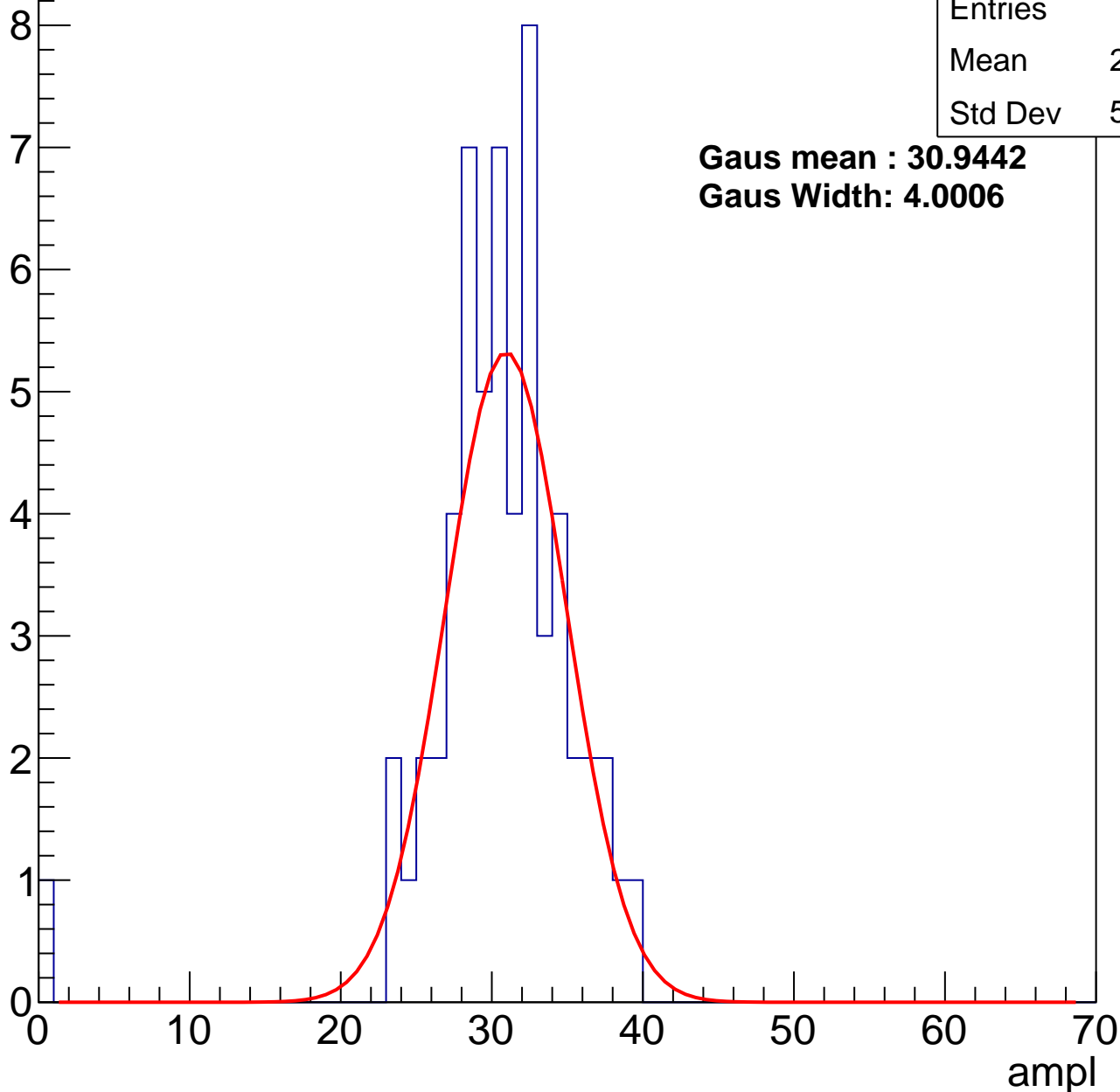
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	29.98
Std Dev	5.377

Gaus mean : 30.9442

Gaus Width: 4.0006



B1L101S, U21-ch118, adc1

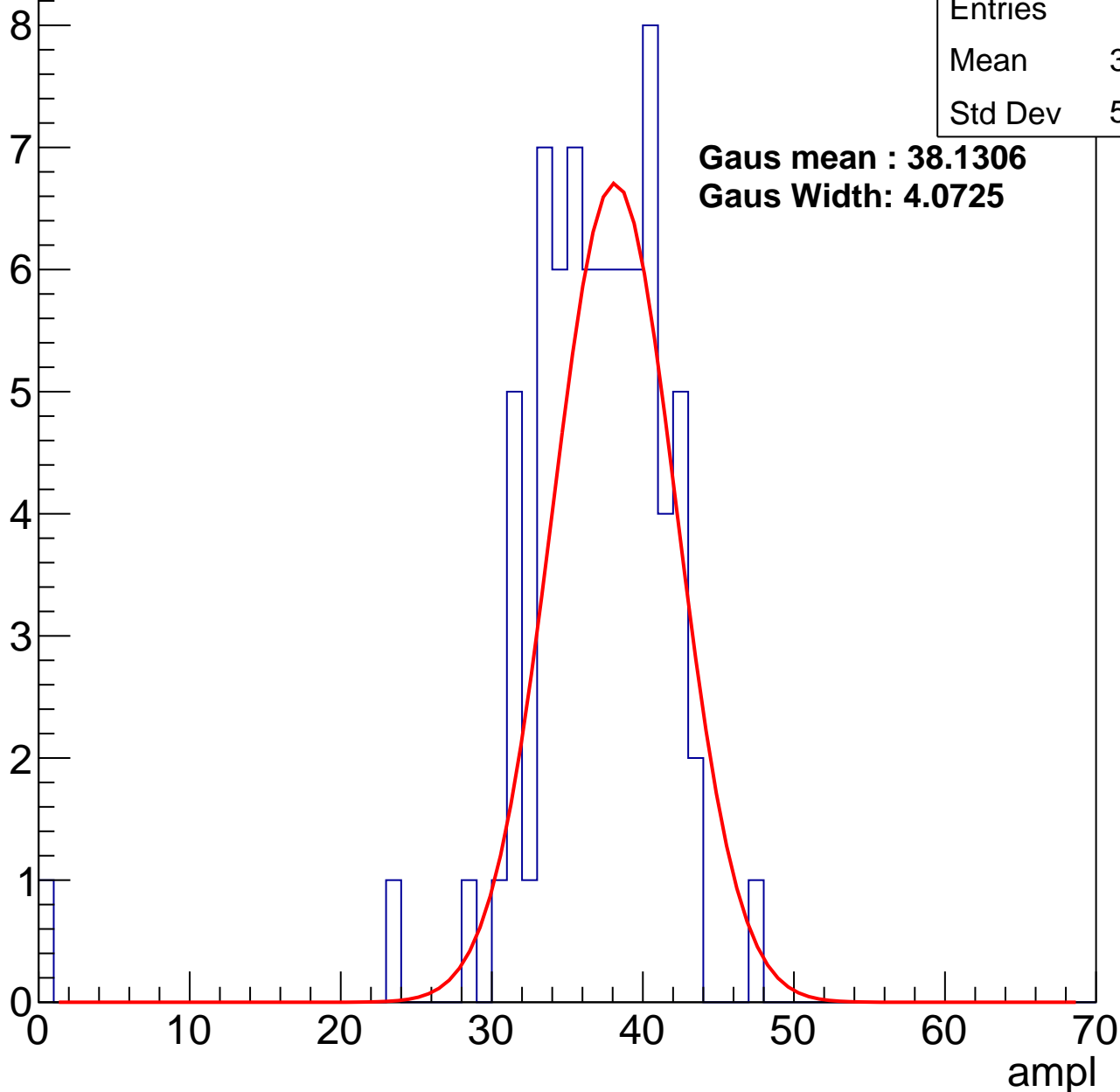
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	36.15
Std Dev	5.823

Gaus mean : 38.1306

Gaus Width: 4.0725



B1L101S, U21-ch118, adc2

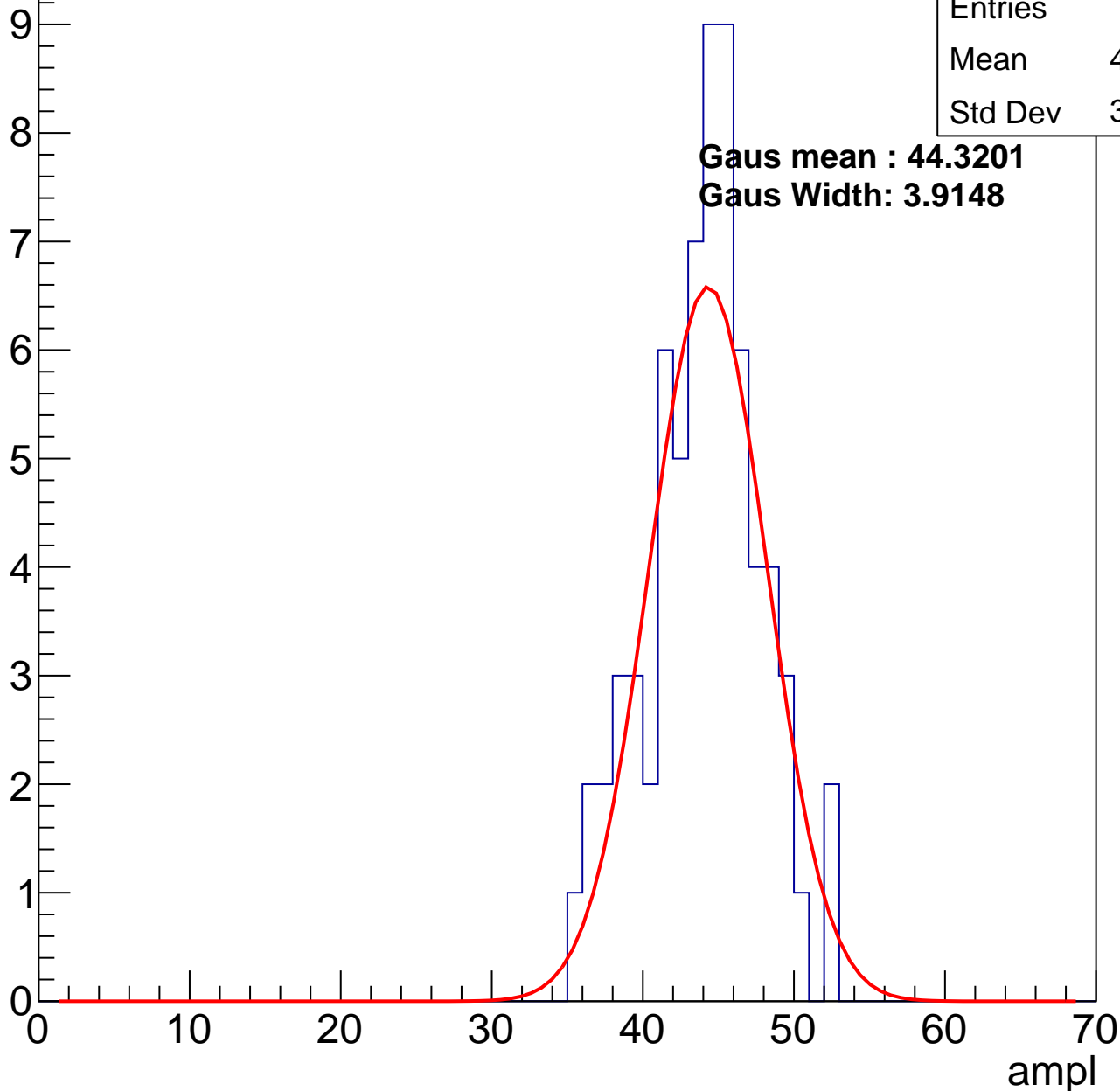
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	43.58
Std Dev	3.735

Gaus mean : 44.3201

Gaus Width: 3.9148

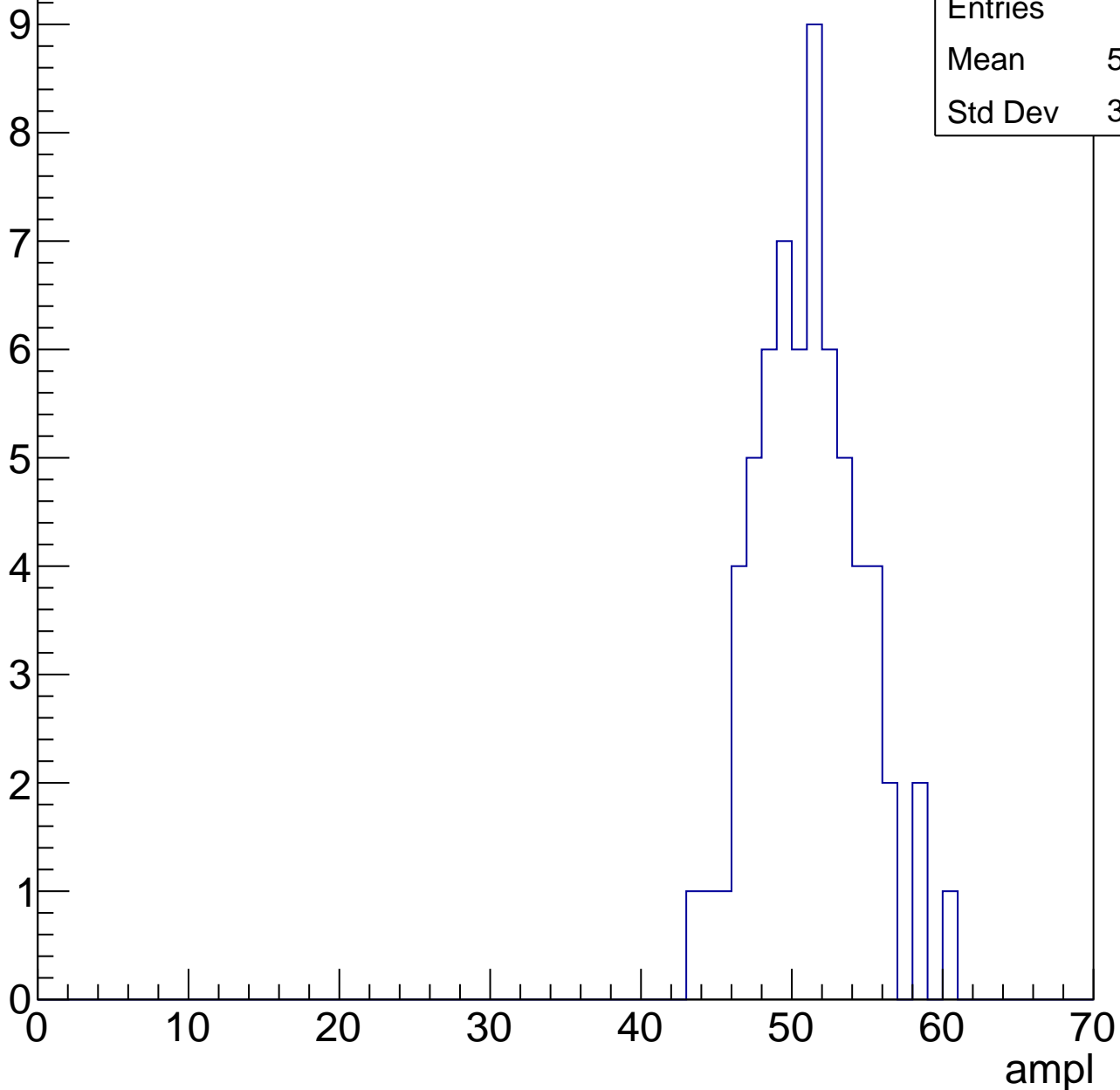


B1L101S, U21-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	50.66
Std Dev	3.456

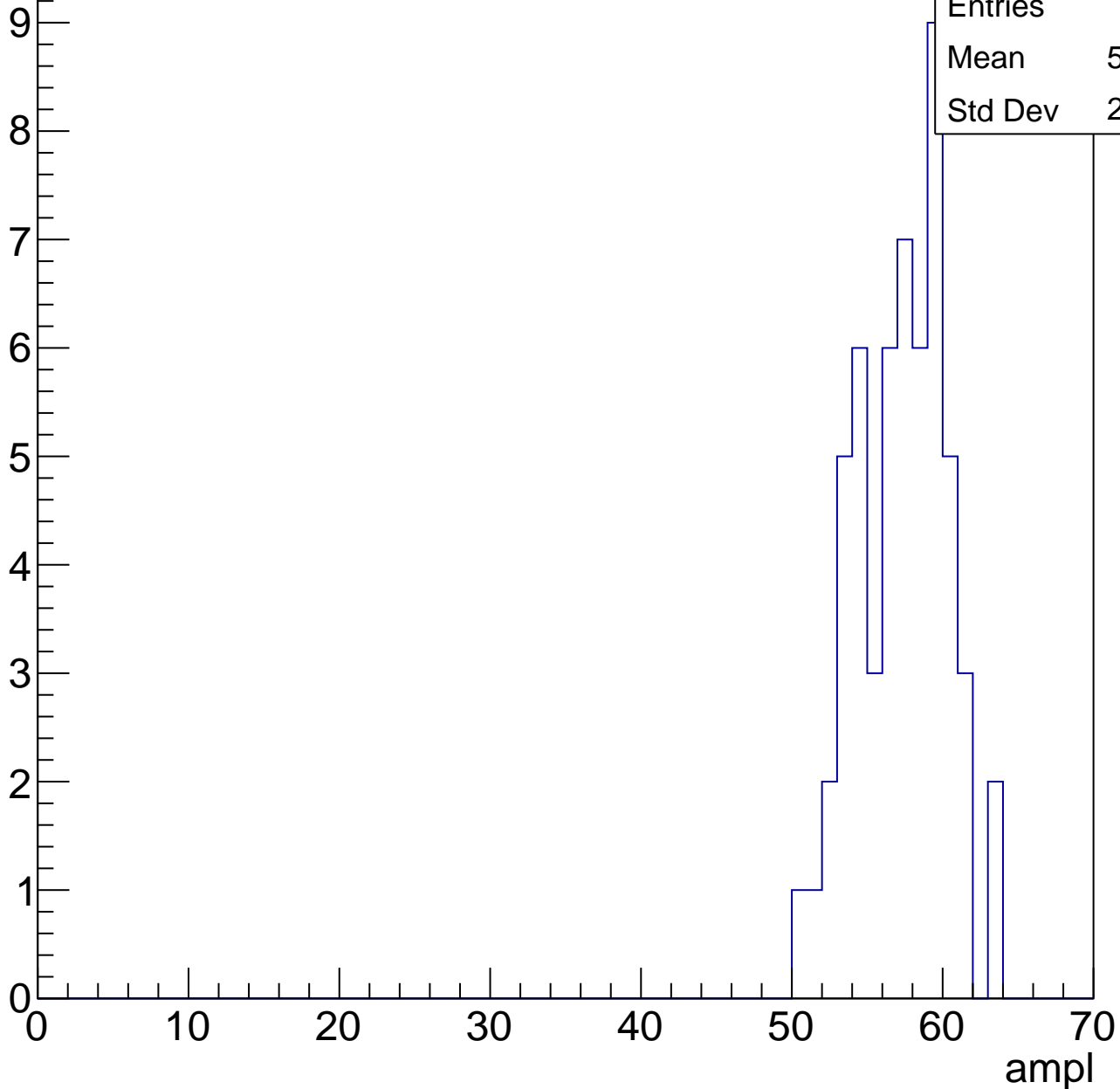


B1L101S, U21-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	56.82
Std Dev	2.965

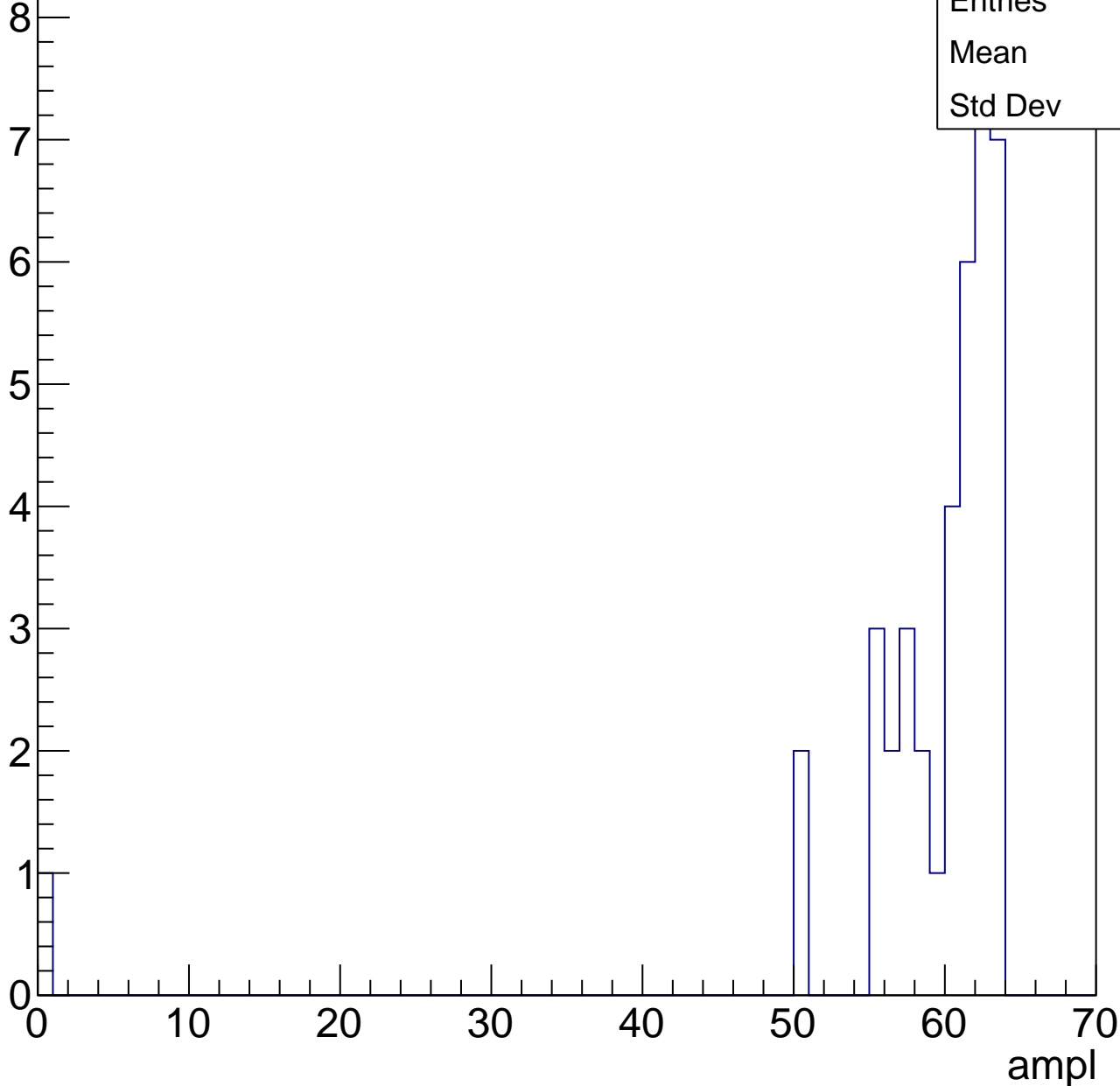


B1L101S, U21-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.1
Std Dev	10



B1L101S, U21-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	62
Std Dev	0.7071

B1L101S, U21-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch119, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	30.23
Std Dev	4.824

Gaus mean : 31.3154

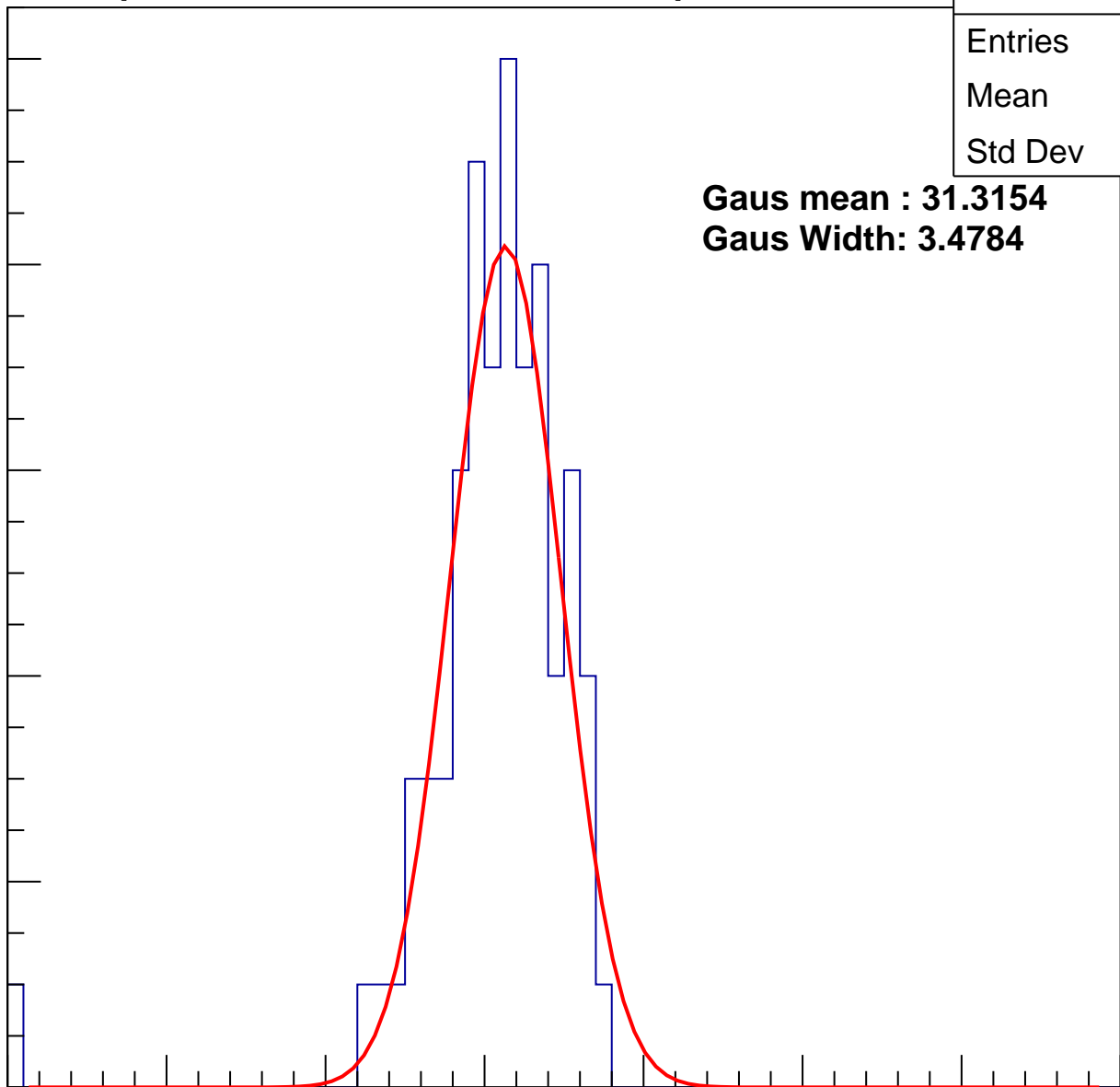
Gaus Width: 3.4784

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch119, adc1

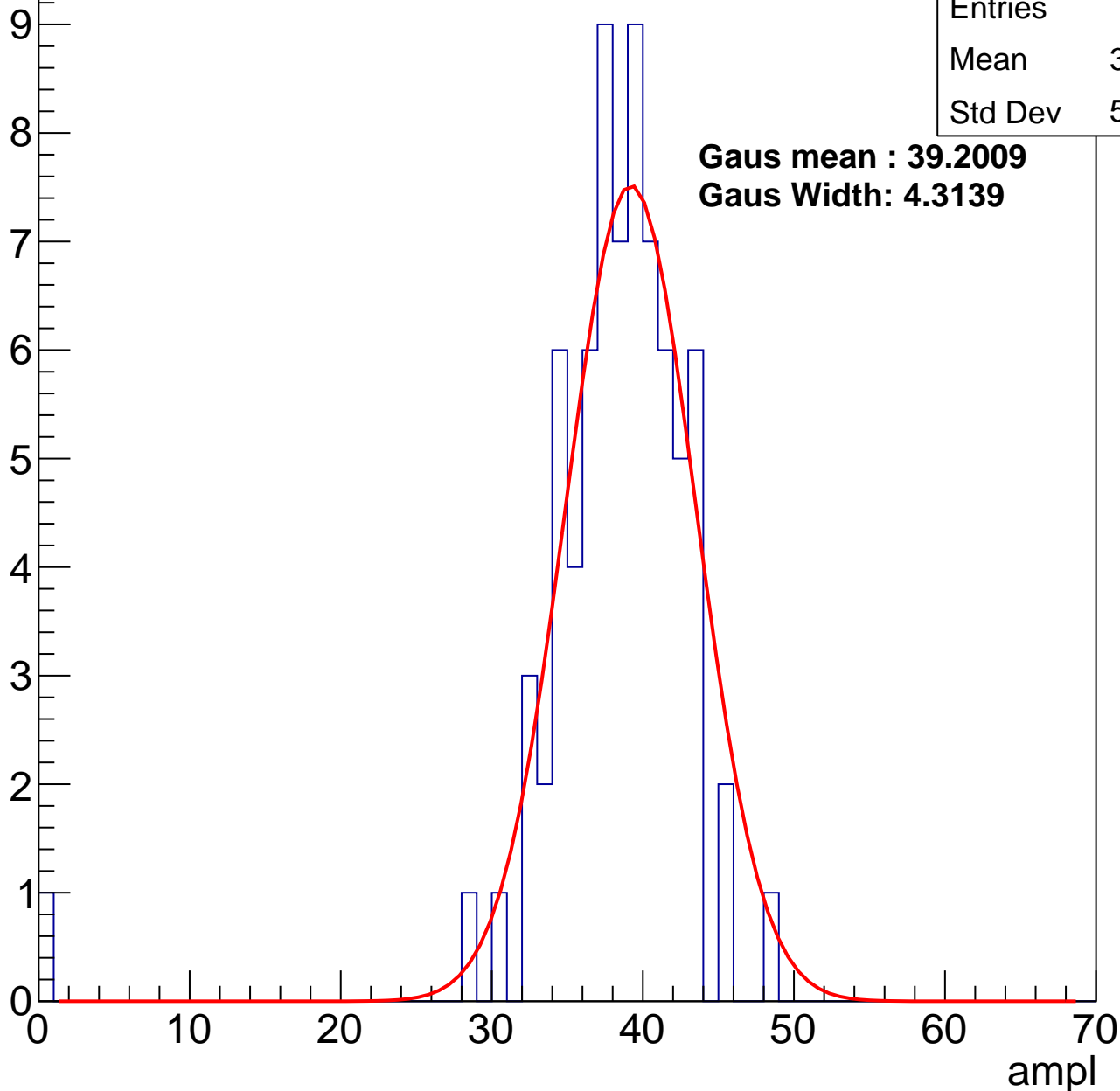
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.66
Std Dev	5.667

Gaus mean : 39.2009

Gaus Width: 4.3139



B1L101S, U21-ch119, adc2

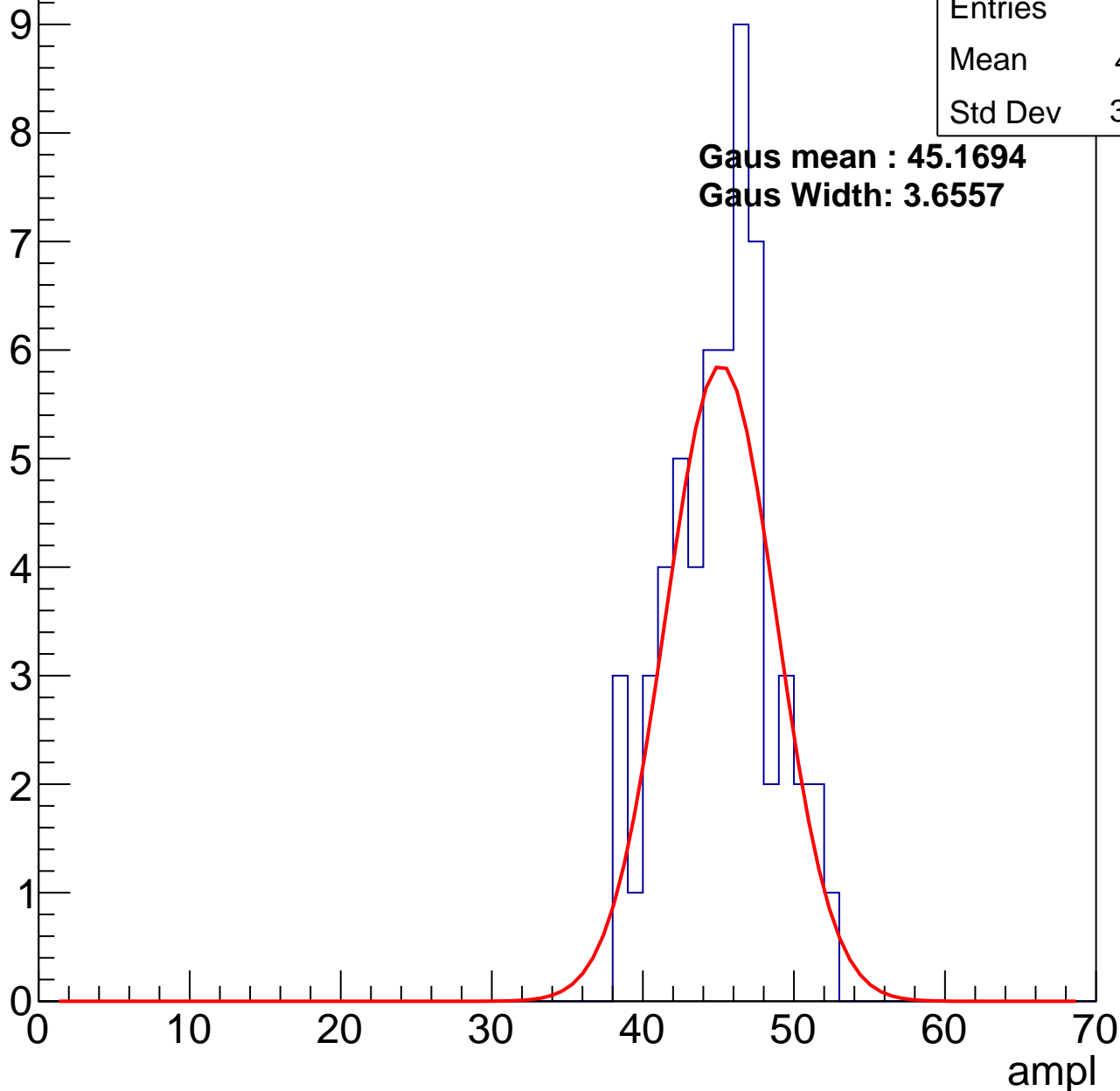
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.71
Std Dev	3.368

Gaus mean : 45.1694

Gaus Width: 3.6557

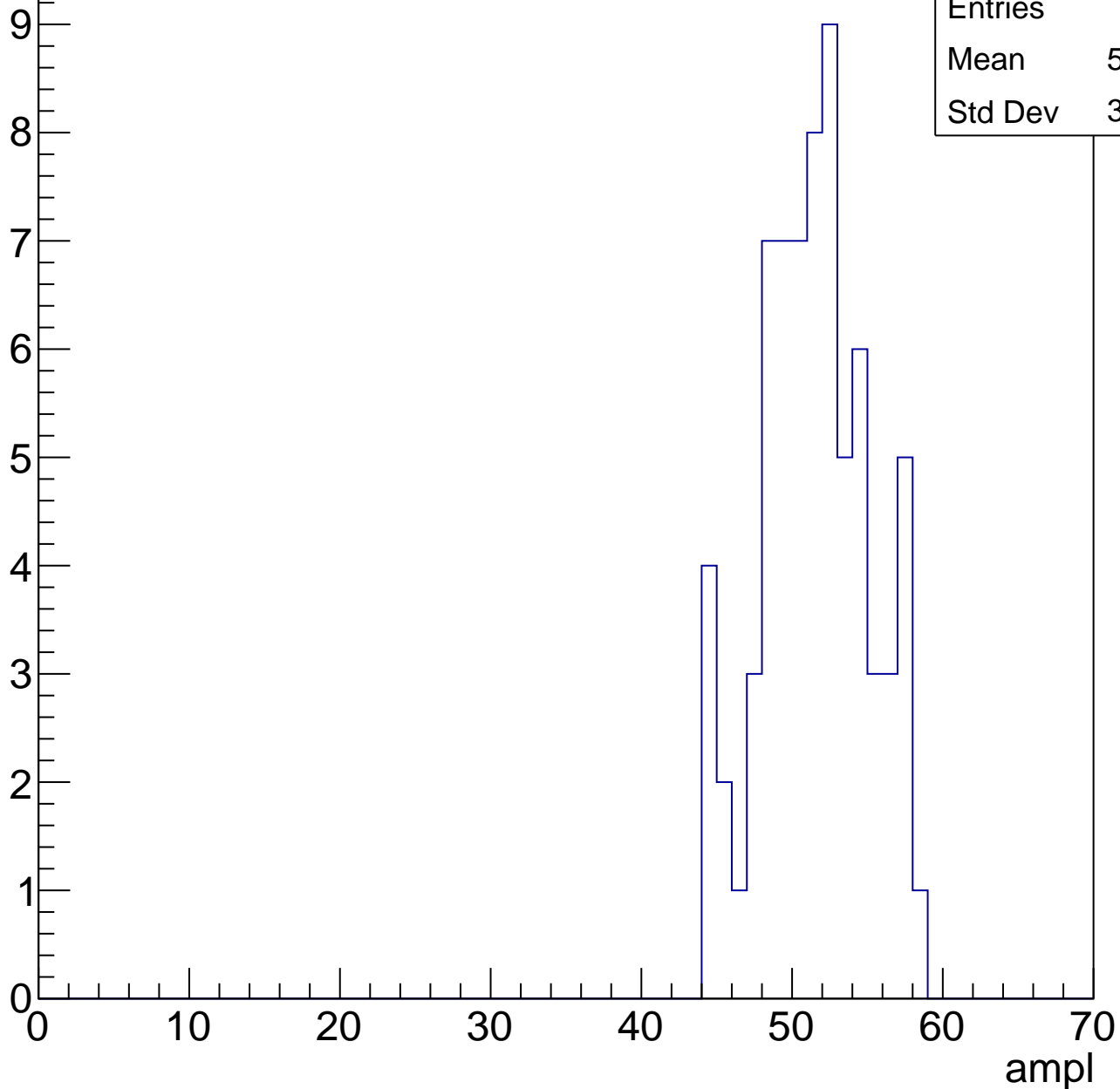


B1L101S, U21-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	51.03
Std Dev	3.508

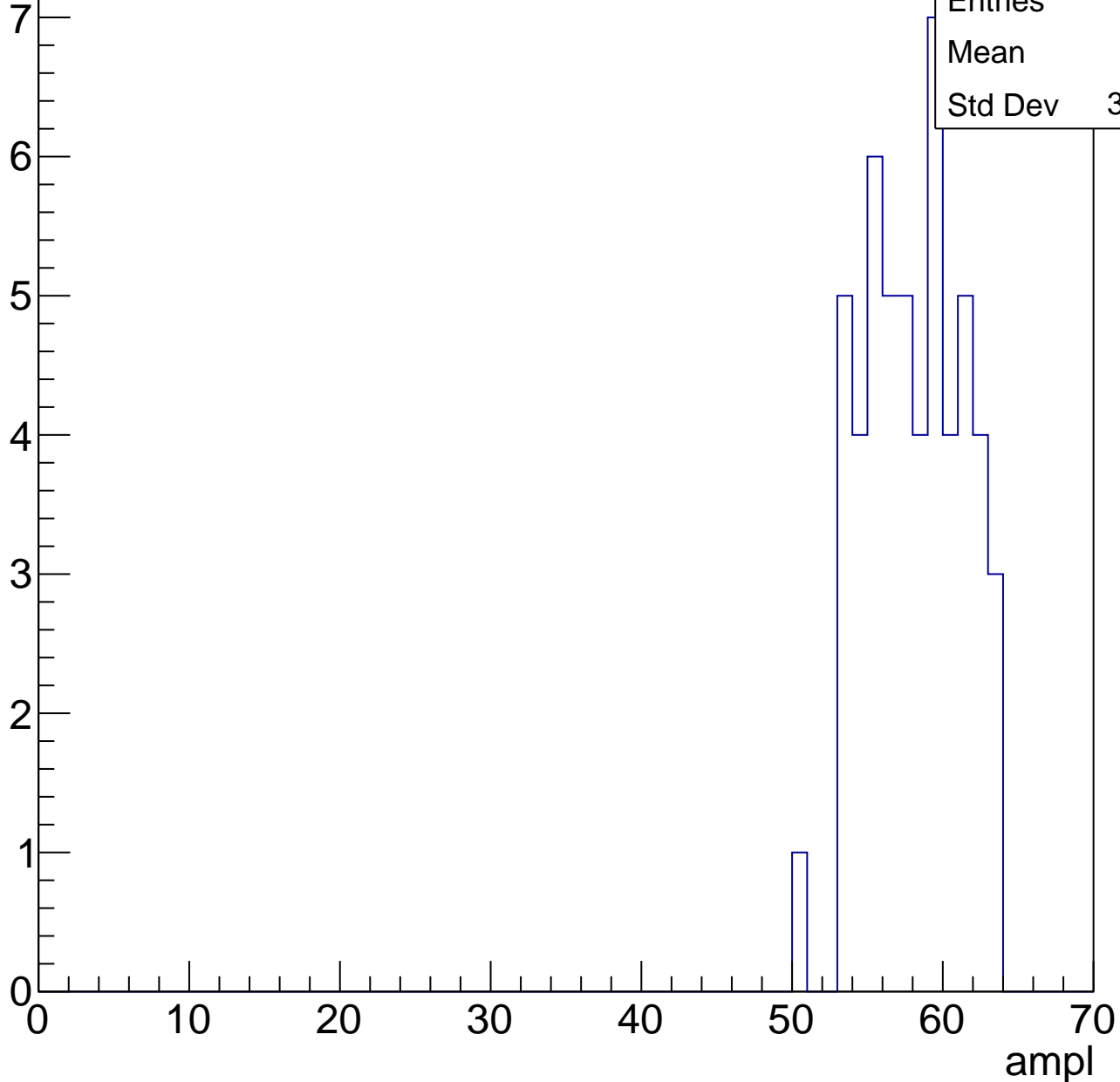


B1L101S, U21-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.6
Std Dev	3.164

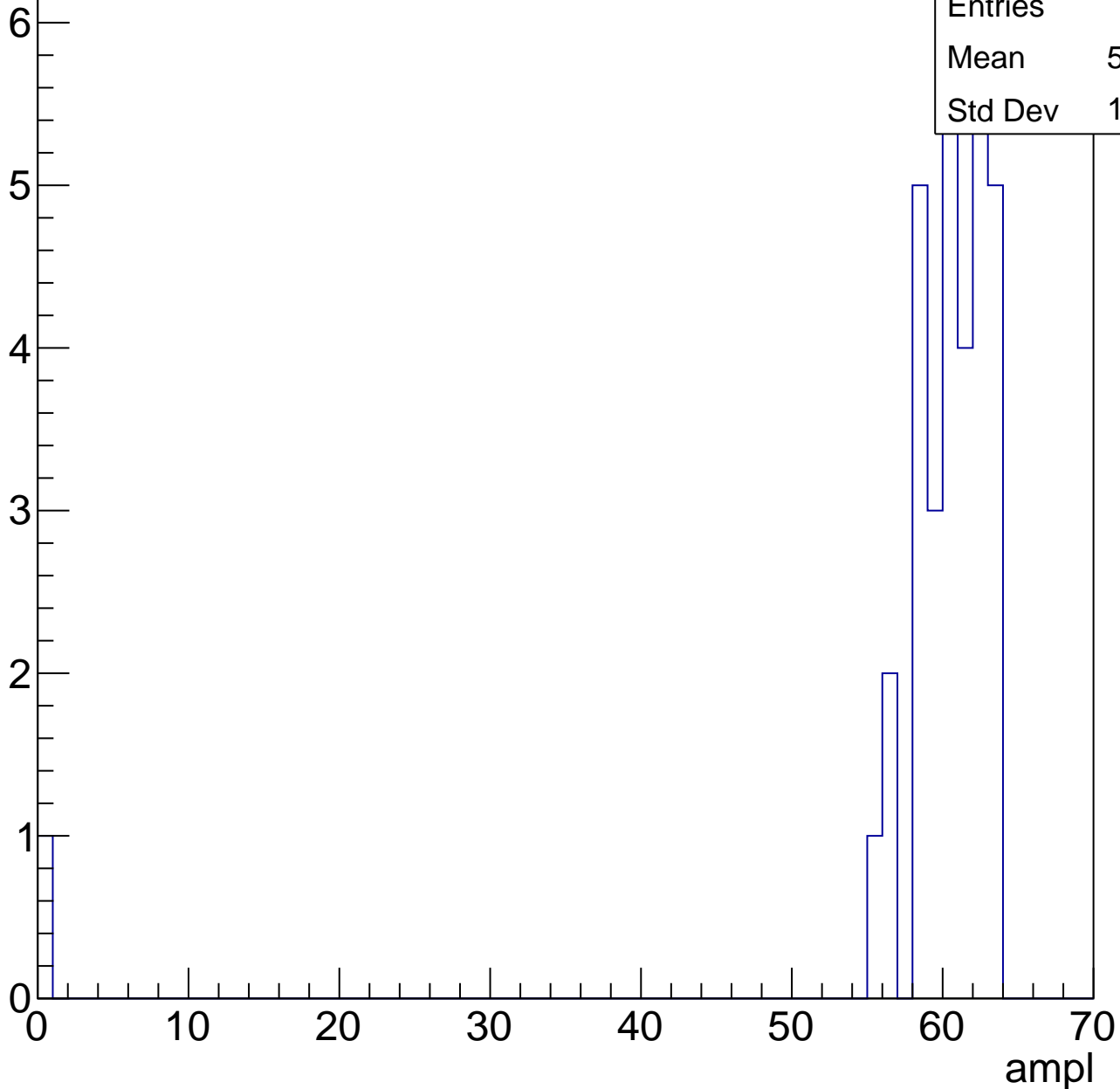


B1L101S, U21-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.33
Std Dev	10.53



B1L101S, U21-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch120, adc0

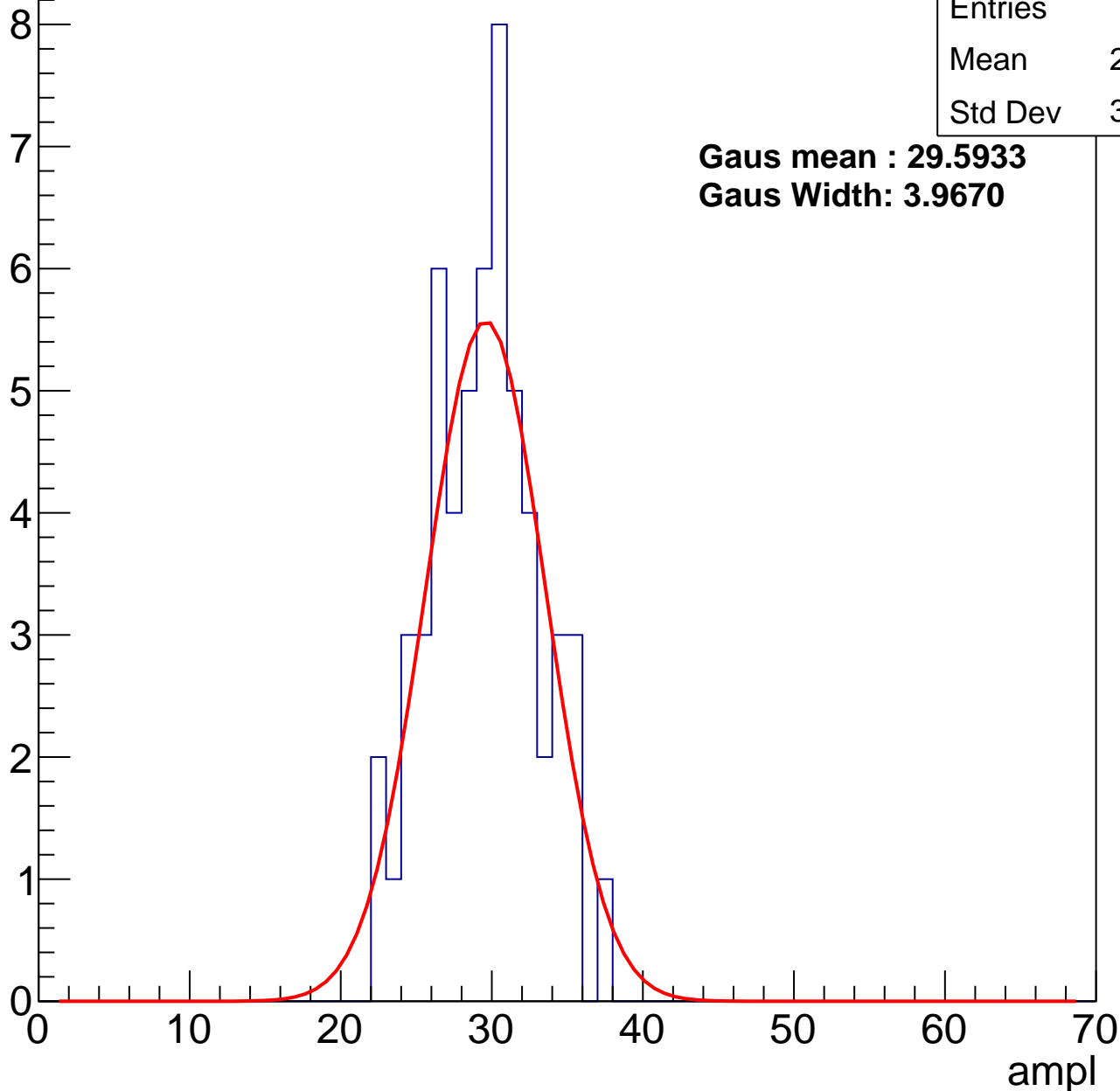
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	29.02
Std Dev	3.472

Gaus mean : 29.5933

Gaus Width: 3.9670



B1L101S, U21-ch120, adc1

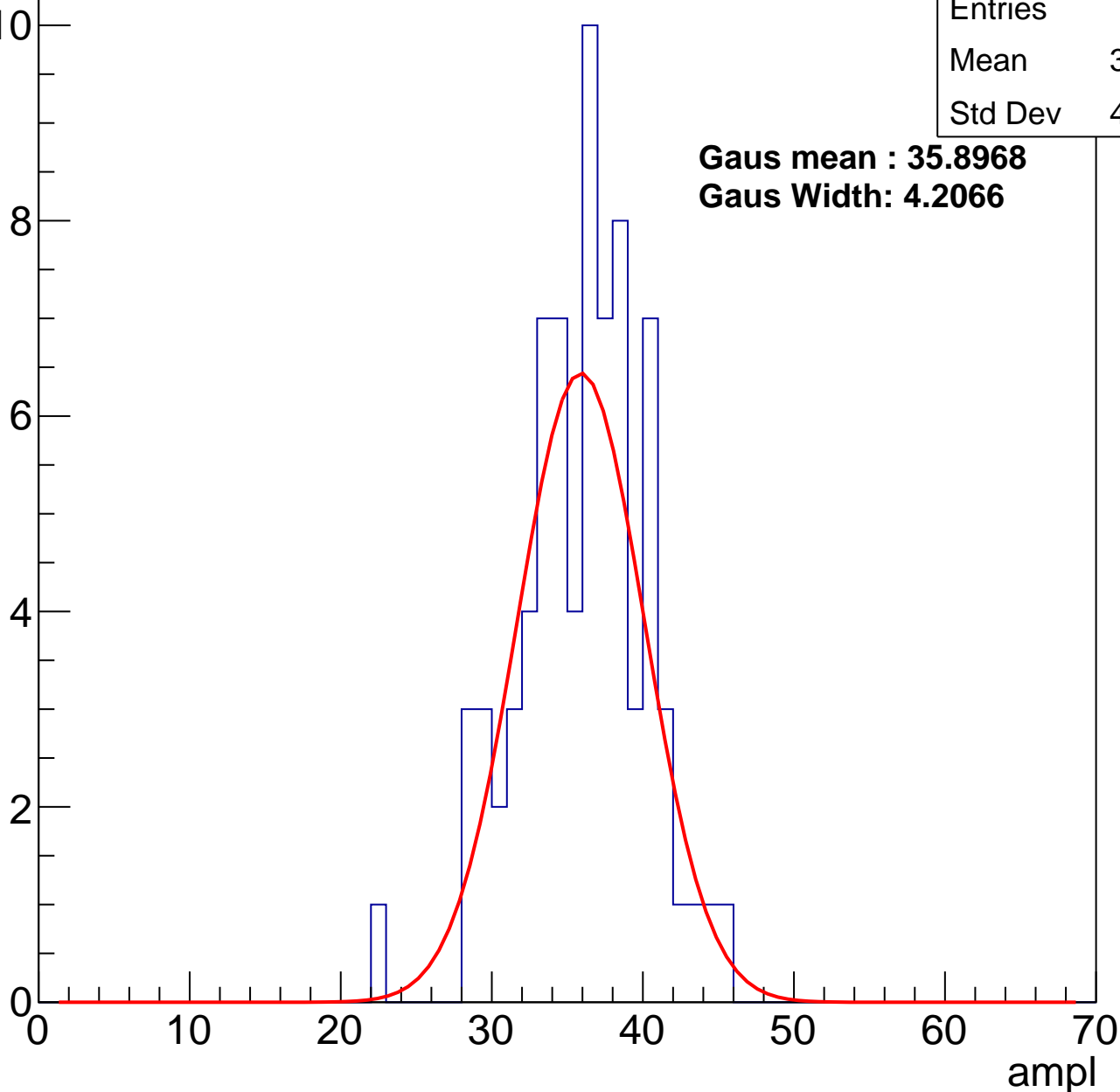
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.53
Std Dev	4.147

Gaus mean : 35.8968

Gaus Width: 4.2066



B1L101S, U21-ch120, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	42.64
Std Dev	4.228

Gaus mean : 43.2954

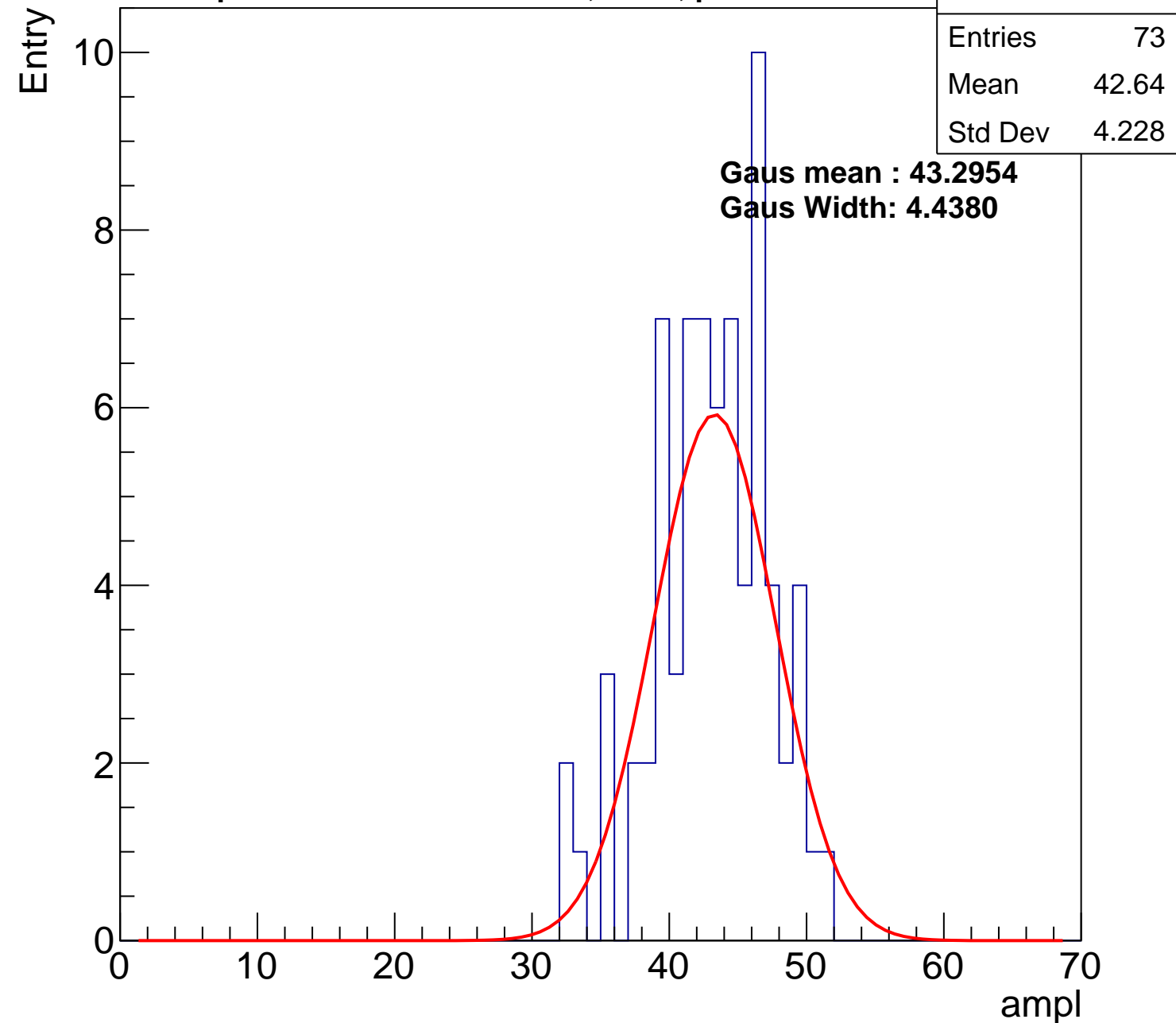
Gaus Width: 4.4380

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

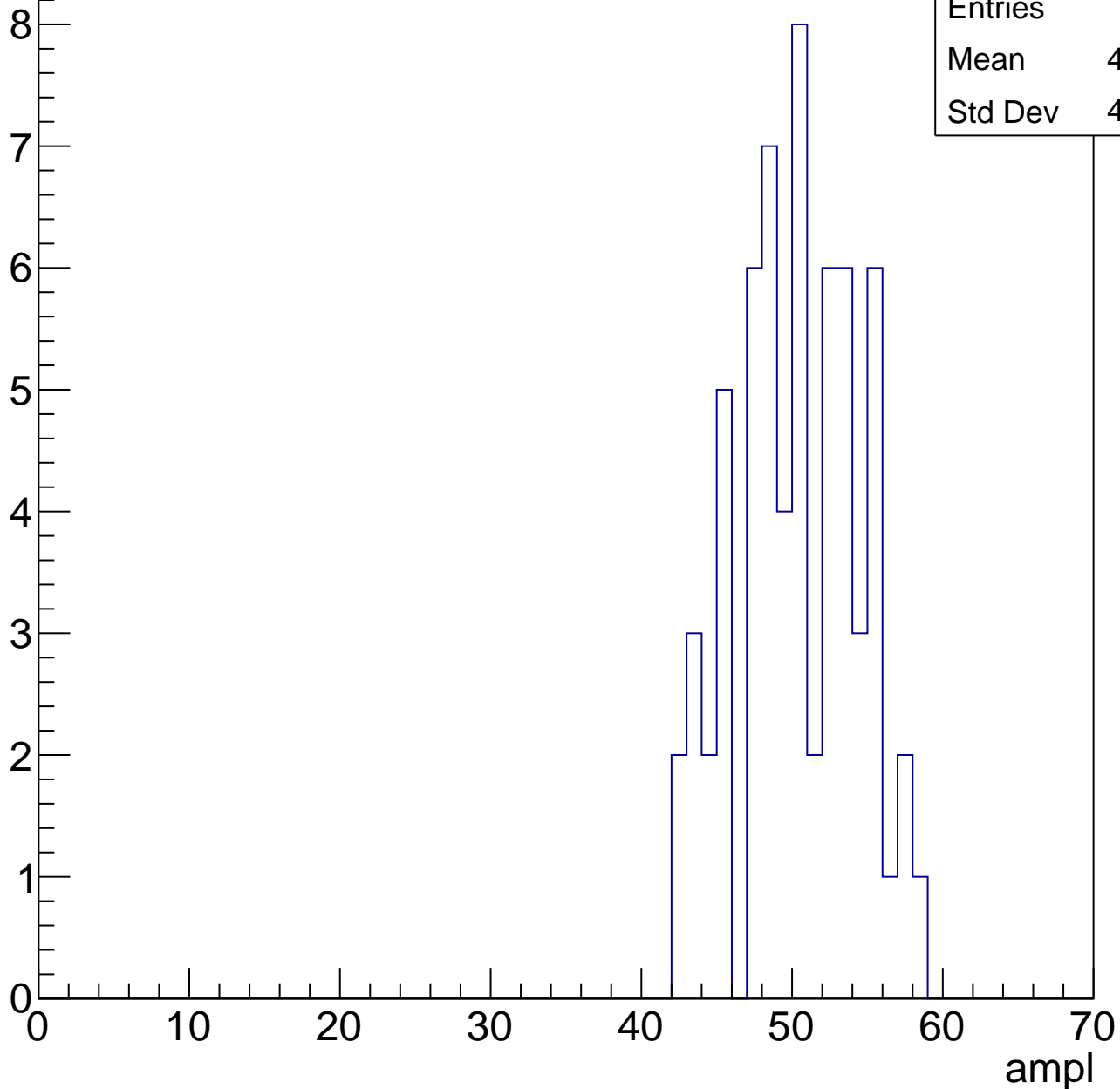


B1L101S, U21-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	49.88
Std Dev	4.018

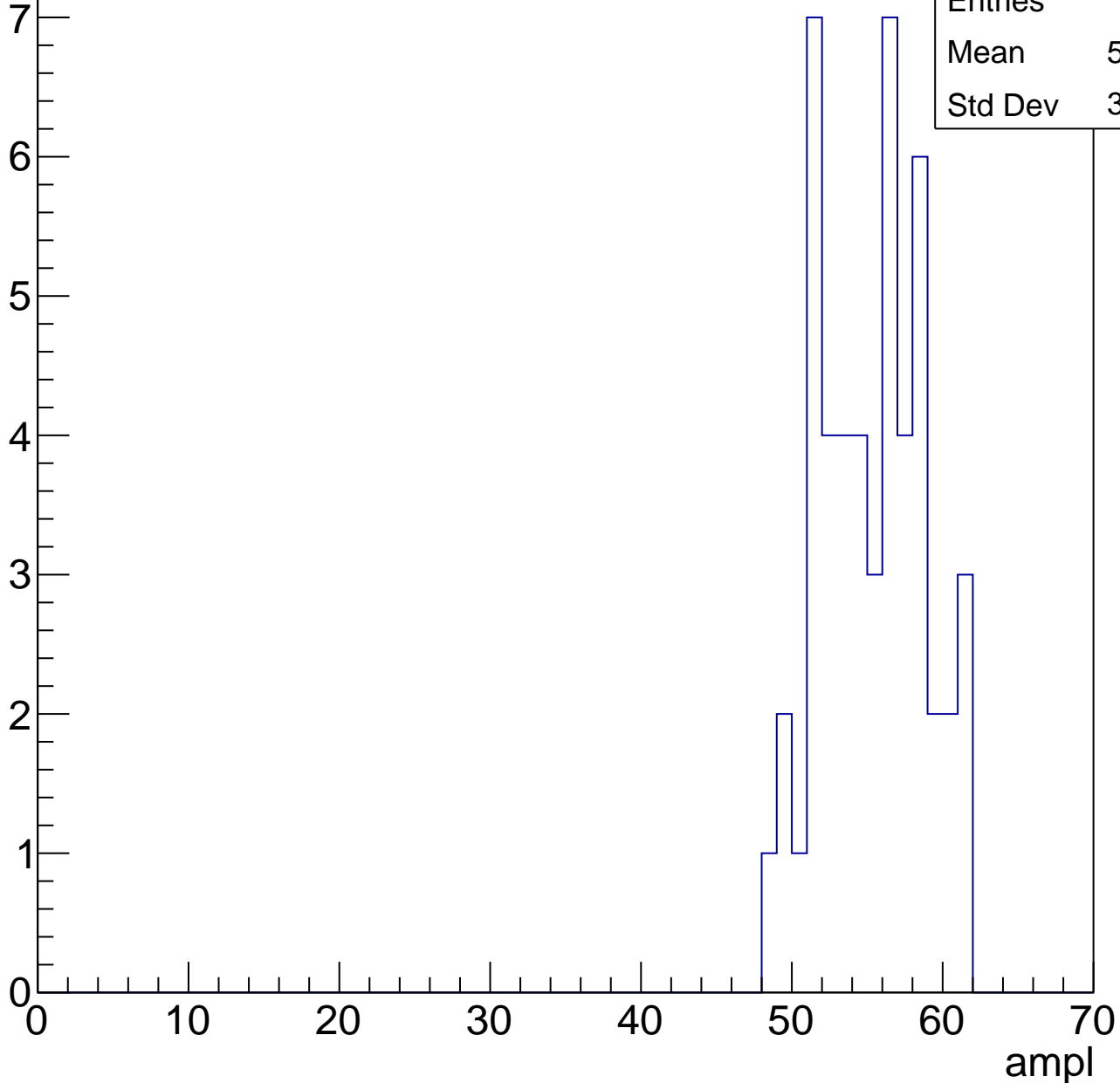


B1L101S, U21-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	54.86
Std Dev	3.406



B1L101S, U21-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.42
Std Dev	8.895

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

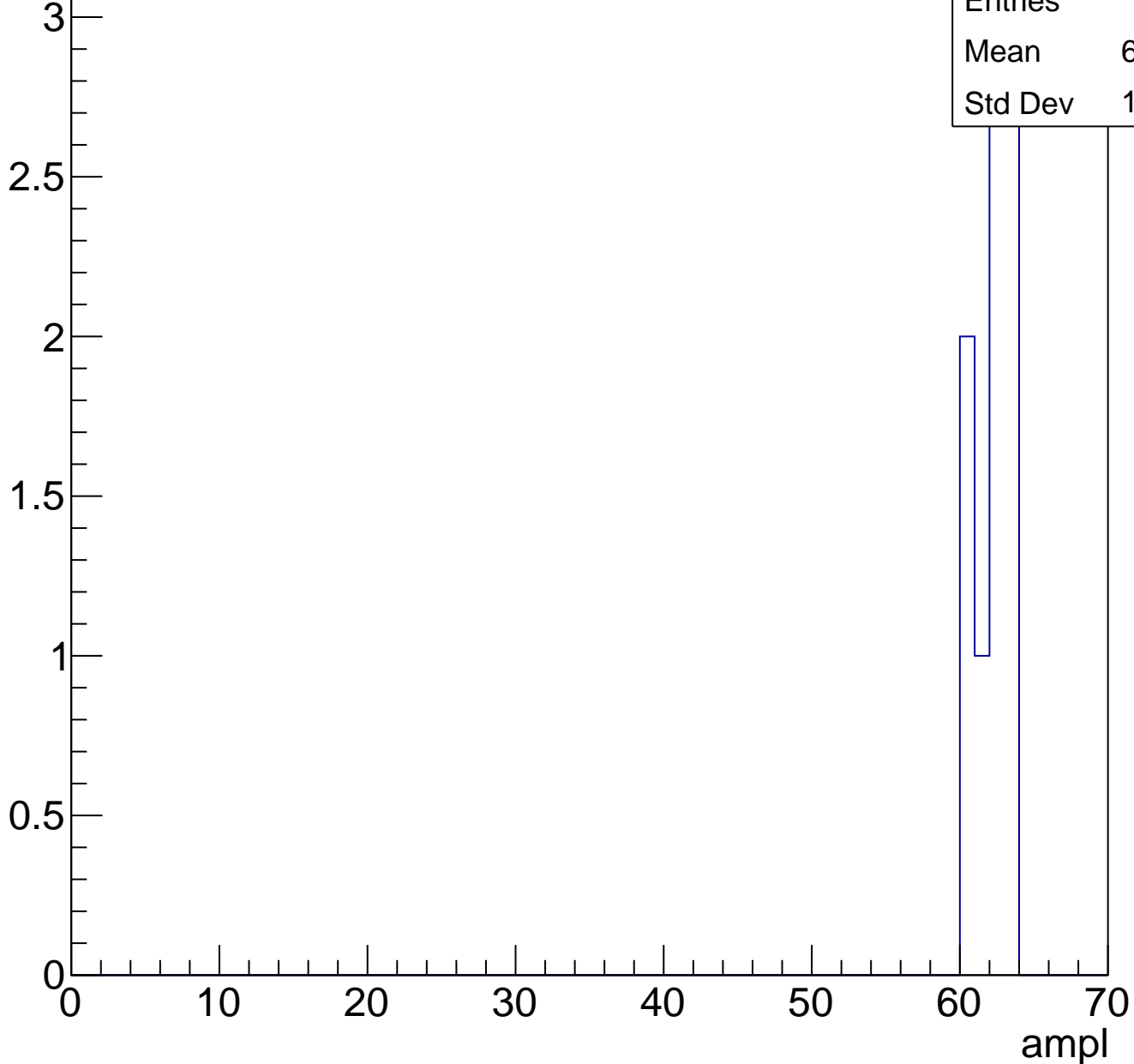
7

8

B1L101S, U21-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch121, adc0

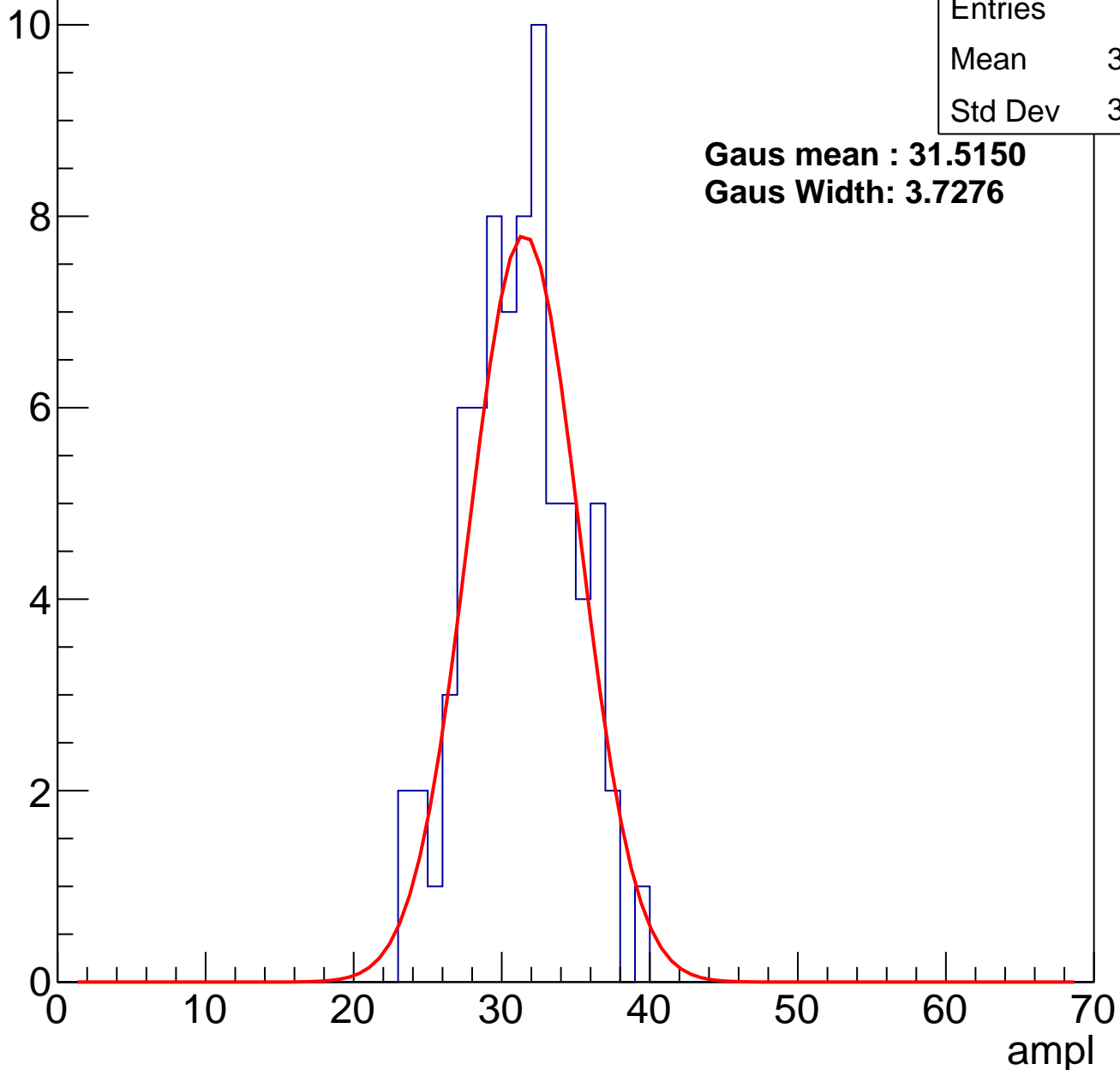
calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	30.73
Std Dev	3.504

Gaus mean : 31.5150

Gaus Width: 3.7276

Entry



B1L101S, U21-ch121, adc1

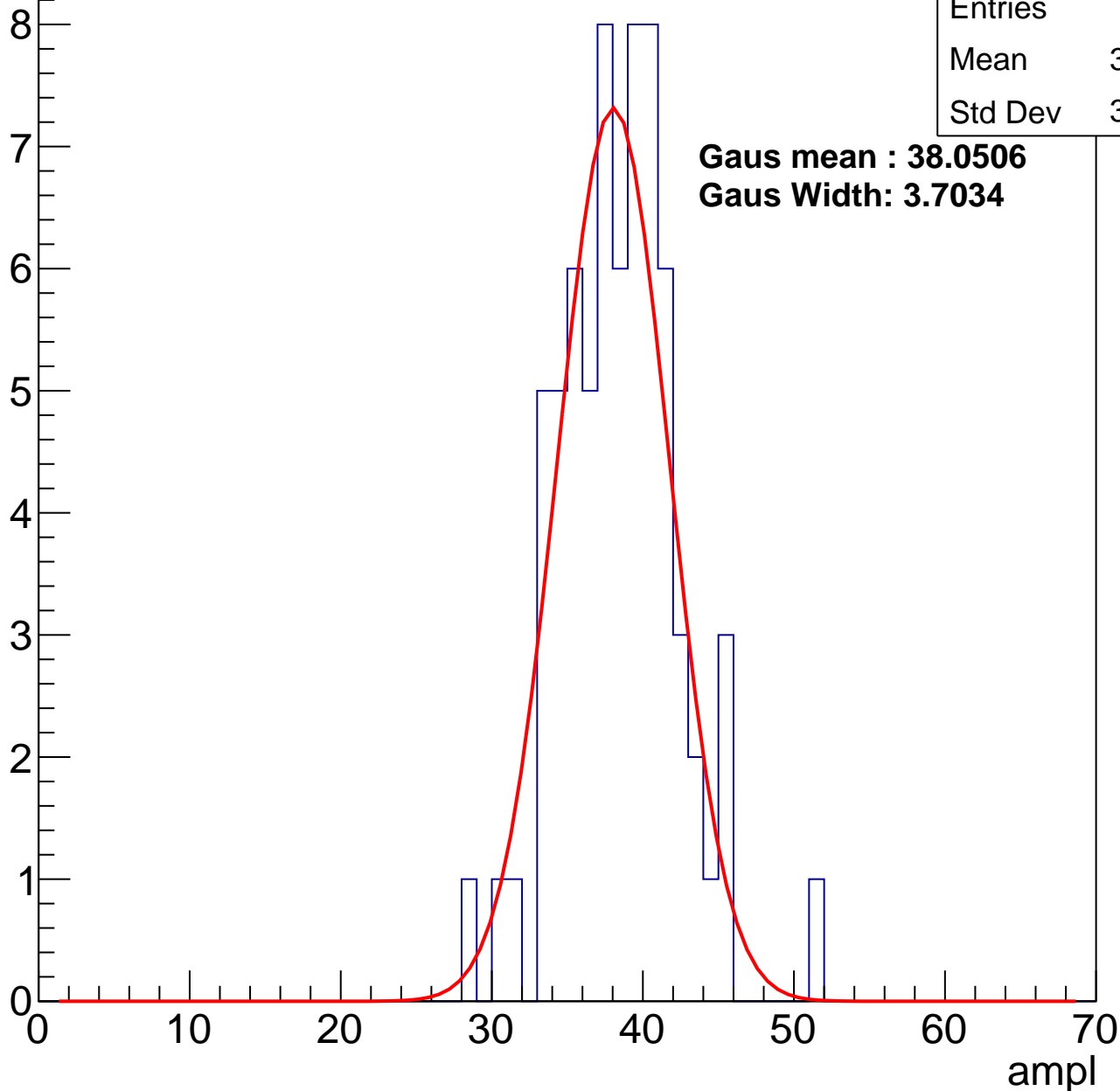
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.97
Std Dev	3.862

Gaus mean : 38.0506

Gaus Width: 3.7034



B1L101S, U21-ch121, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	73
Mean	44.86
Std Dev	3.353

Gaus mean : 45.2063

Gaus Width: 3.0305

Entry

10

8

6

4

2

0

0

10

20

30

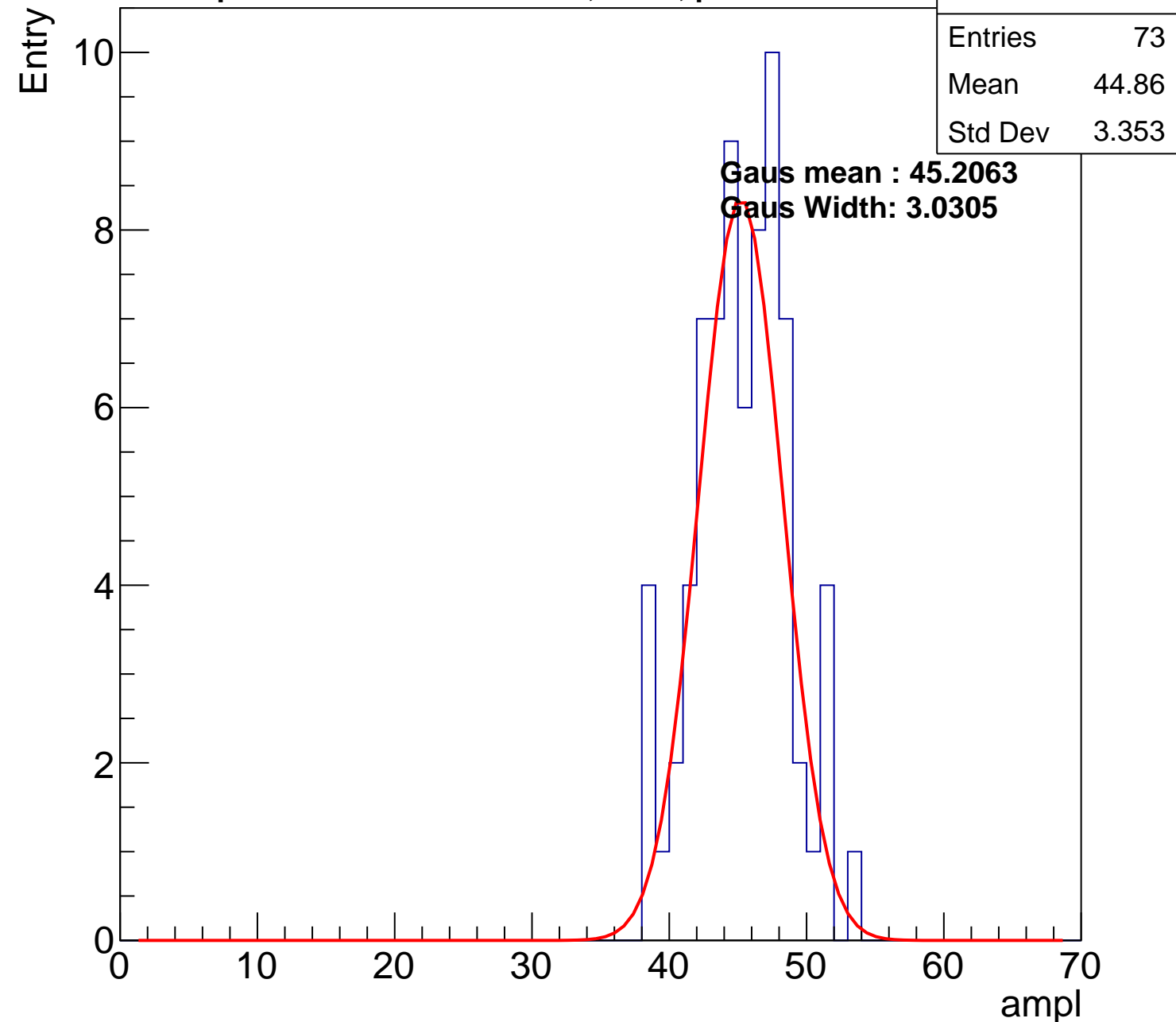
40

50

60

70

ampl

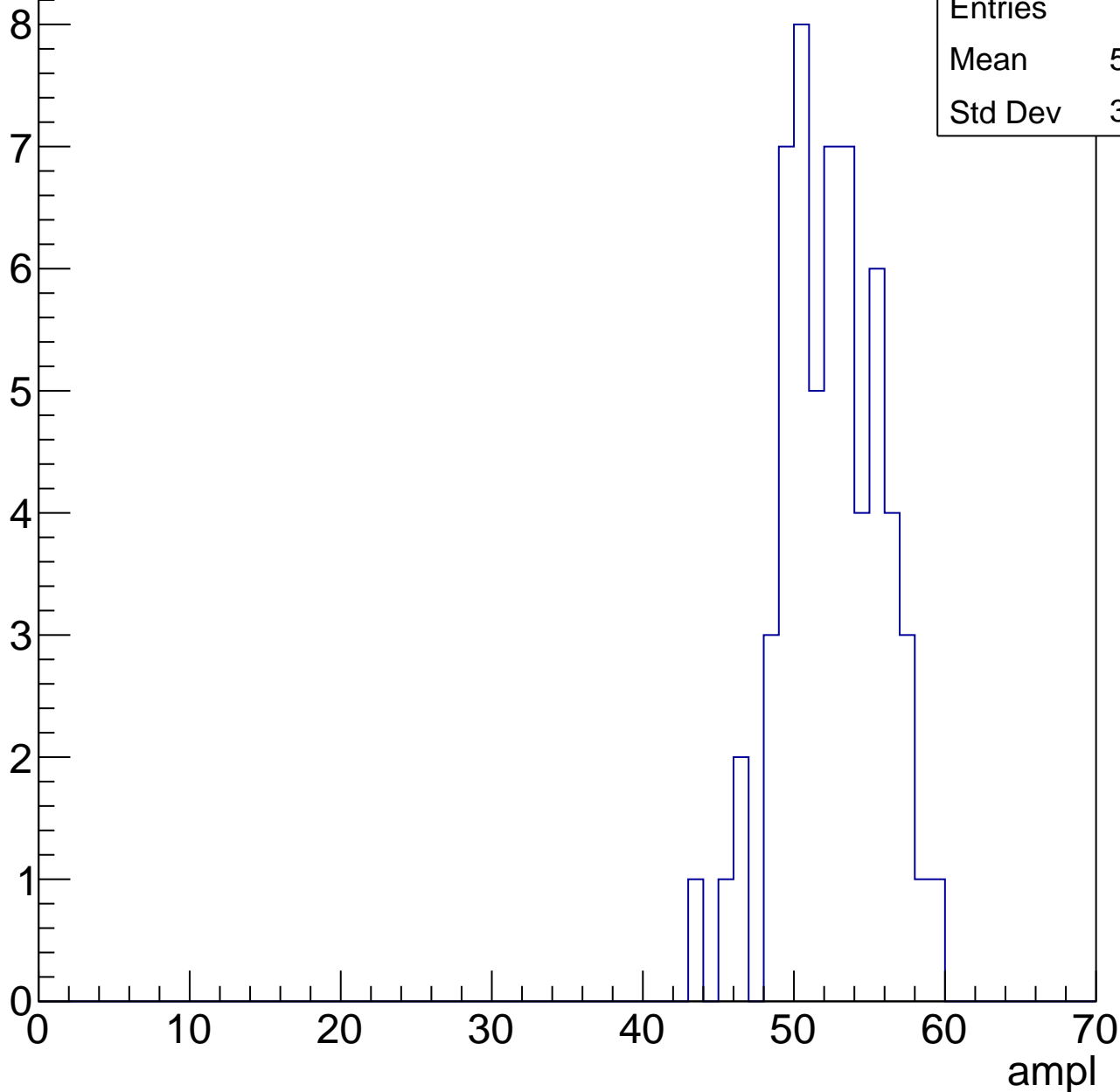


B1L101S, U21-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	51.92
Std Dev	3.293

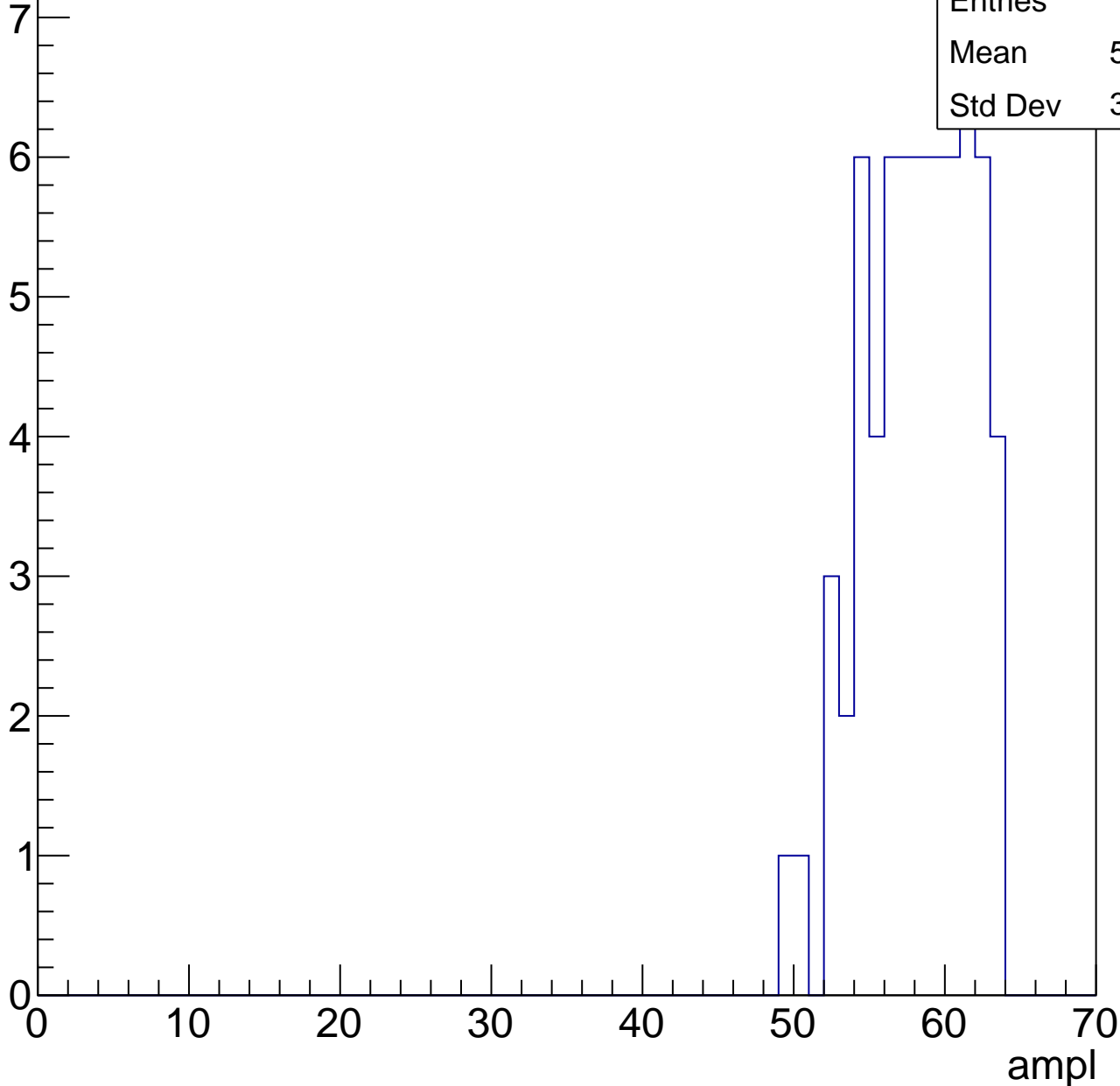


B1L101S, U21-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	57.75
Std Dev	3.423

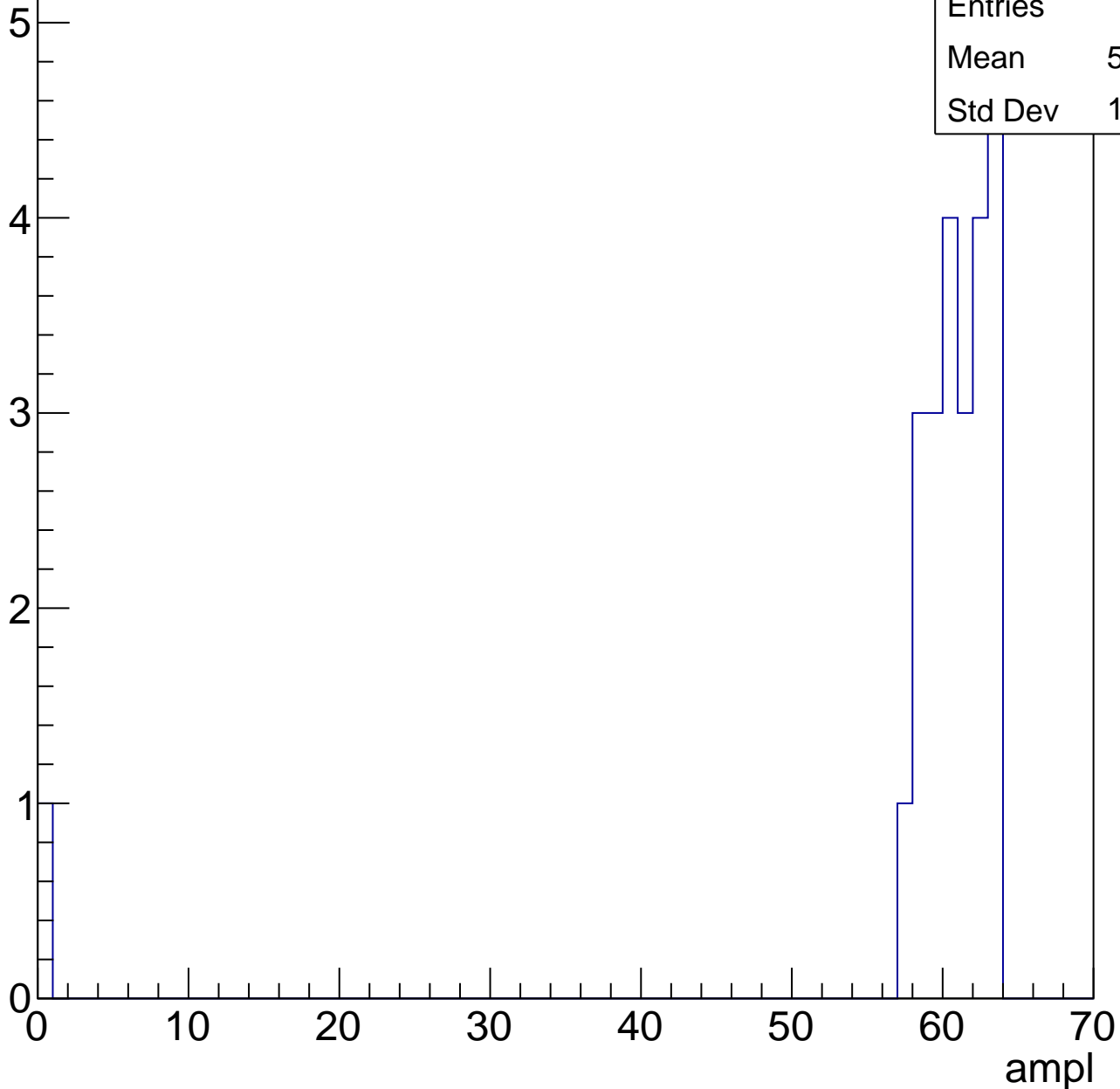


B1L101S, U21-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	58.08
Std Dev	12.25



B1L101S, U21-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch122, adc0

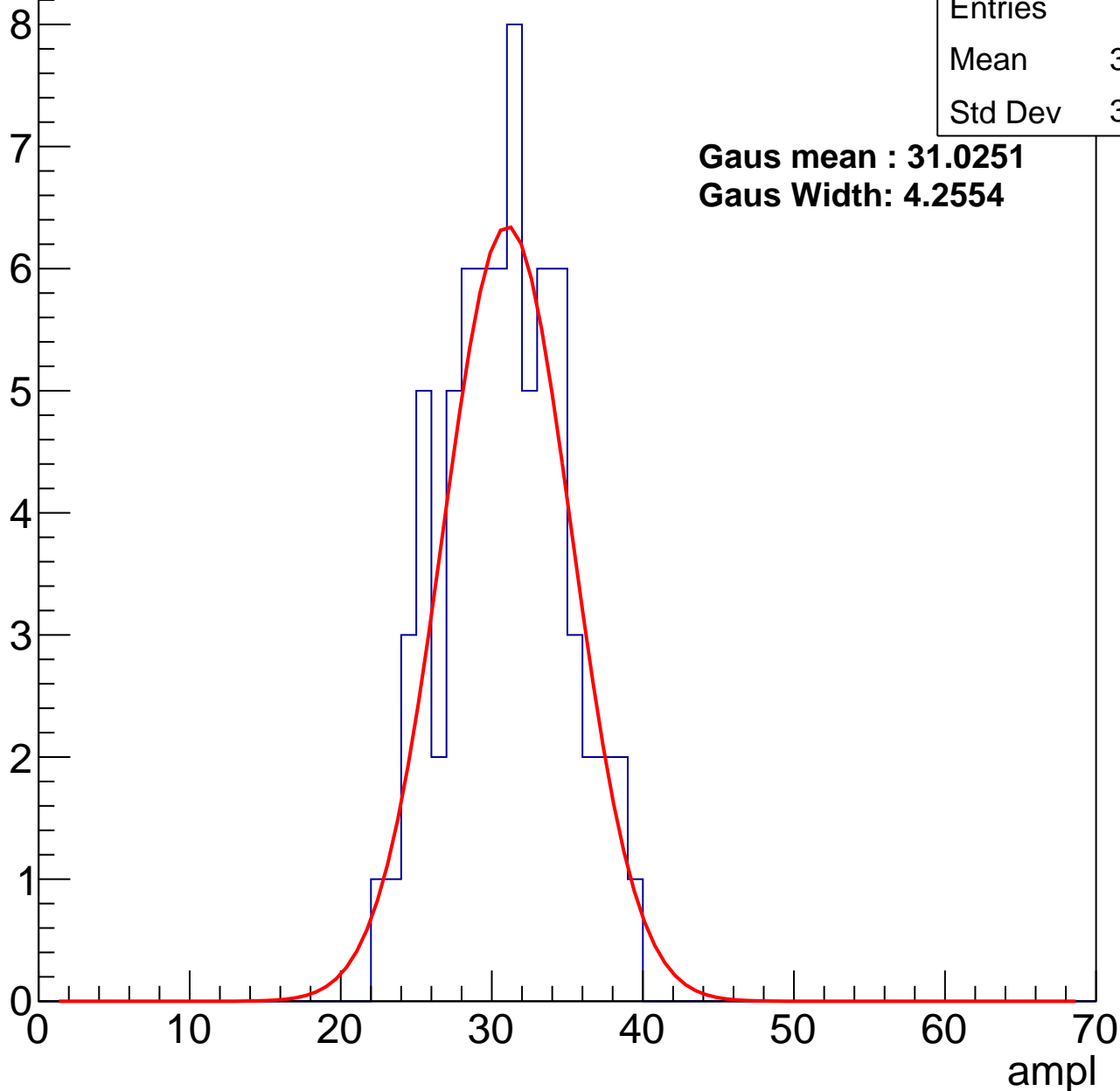
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	30.39
Std Dev	3.918

Gaus mean : 31.0251

Gaus Width: 4.2554



B1L101S, U21-ch122, adc1

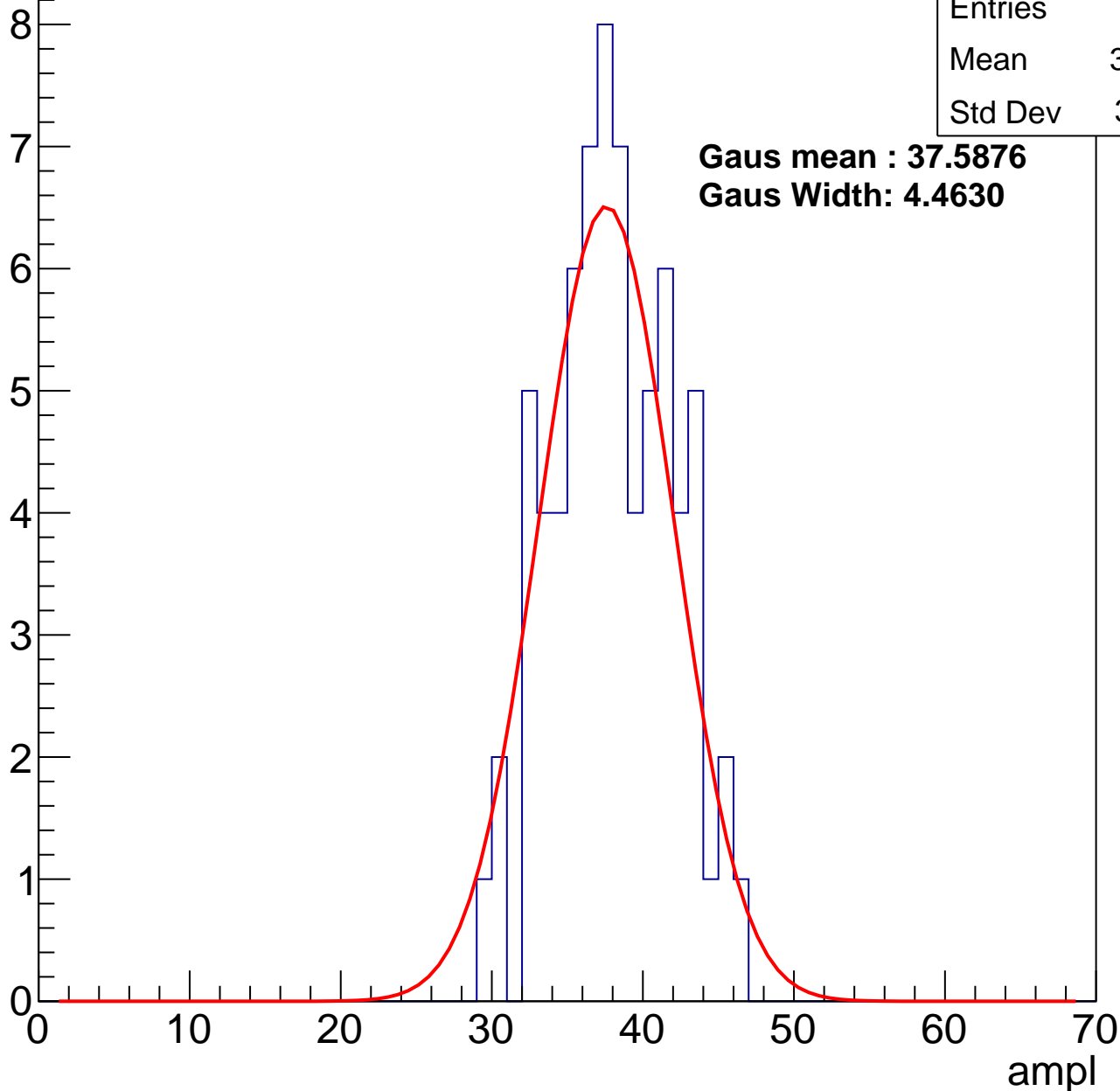
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.58
Std Dev	3.901

Gaus mean : 37.5876

Gaus Width: 4.4630



B1L101S, U21-ch122, adc2

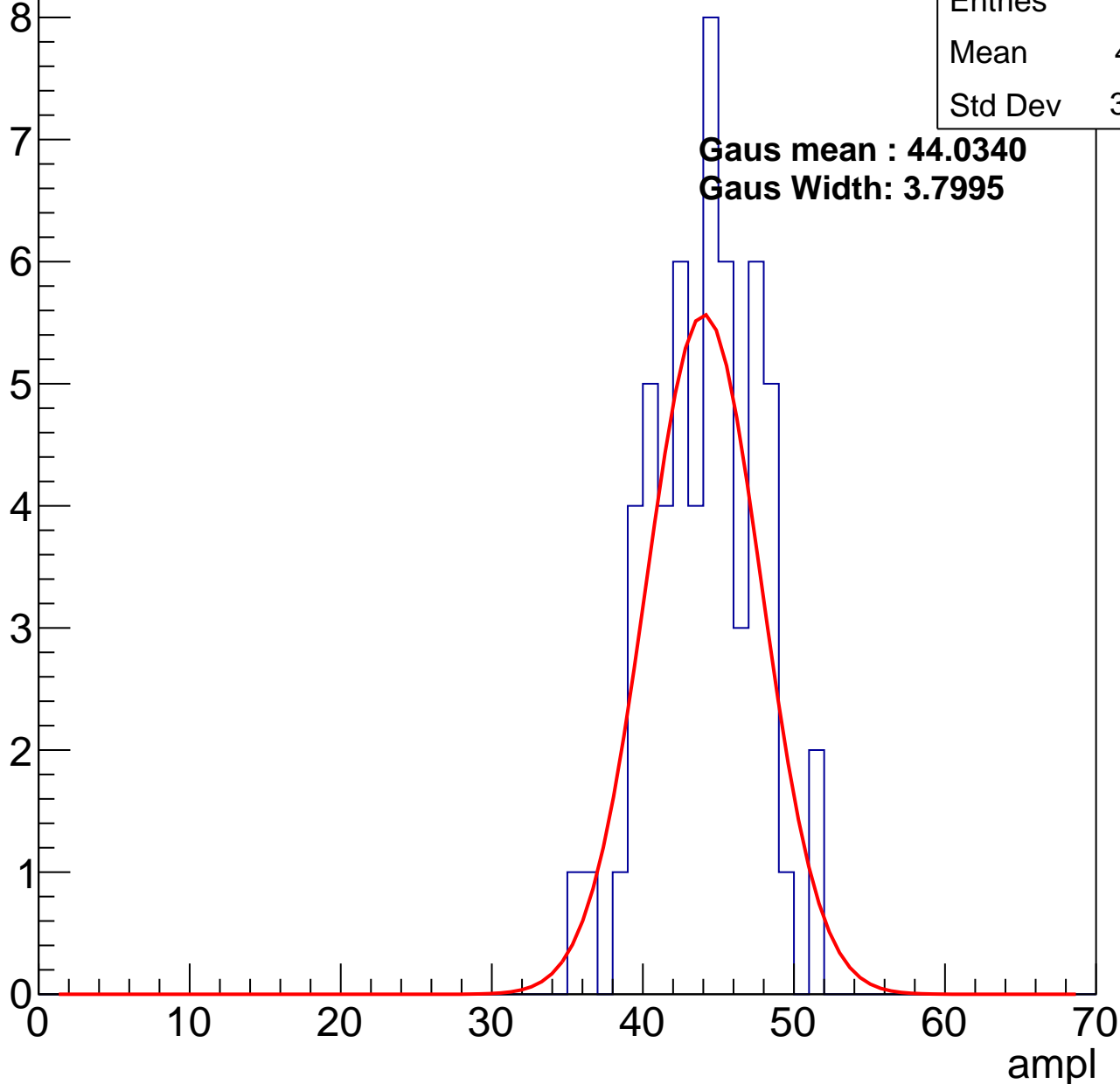
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.61
Std Dev	3.483

Gaus mean : 44.0340

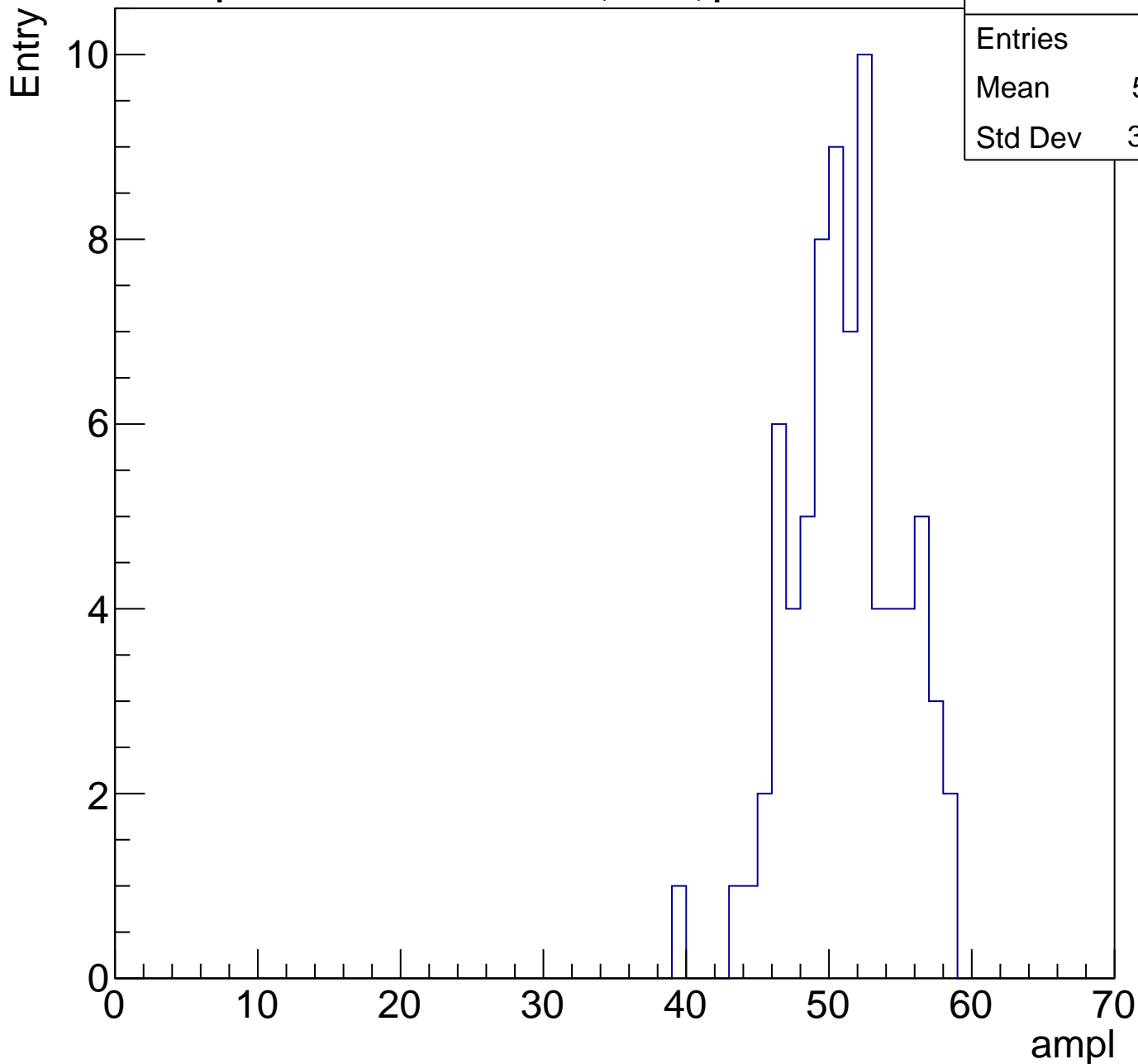
Gaus Width: 3.7995



B1L101S, U21-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	50.71
Std Dev	3.769

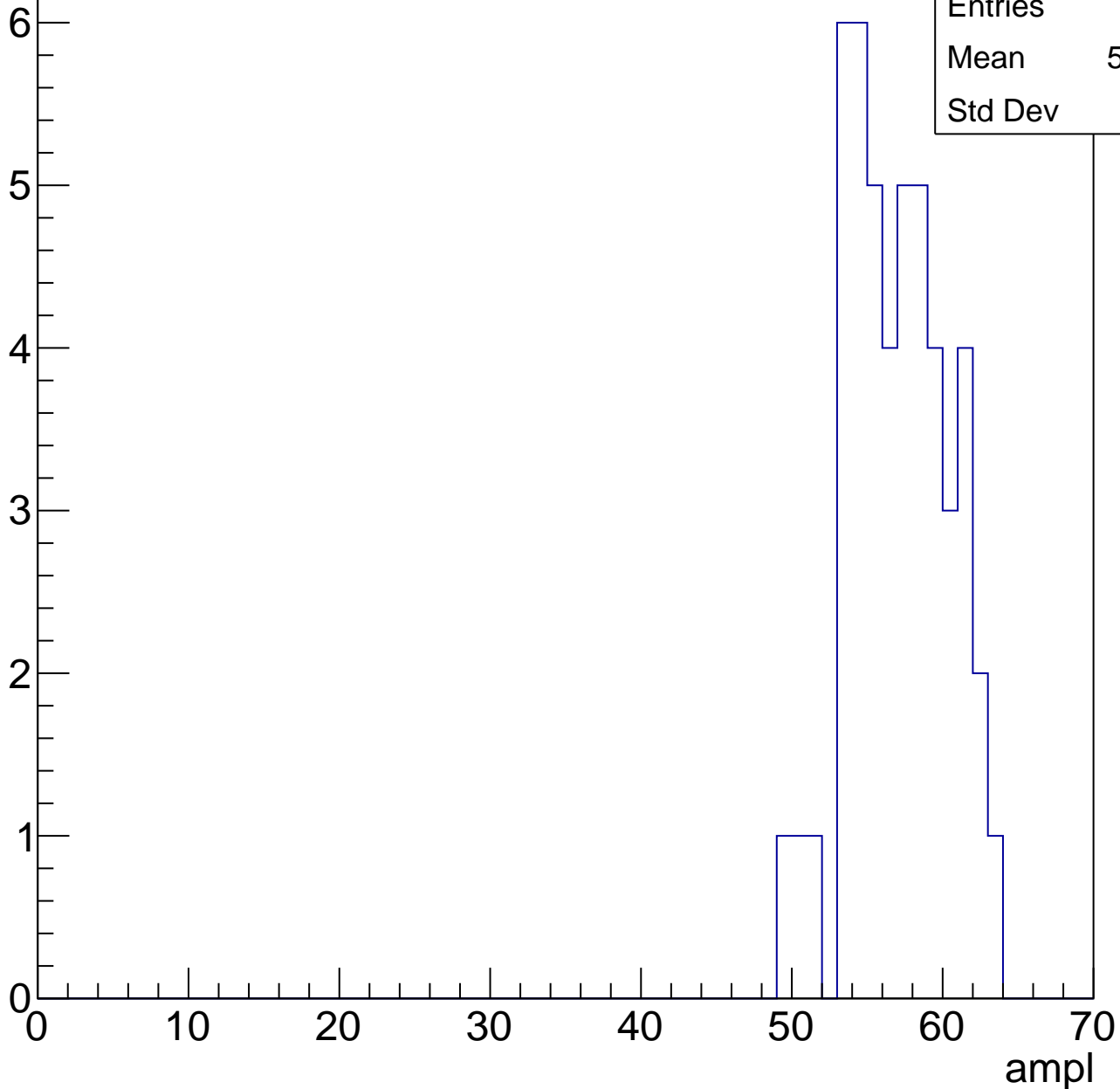


B1L101S, U21-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	56.52
Std Dev	3.26

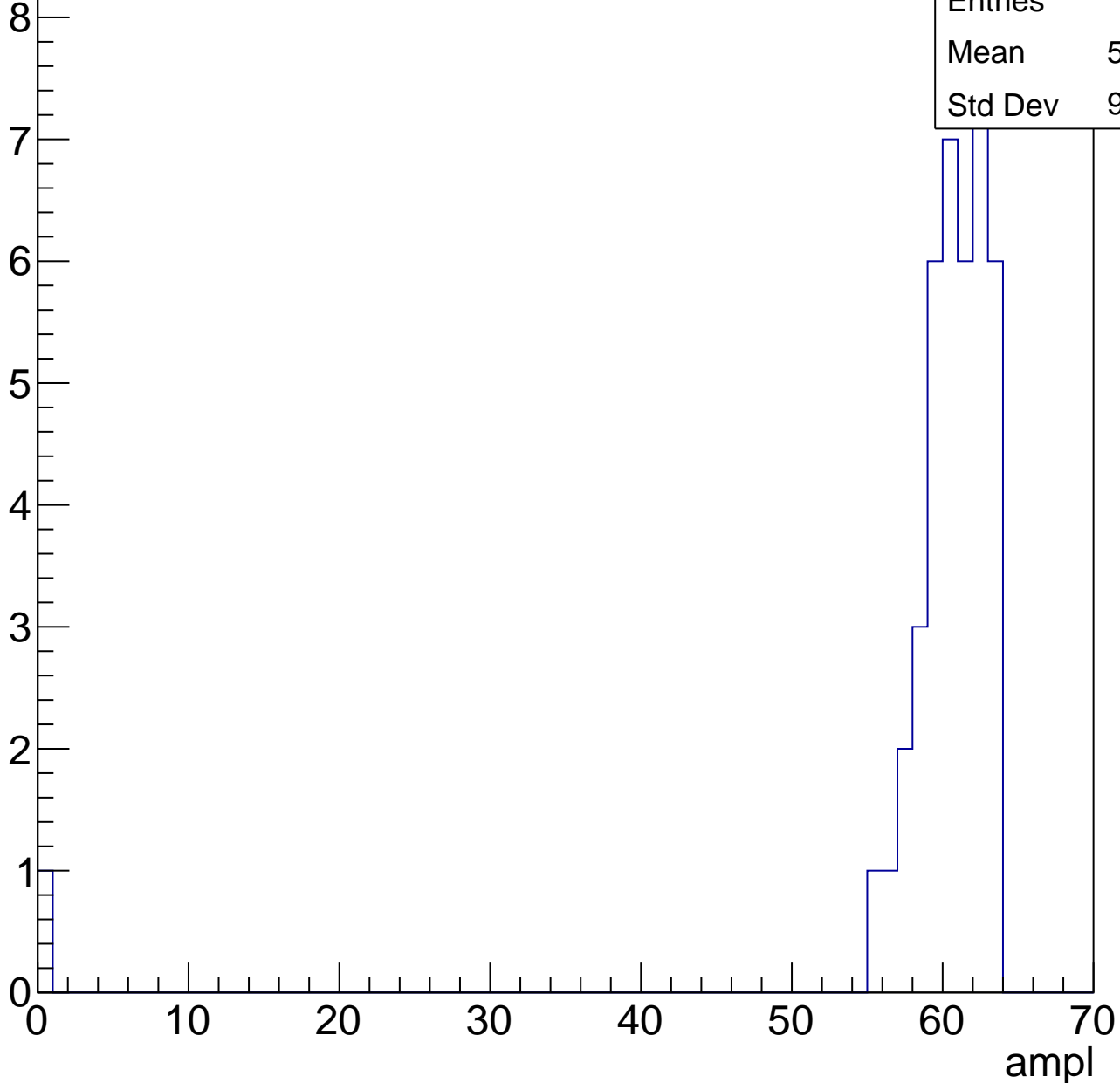


B1L101S, U21-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.85
Std Dev	9.519



B1L101S, U21-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	63
Std Dev	0

B1L101S, U21-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch123, adc0

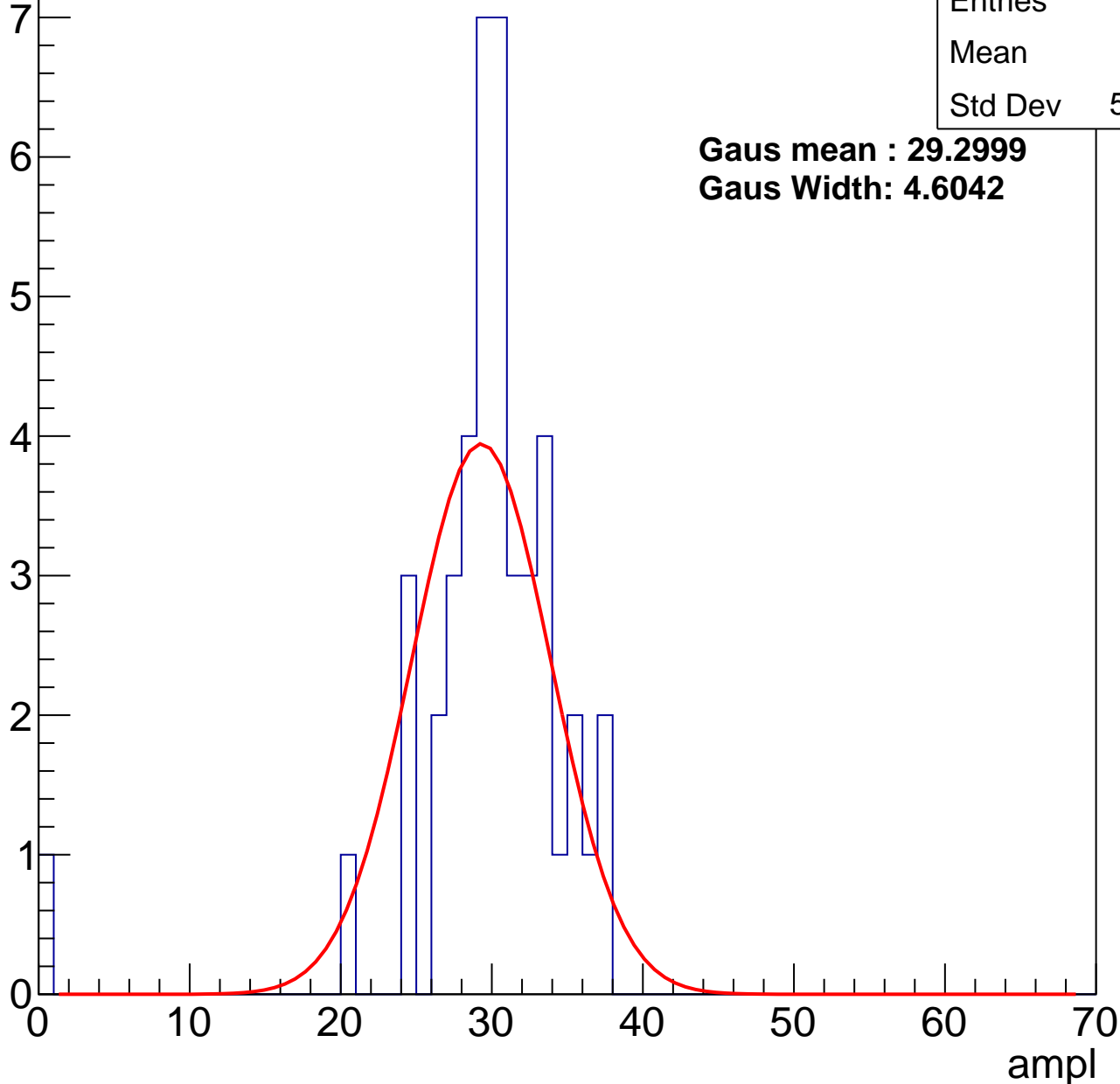
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	29.2
Std Dev	5.667

Gaus mean : 29.2999

Gaus Width: 4.6042



B1L101S, U21-ch123, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	96
Mean	36.98
Std Dev	3.824

Gaus mean : 37.6270

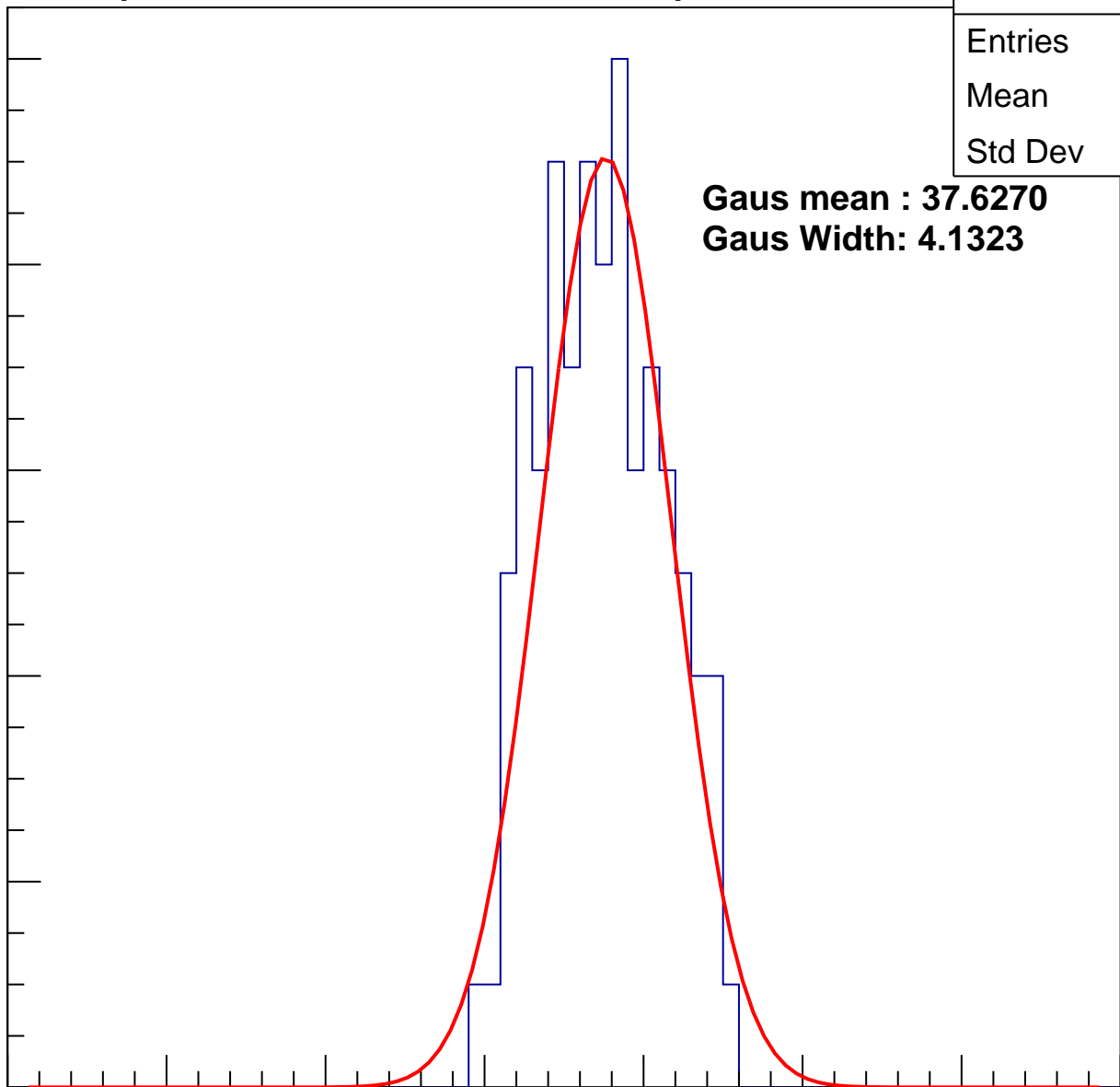
Gaus Width: 4.1323

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U21-ch123, adc2

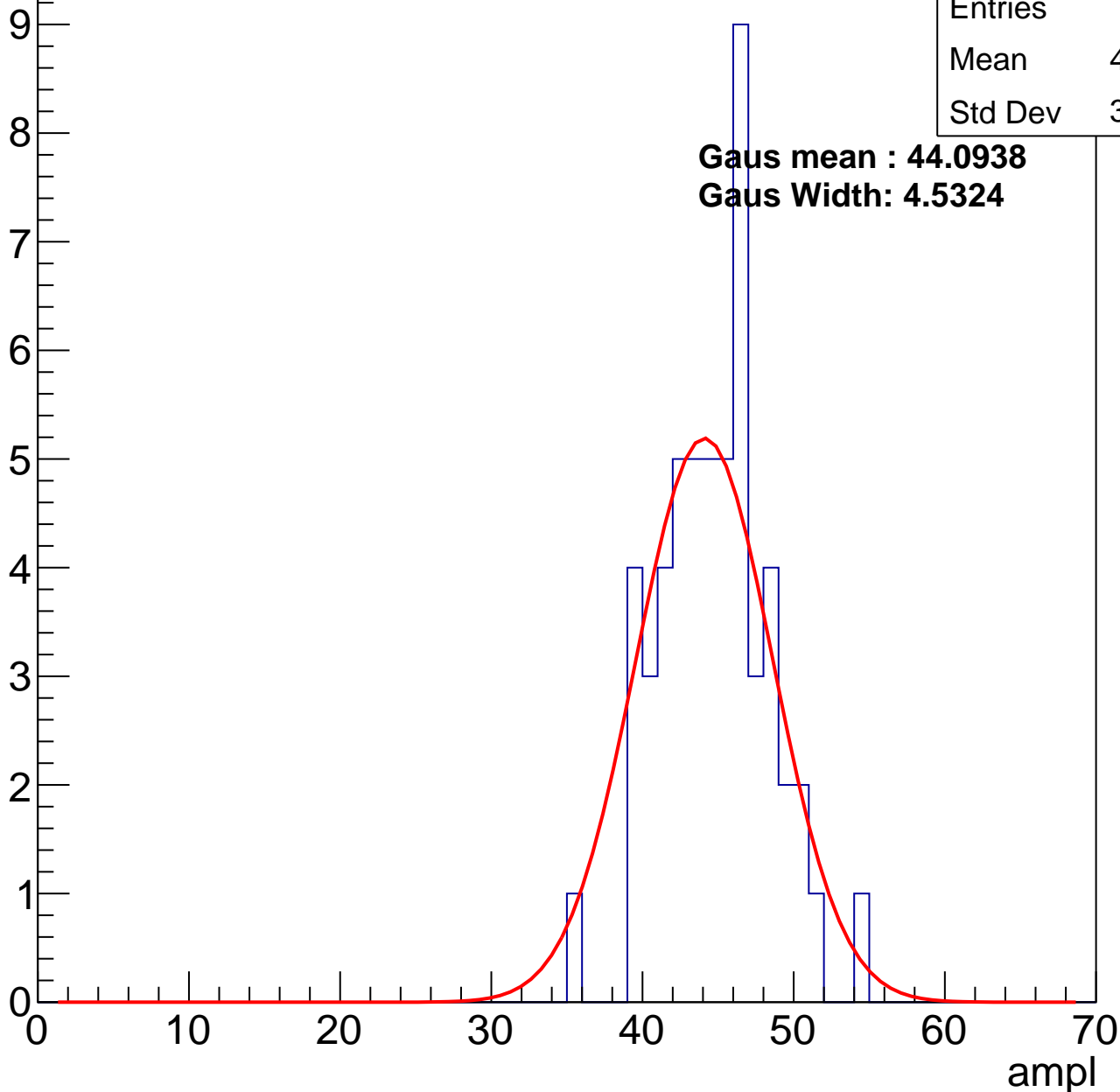
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	44.35
Std Dev	3.565

Gaus mean : 44.0938

Gaus Width: 4.5324

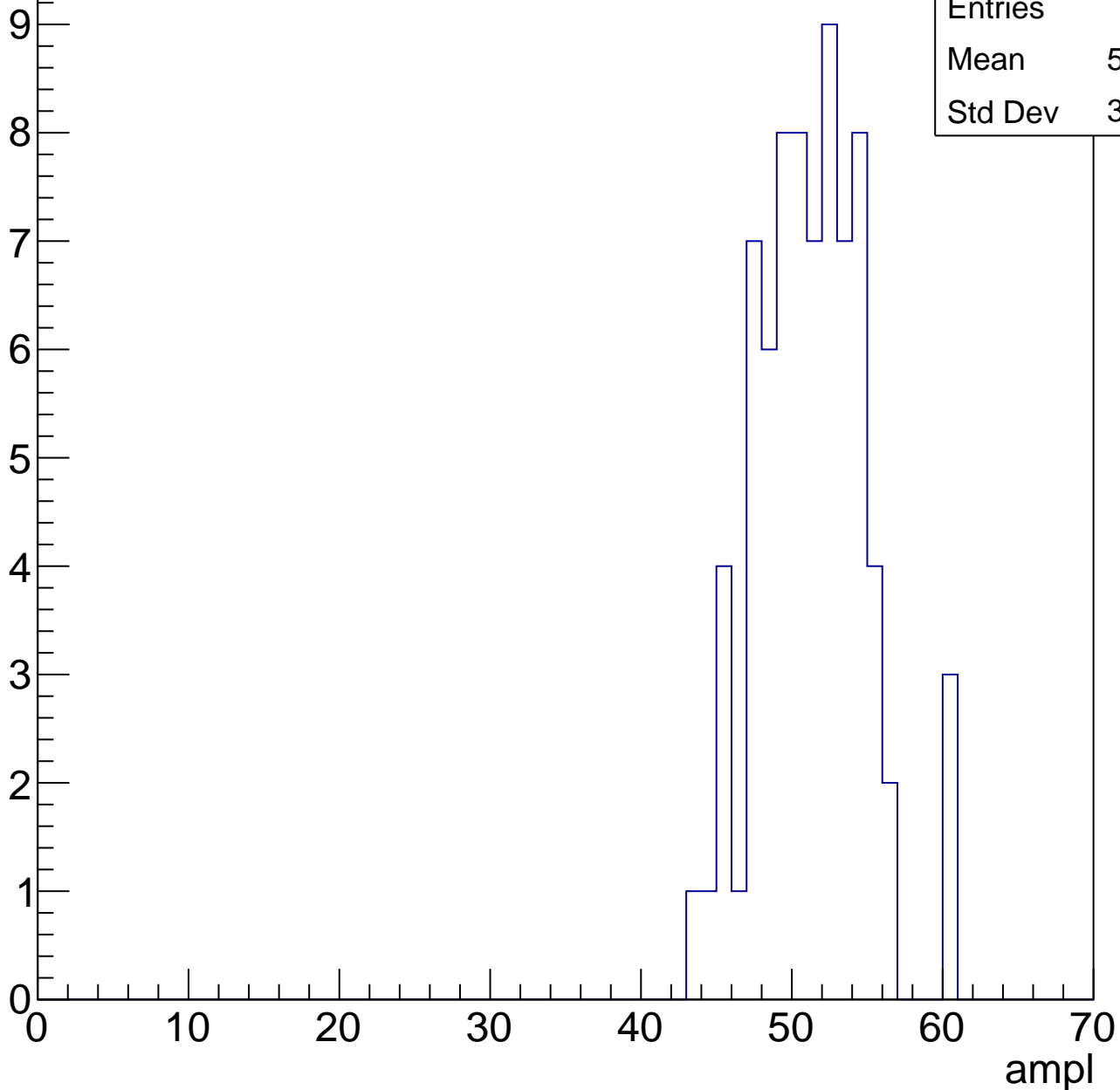


B1L101S, U21-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	50.82
Std Dev	3.538

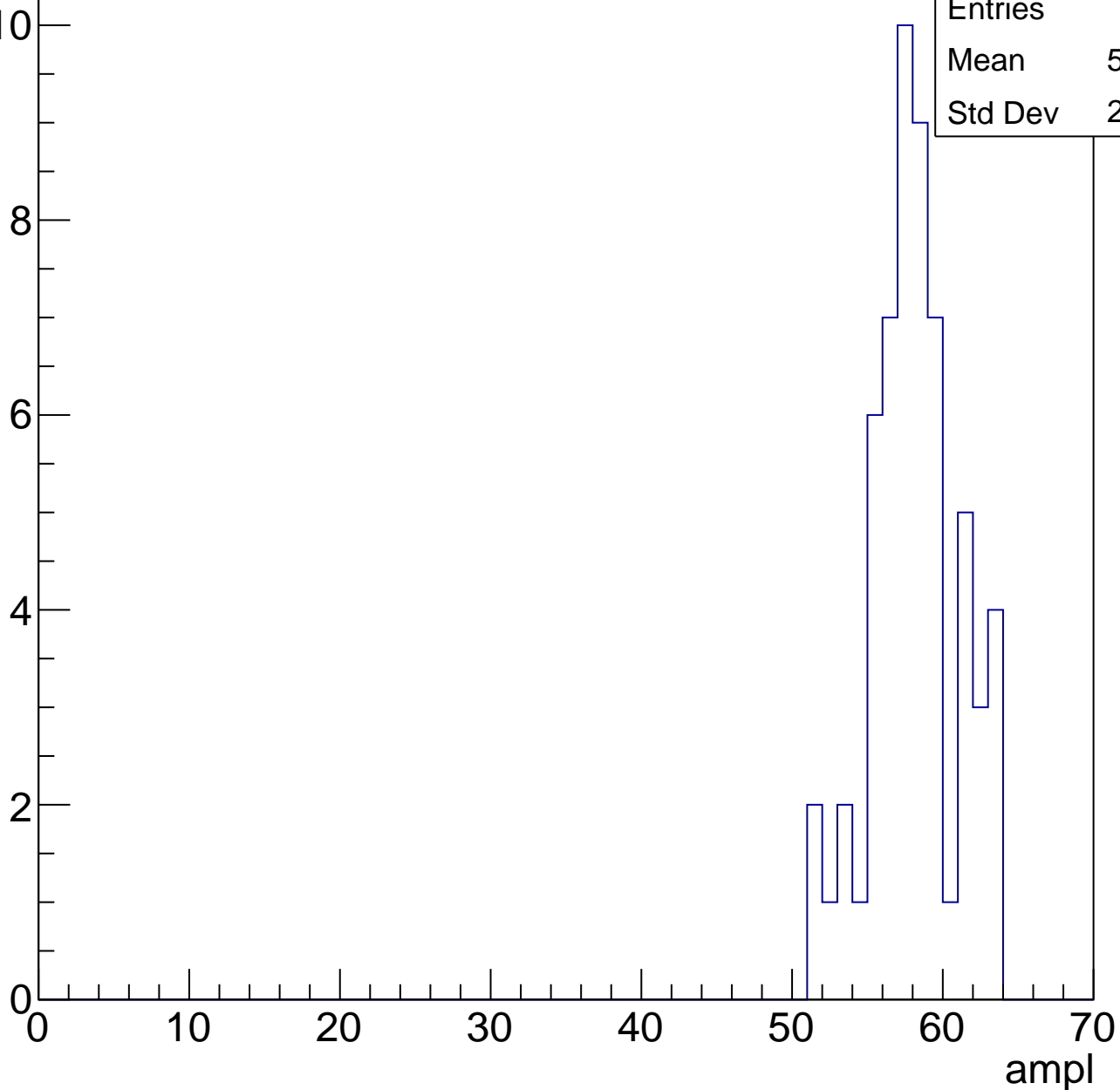


B1L101S, U21-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	57.66
Std Dev	2.904

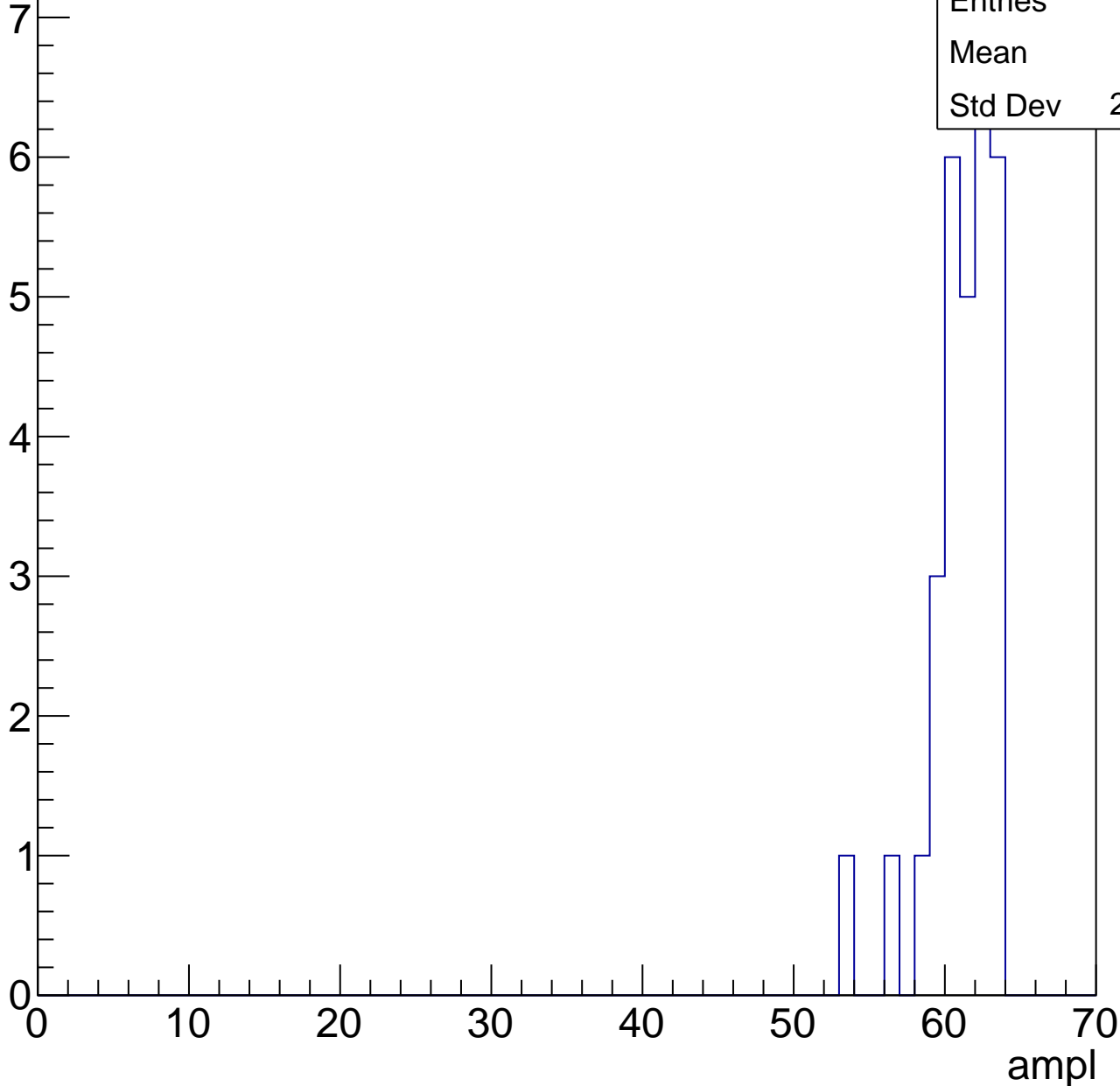


B1L101S, U21-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	30
Mean	60.7
Std Dev	2.193



B1L101S, U21-ch123, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

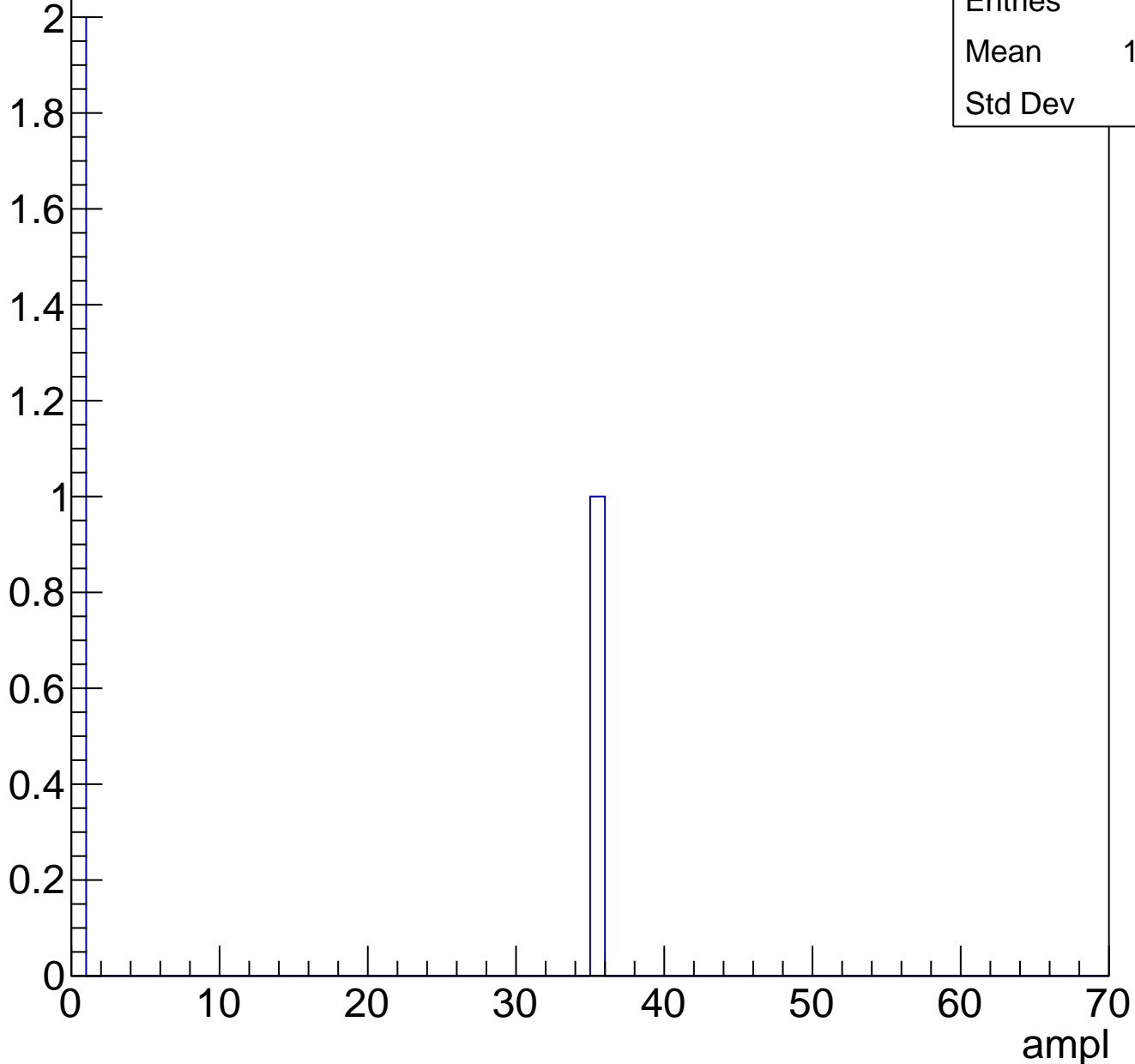


Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	11.67
Std Dev	16.5

B1L101S, U21-ch124, adc0

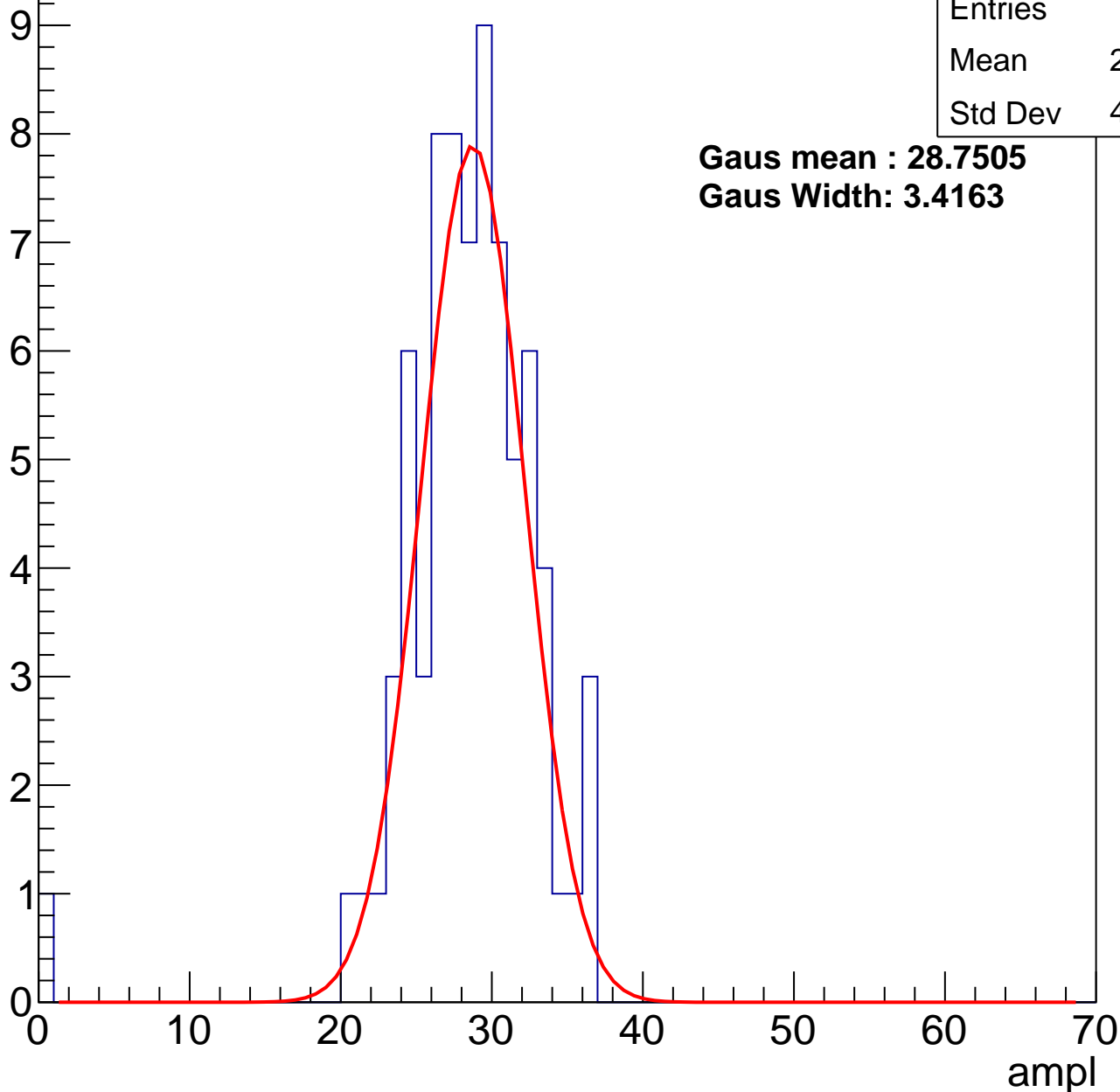
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	27.97
Std Dev	4.797

Gaus mean : 28.7505

Gaus Width: 3.4163



B1L101S, U21-ch124, adc1

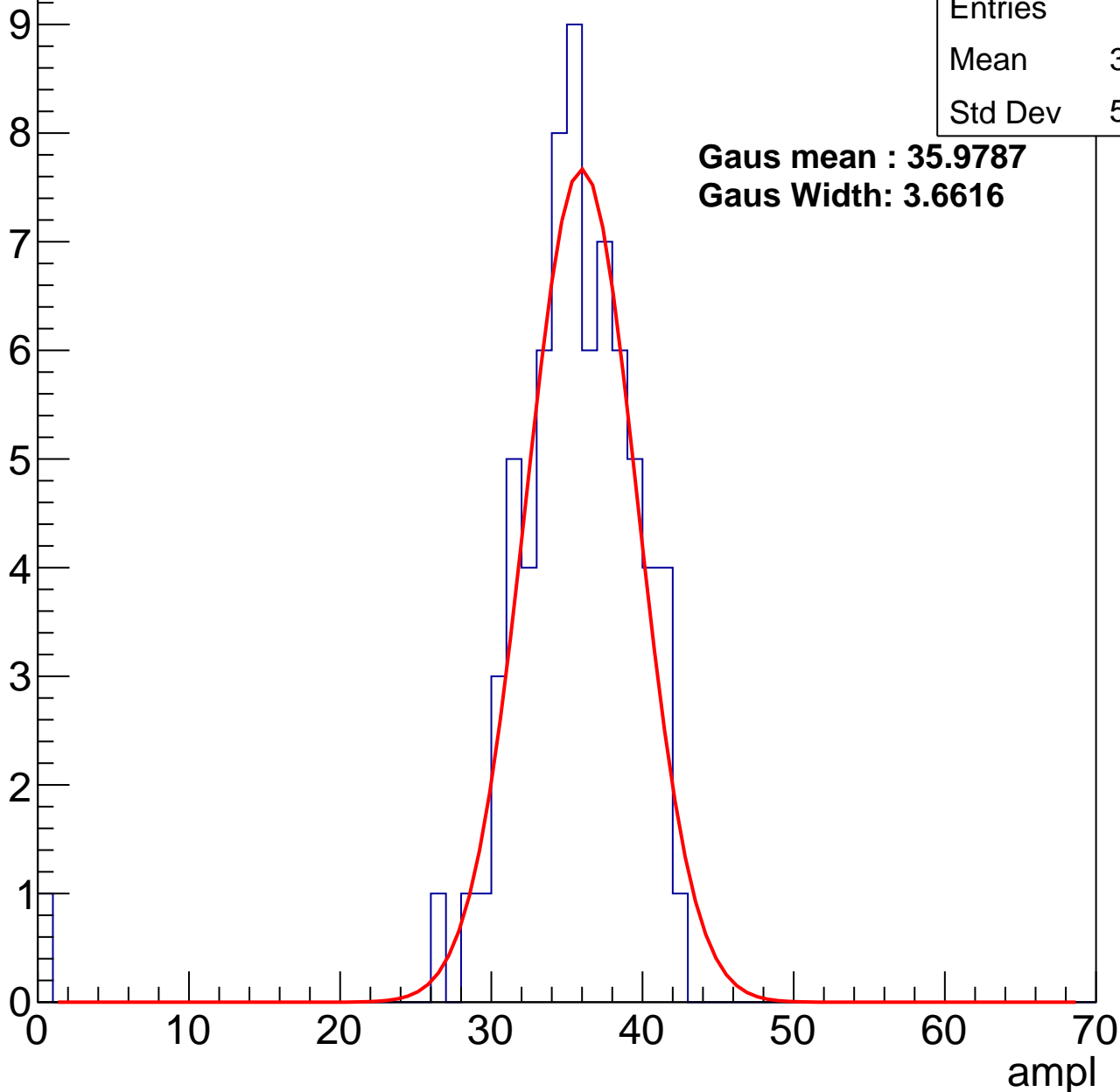
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	34.79
Std Dev	5.364

Gaus mean : 35.9787

Gaus Width: 3.6616



B1L101S, U21-ch124, adc2

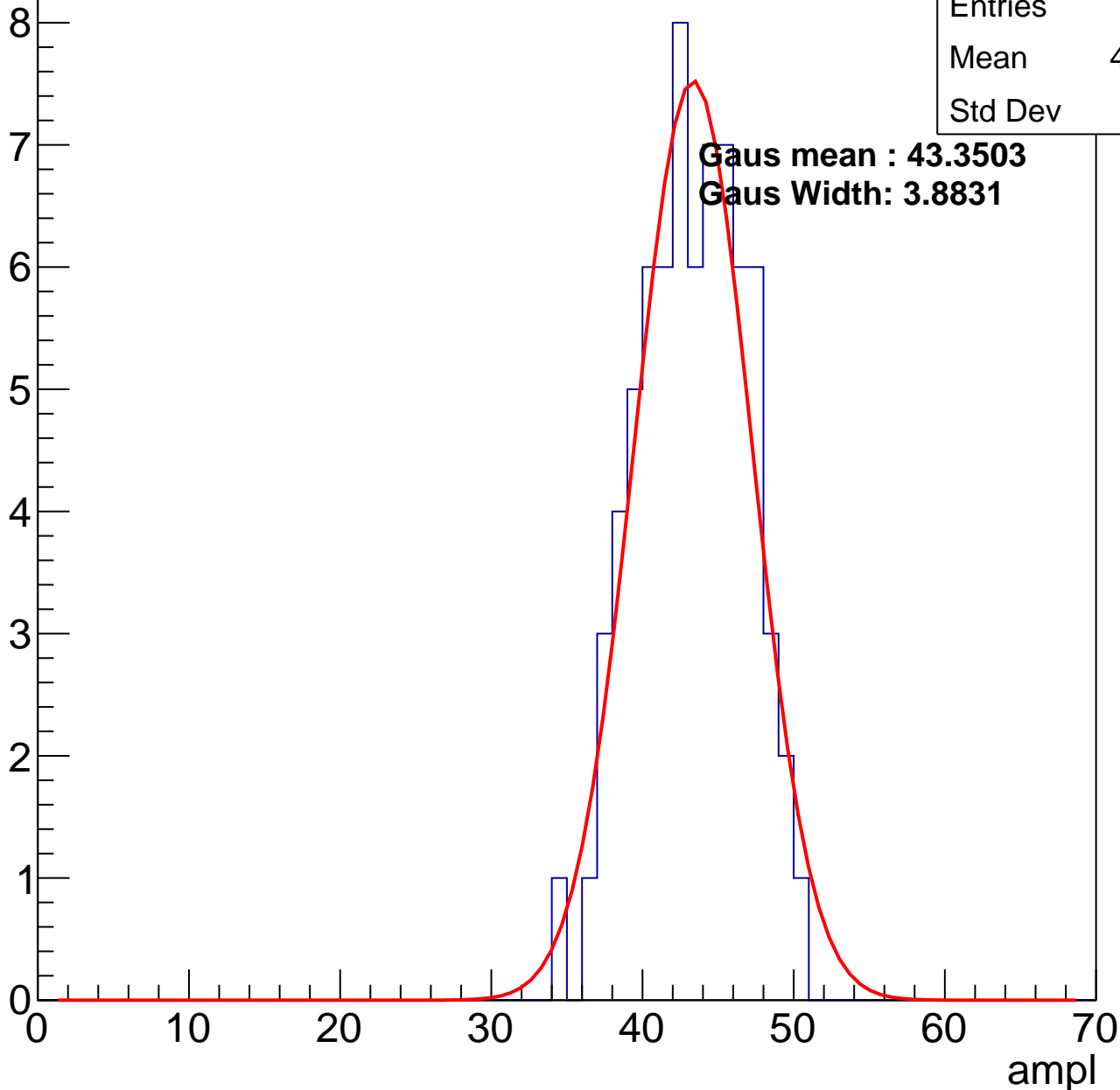
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.79
Std Dev	3.5

Gaus mean : 43.3503

Gaus Width: 3.8831

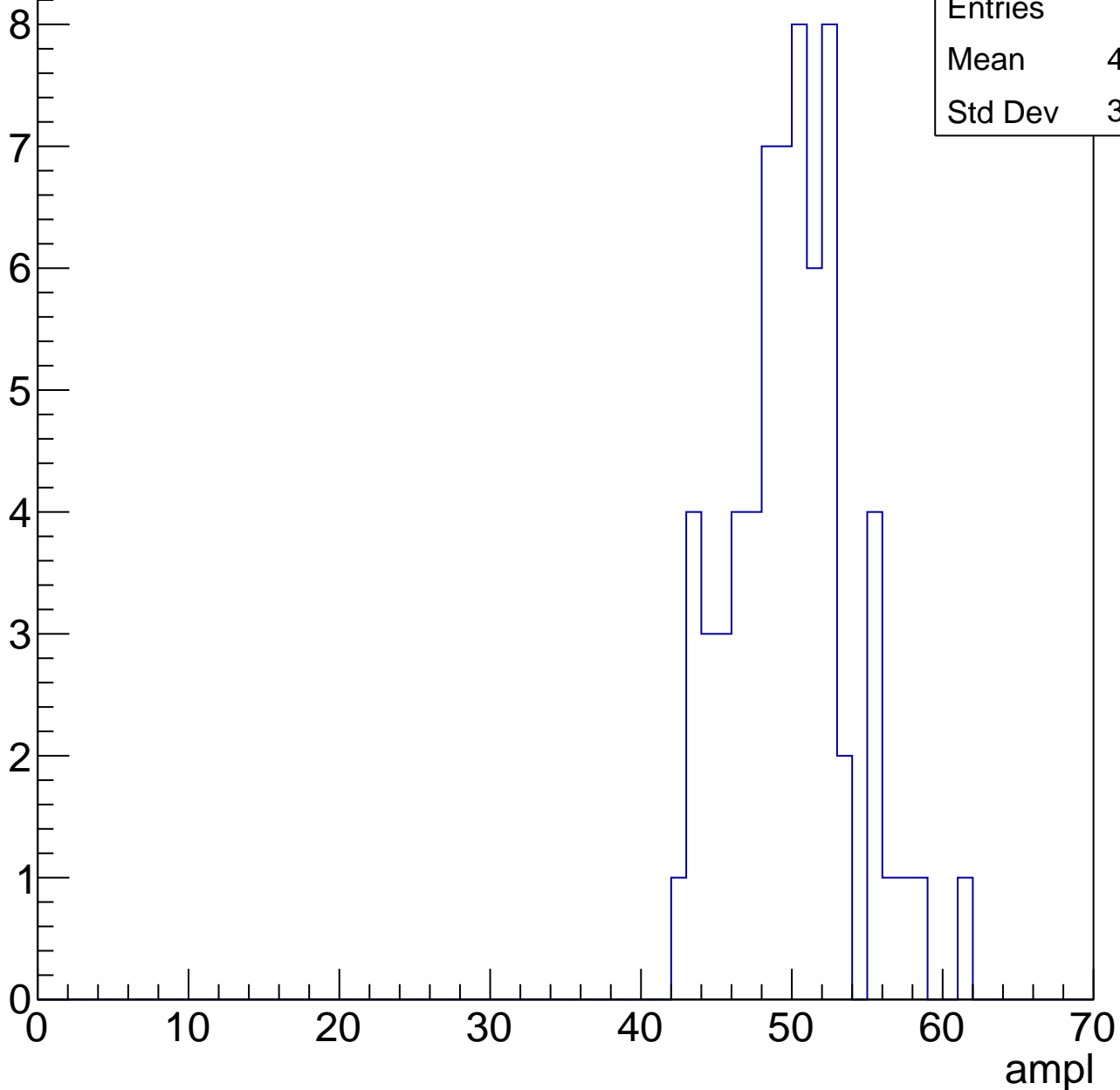


B1L101S, U21-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	49.42
Std Dev	3.886

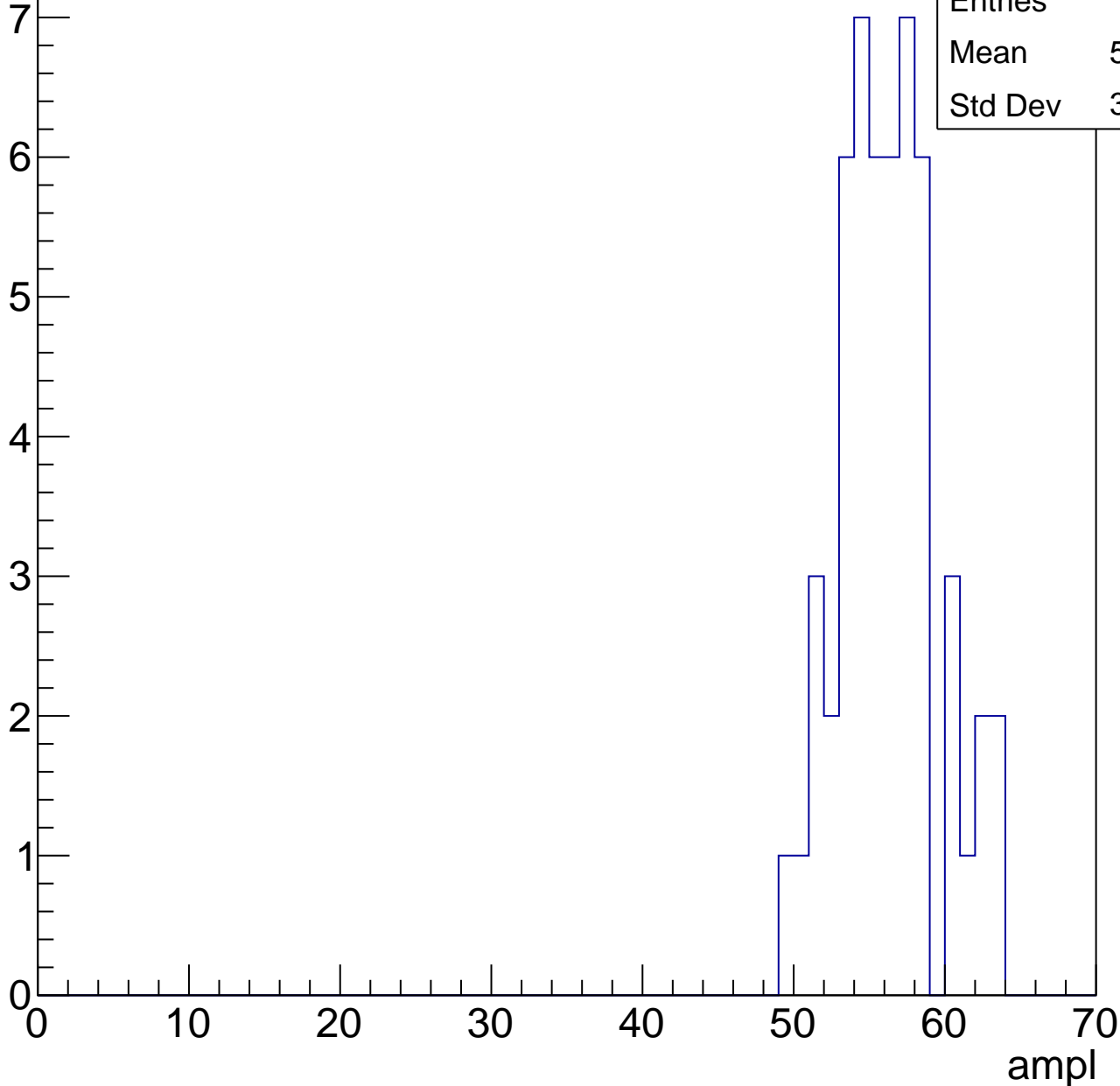


B1L101S, U21-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	55.77
Std Dev	3.225

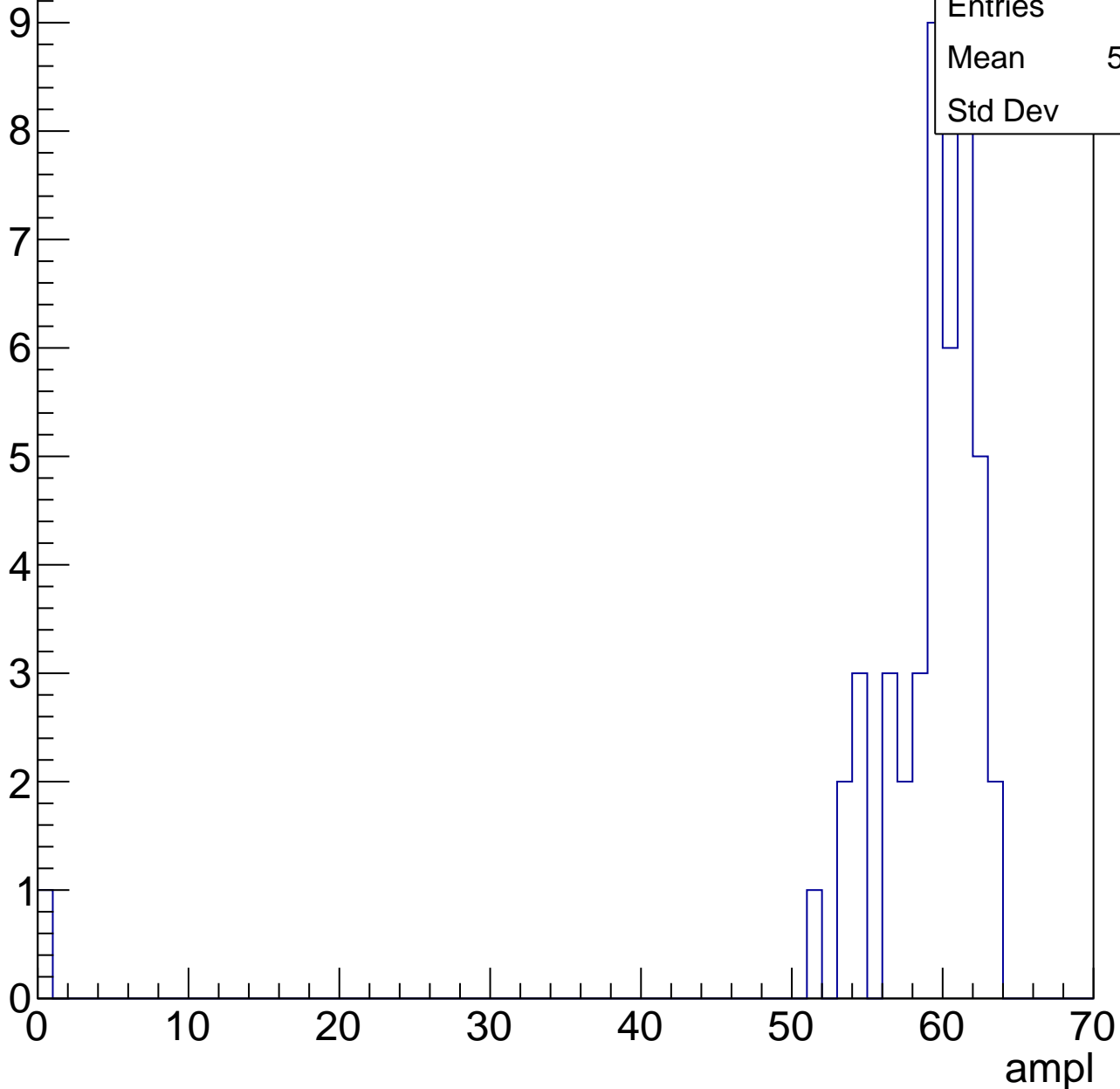


B1L101S, U21-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	57.56
Std Dev	9.13

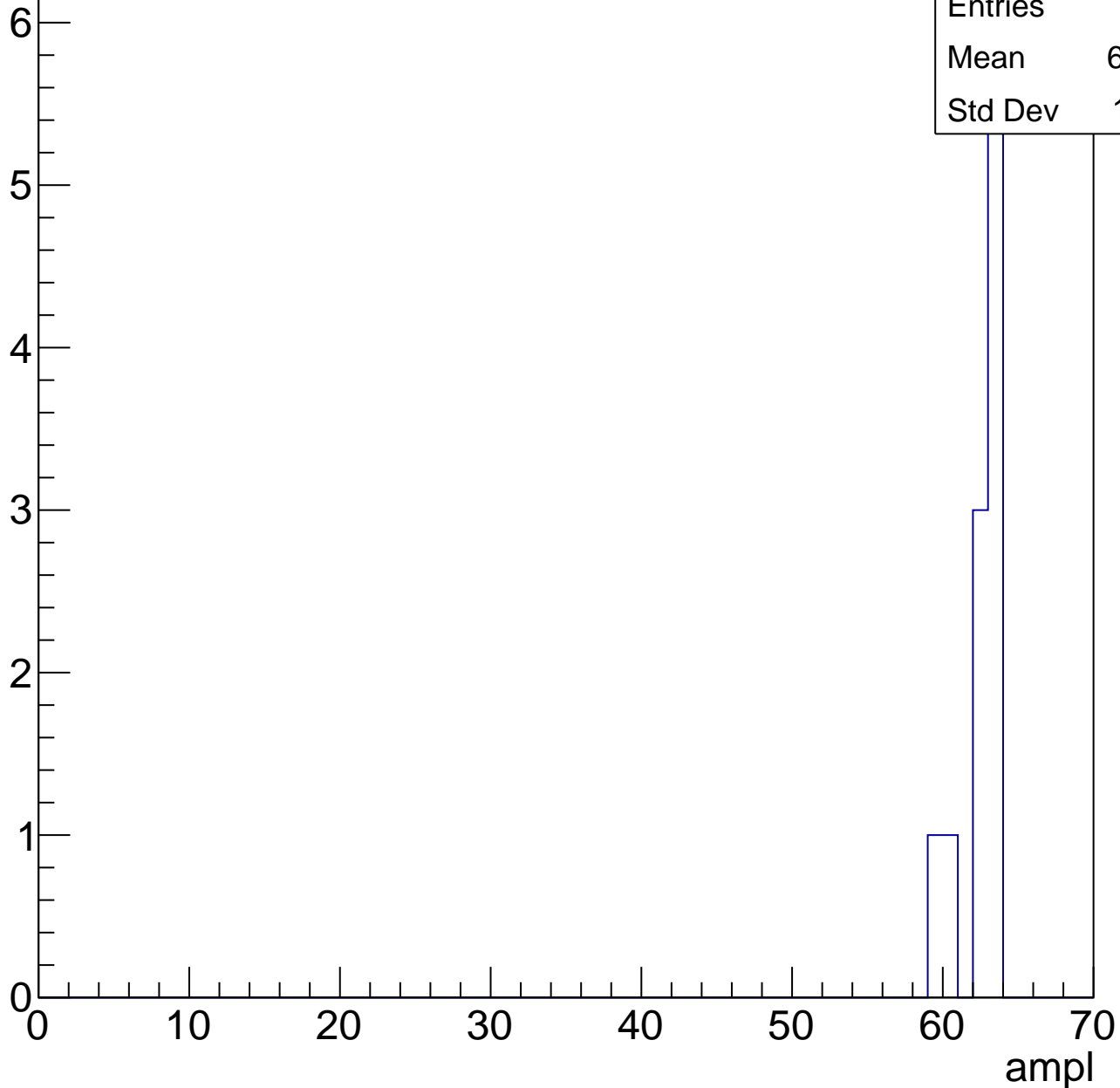


B1L101S, U21-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	62.09
Std Dev	1.311



B1L101S, U21-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U21-ch125, adc0

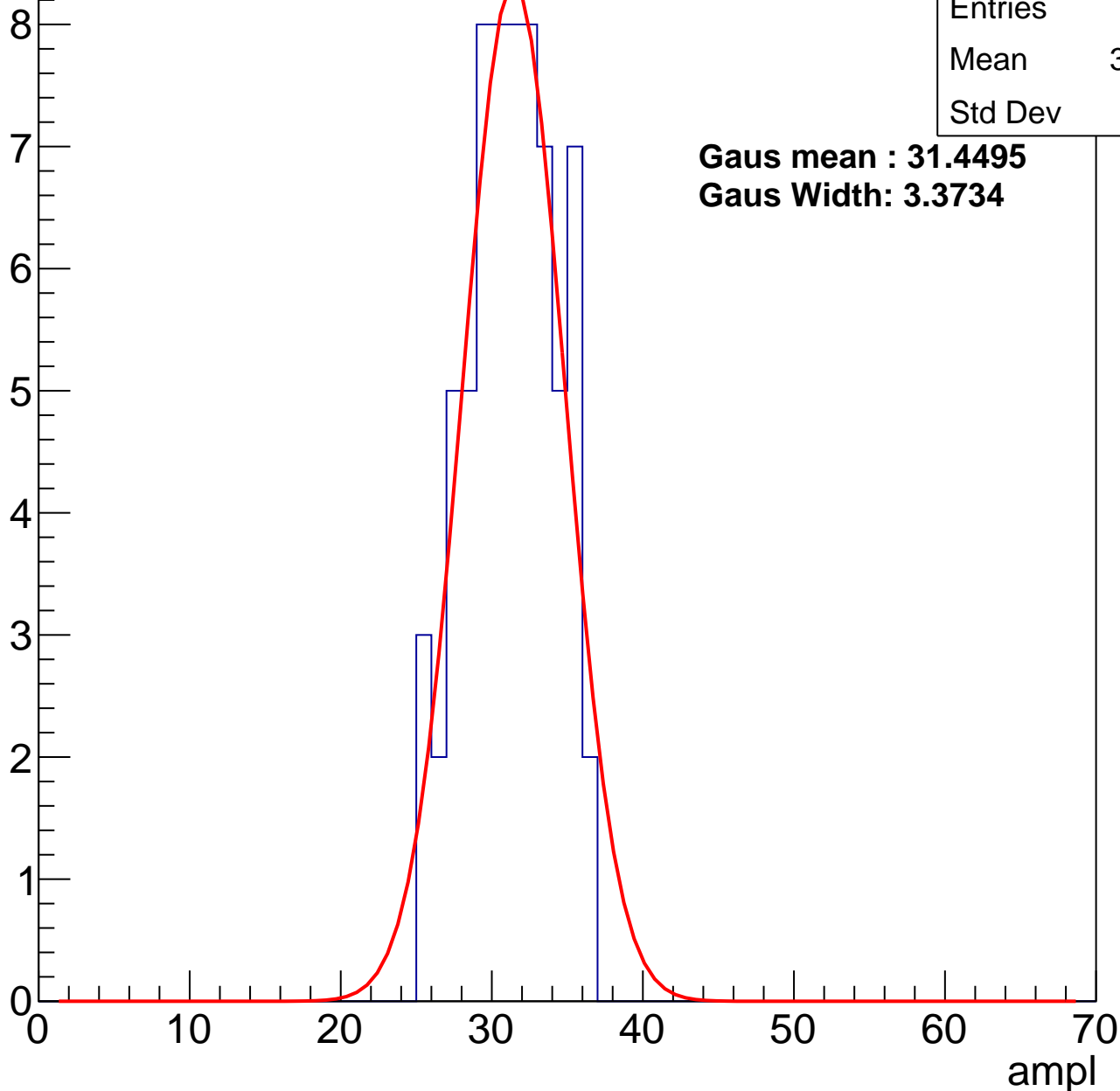
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.82
Std Dev	2.88

Gaus mean : 31.4495

Gaus Width: 3.3734



B1L101S, U21-ch125, adc1

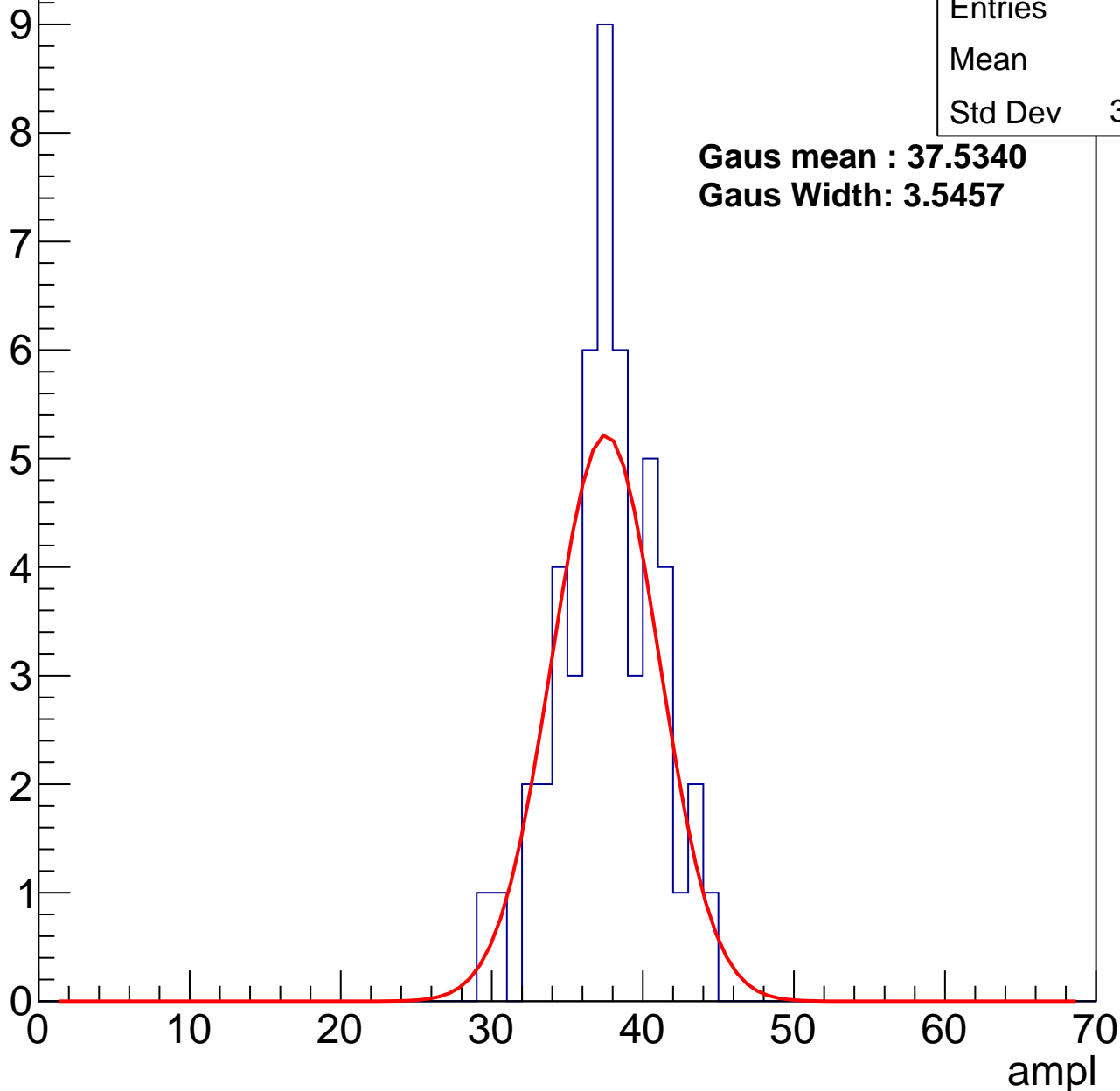
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	37.2
Std Dev	3.225

Gaus mean : 37.5340

Gaus Width: 3.5457



B1L101S, U21-ch125, adc2

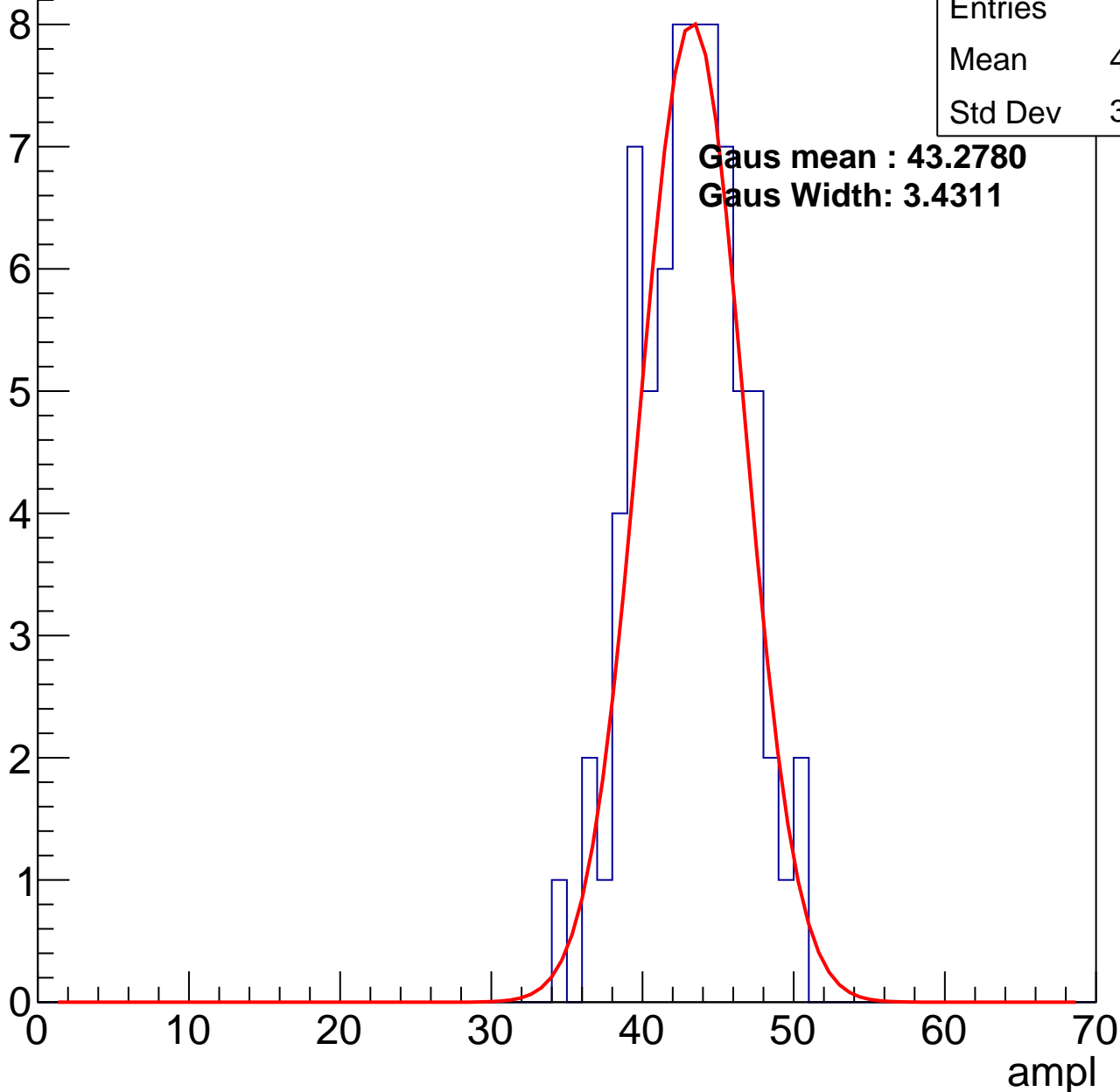
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.65
Std Dev	3.416

Gaus mean : 43.2780

Gaus Width: 3.4311

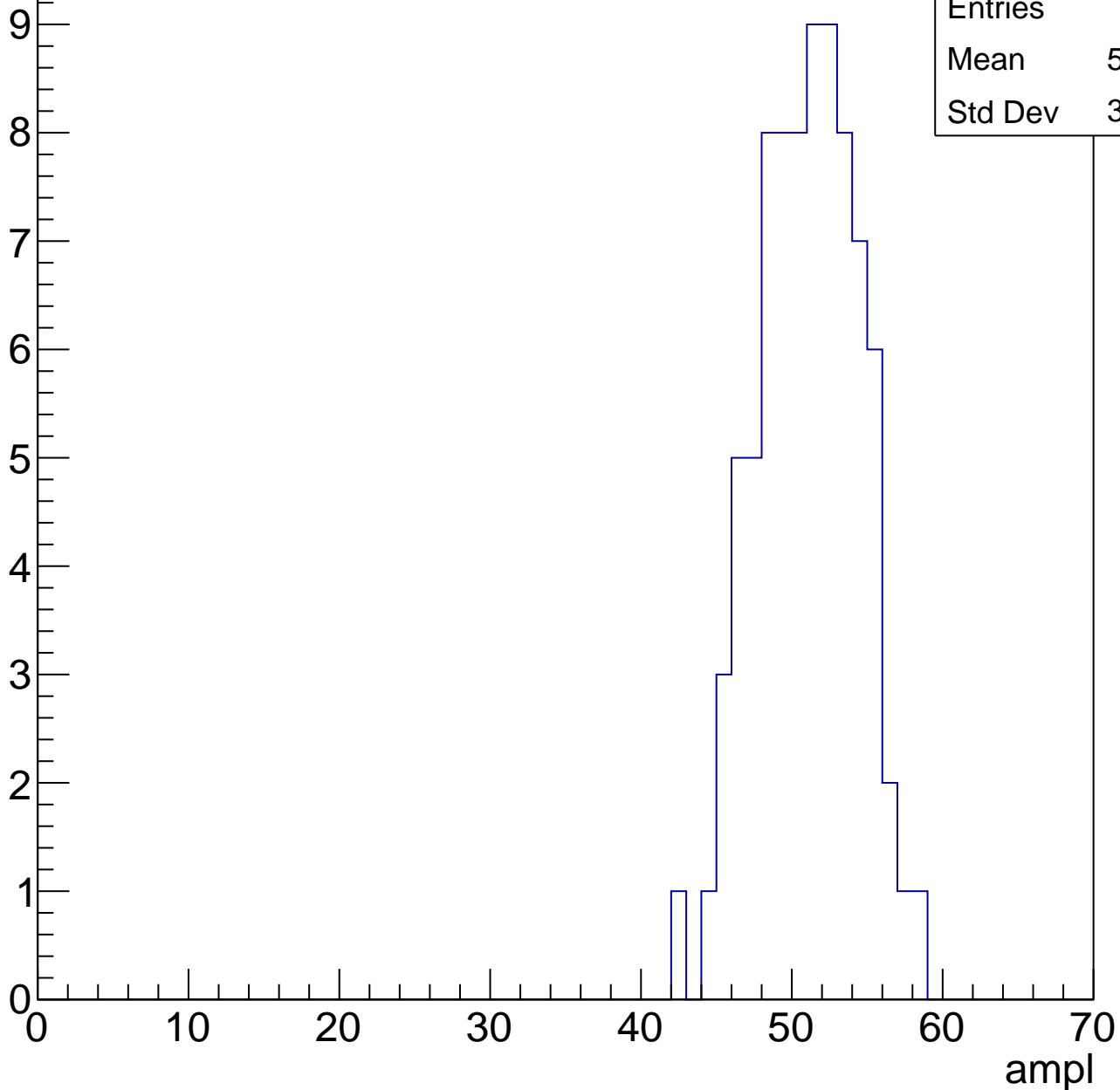


B1L101S, U21-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	50.59
Std Dev	3.268

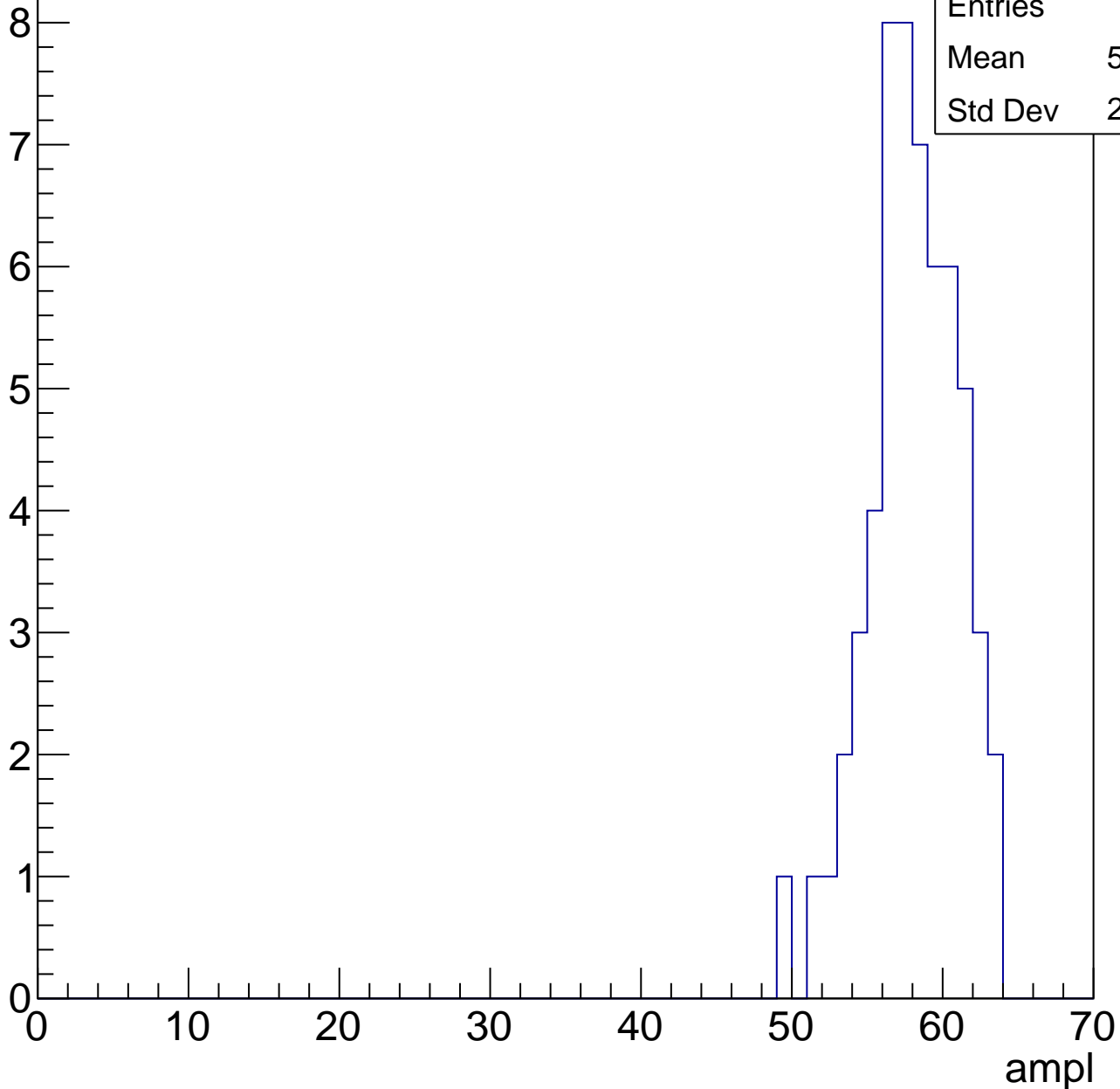


B1L101S, U21-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	57.56
Std Dev	2.968

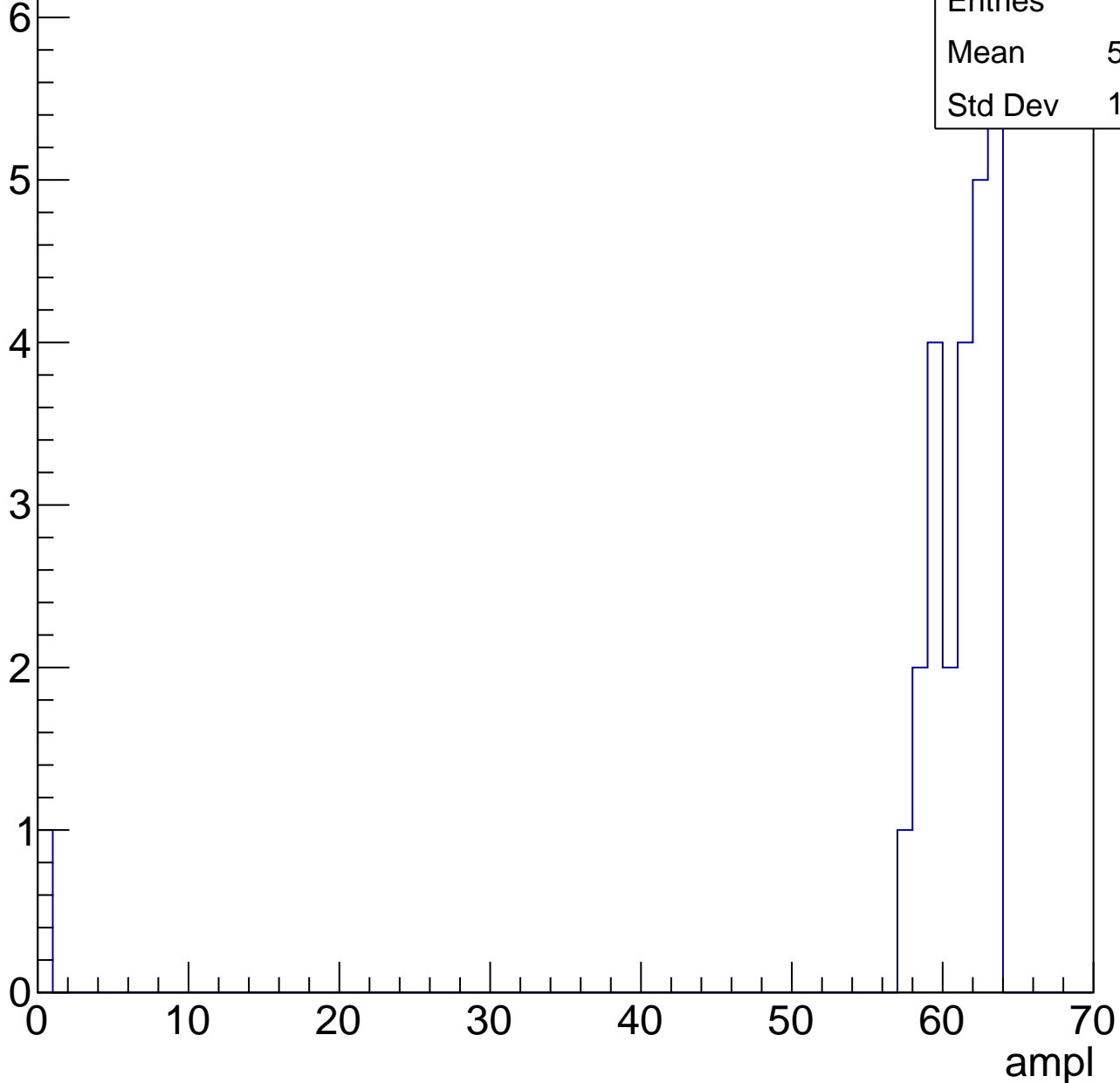


B1L101S, U21-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.44
Std Dev	12.06



B1L101S, U21-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.33
Std Dev	1.106

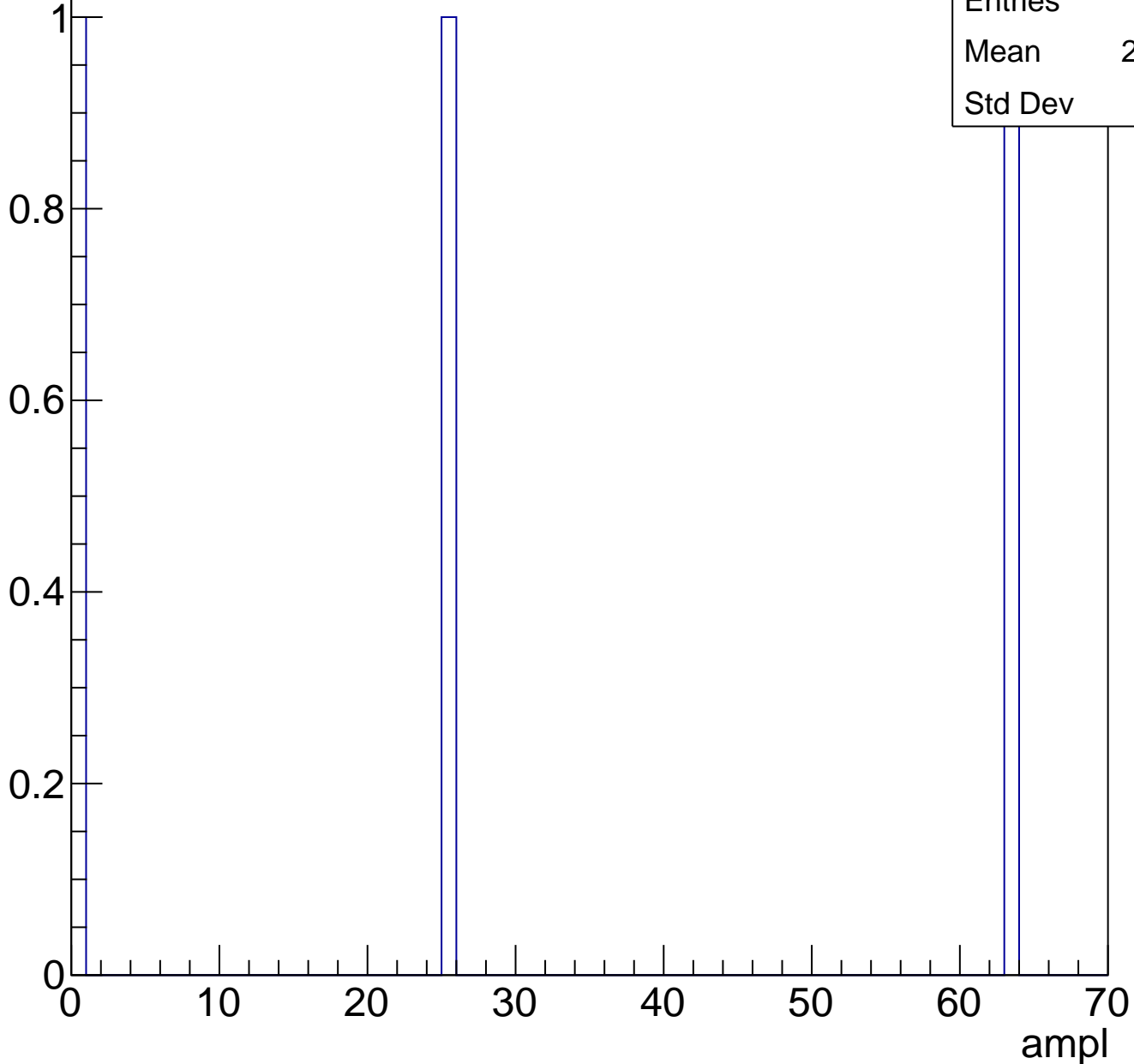
ampl

0 10 20 30 40 50 60 70

B1L101S, U21-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	29.33
Std Dev	25.9

B1L101S, U21-ch126, adc0

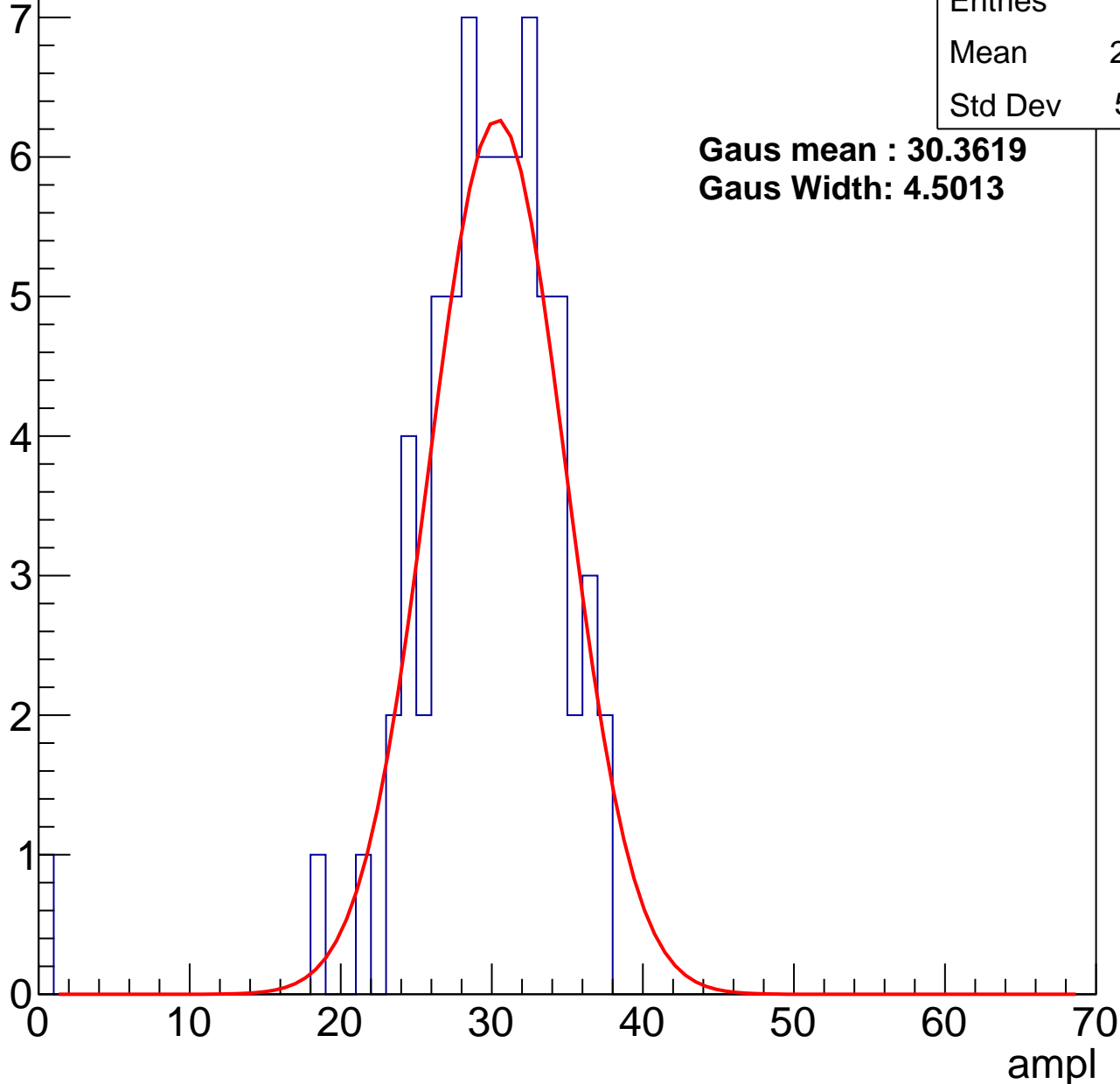
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.19
Std Dev	5.271

Gaus mean : 30.3619

Gaus Width: 4.5013



B1L101S, U21-ch126, adc1

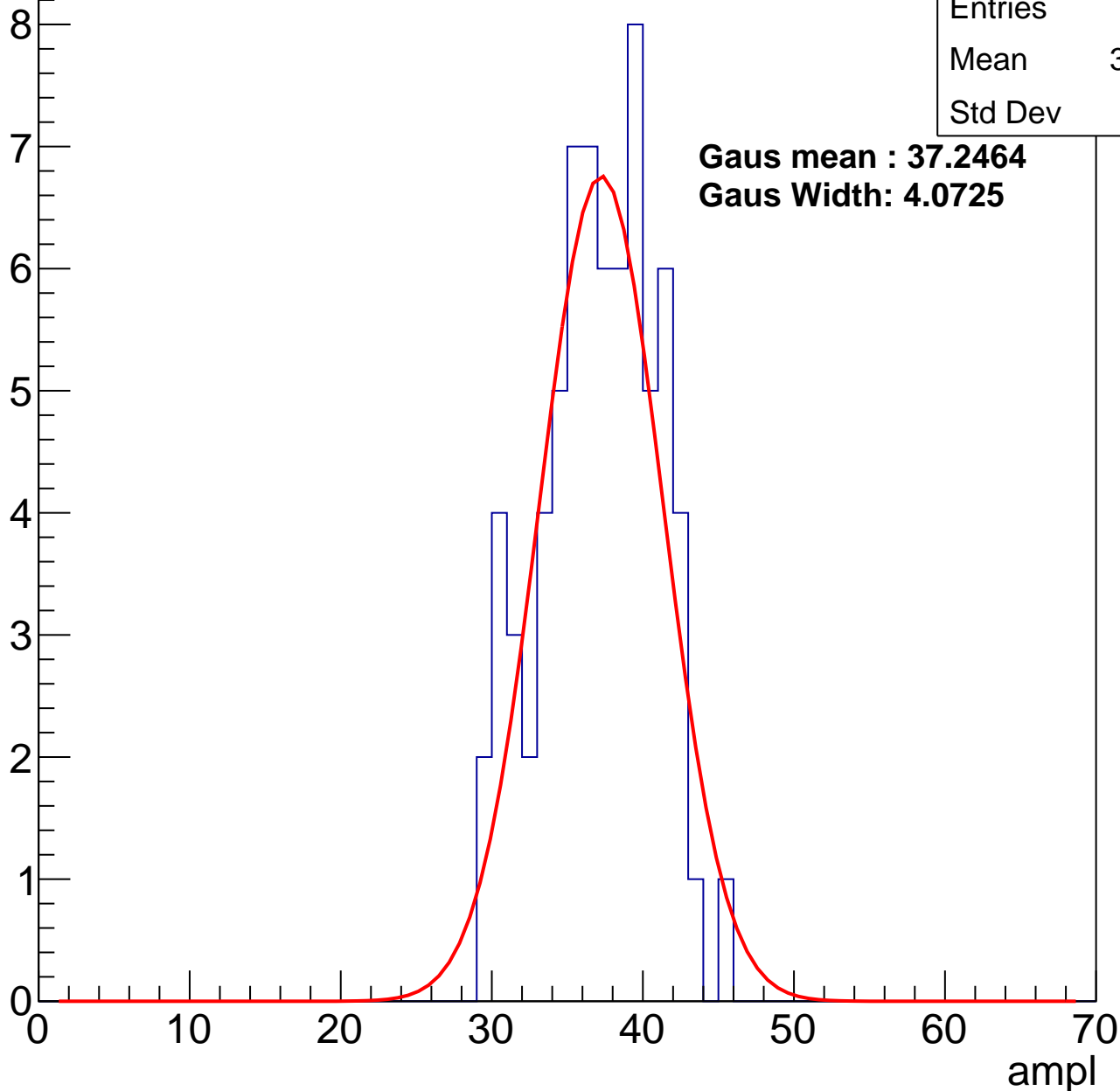
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	36.59
Std Dev	3.74

Gaus mean : 37.2464

Gaus Width: 4.0725



B1L101S, U21-ch126, adc2

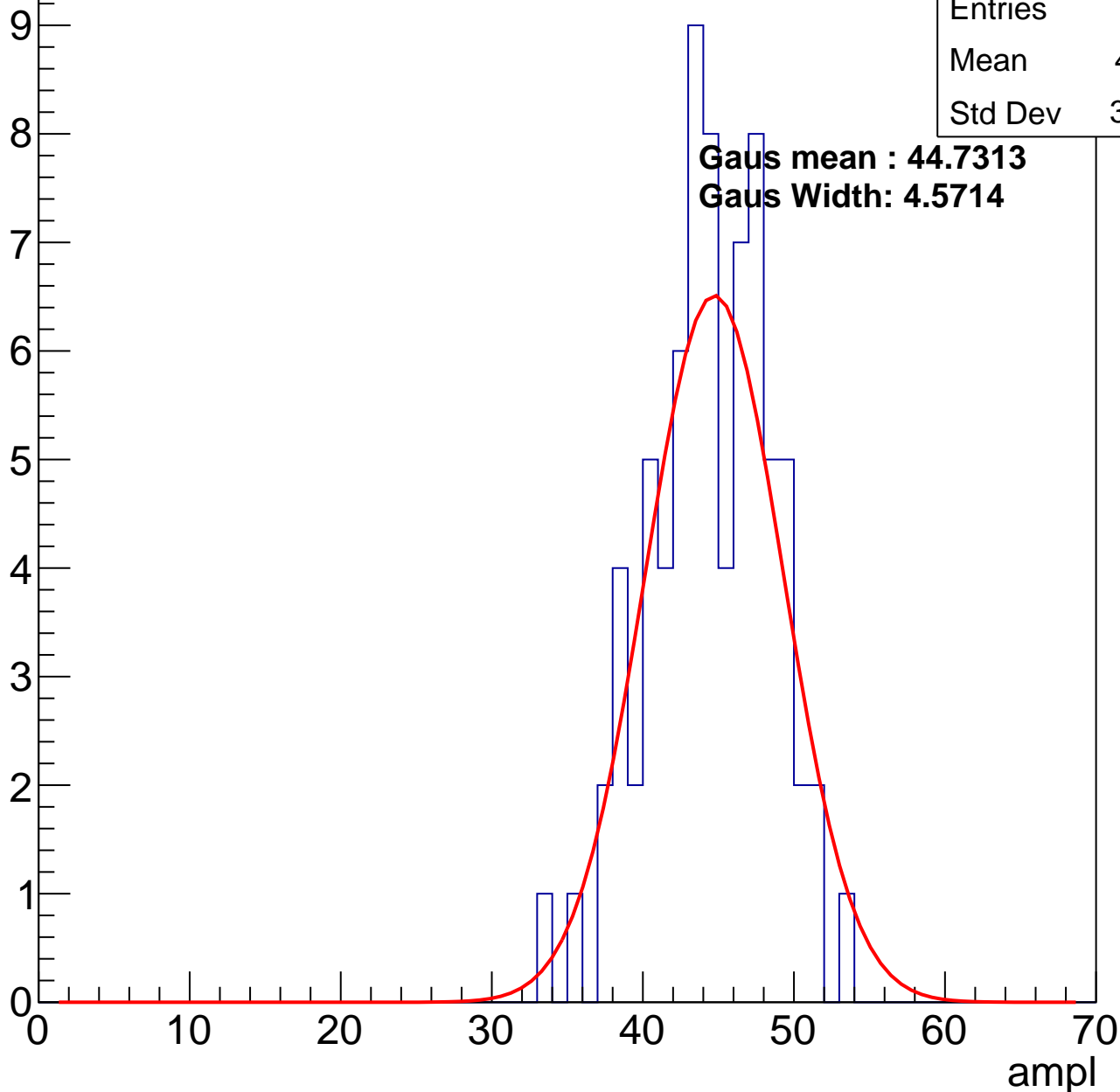
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	44.01
Std Dev	3.979

Gaus mean : 44.7313

Gaus Width: 4.5714

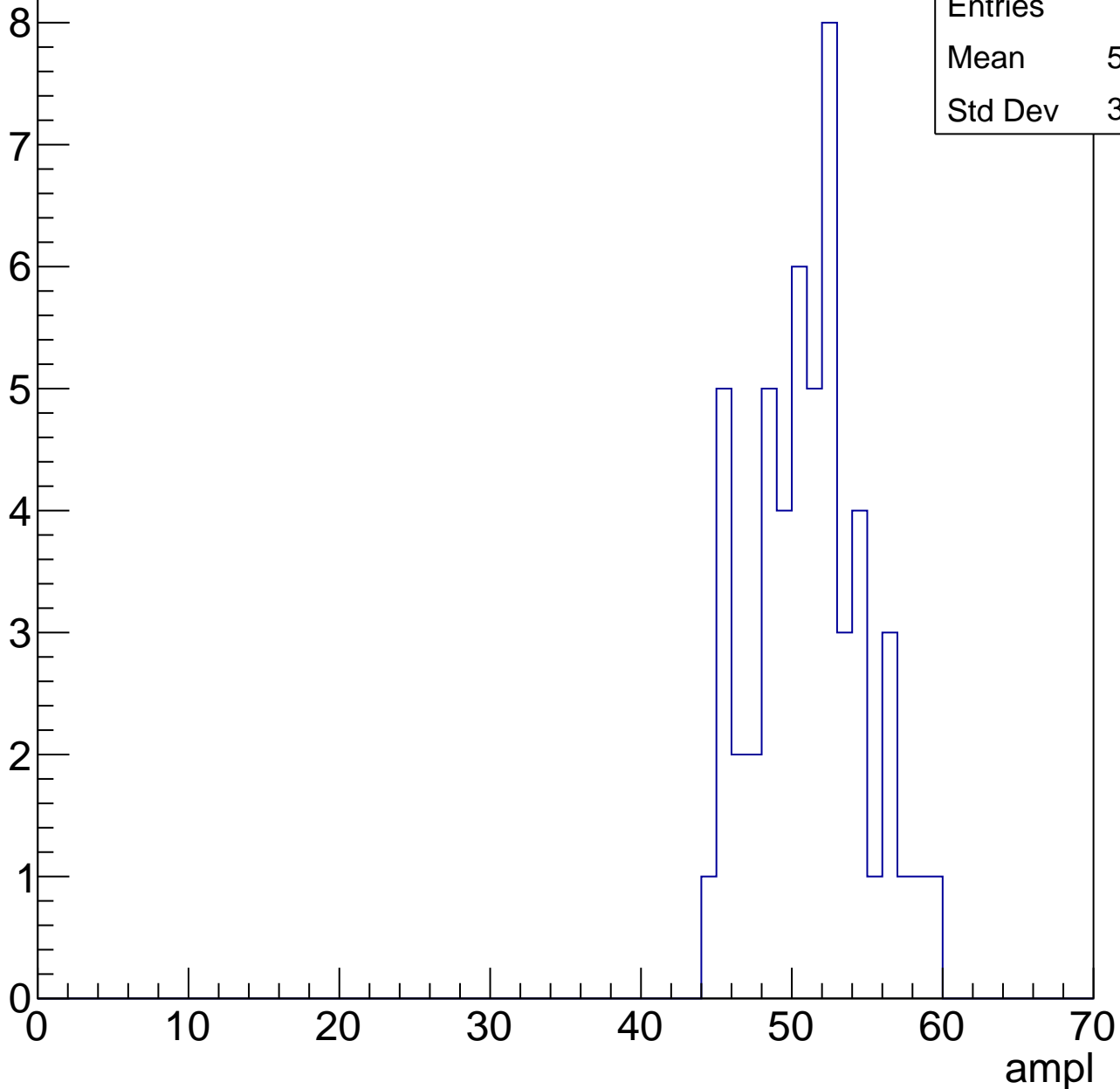


B1L101S, U21-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	50.65
Std Dev	3.584

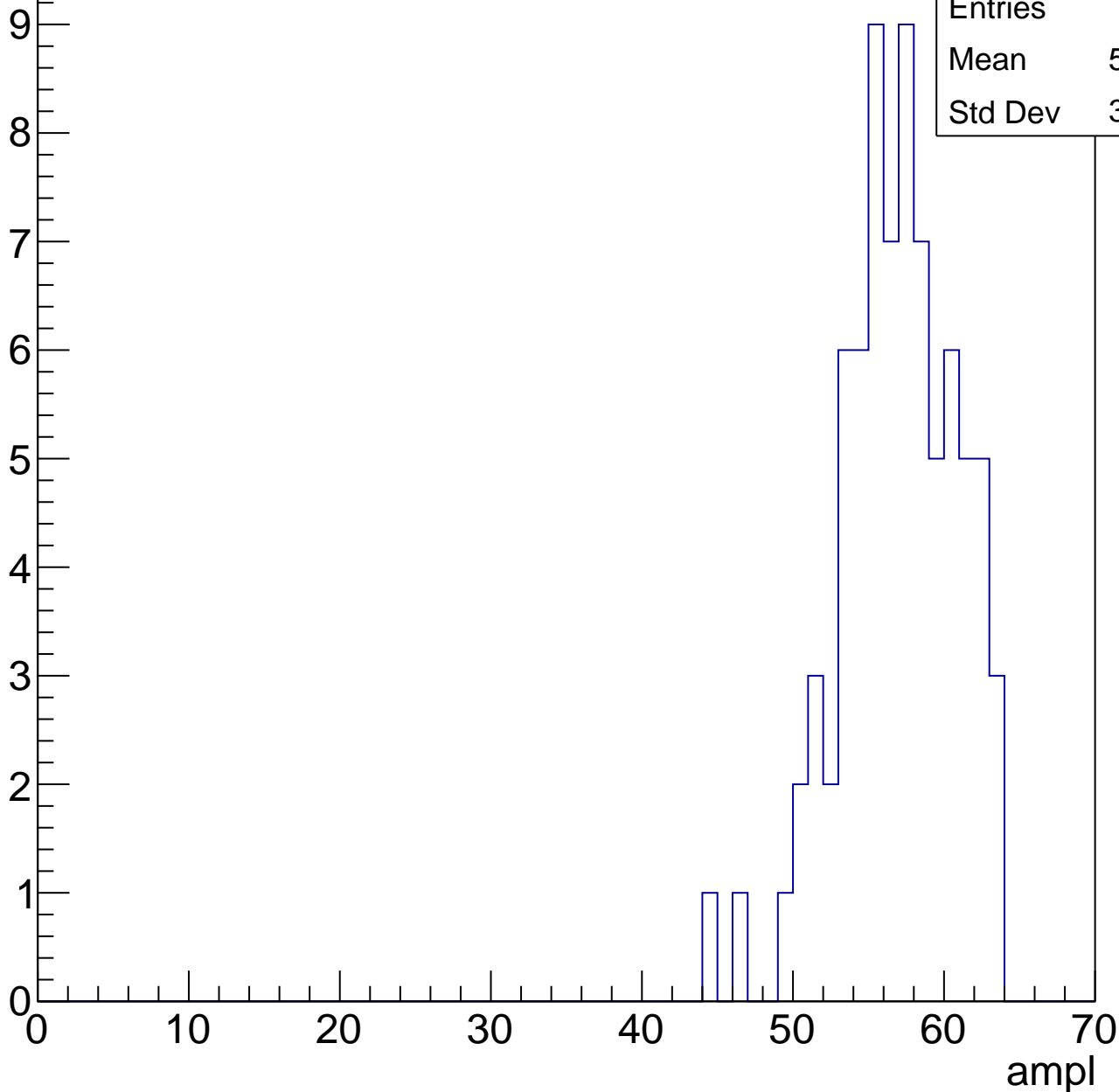


B1L101S, U21-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	56.45
Std Dev	3.878

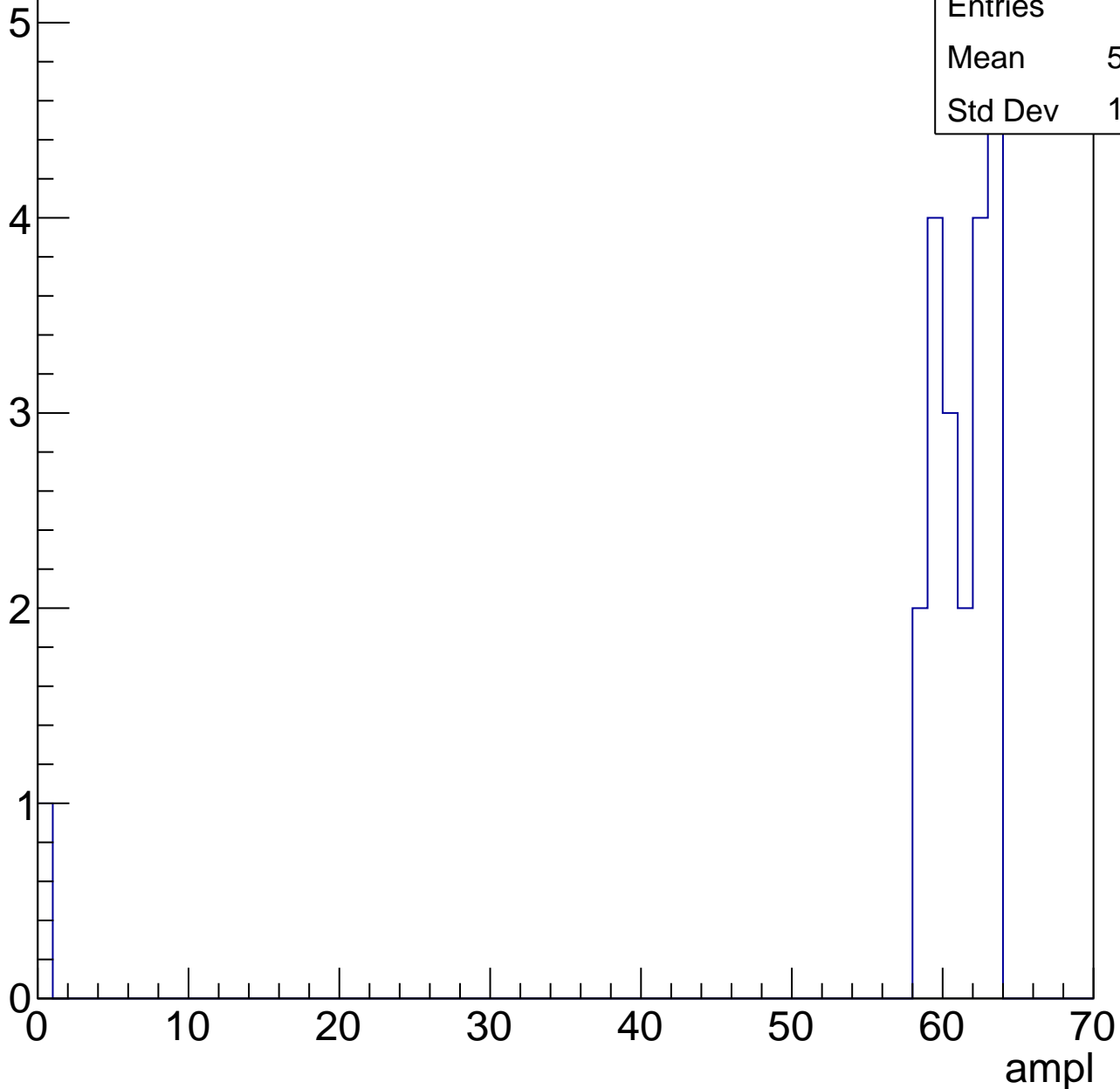


B1L101S, U21-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

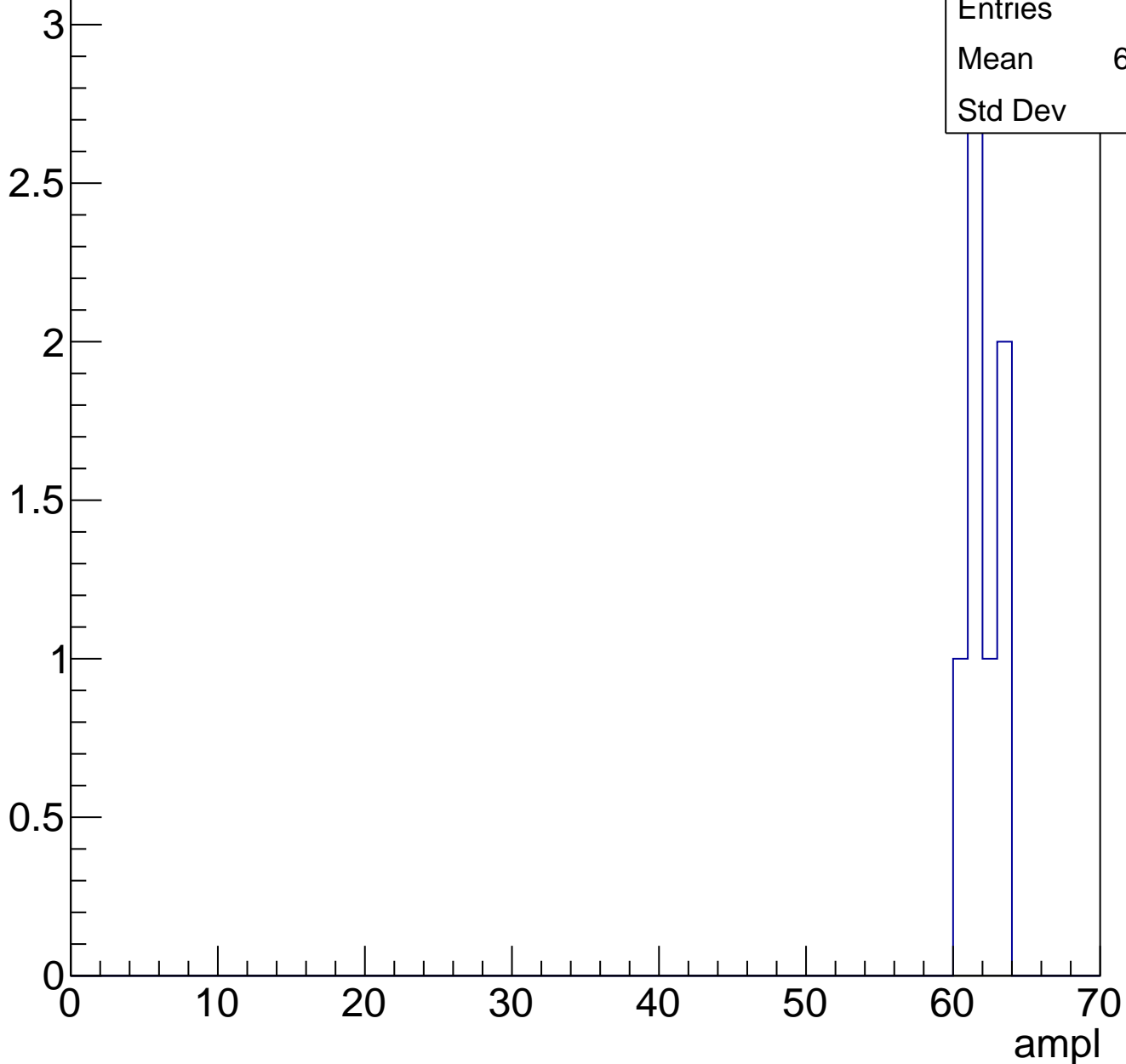
Entries	21
Mean	57.95
Std Dev	13.07



B1L101S, U21-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U21-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U21-ch127, adc0

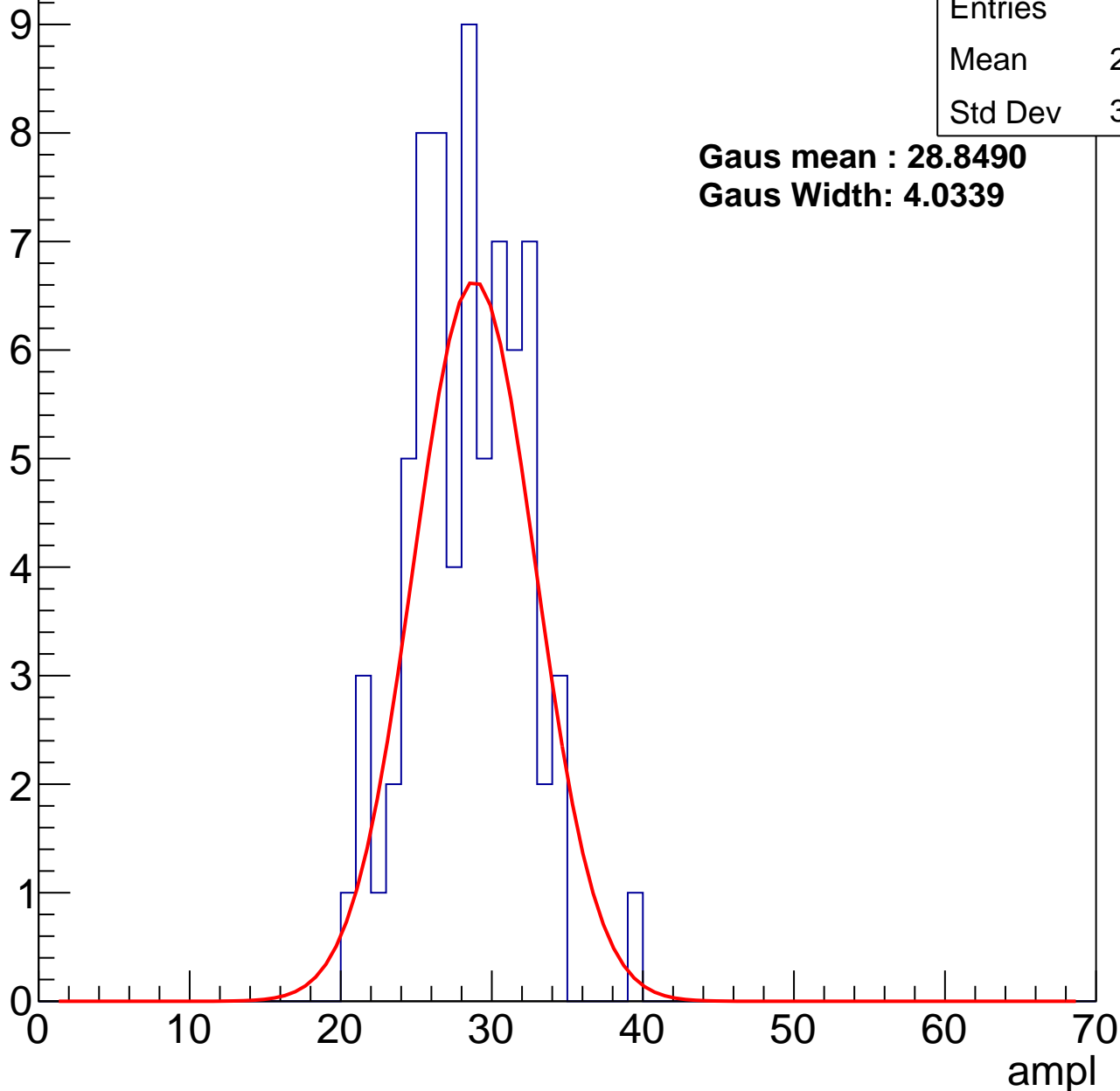
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.93
Std Dev	3.657

Gaus mean : 28.8490

Gaus Width: 4.0339



B1L101S, U21-ch127, adc1

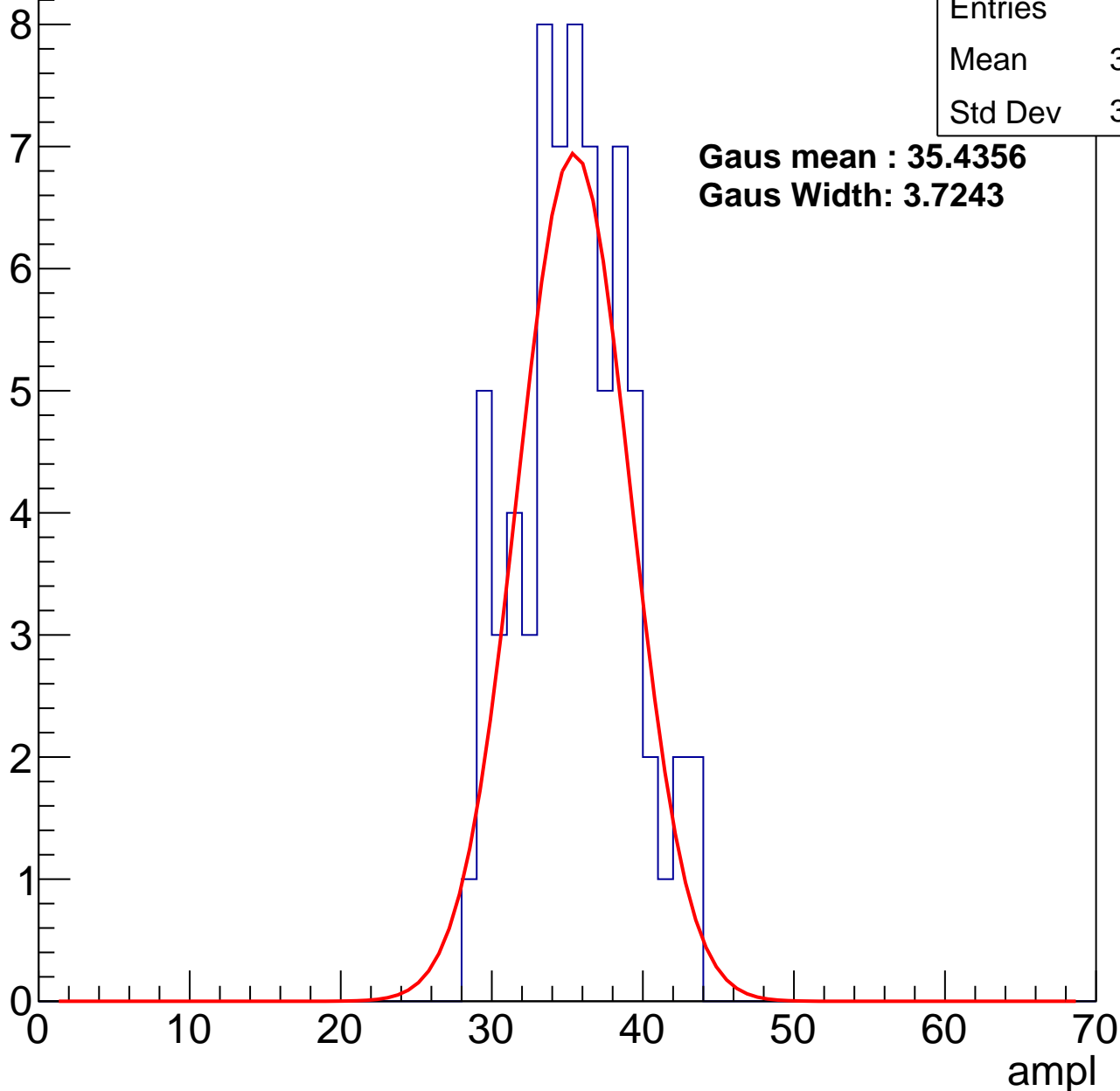
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.06
Std Dev	3.617

Gaus mean : 35.4356

Gaus Width: 3.7243



B1L101S, U21-ch127, adc2

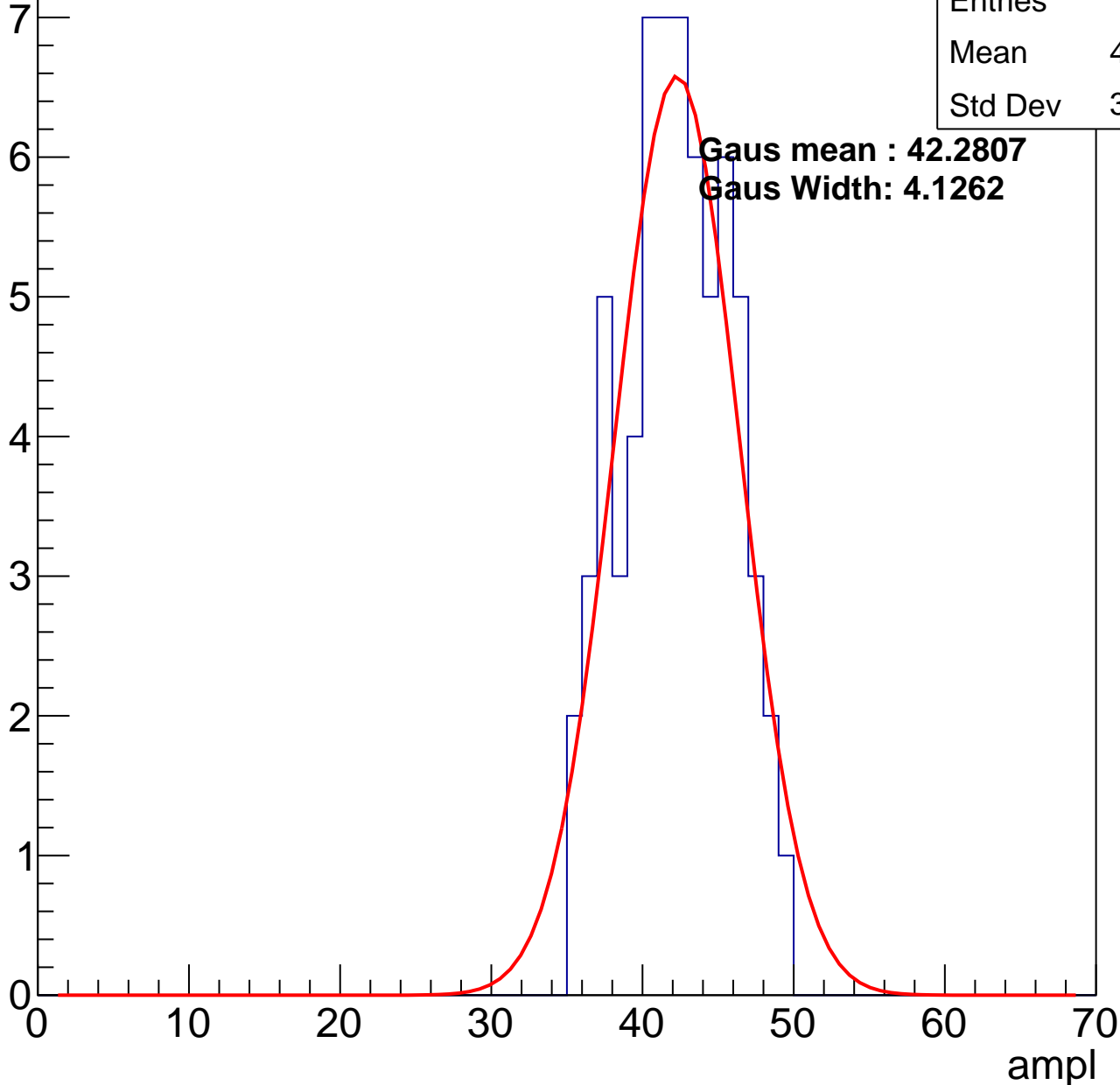
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	41.79
Std Dev	3.488

Gaus mean : 42.2807

Gaus Width: 4.1262

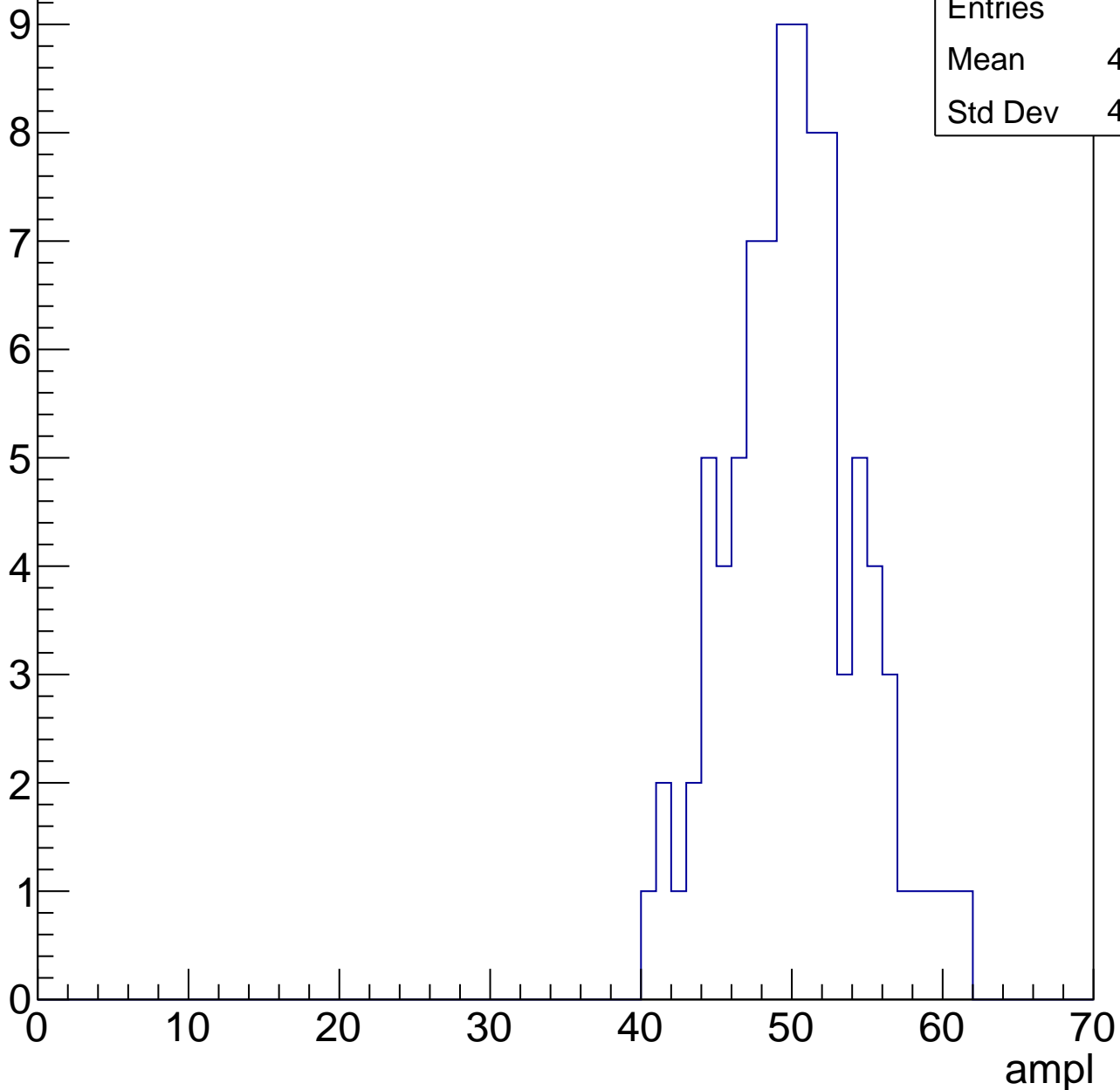


B1L101S, U21-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	49.68
Std Dev	4.324

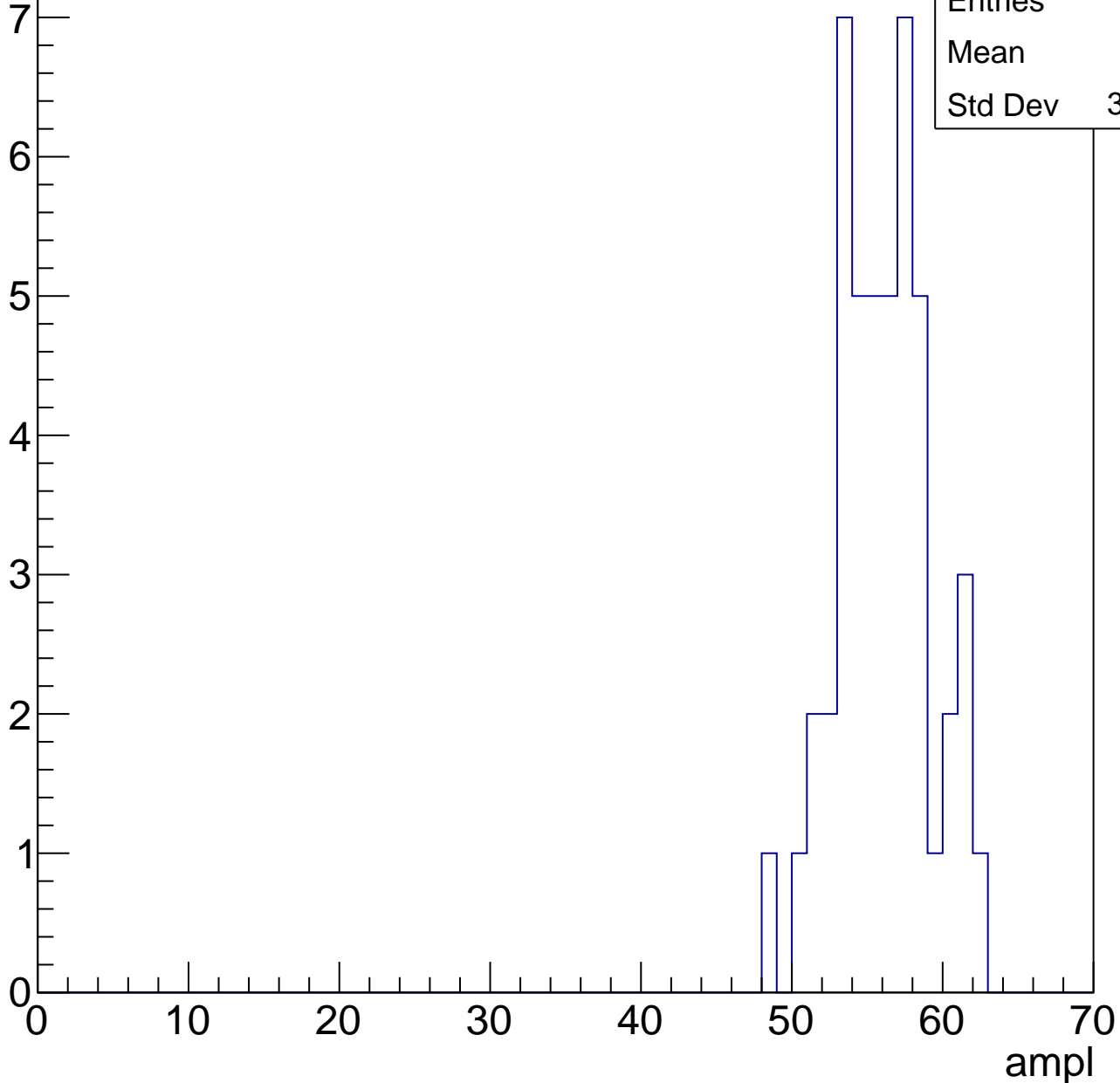


B1L101S, U21-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

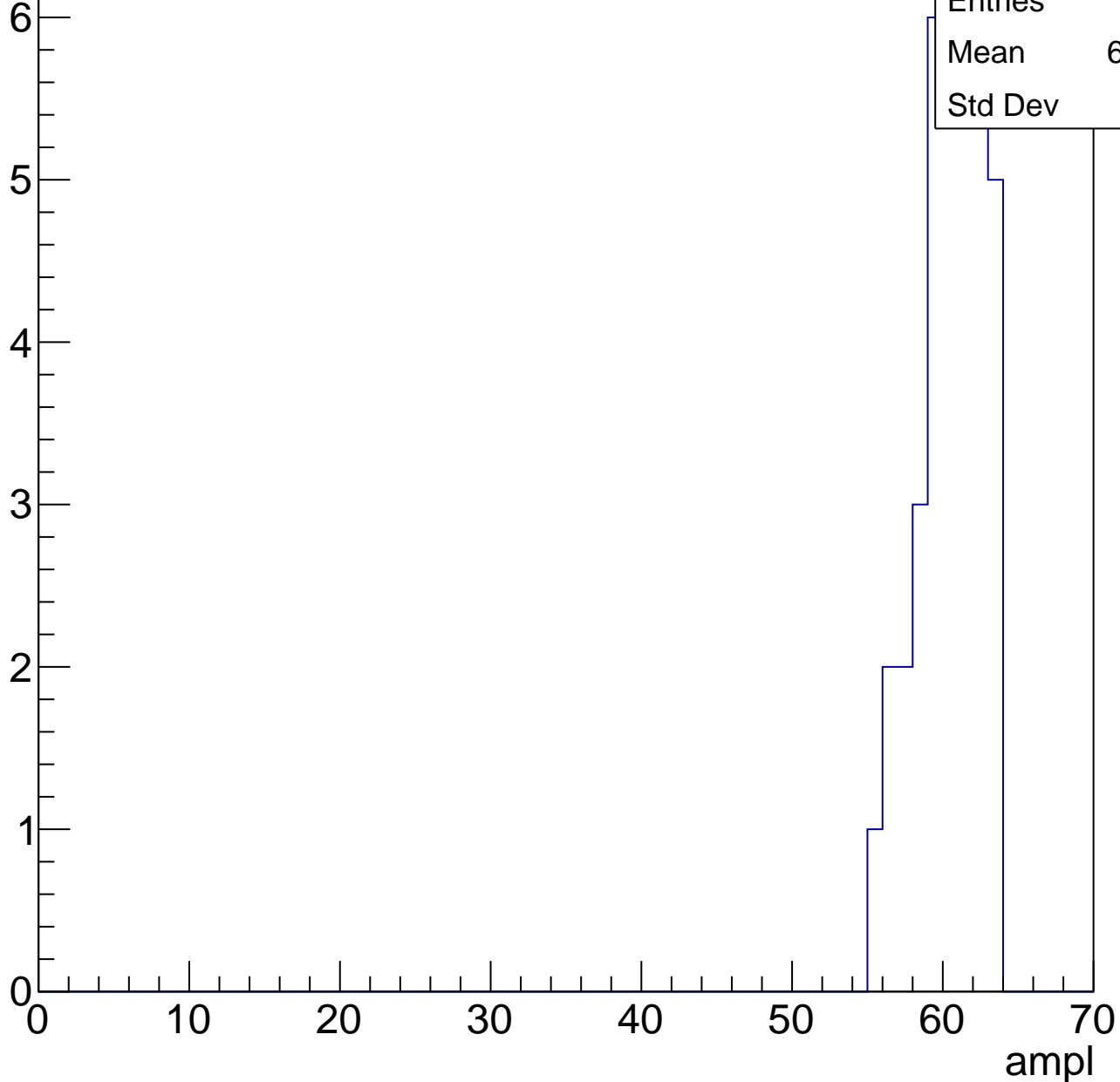
Entries	47
Mean	55.6
Std Dev	3.064



B1L101S, U21-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

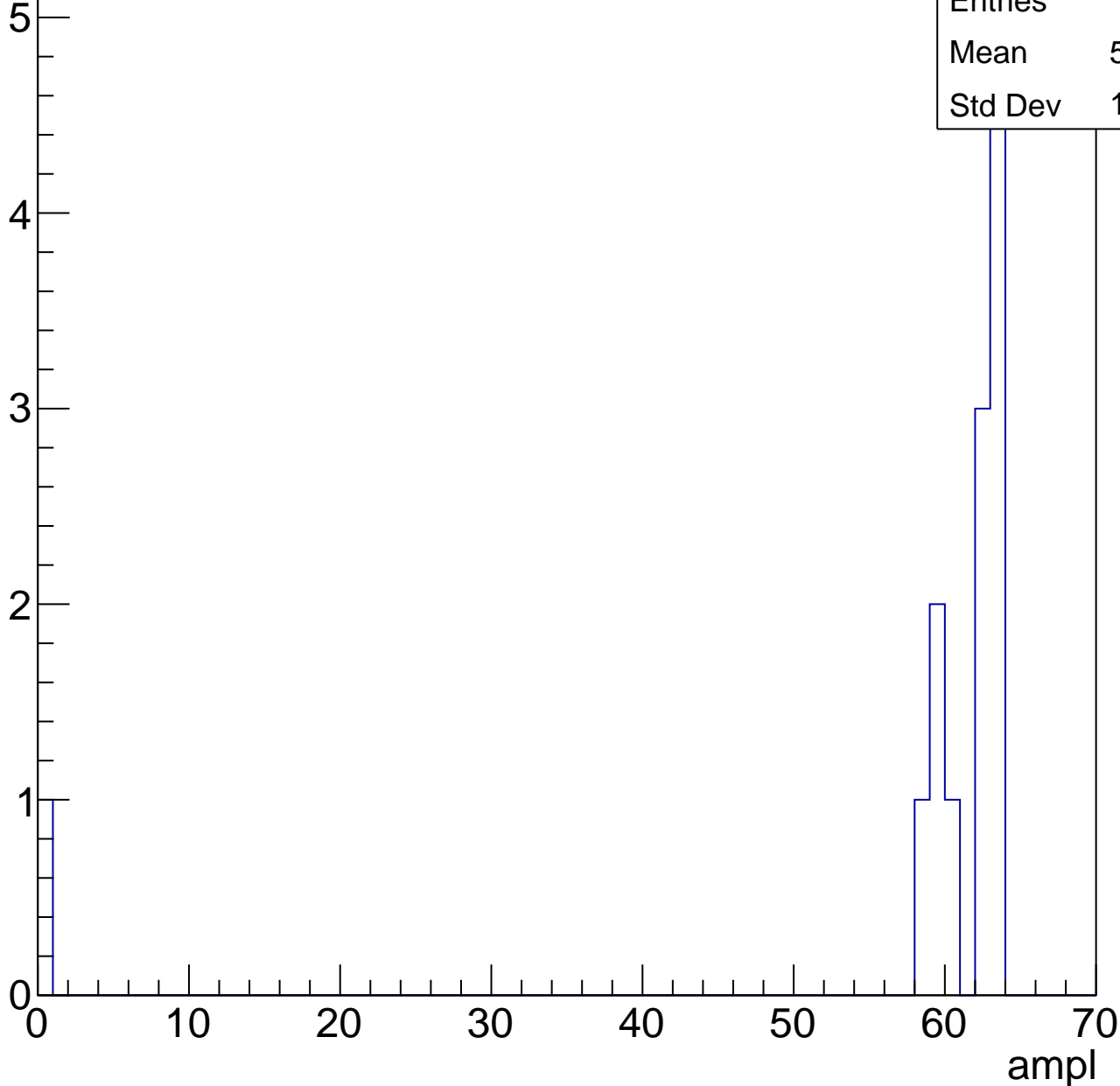


B1L101S, U21-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	56.69
Std Dev	16.46



B1L101S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

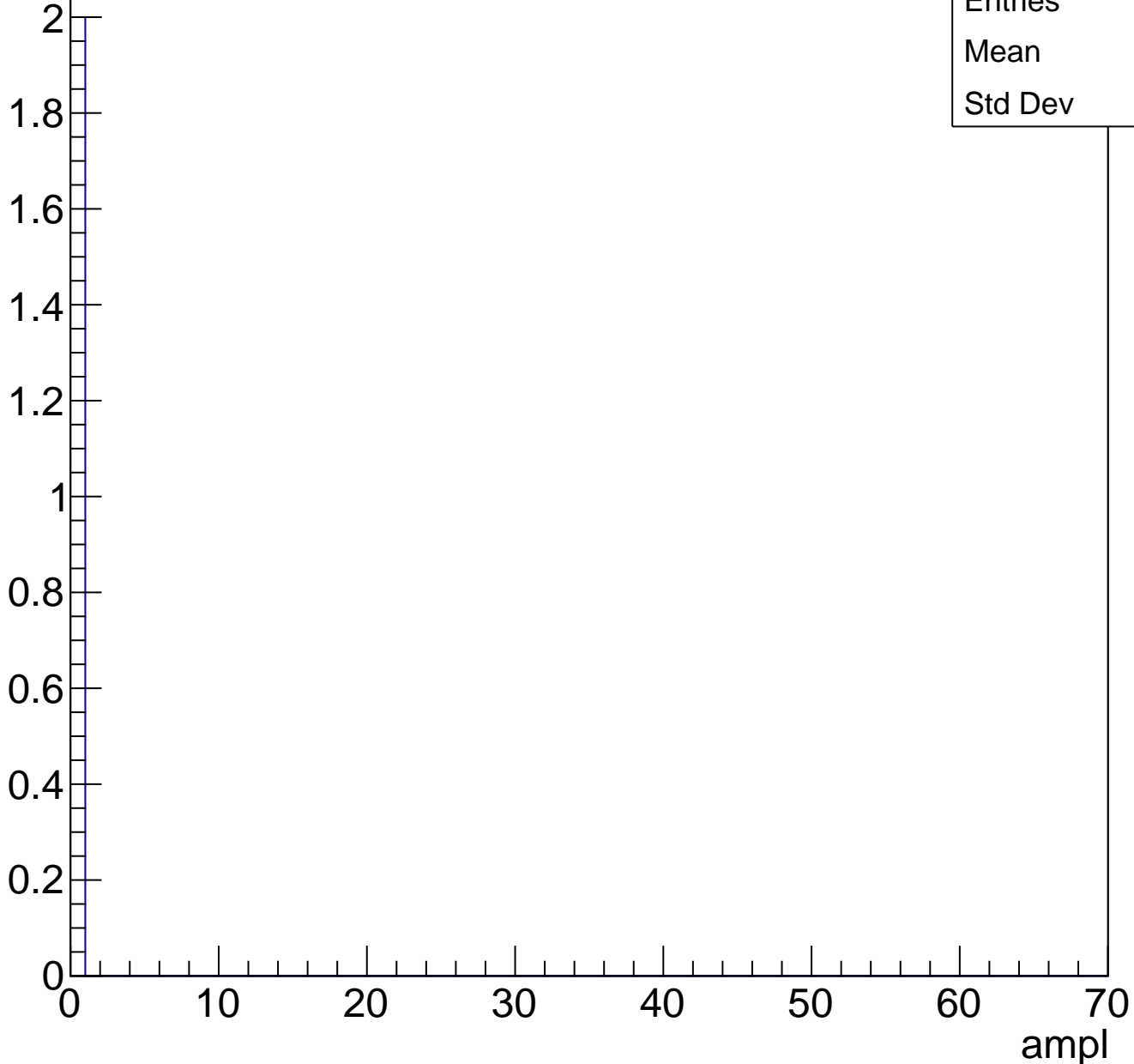


Entries	2
Mean	0
Std Dev	0

B1L101S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0