

B0L102S, U23-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.4887

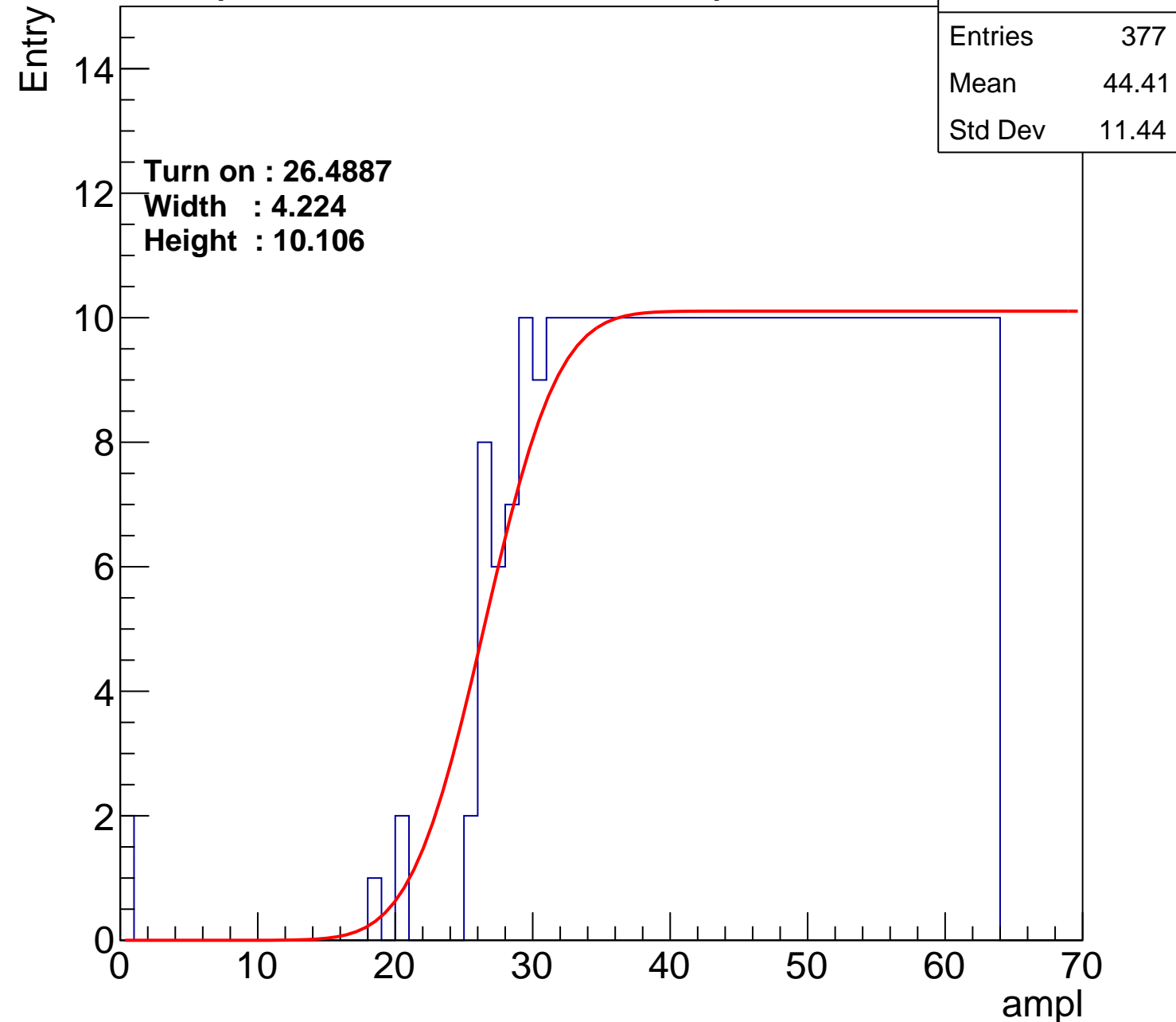
Width : 4.224

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.7697

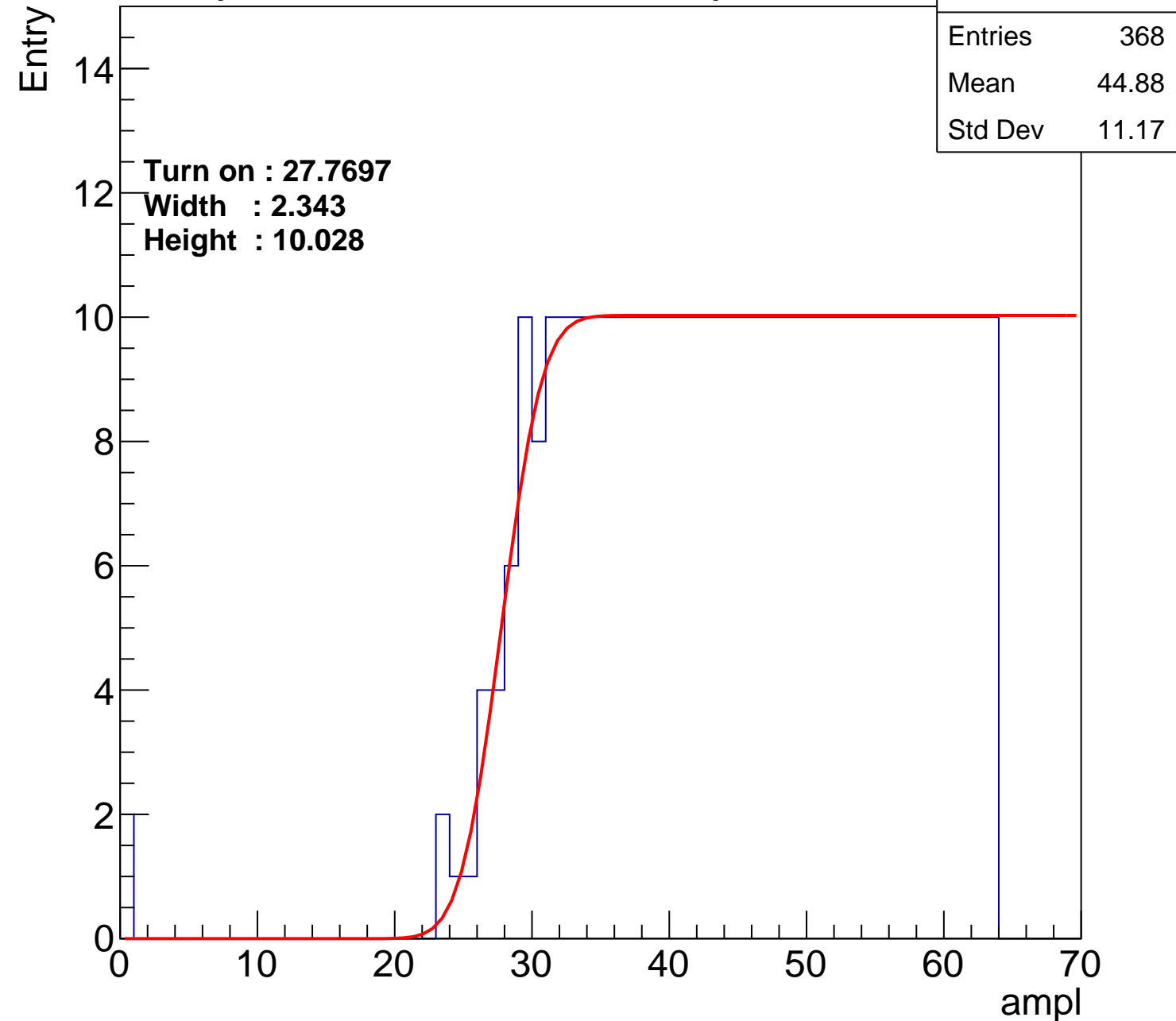
Width : 2.343

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.29
Std Dev	11.34

Turn on : 26.1559

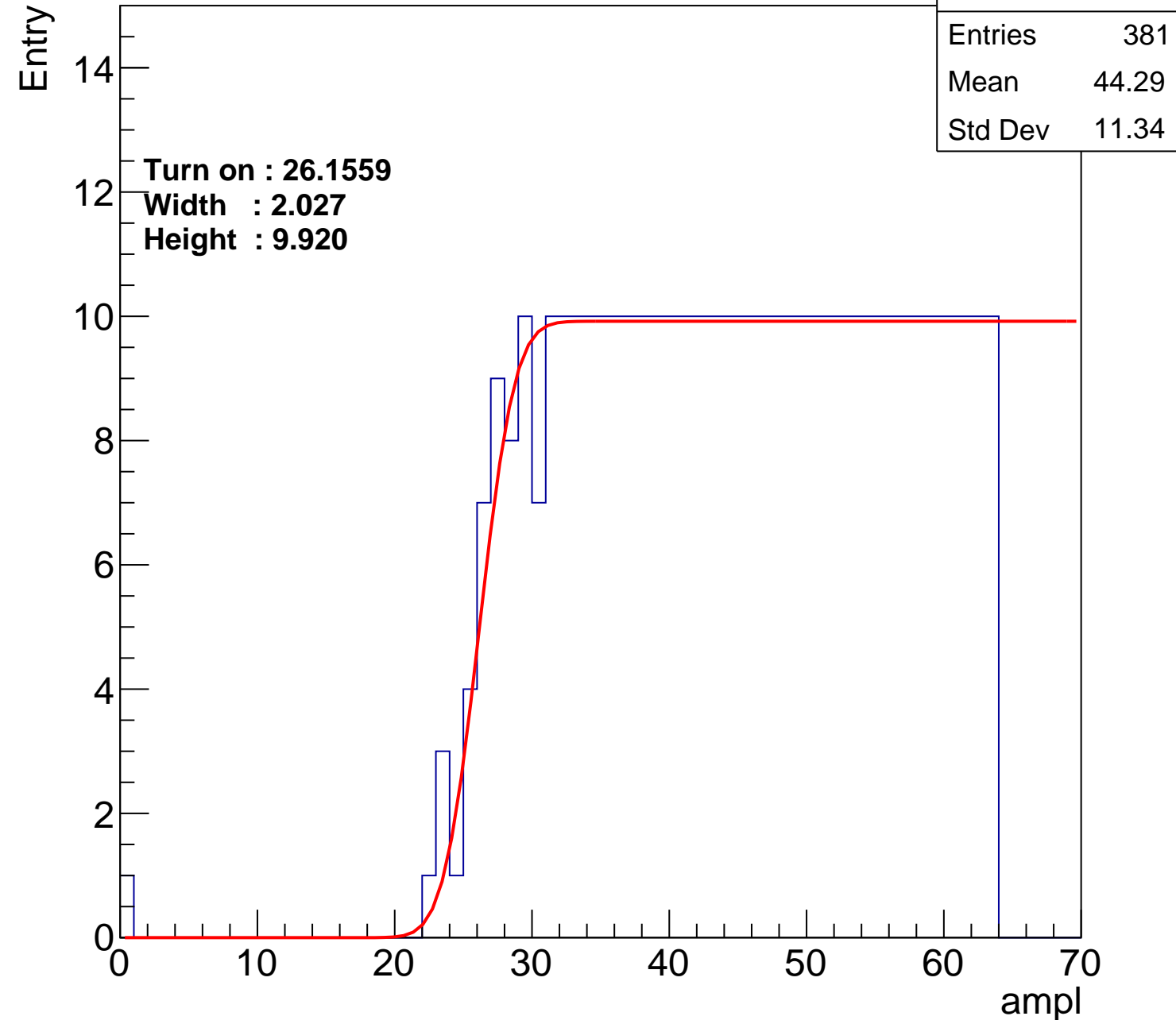
Width : 2.027

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.04
Std Dev	11.12

Turn on : 27.9284

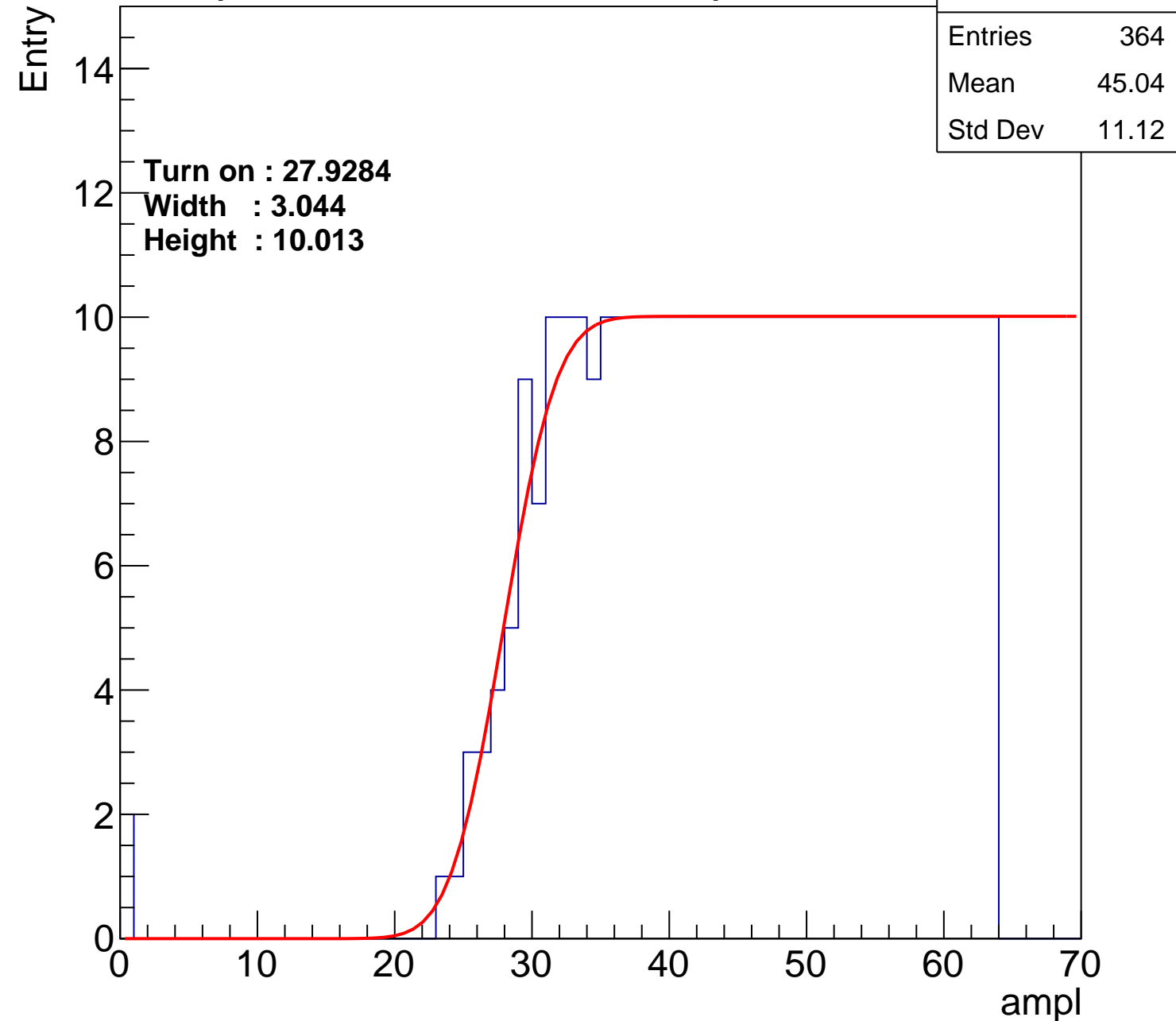
Width : 3.044

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.76
Std Dev	11.79

Turn on : 25.0896

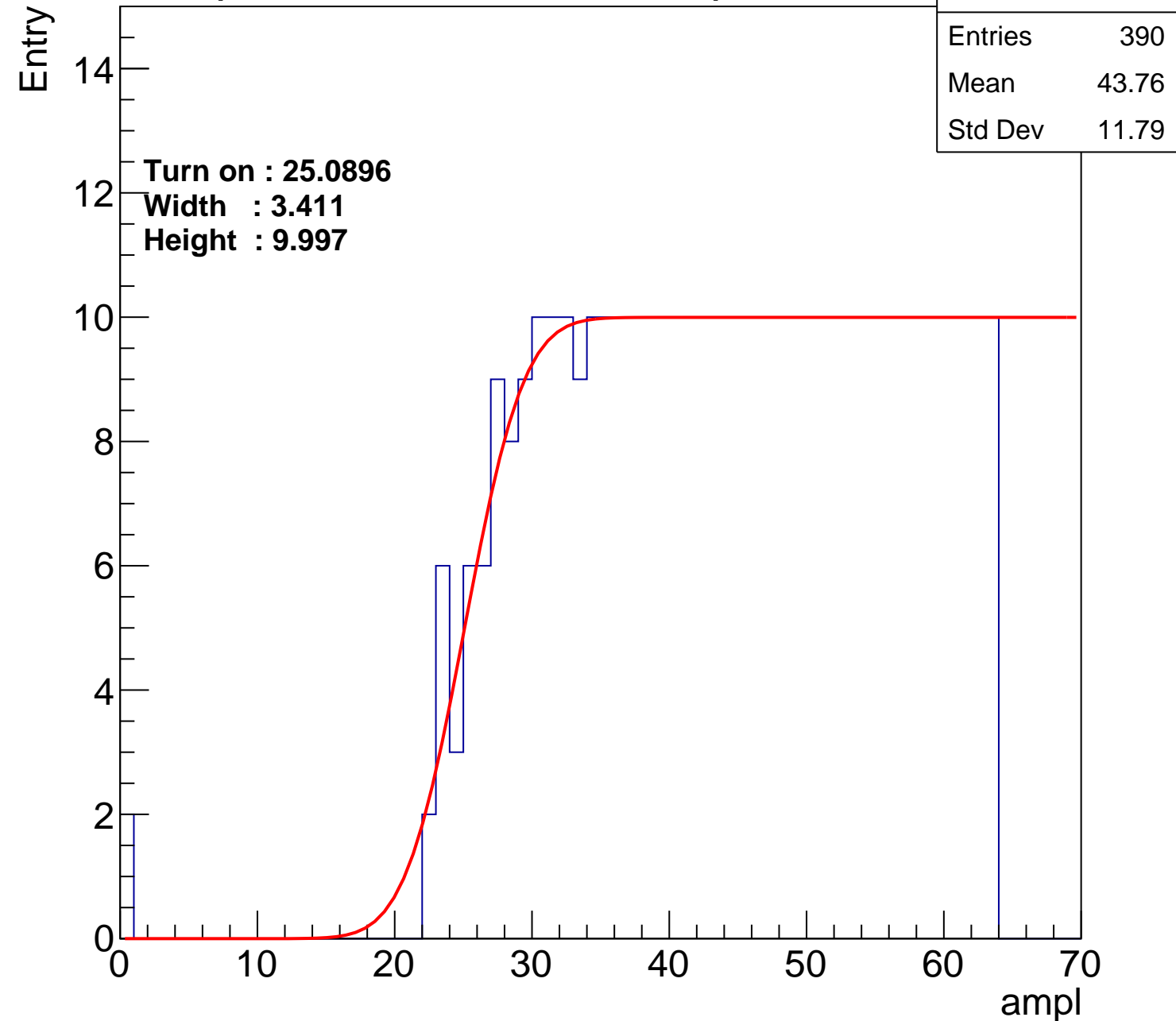
Width : 3.411

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.74
Std Dev	11.56

Turn on : 27.5062

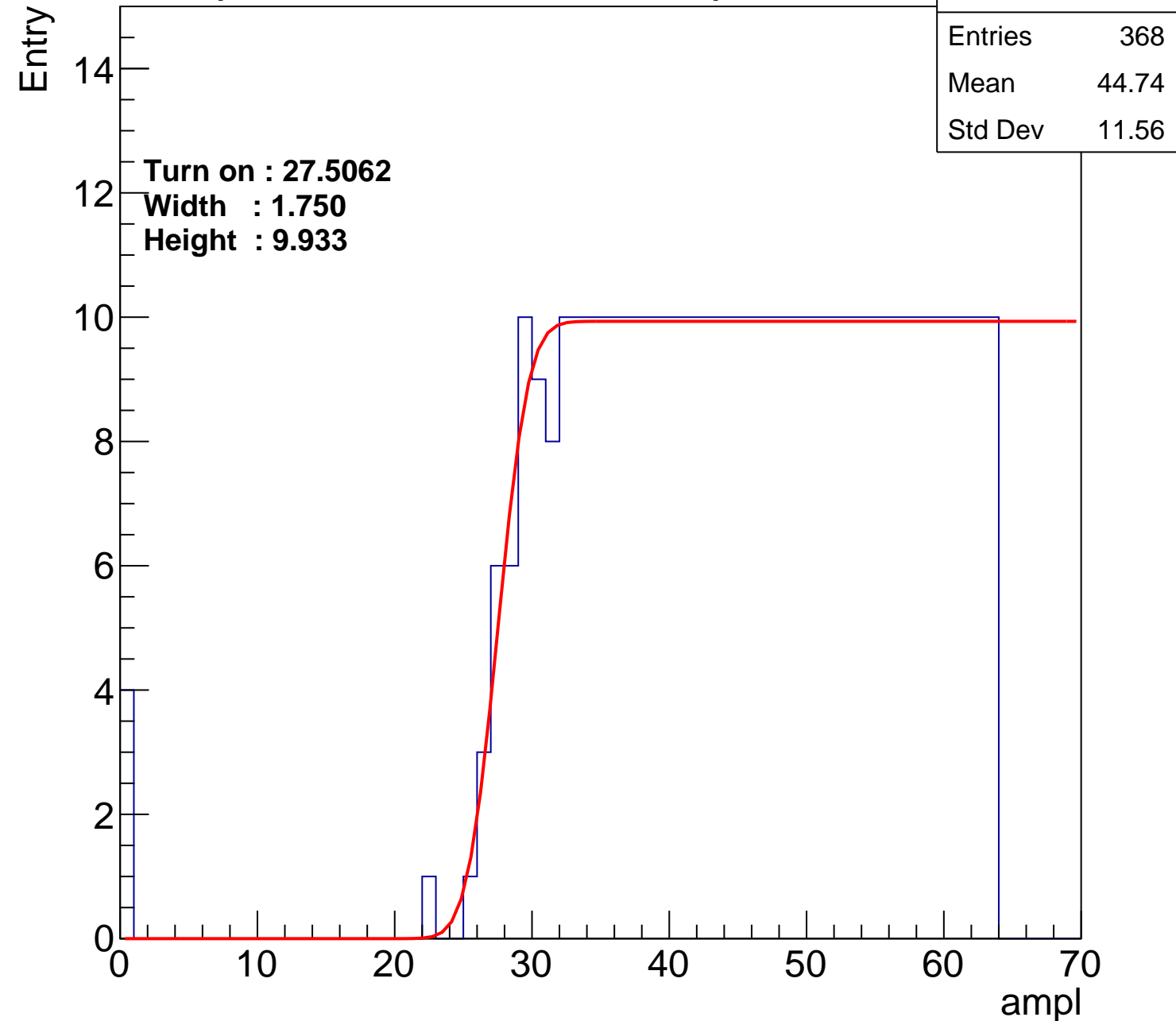
Width : 1.750

Height : 9.933

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	352
Mean	45.68
Std Dev	10.74

Turn on : 29.1021

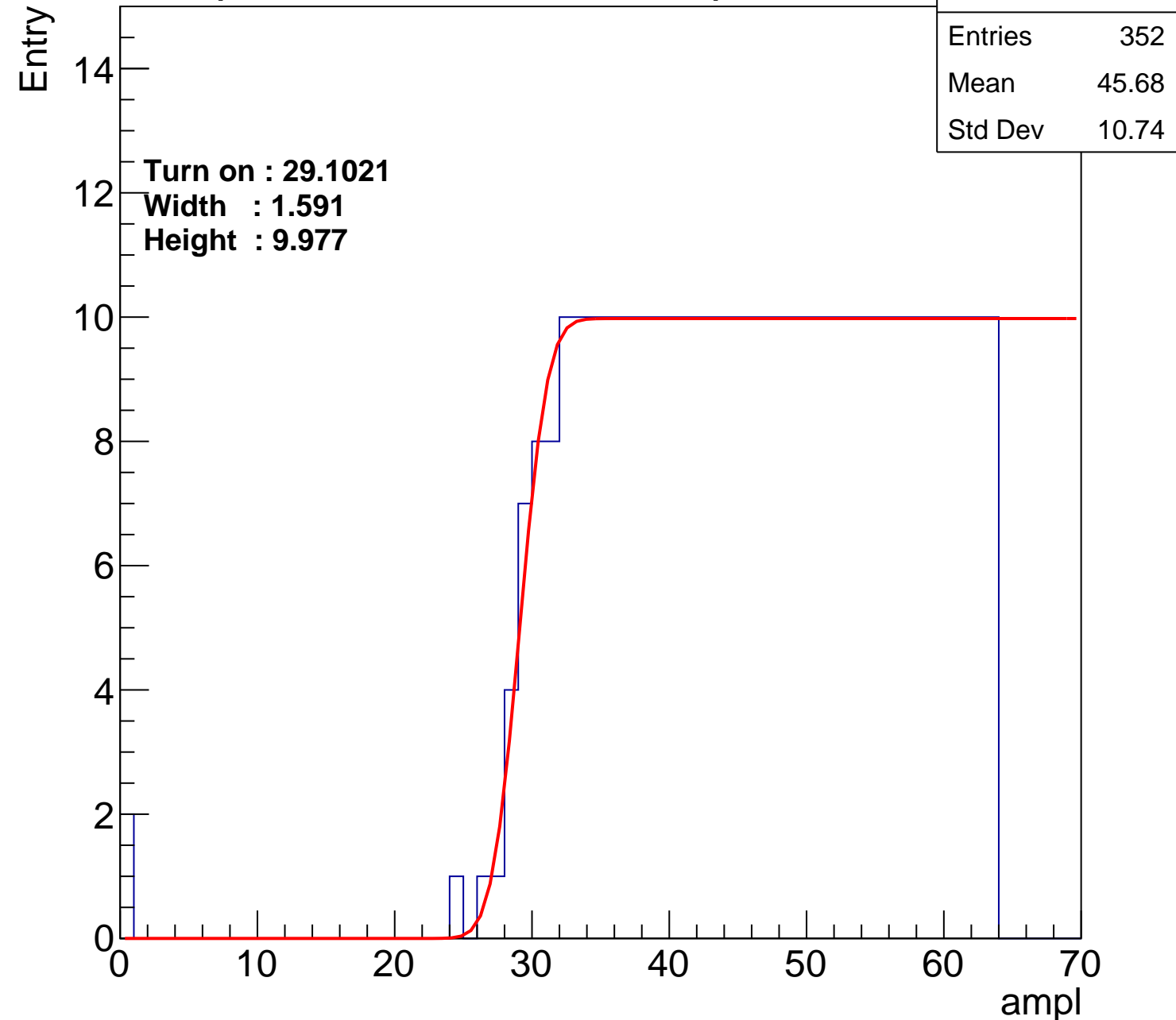
Width : 1.591

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.9037

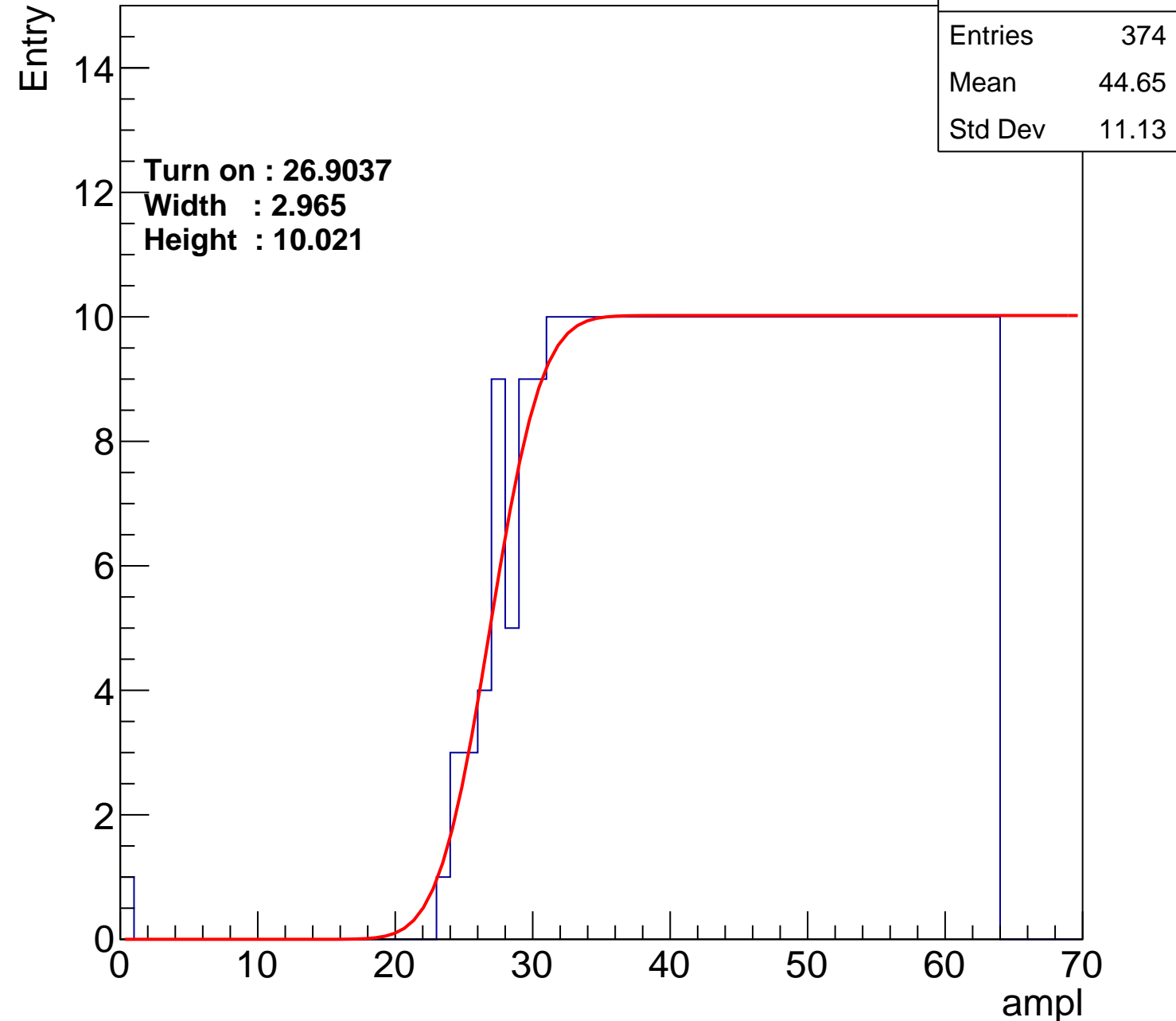
Width : 2.965

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.6567

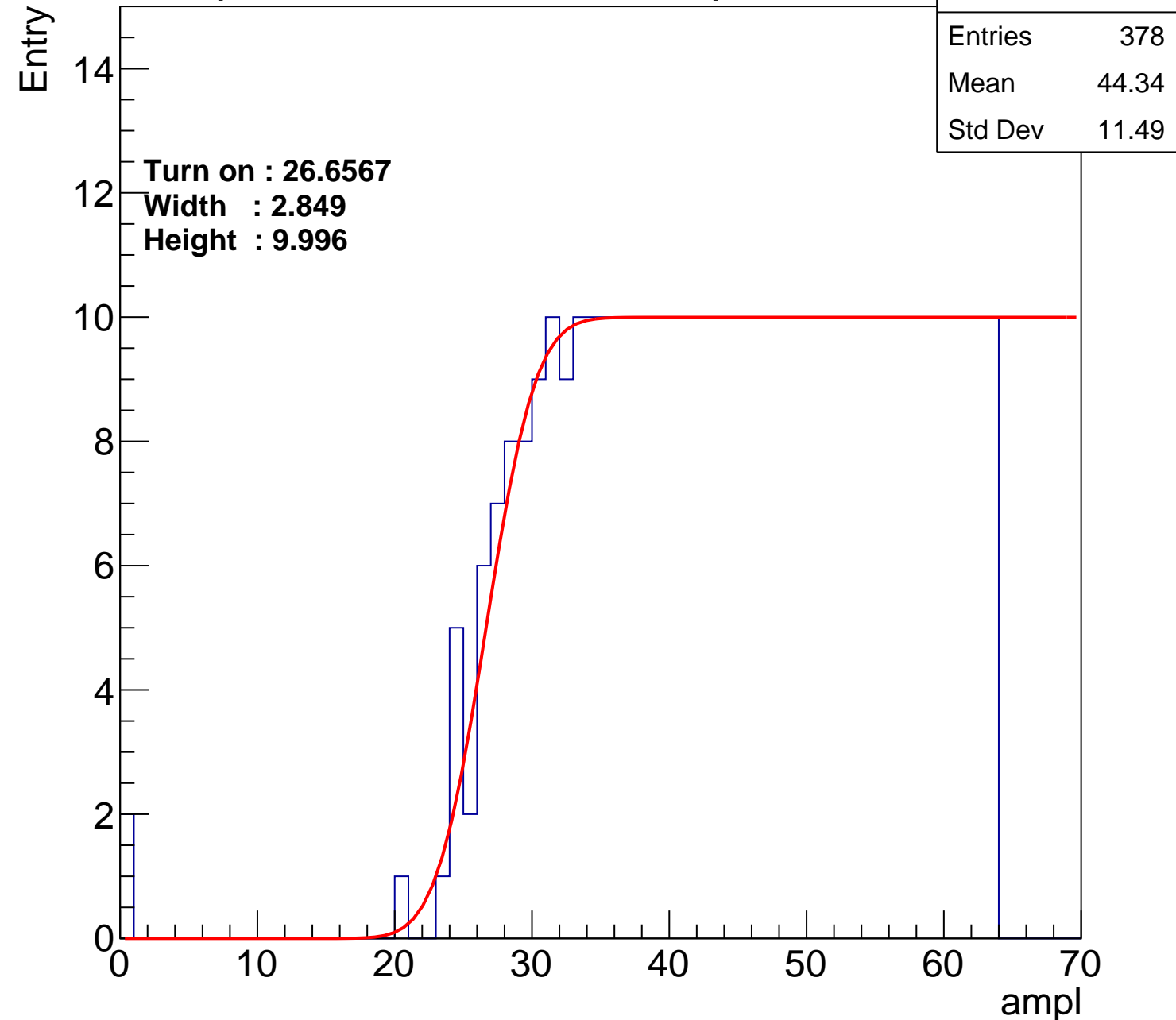
Width : 2.849

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.61
Std Dev	11.94

Turn on : 25.2915

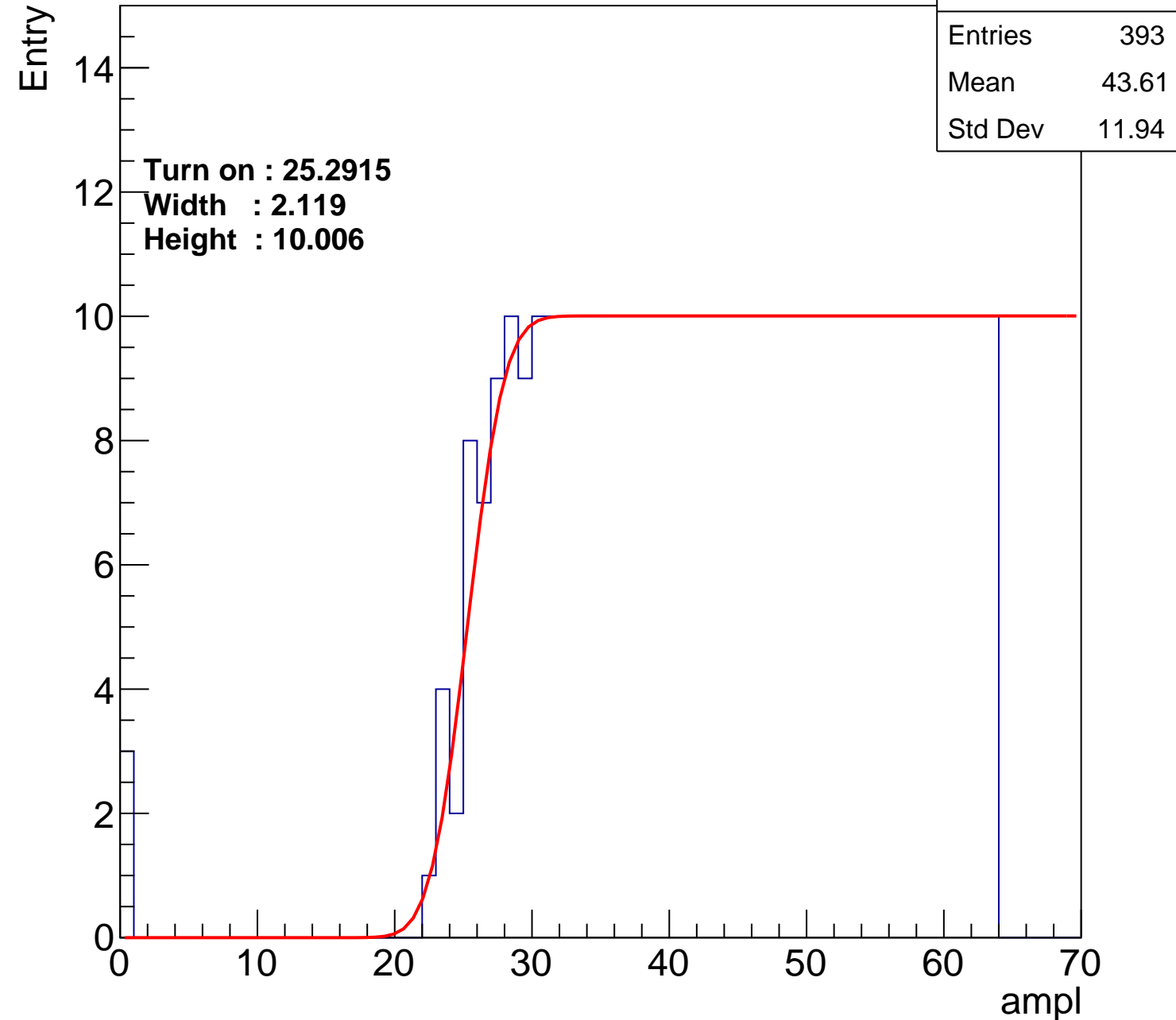
Width : 2.119

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.67
Std Dev	12

Turn on : 25.7400

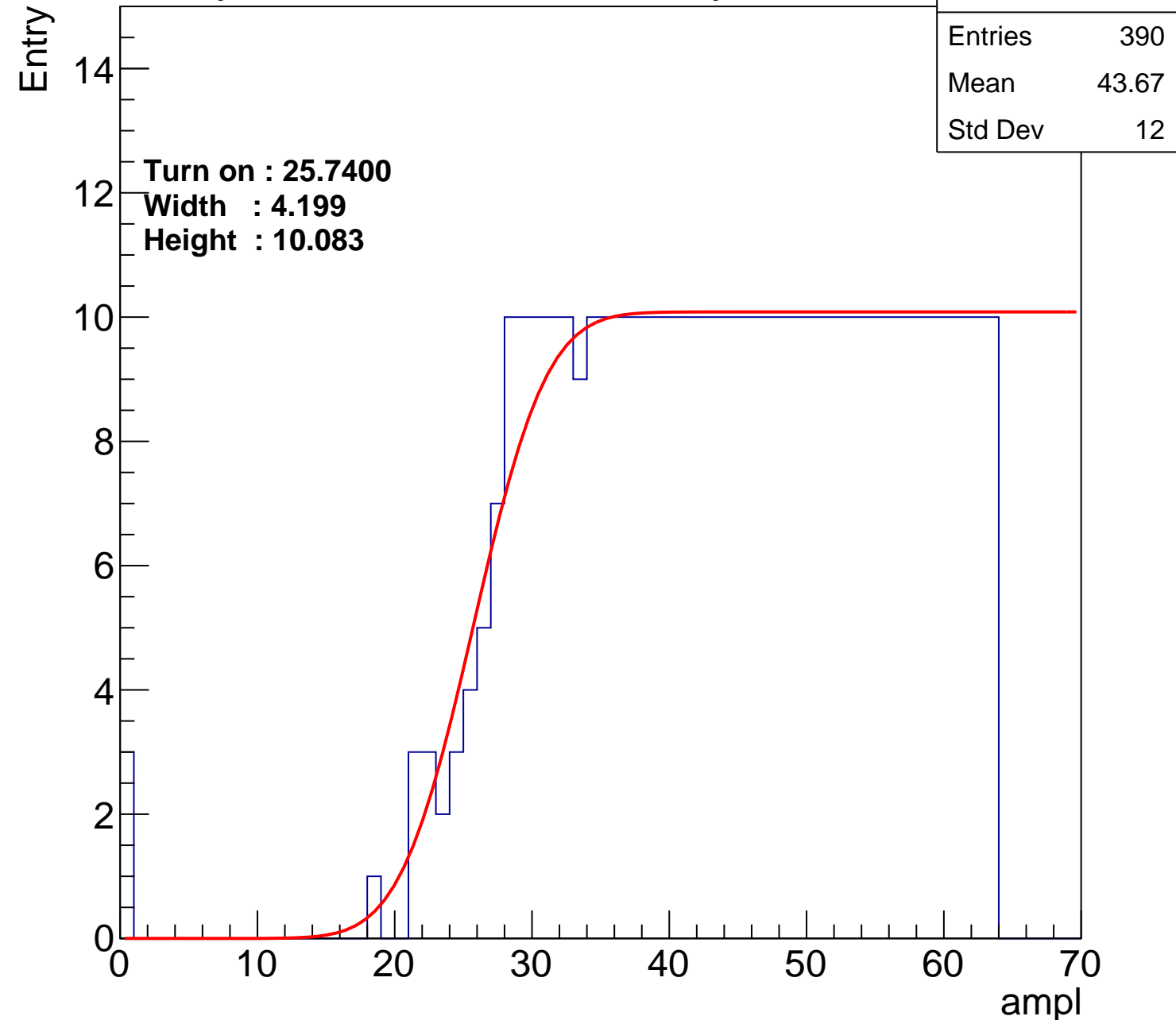
Width : 4.199

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch11

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.45
Std Dev	11.69

Turn on : 26.8827

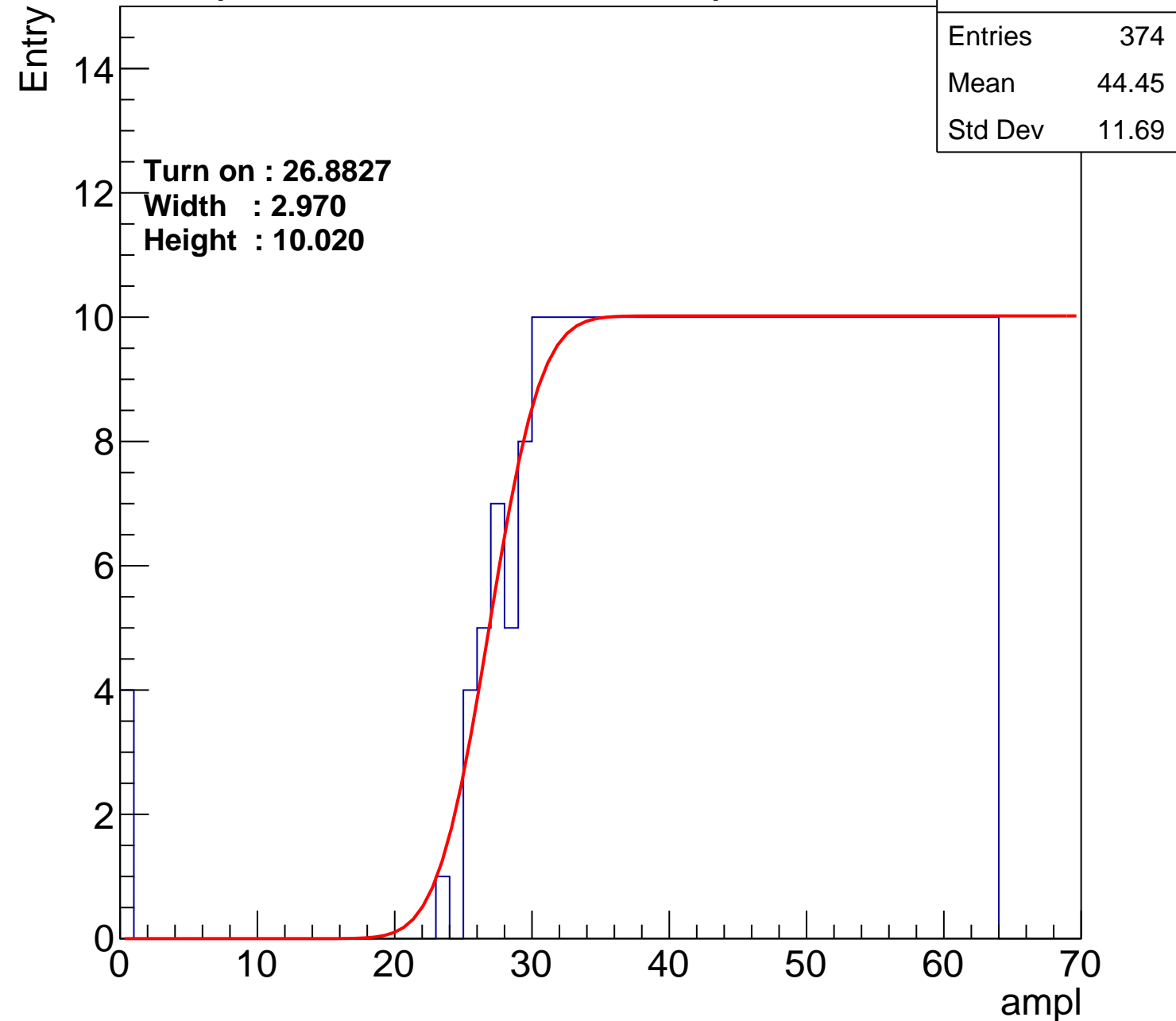
Width : 2.970

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.1
Std Dev	12.47

Turn on : 23.8831

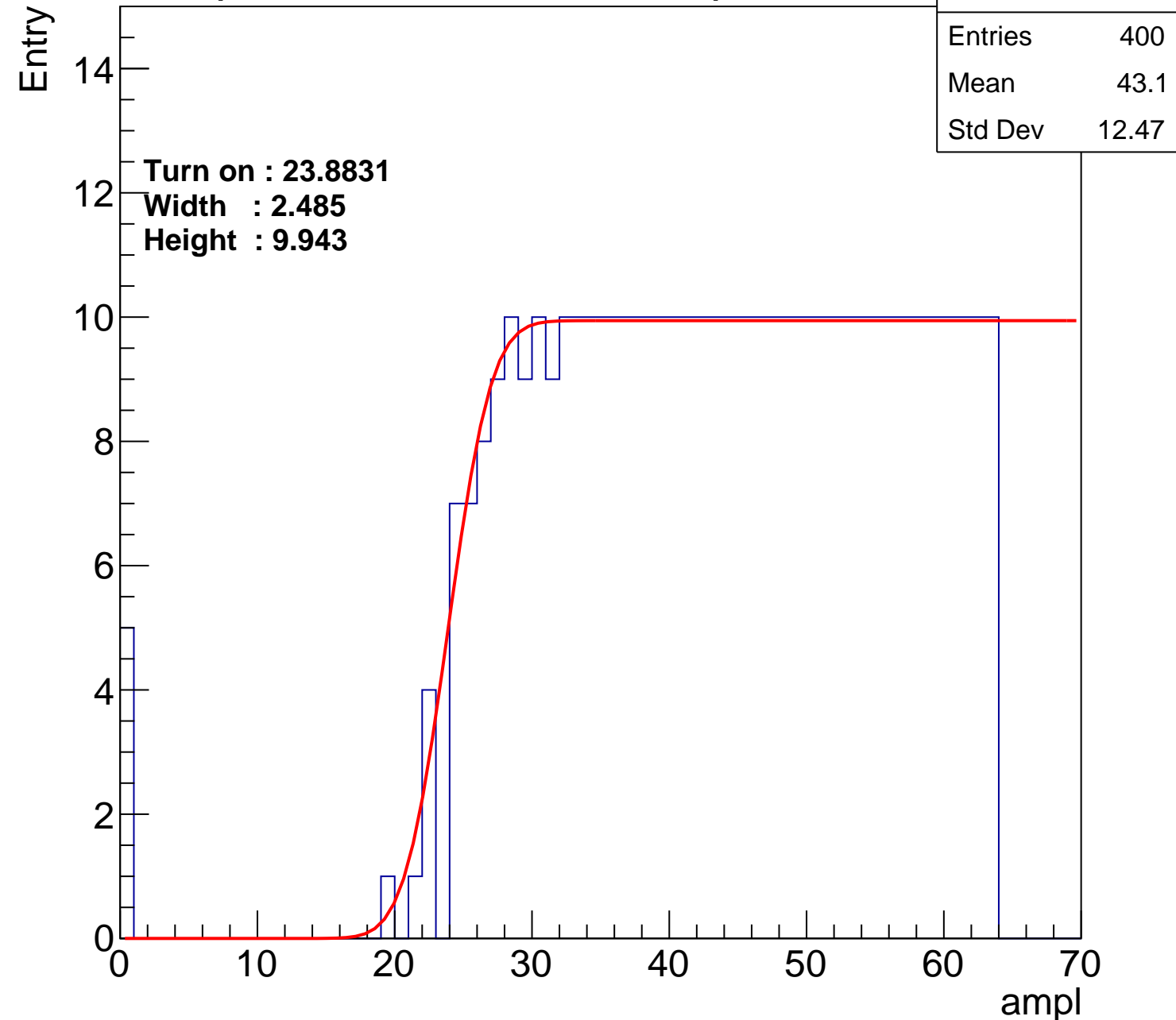
Width : 2.485

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.6070

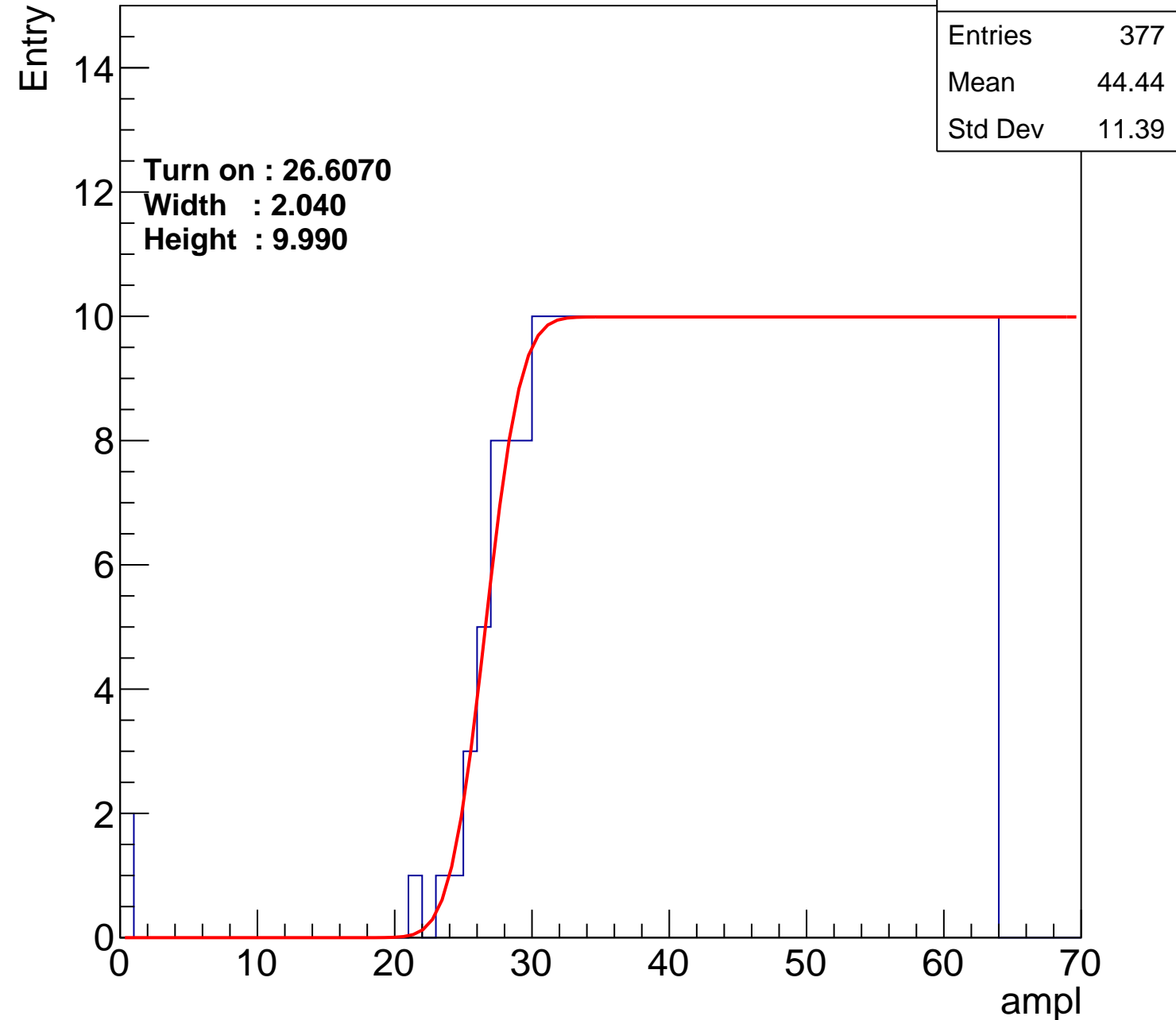
Width : 2.040

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch14

calib_packv5_042523_0143.root, FC#12, port B1

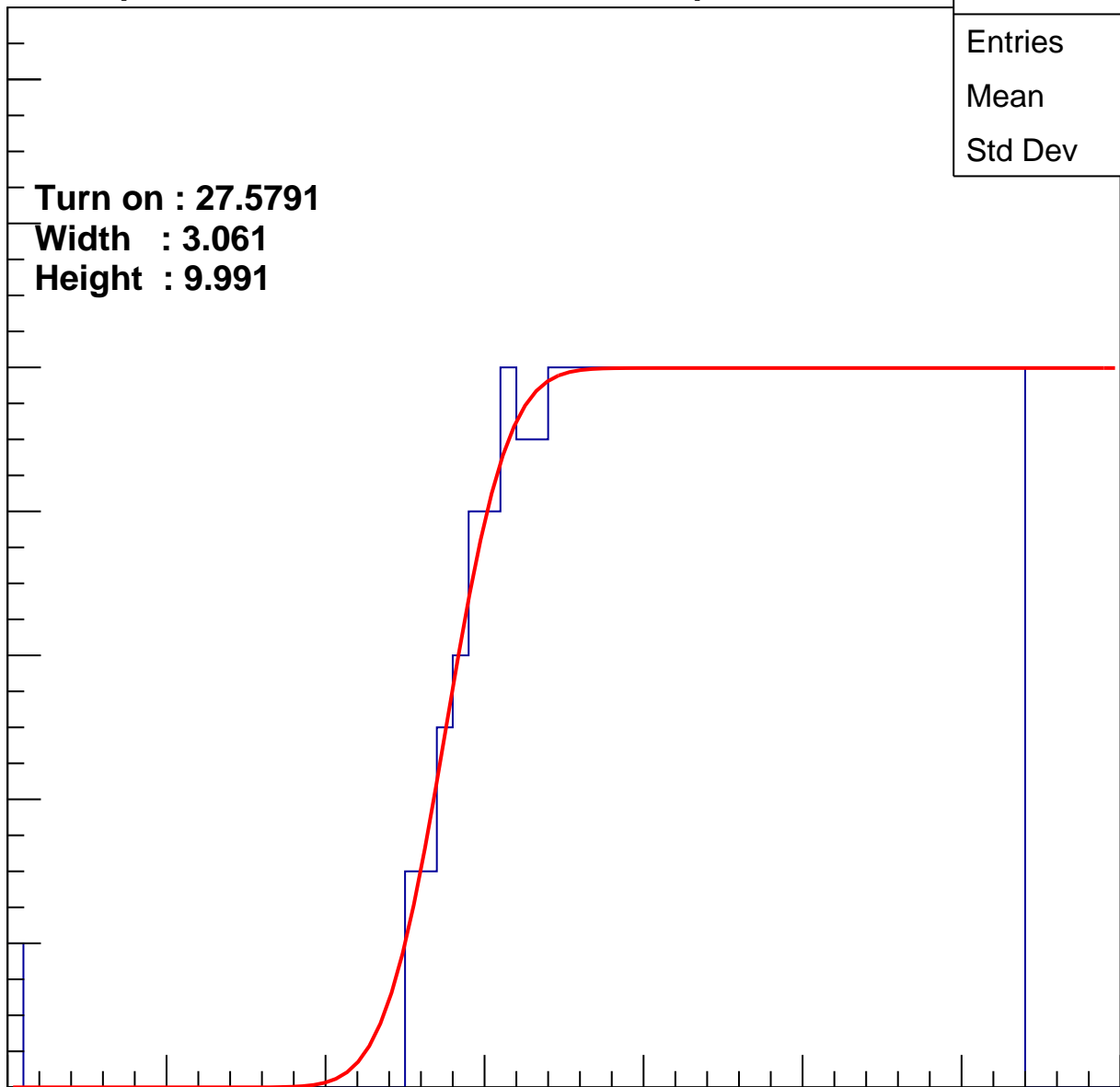
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5791
Width : 3.061
Height : 9.991

Entries	363
Mean	45.1
Std Dev	11.07

ampl



B0L102S, U23-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.29
Std Dev	11.41

Turn on : 26.5031

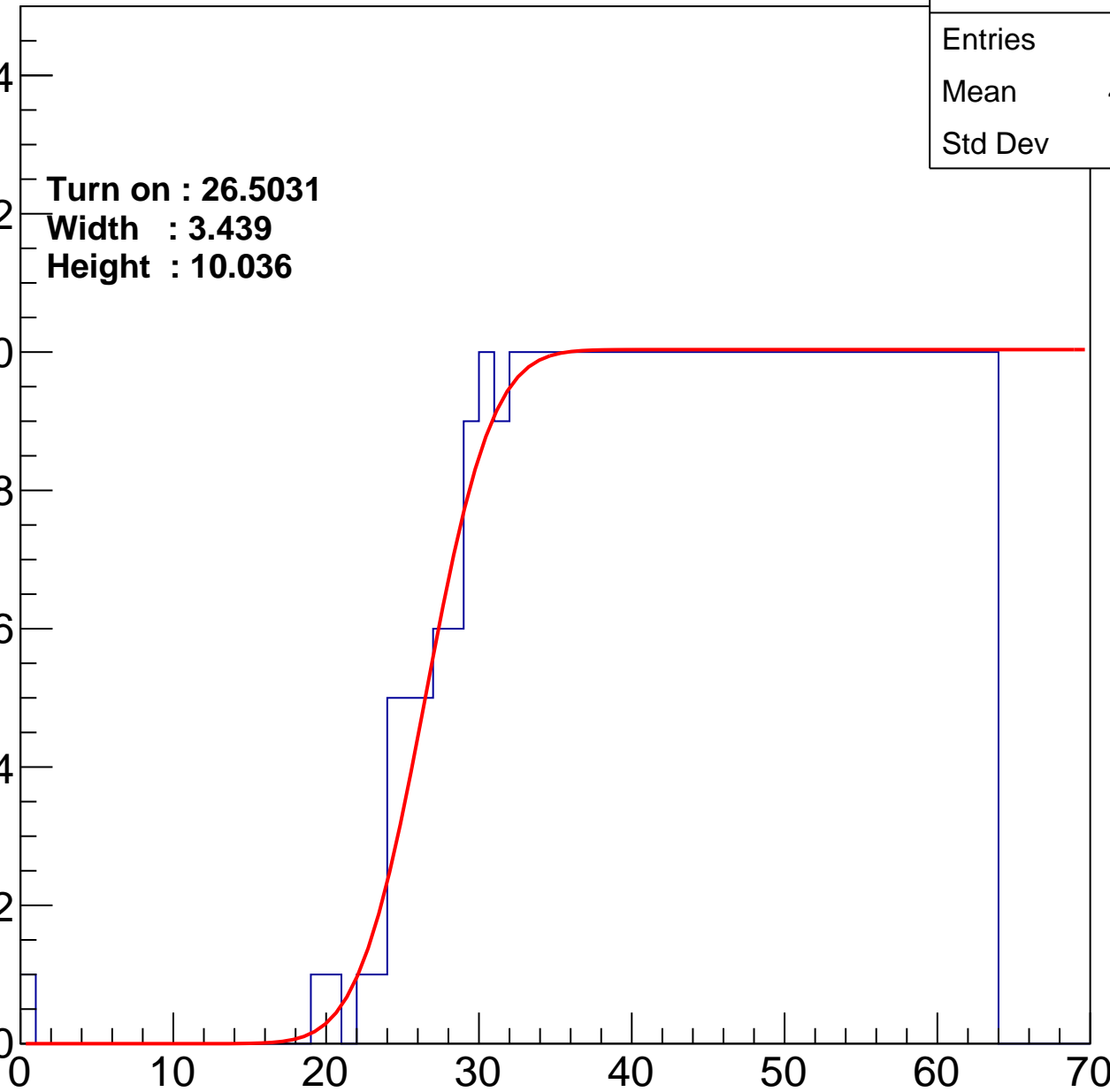
Width : 3.439

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.31
Std Dev	11.31

Turn on : 25.8288

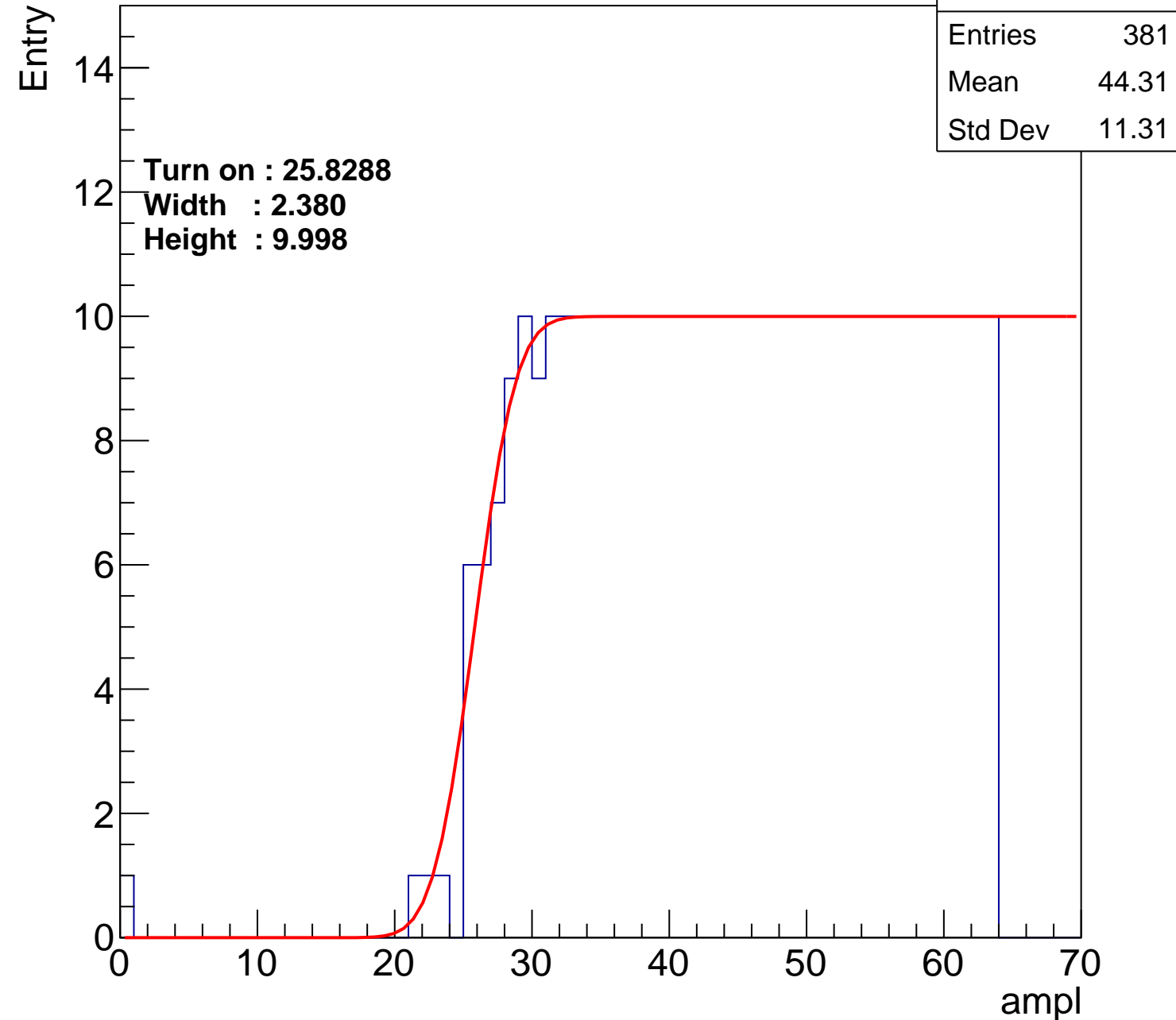
Width : 2.380

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 28.3587

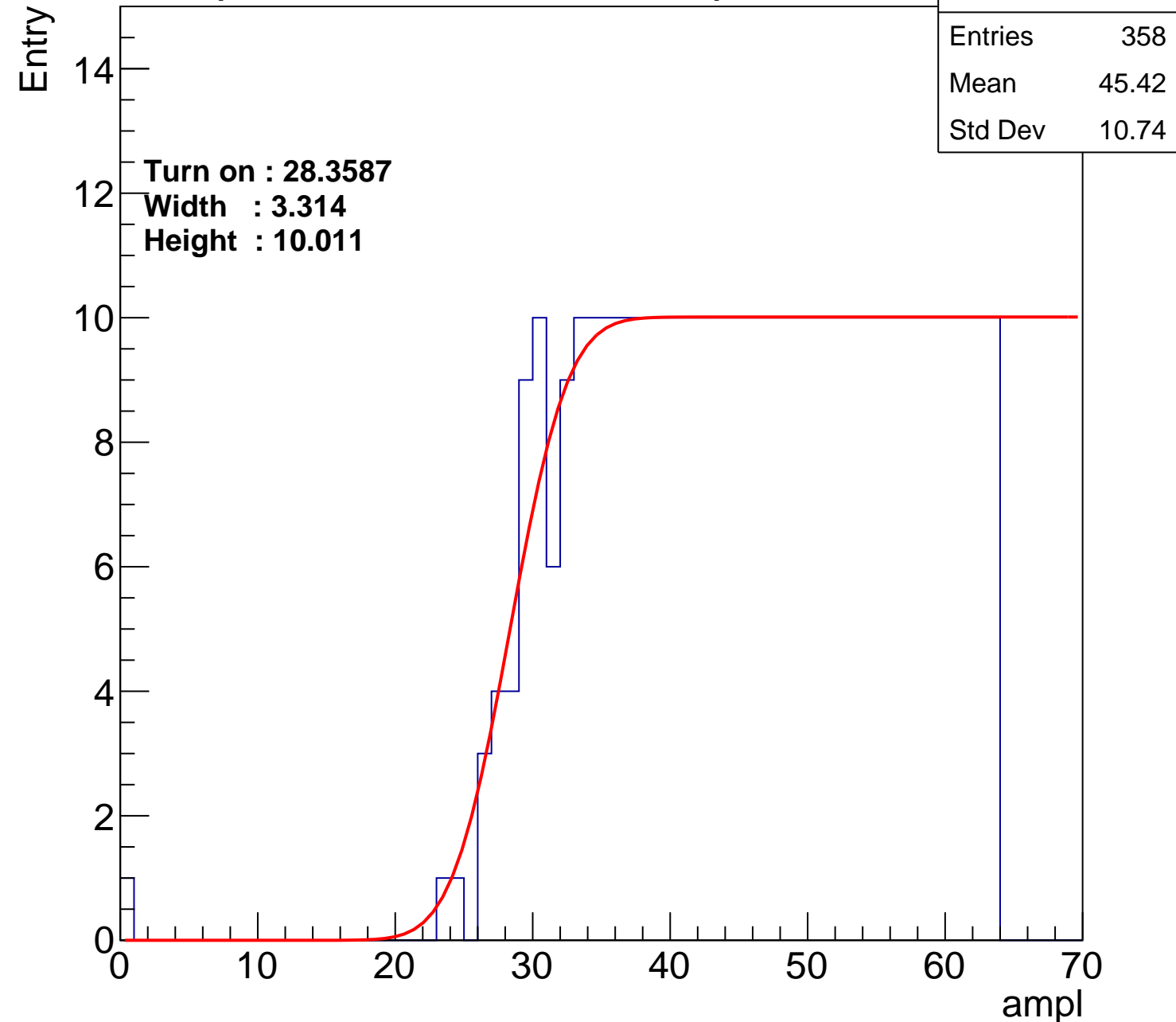
Width : 3.314

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.63
Std Dev	12.07

Turn on : 25.5119

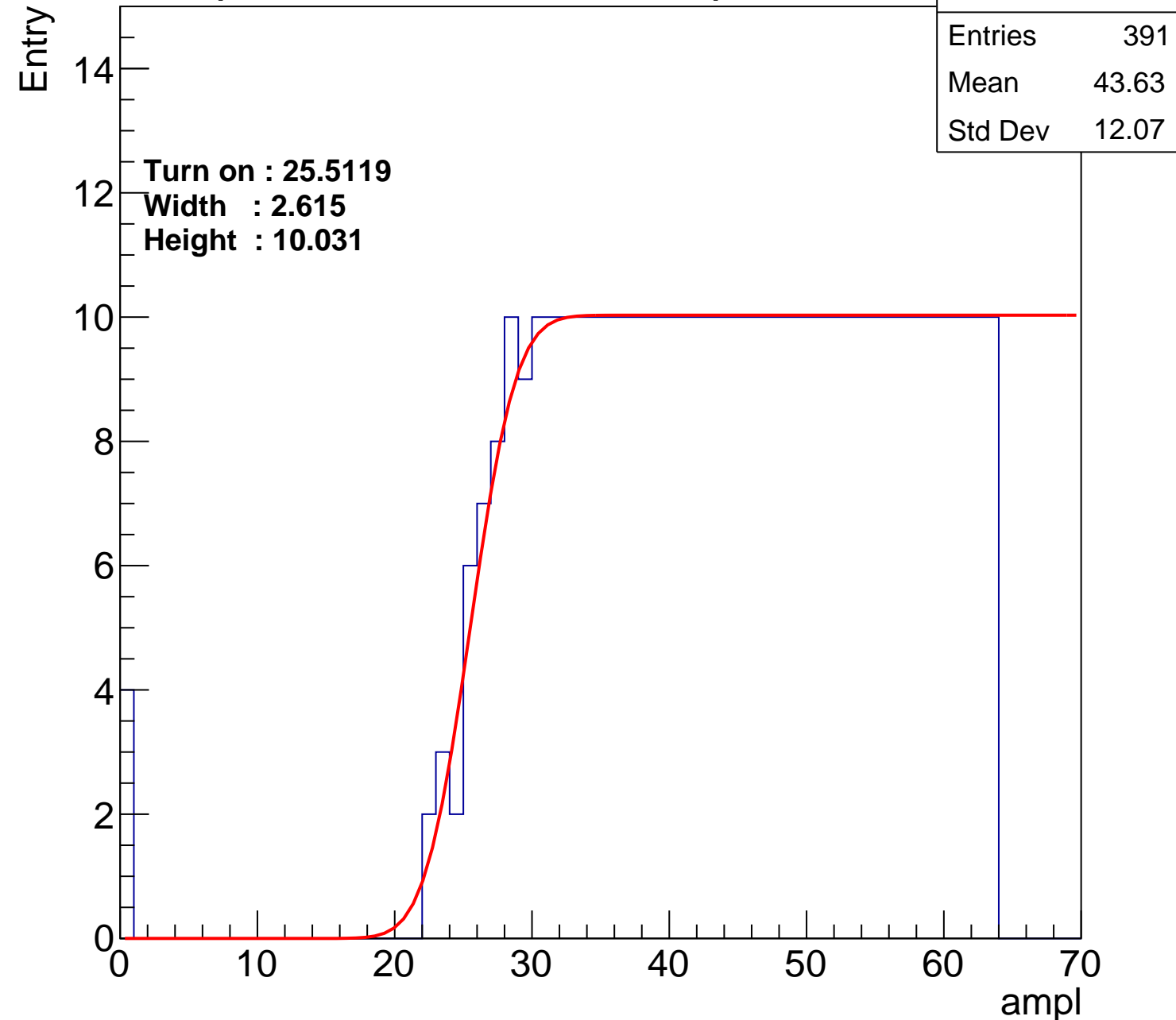
Width : 2.615

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.1543

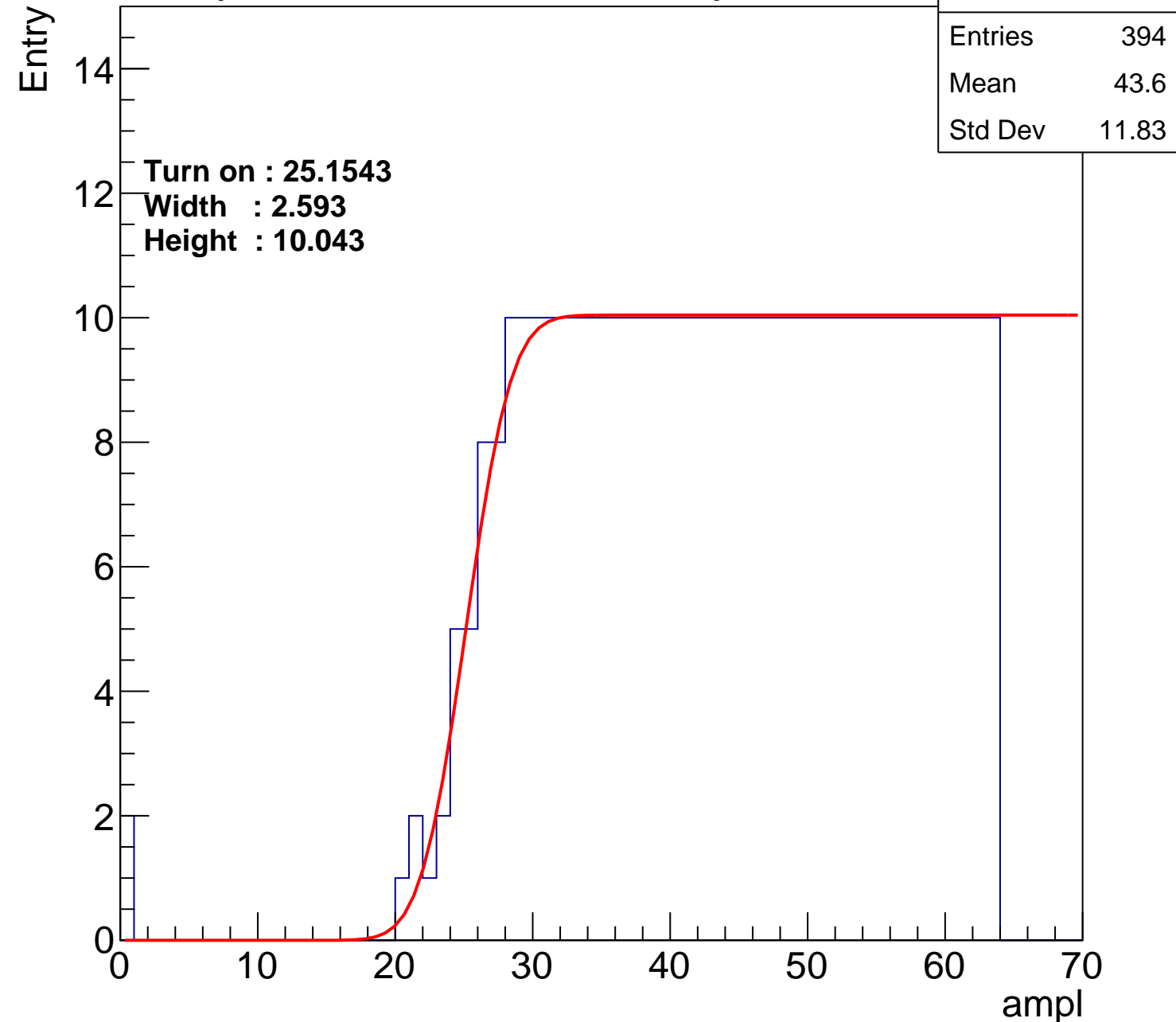
Width : 2.593

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44
Std Dev	11.72

Turn on : 25.9953

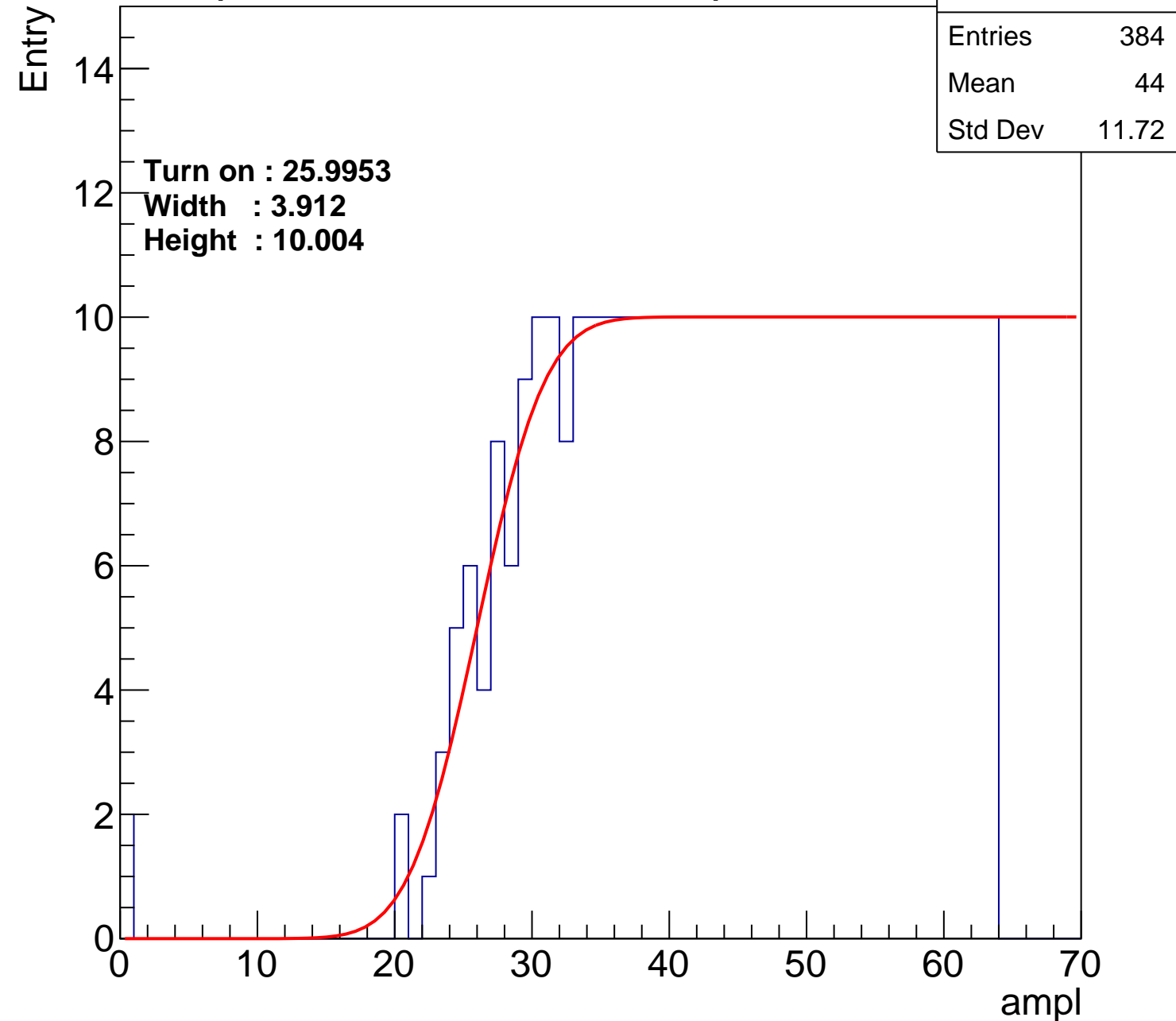
Width : 3.912

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.9057

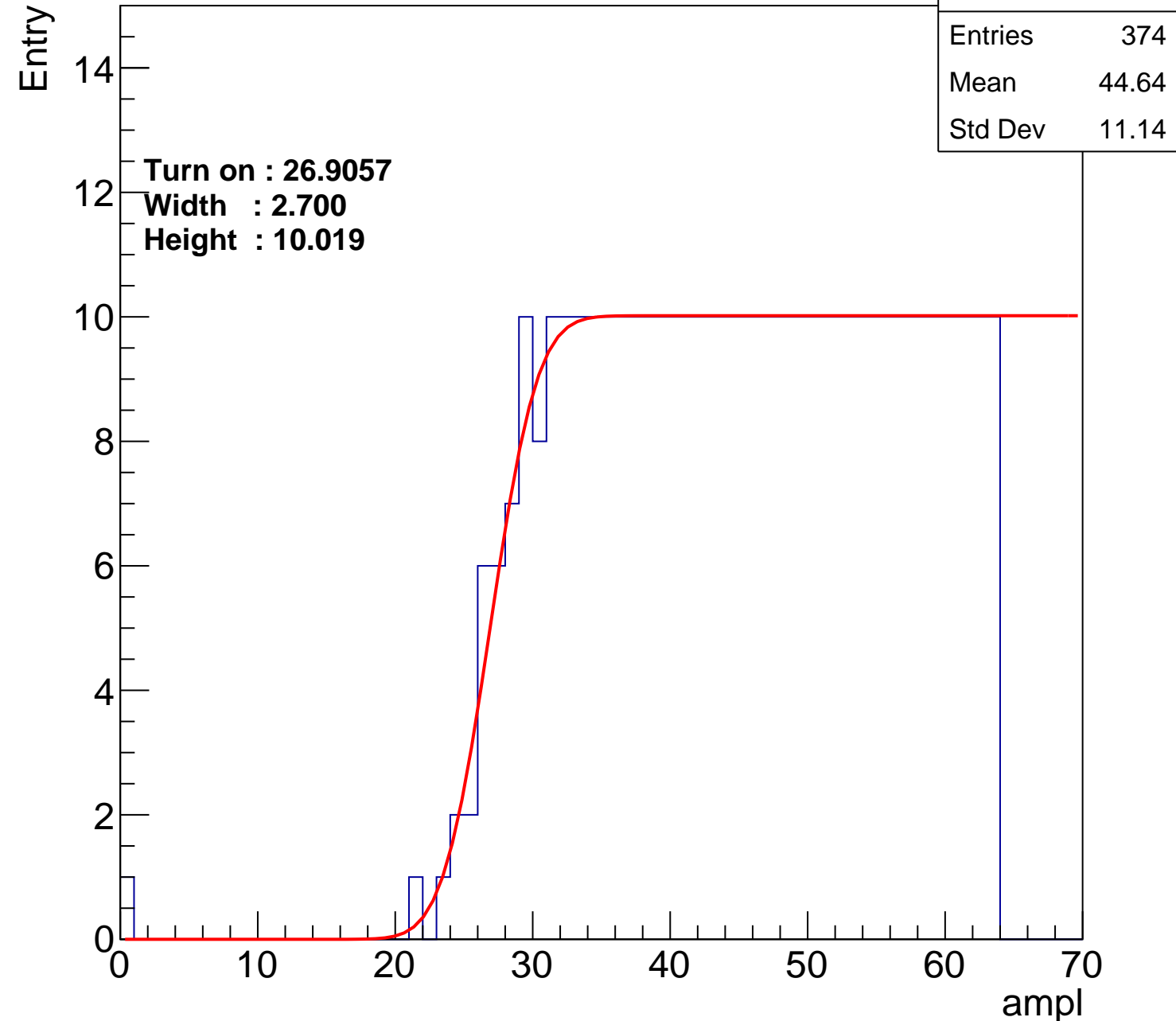
Width : 2.700

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.7446

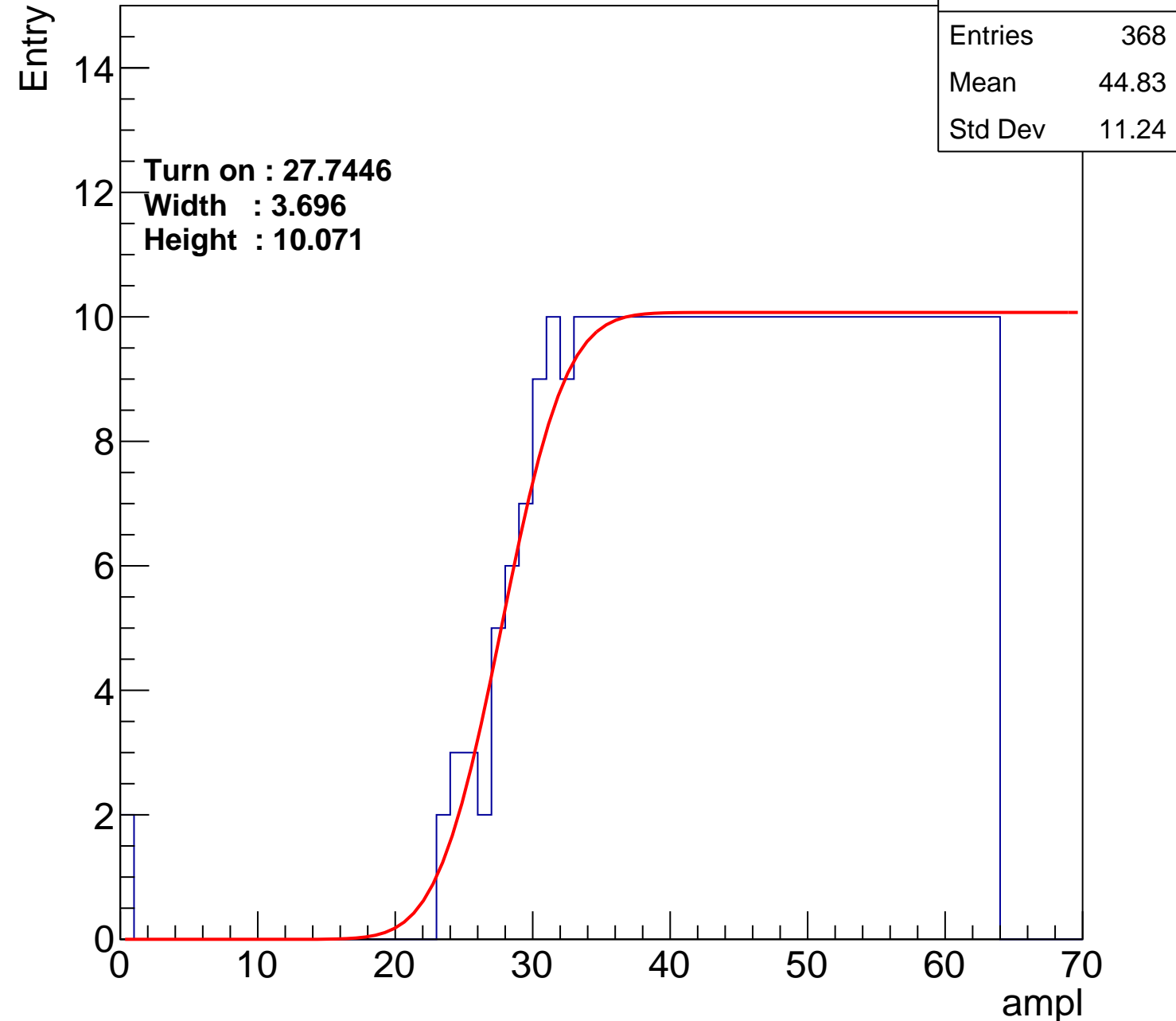
Width : 3.696

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.1
Std Dev	11.08

Turn on : 27.9201

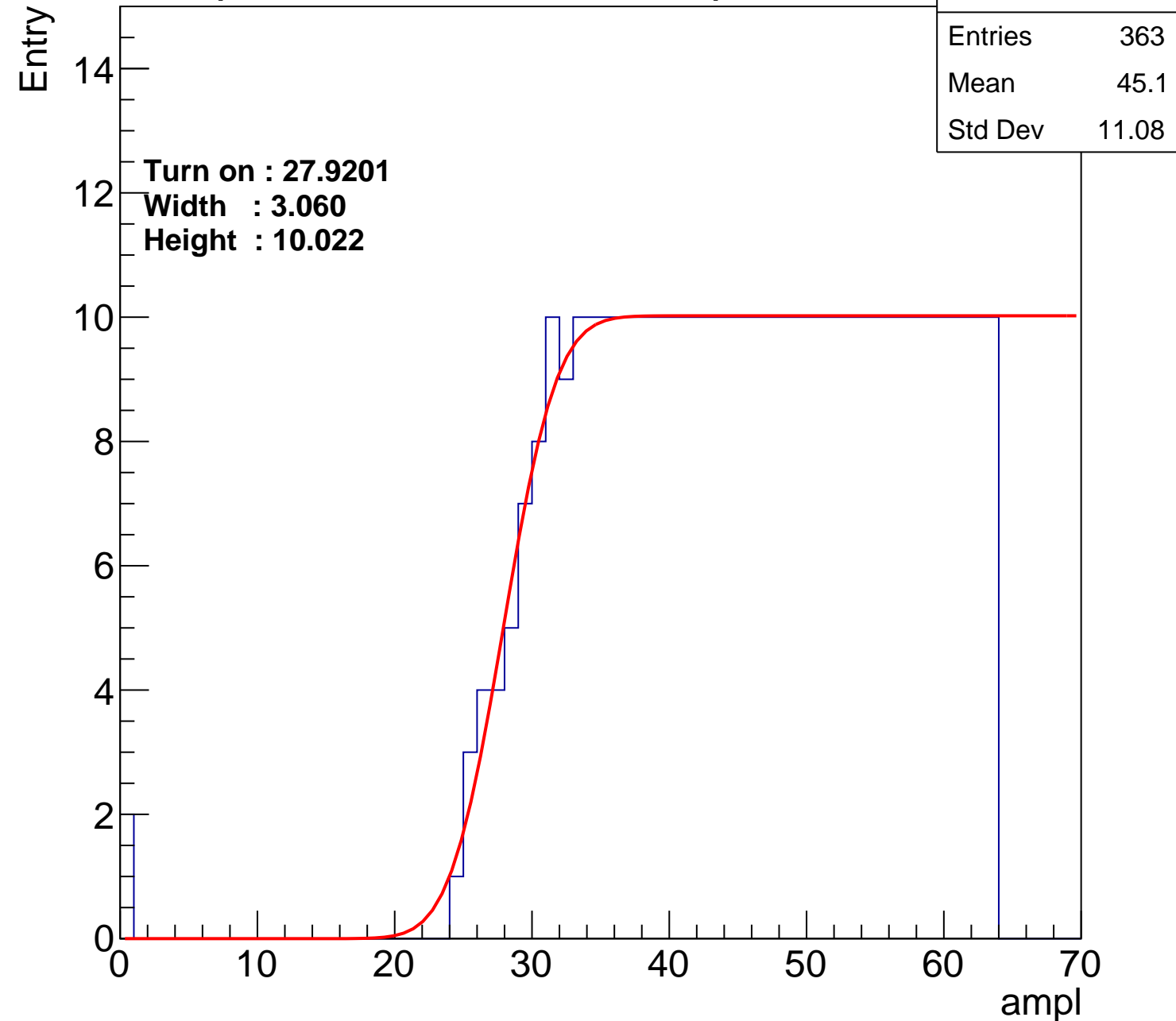
Width : 3.060

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.11
Std Dev	11.07

Turn on : 28.5299

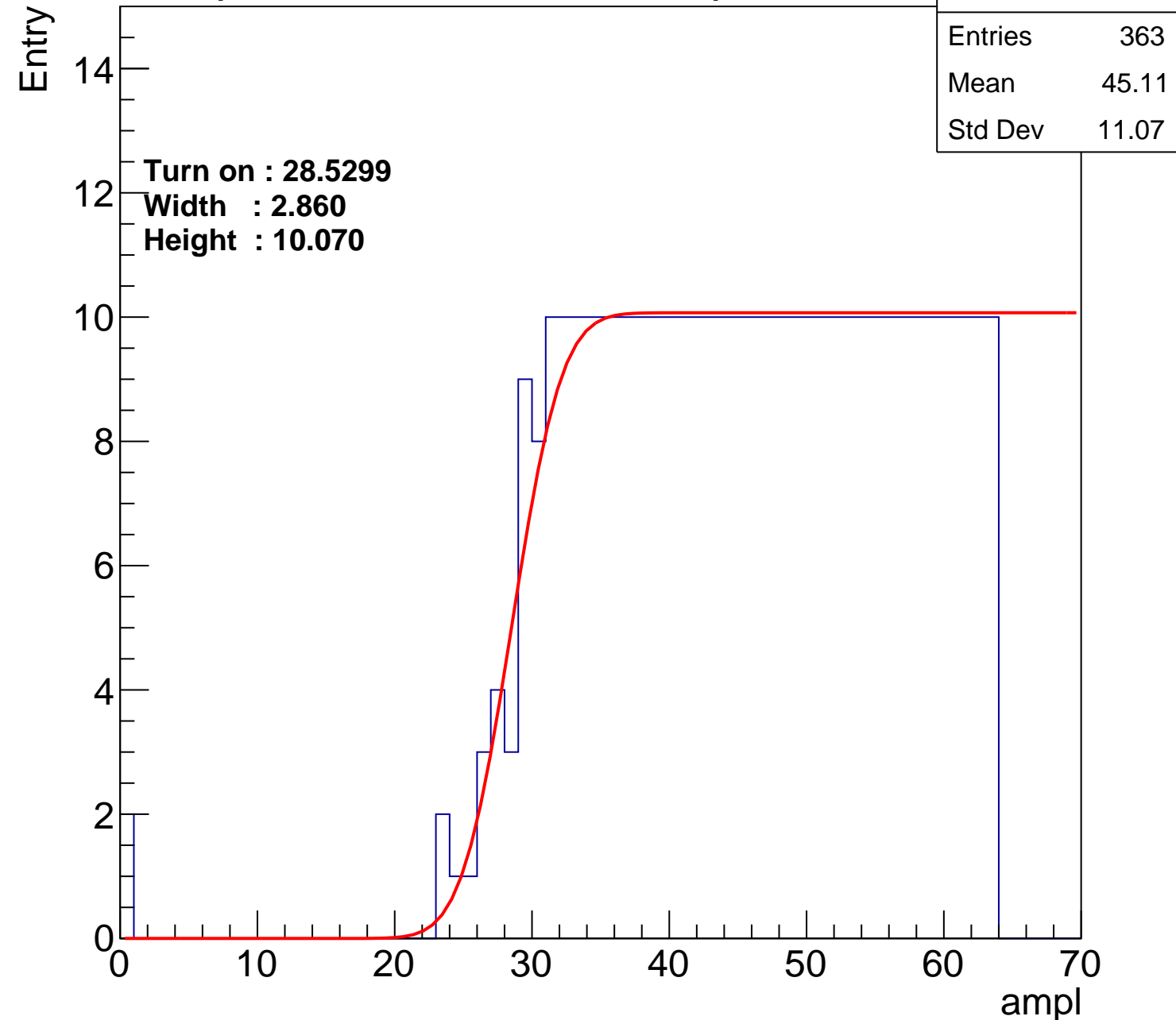
Width : 2.860

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.72
Std Dev	11.08

Turn on : 26.9306

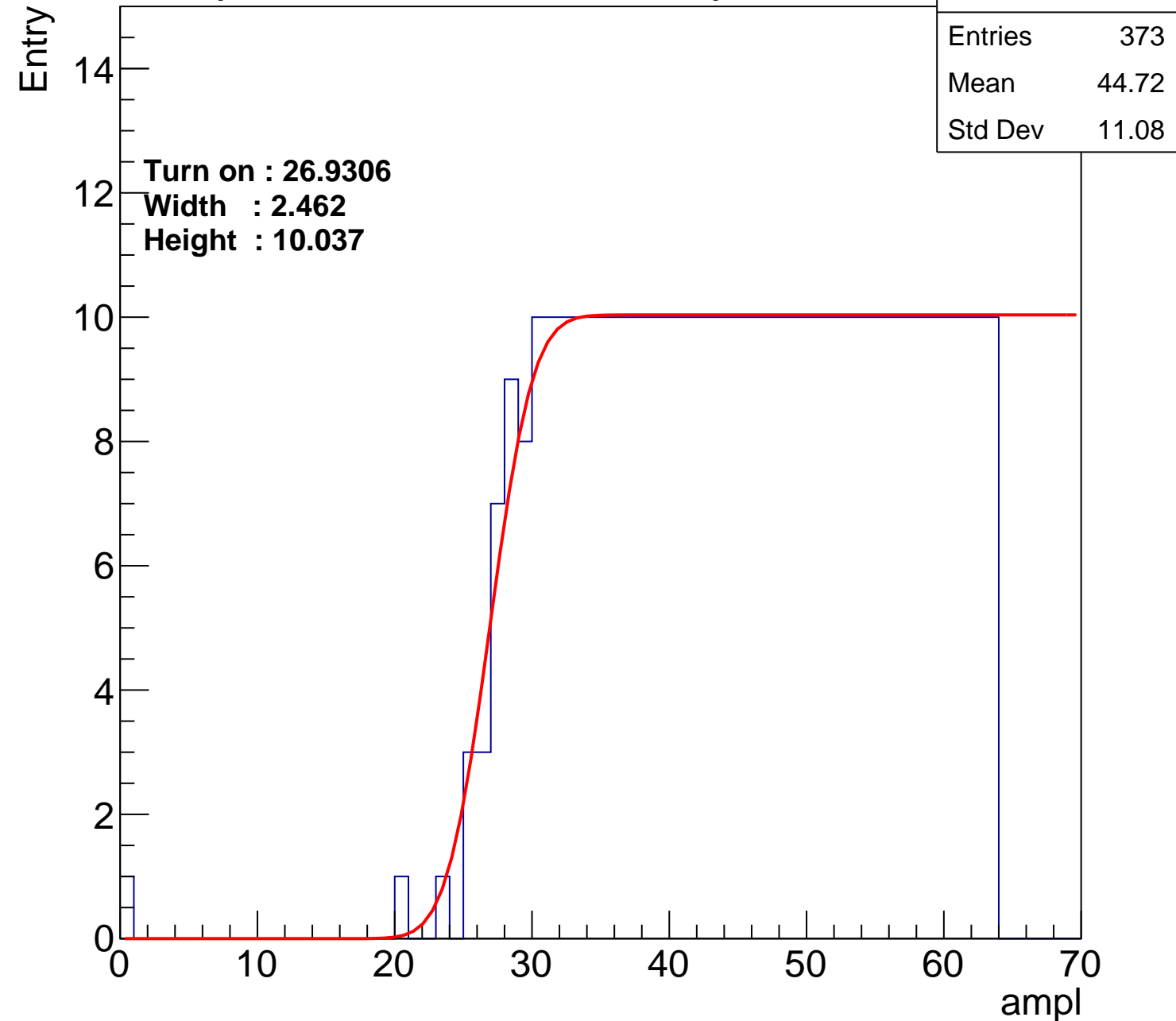
Width : 2.462

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.4934

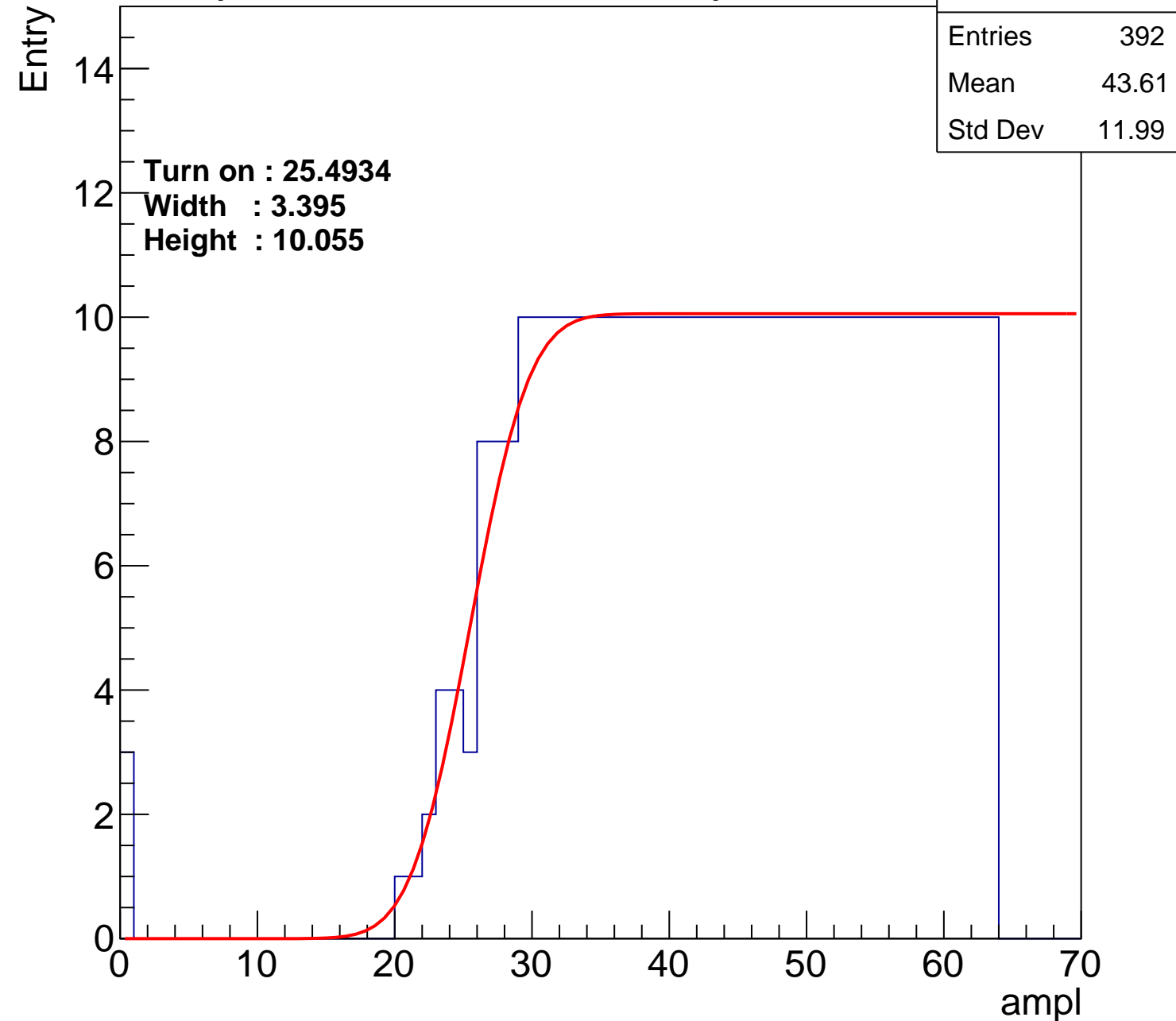
Width : 3.395

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch27

calib_packv5_042523_0143.root, FC#12, port B1

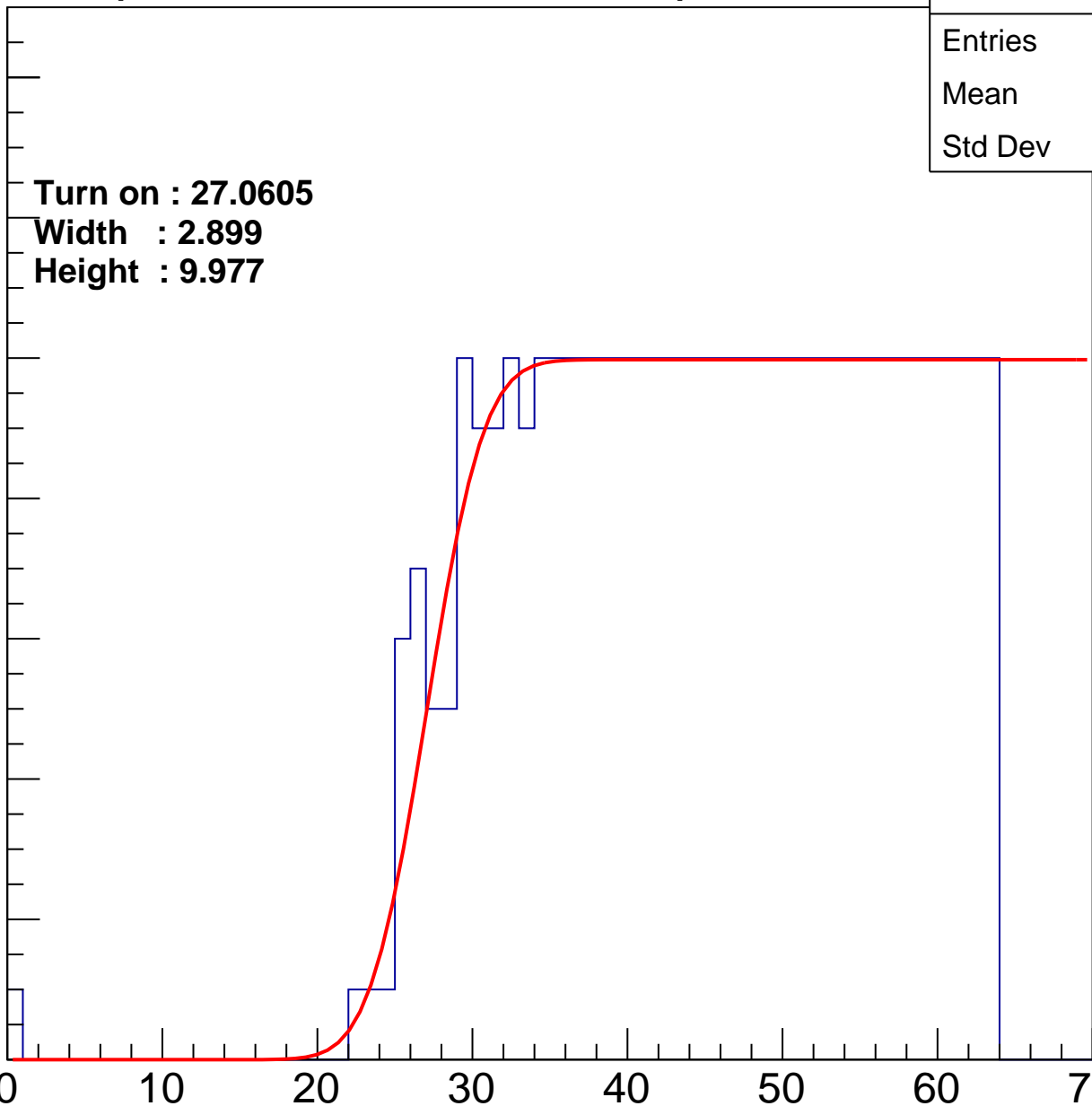
Entry

14
12
10
8
6
4
2
0

Turn on : 27.0605
Width : 2.899
Height : 9.977

Entries	374
Mean	44.6
Std Dev	11.2

ampl



B0L102S, U23-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.4394

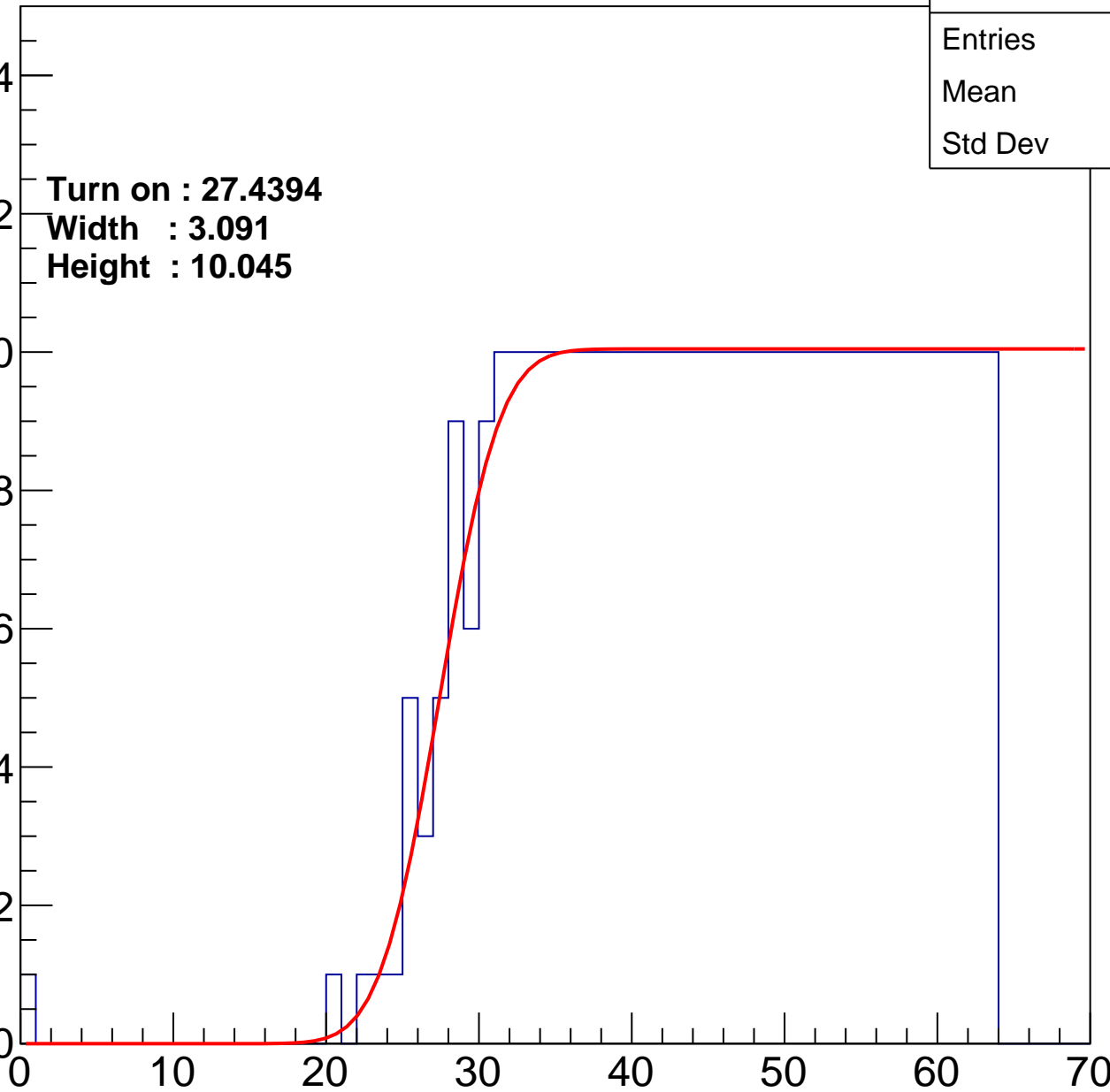
Width : 3.091

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch29

calib_packv5_042523_0143.root, FC#12, port B1

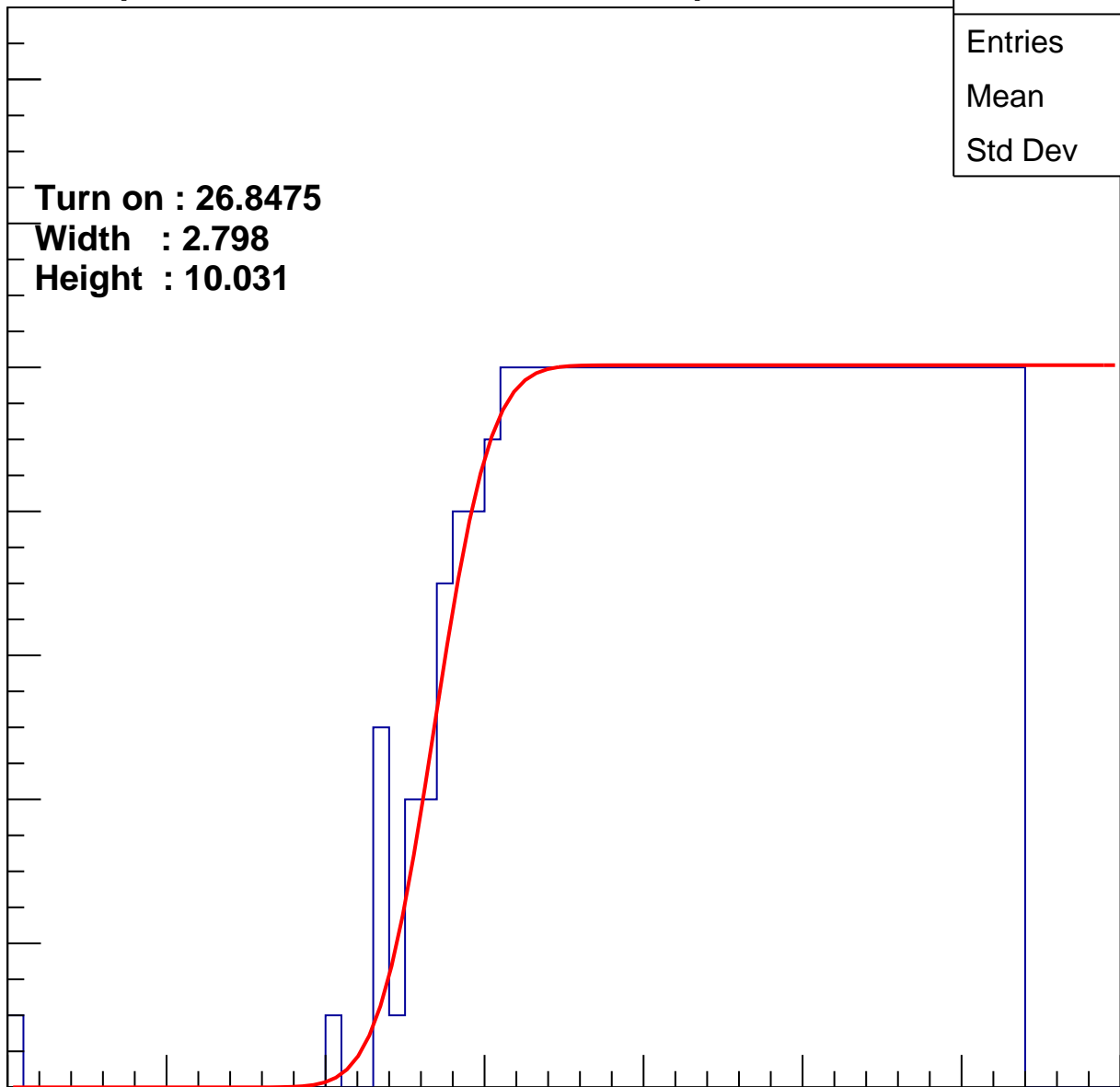
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8475
Width : 2.798
Height : 10.031

Entries	378
Mean	44.41
Std Dev	11.31

ampl



B0L102S, U23-ch30

calib_packv5_042523_0143.root, FC#12, port B1

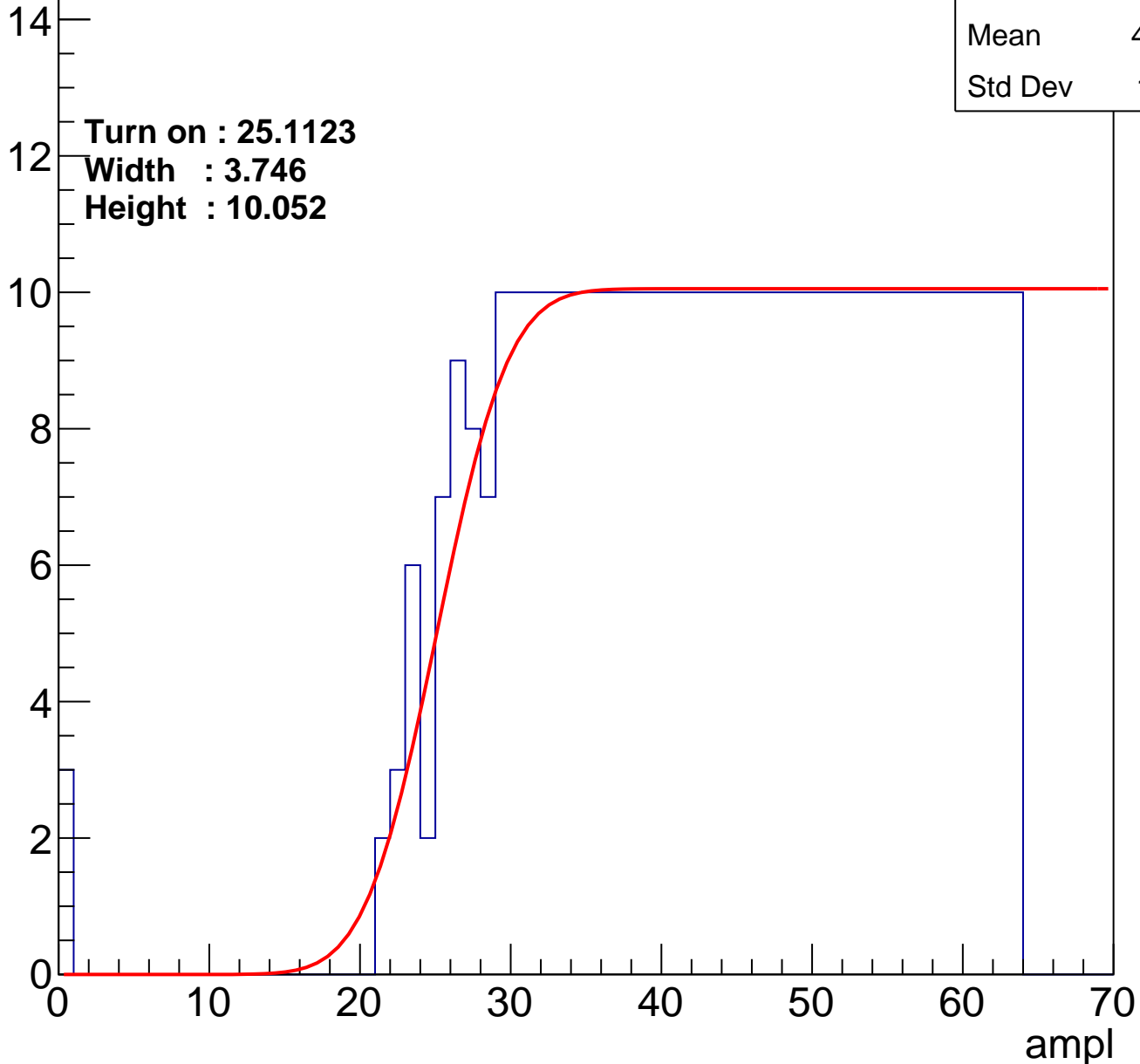
Entries	397
Mean	43.36
Std Dev	12.11

Turn on : 25.1123

Width : 3.746

Height : 10.052

Entry



B0L102S, U23-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.3273

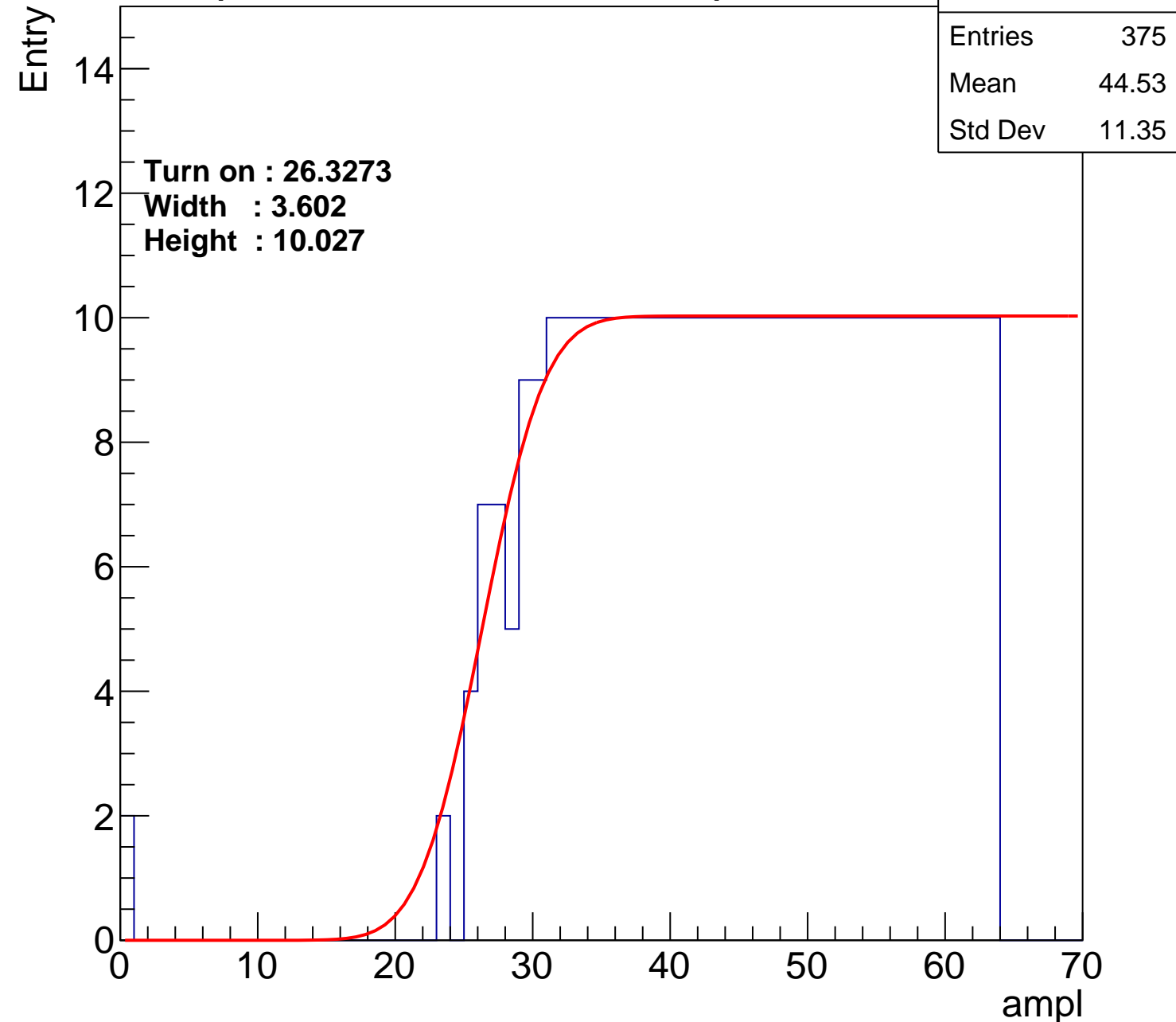
Width : 3.602

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.21
Std Dev	11.41

Turn on : 26.3153

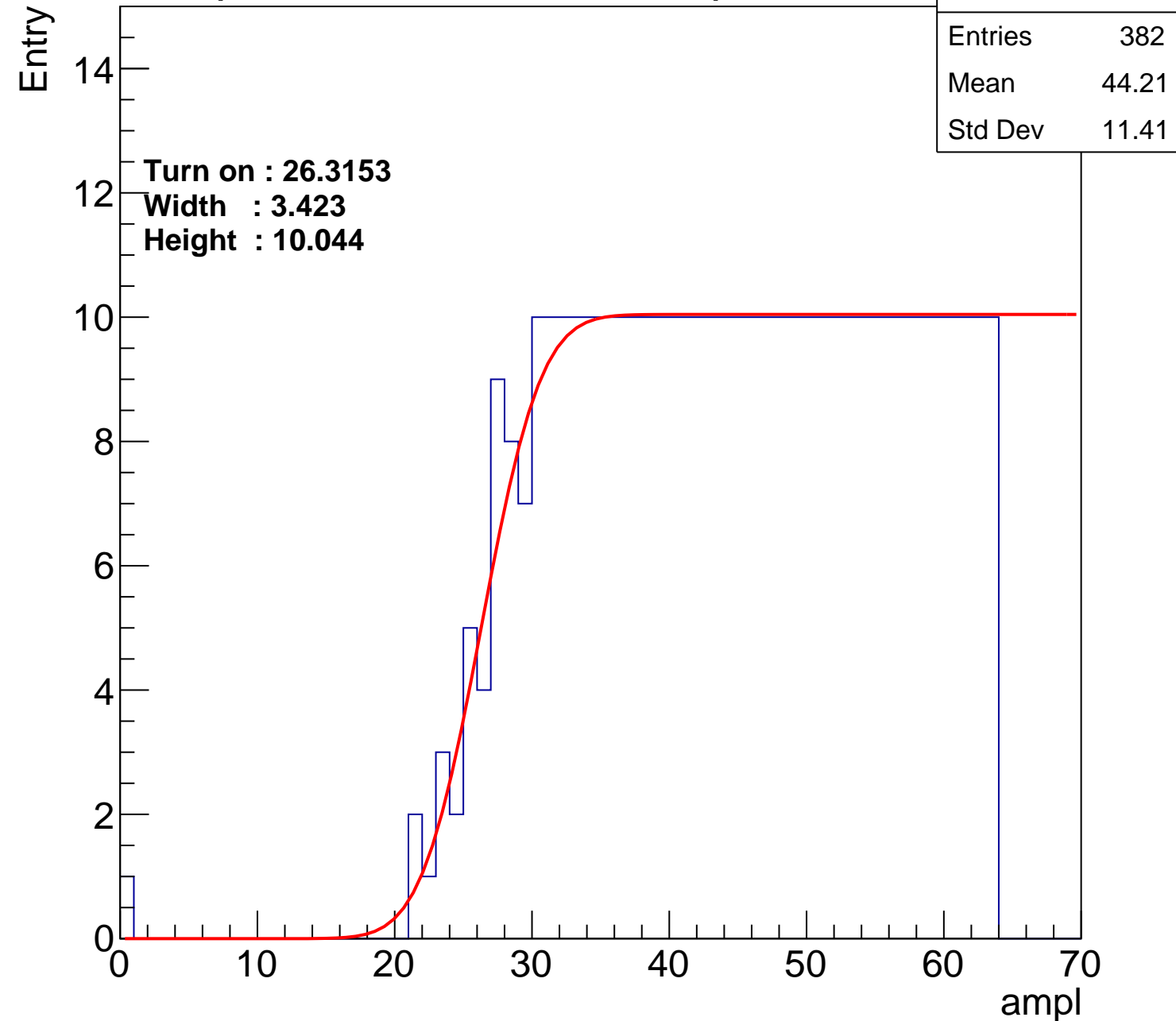
Width : 3.423

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch33

calib_packv5_042523_0143.root, FC#12, port B1

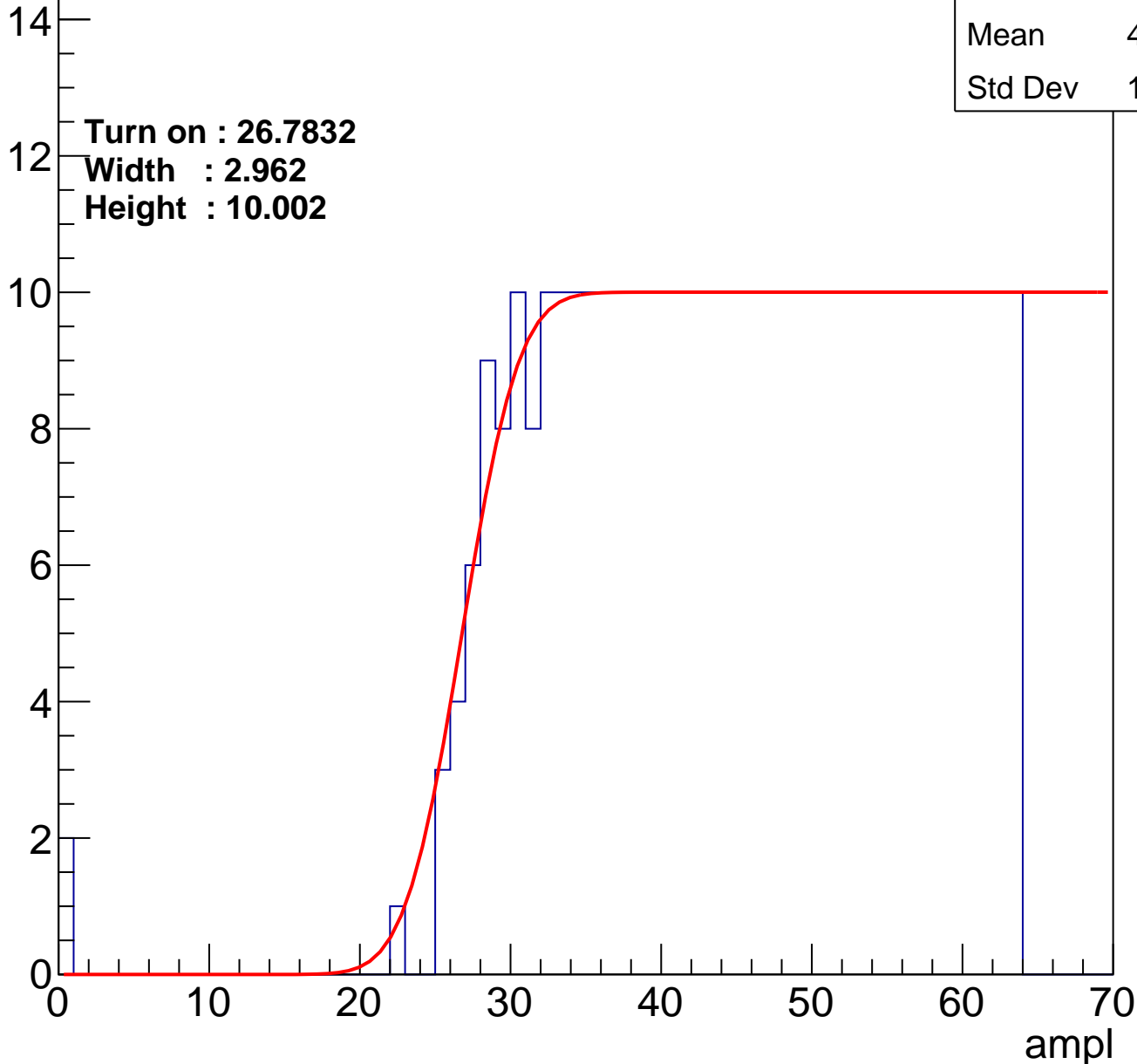
Entries	371
Mean	44.73
Std Dev	11.24

Turn on : 26.7832

Width : 2.962

Height : 10.002

Entry



B0L102S, U23-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.7757

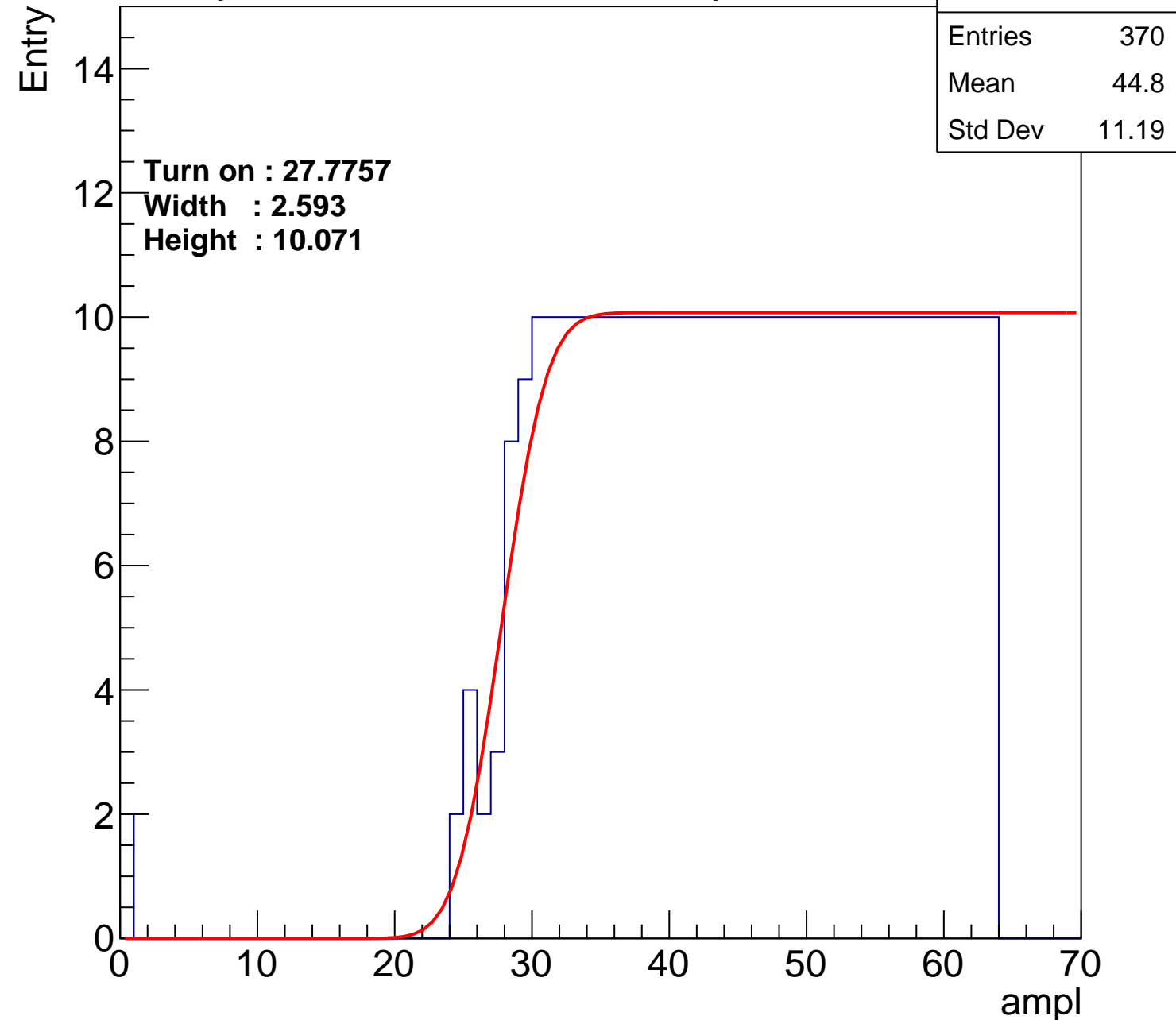
Width : 2.593

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.57
Std Dev	11.19

Std Dev	11.19
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Height : 9.973



B0L102S, U23-ch36

calib_packv5_042523_0143.root, FC#12, port B1

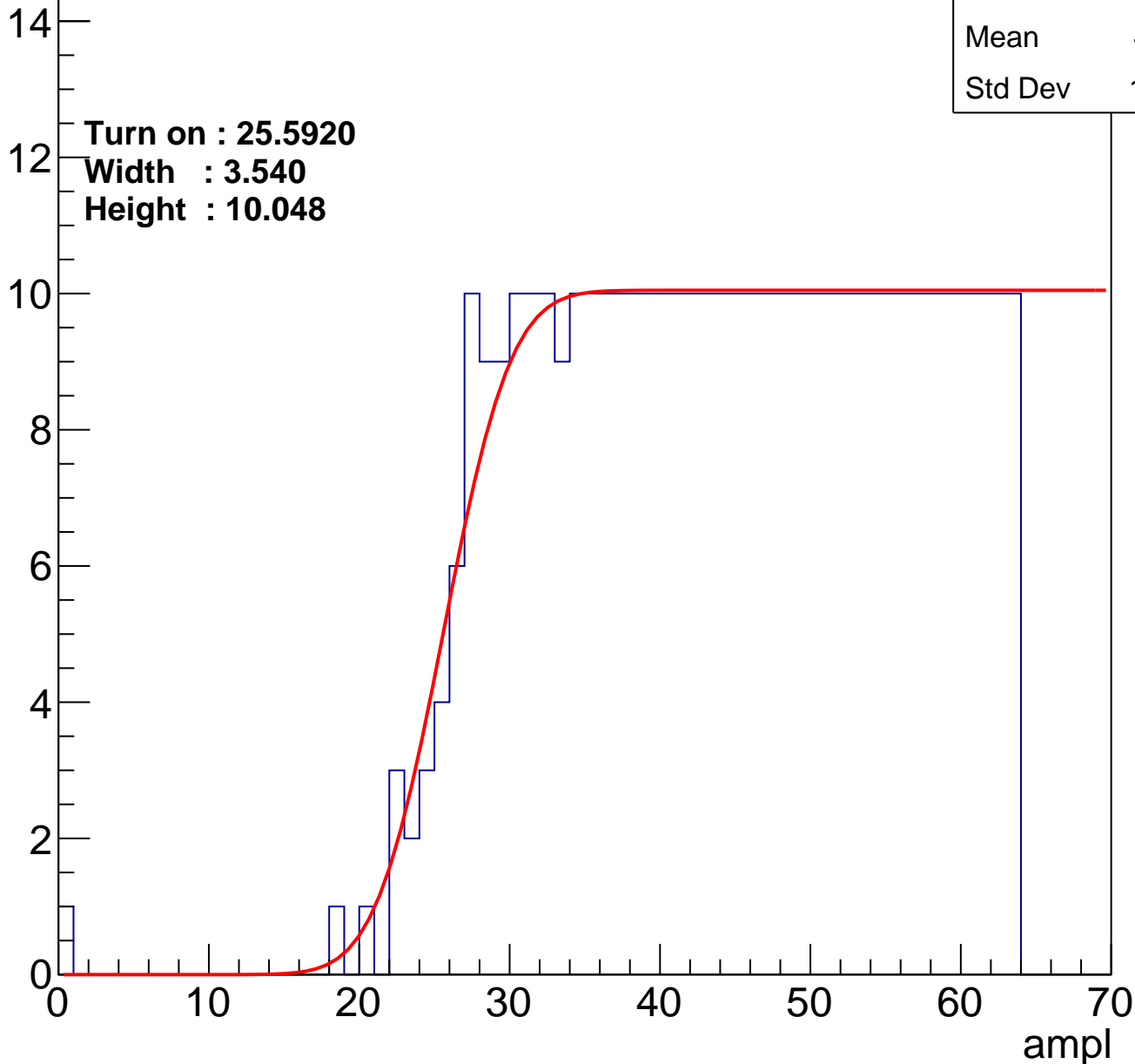
Entries	388
Mean	43.91
Std Dev	11.58

Turn on : 25.5920

Width : 3.540

Height : 10.048

Entry



B0L102S, U23-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 26.8900

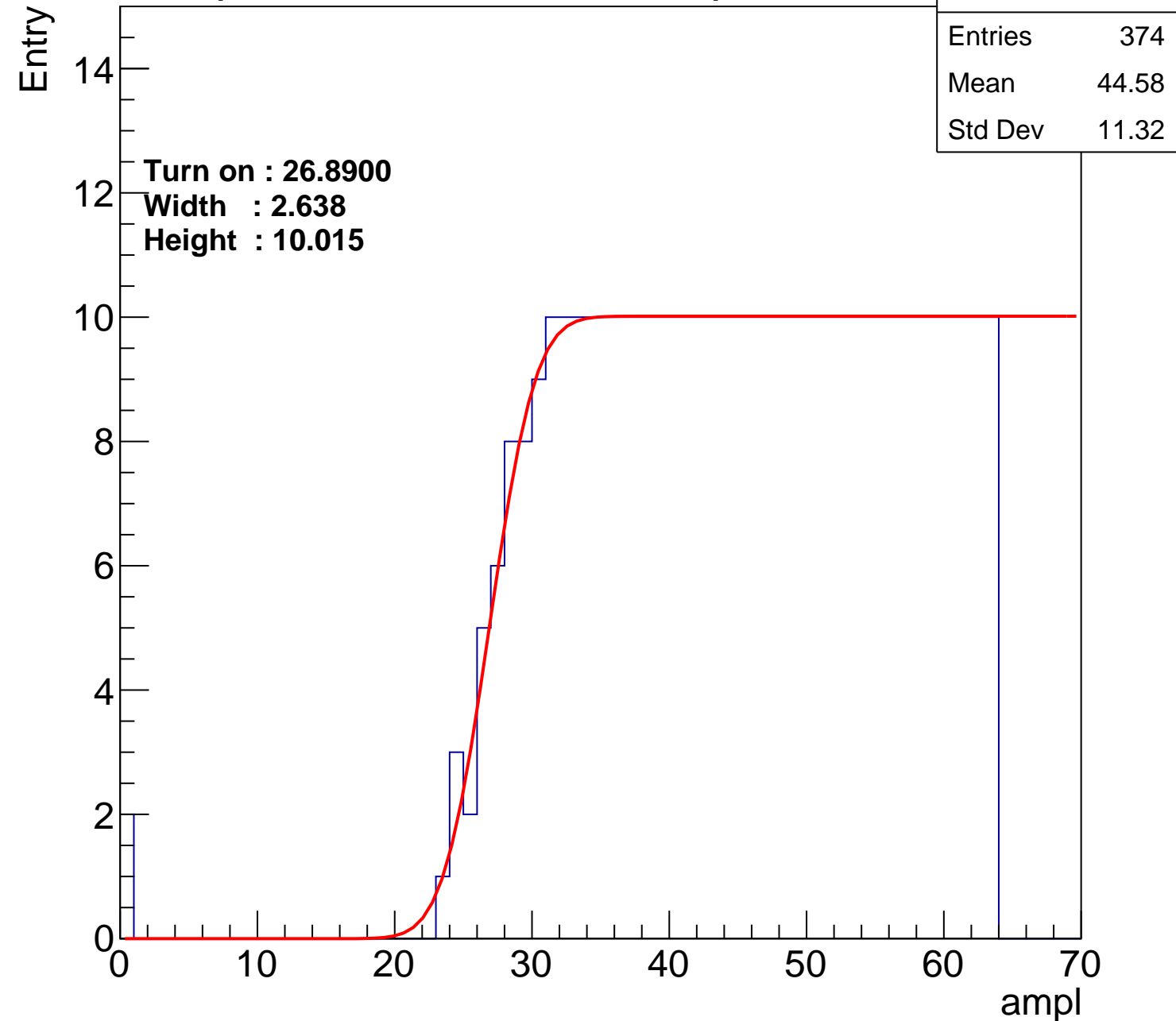
Width : 2.638

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.2
Std Dev	11.87

Turn on : 27.1921

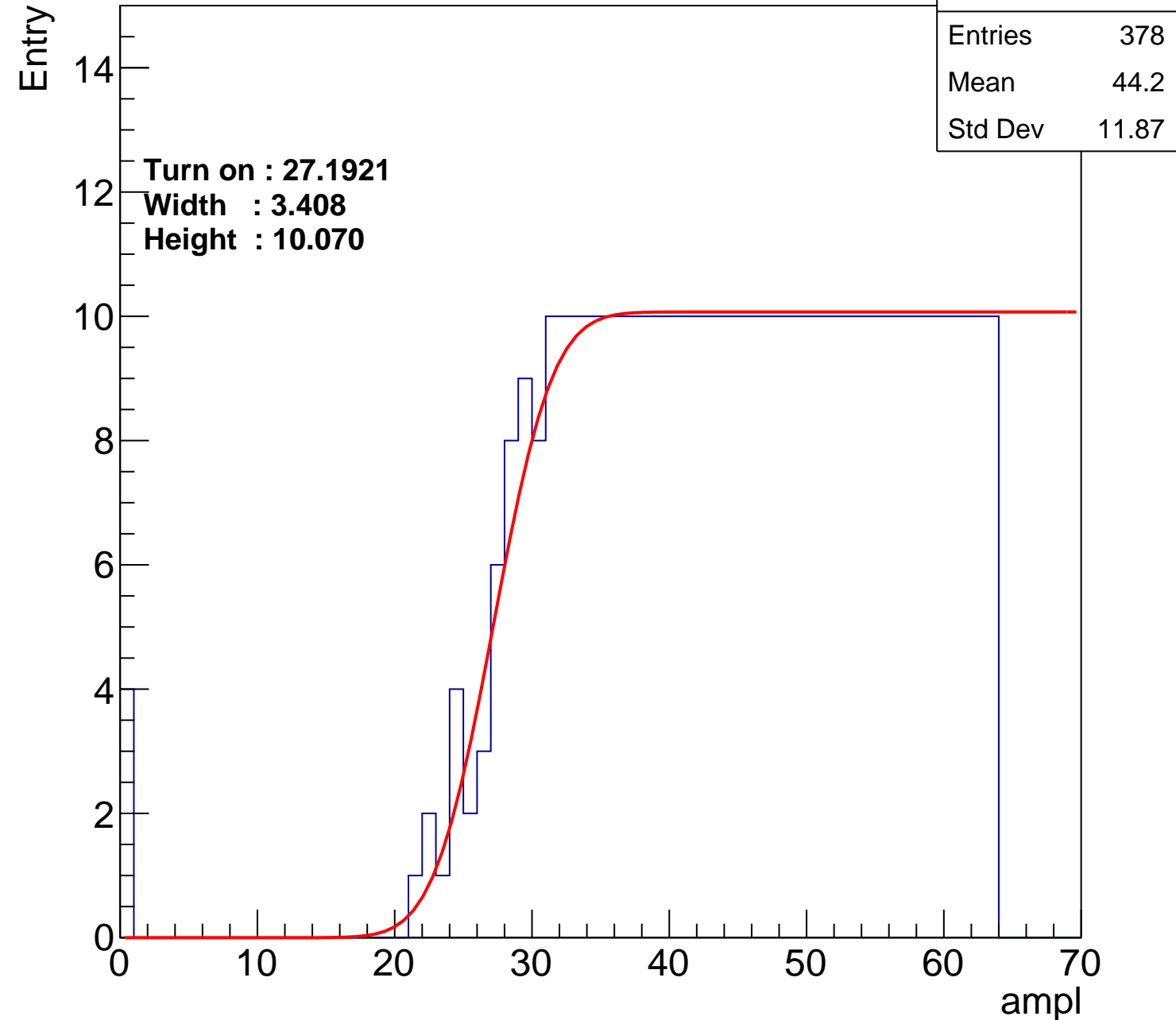
Width : 3.408

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.7106

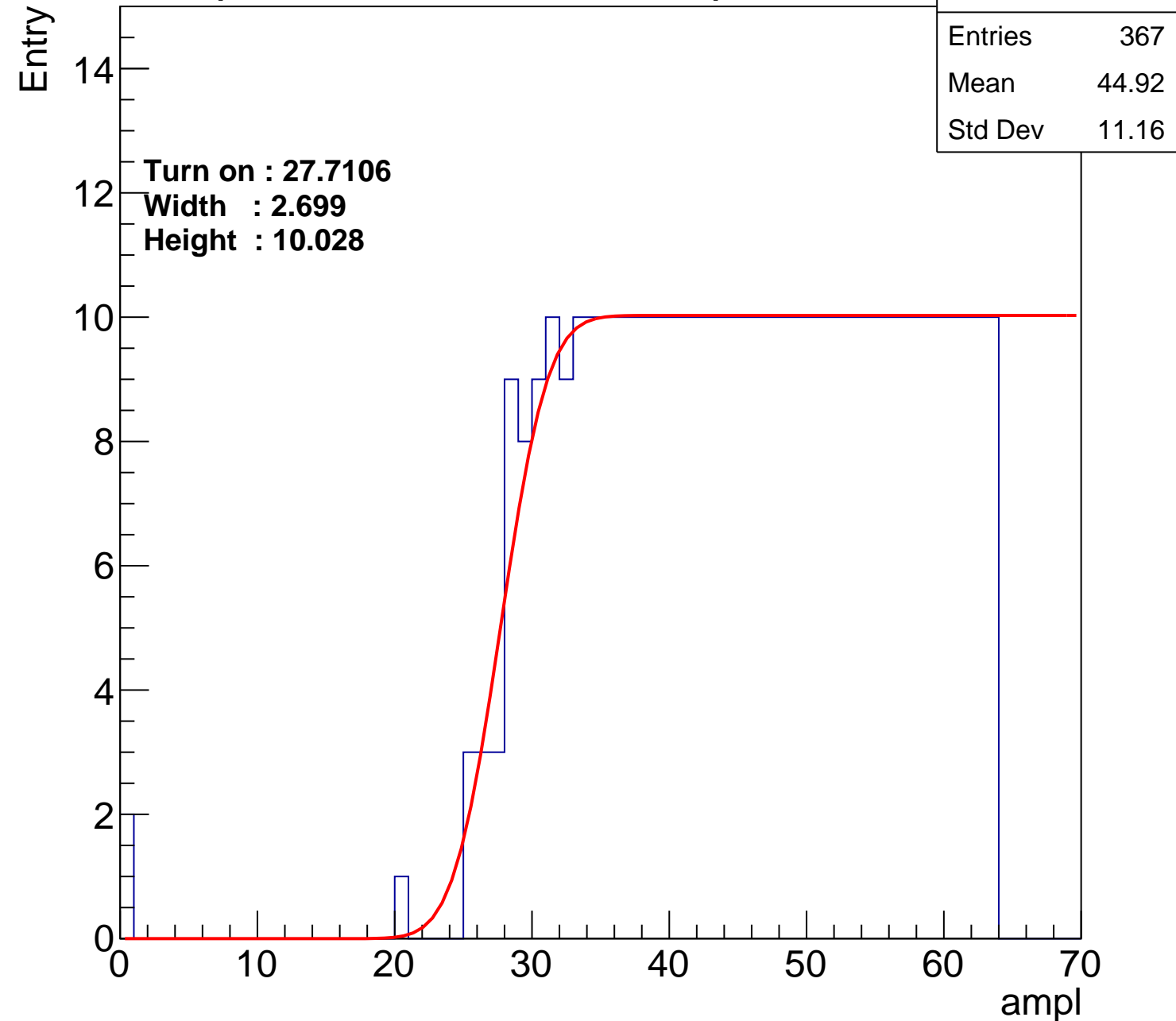
Width : 2.699

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 26.3648
Width : 2.005
Height : 9.909



B0L102S, U23-ch41

calib_packv5_042523_0143.root, FC#12, port B1

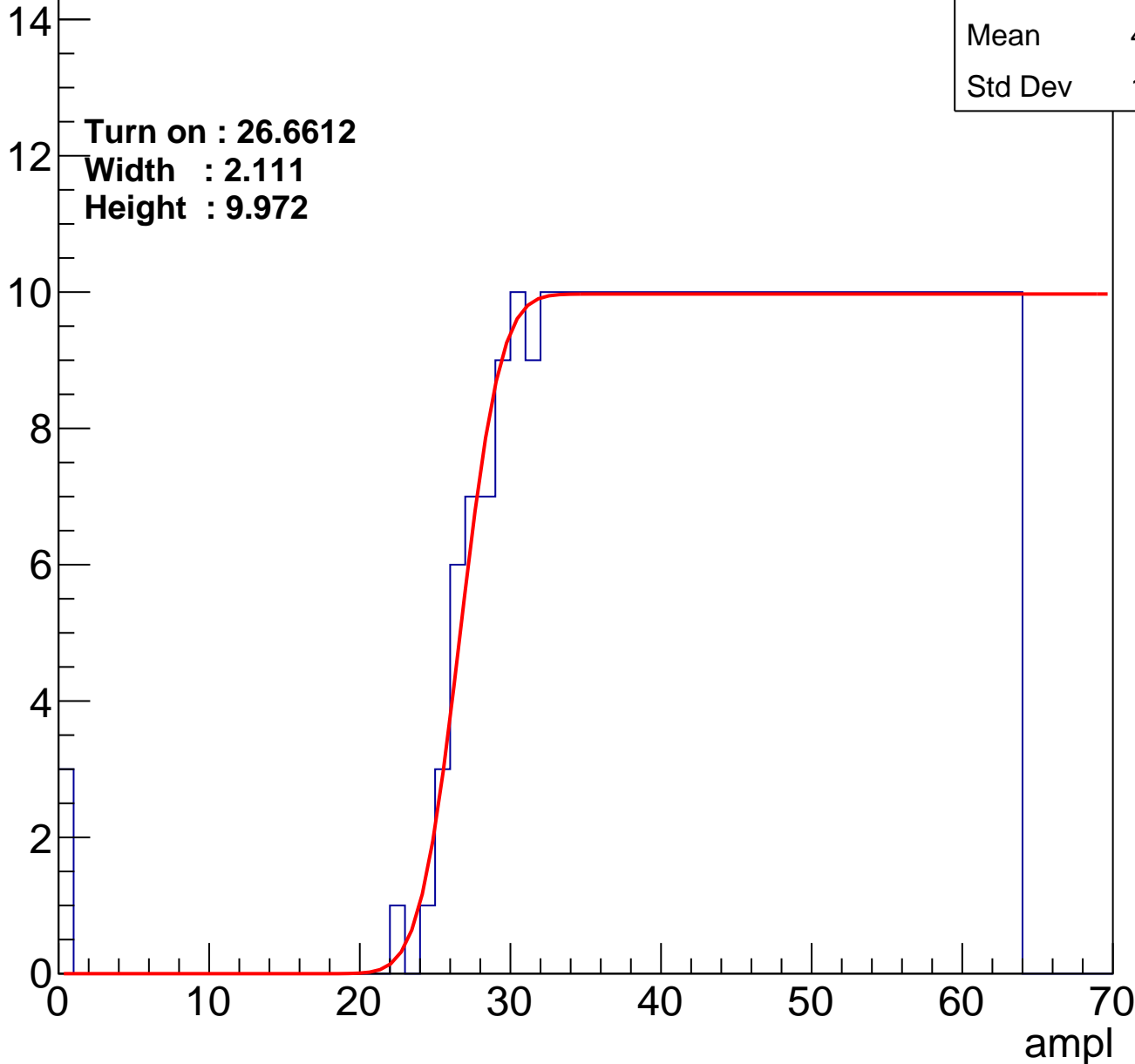
Entries	376
Mean	44.42
Std Dev	11.55

Turn on : 26.6612

Width : 2.111

Height : 9.972

Entry



B0L102S, U23-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.45
Std Dev	11.69

Turn on : 26.7707

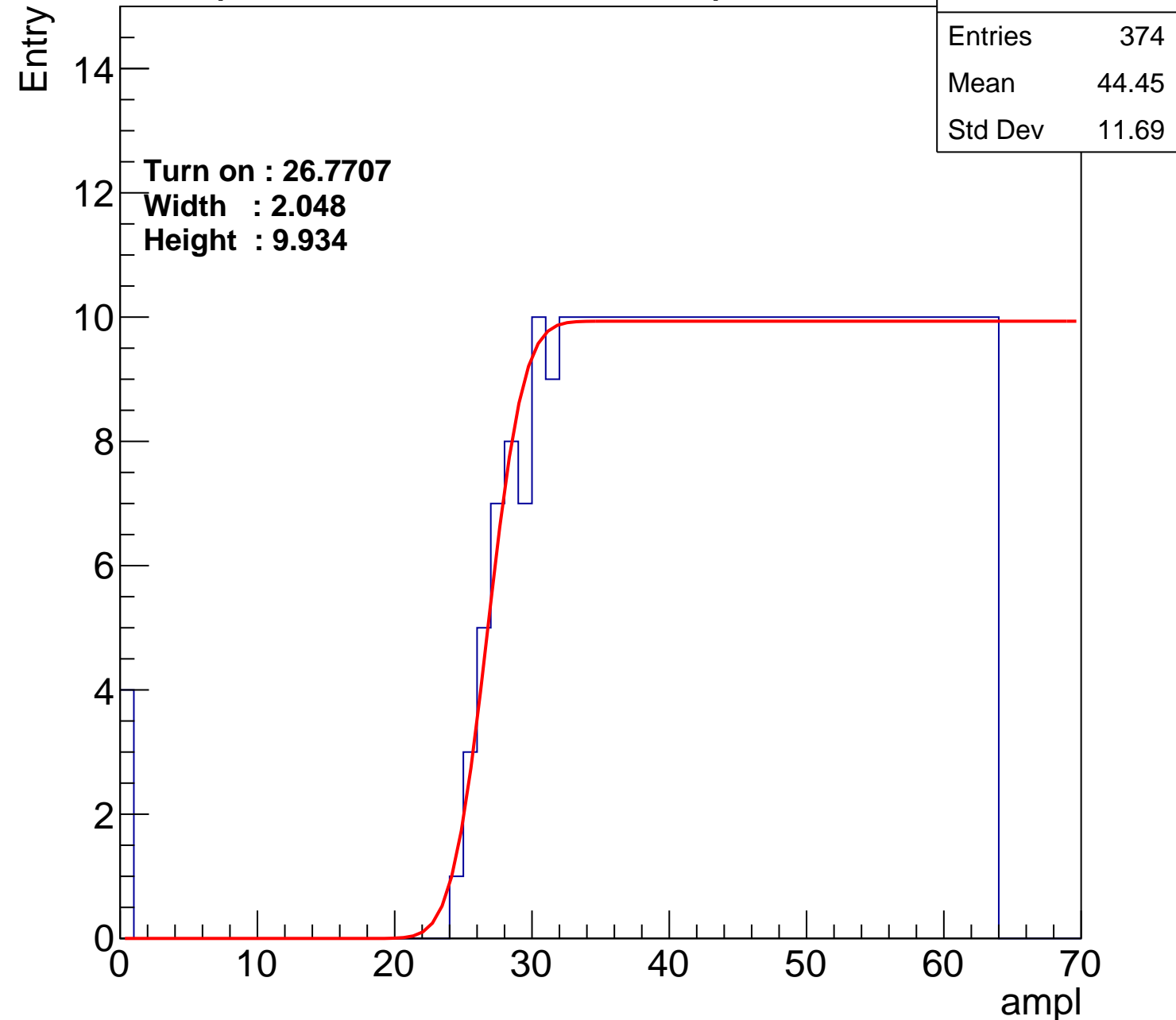
Width : 2.048

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch43

calib_packv5_042523_0143.root, FC#12, port B1

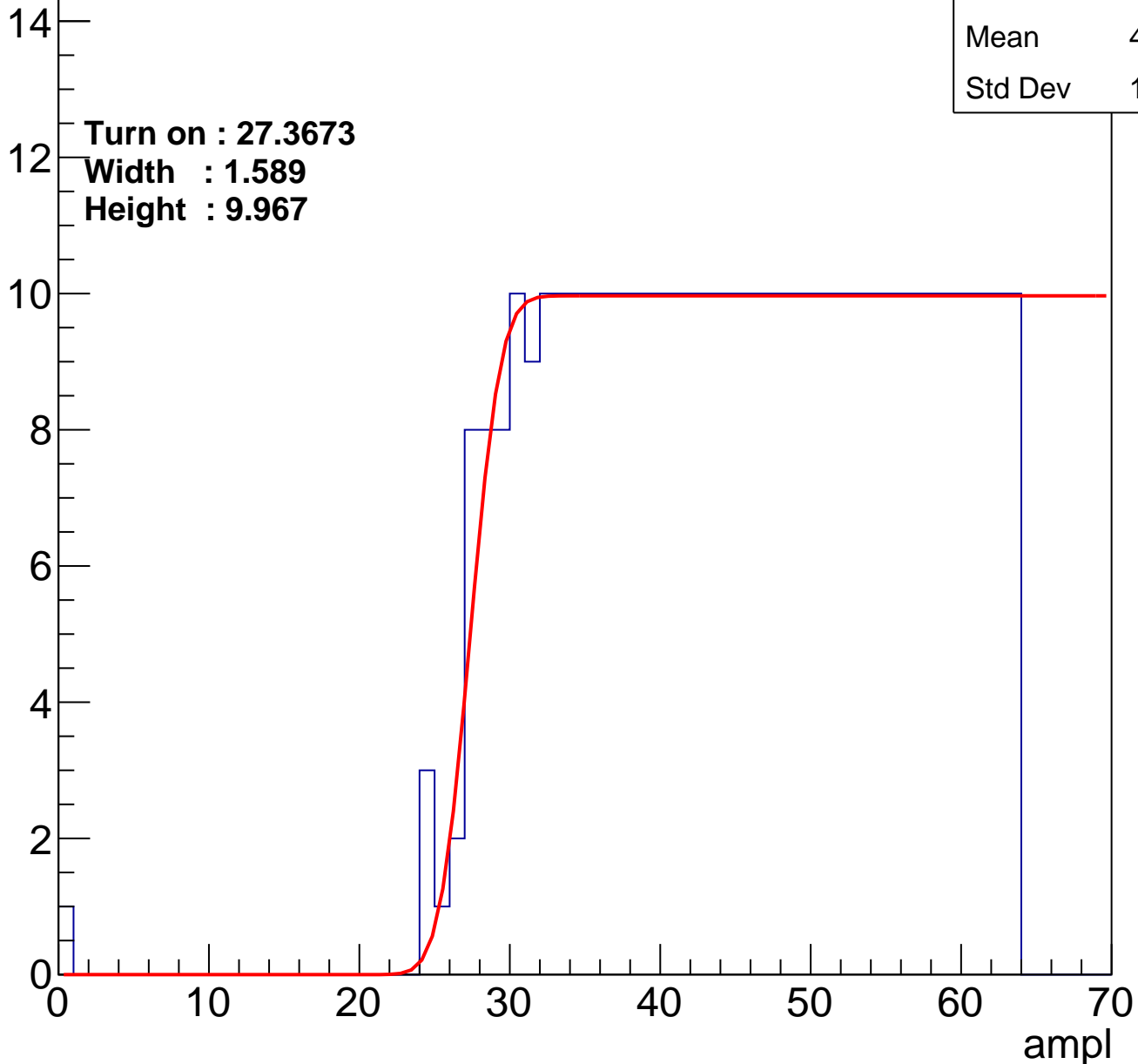
Entries	370
Mean	44.86
Std Dev	10.99

Turn on : 27.3673

Width : 1.589

Height : 9.967

Entry



B0L102S, U23-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.87
Std Dev	11.78

Turn on : 25.7778

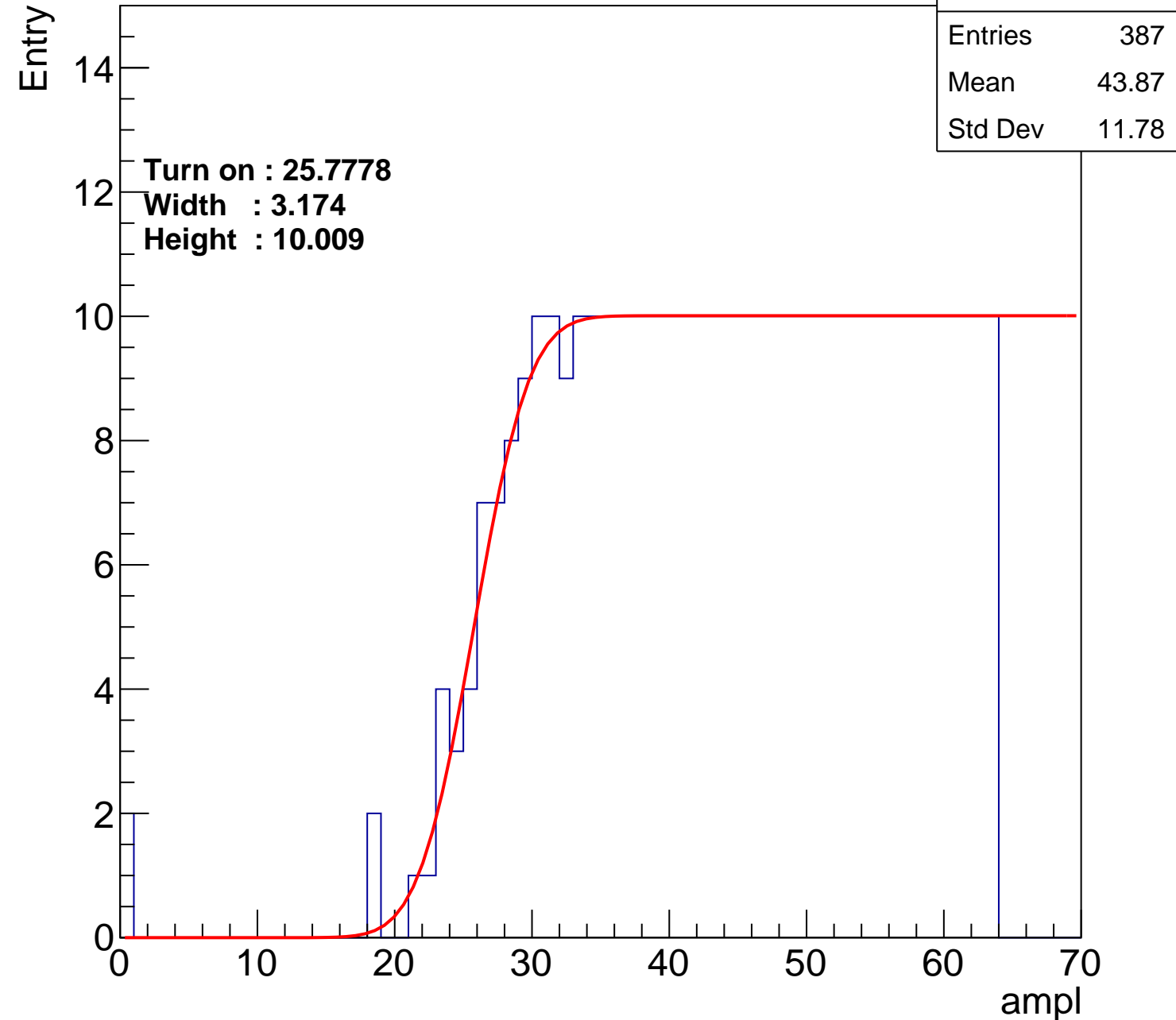
Width : 3.174

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.38
Std Dev	11.75

Turn on : 26.9155

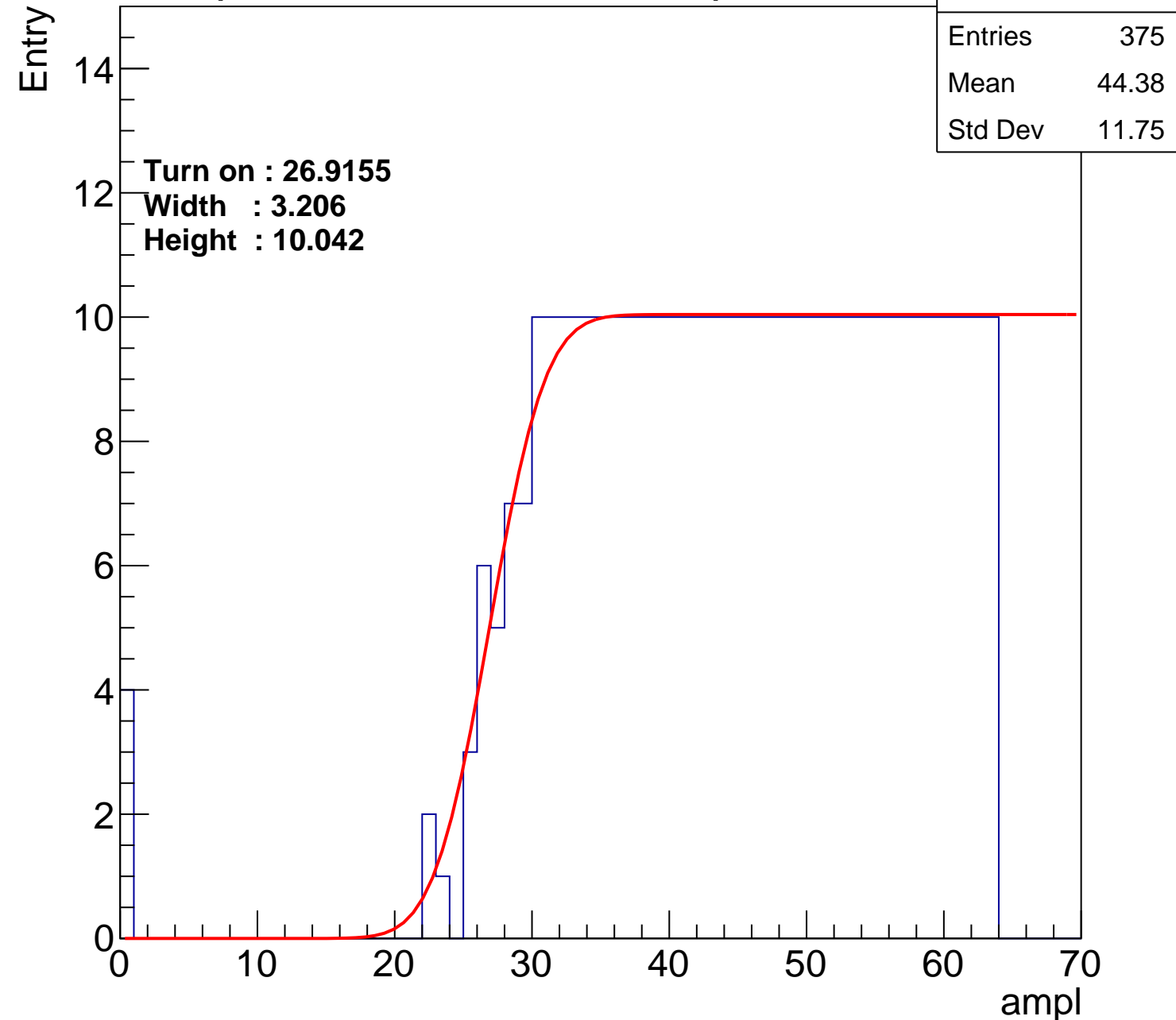
Width : 3.206

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.35
Std Dev	12.22

Turn on : 27.7276

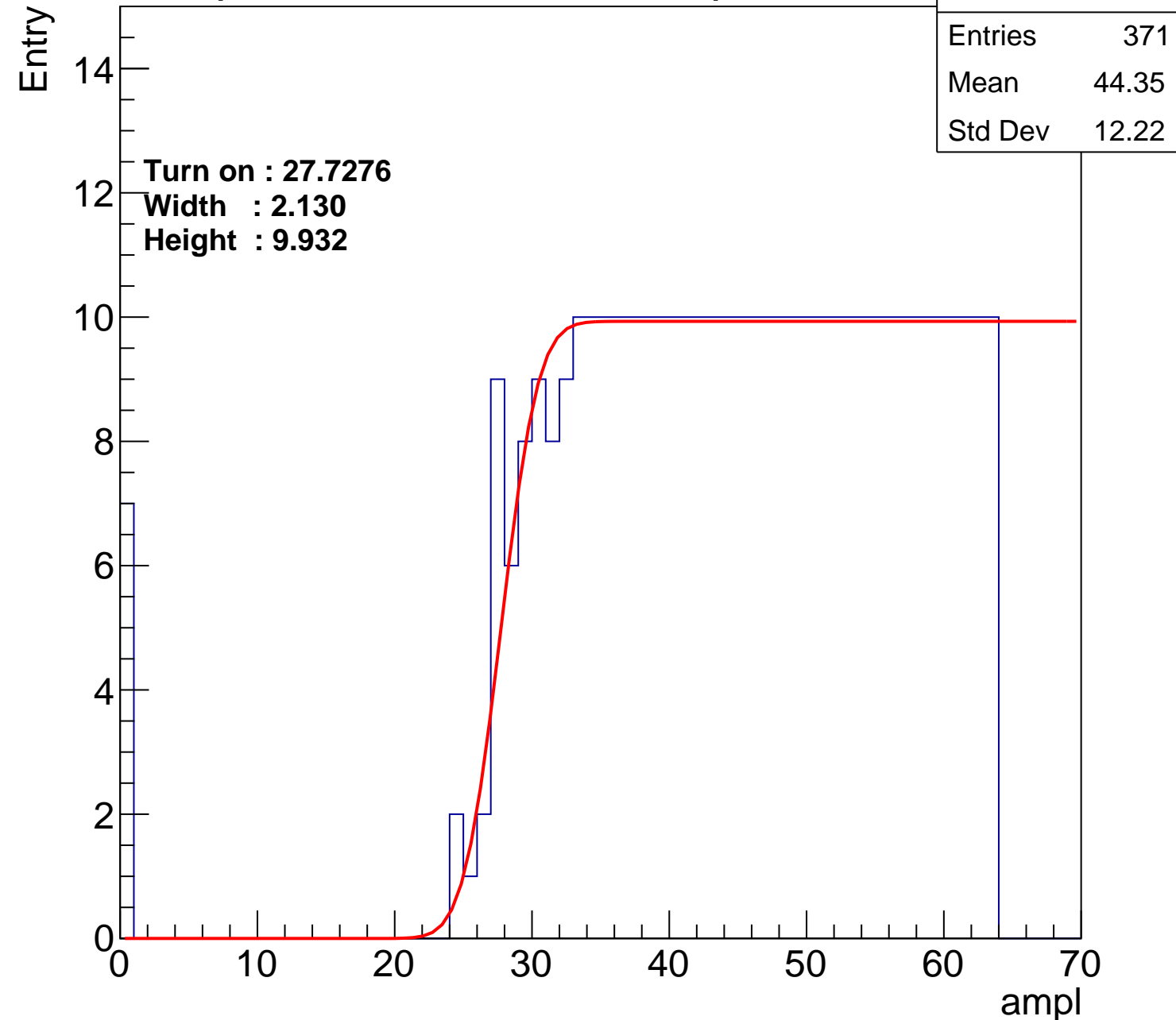
Width : 2.130

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.3
Std Dev	11.51

Turn on : 26.4287

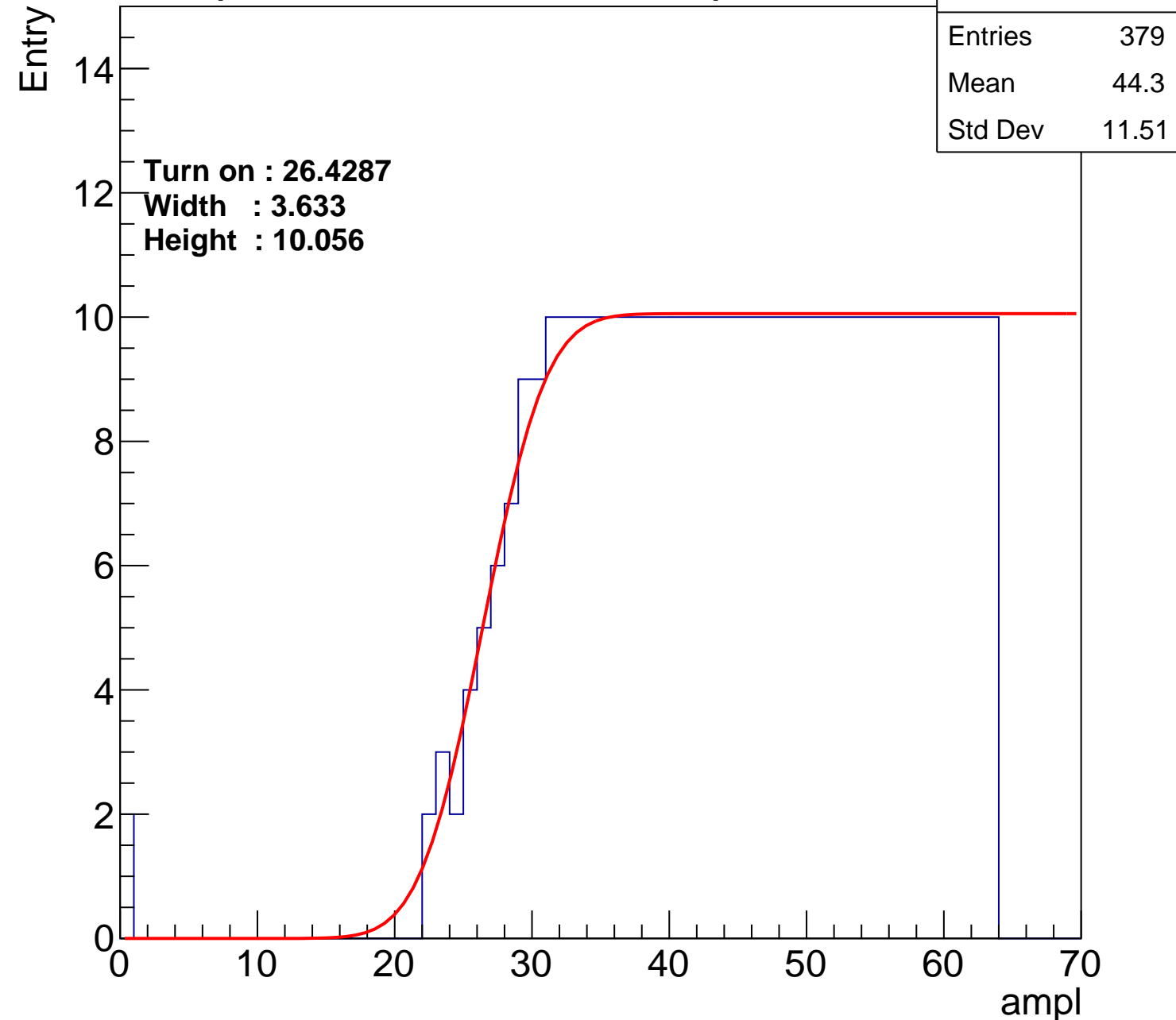
Width : 3.633

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.3052

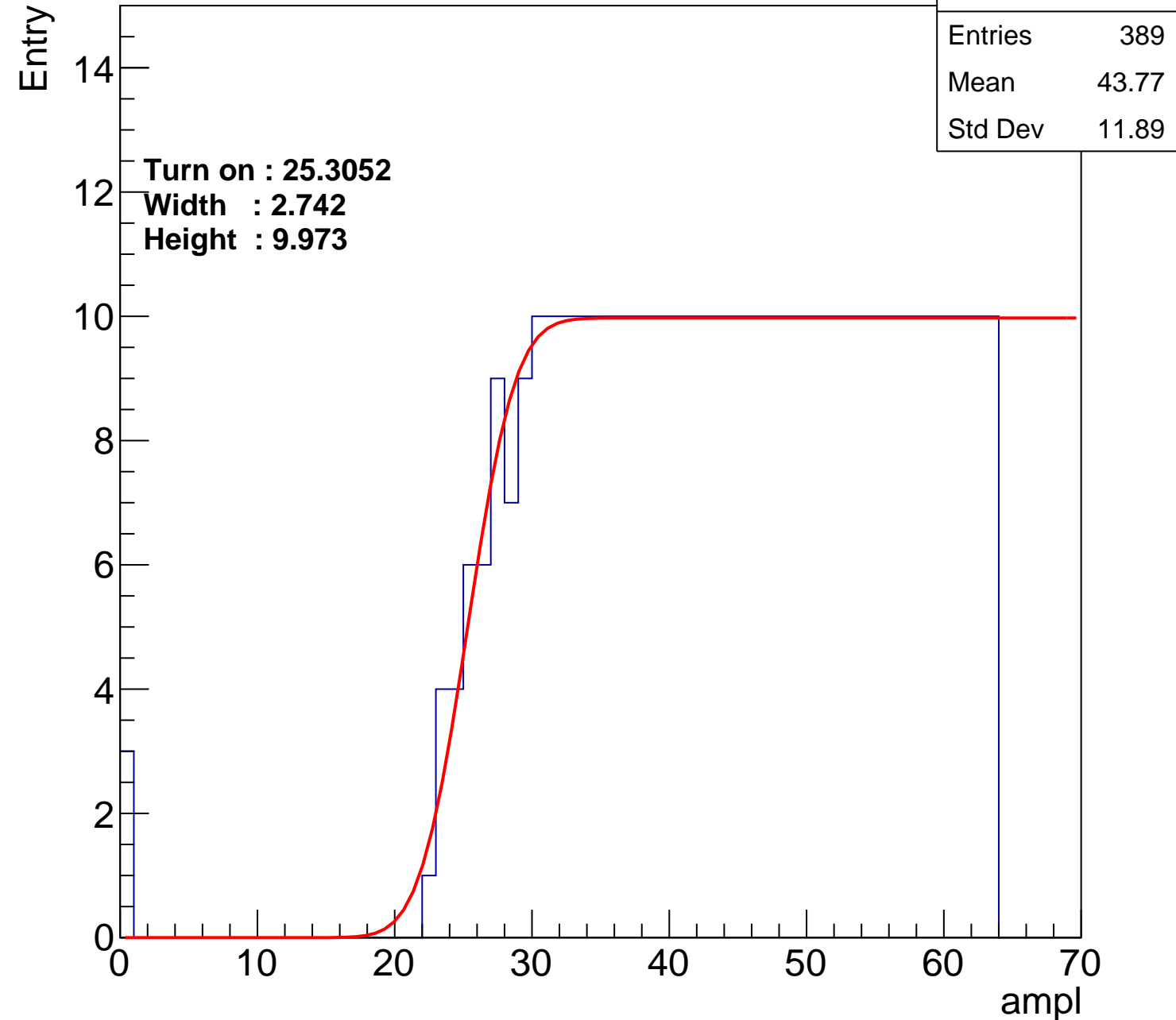
Width : 2.742

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.26
Std Dev	11.69

Turn on : 27.3333

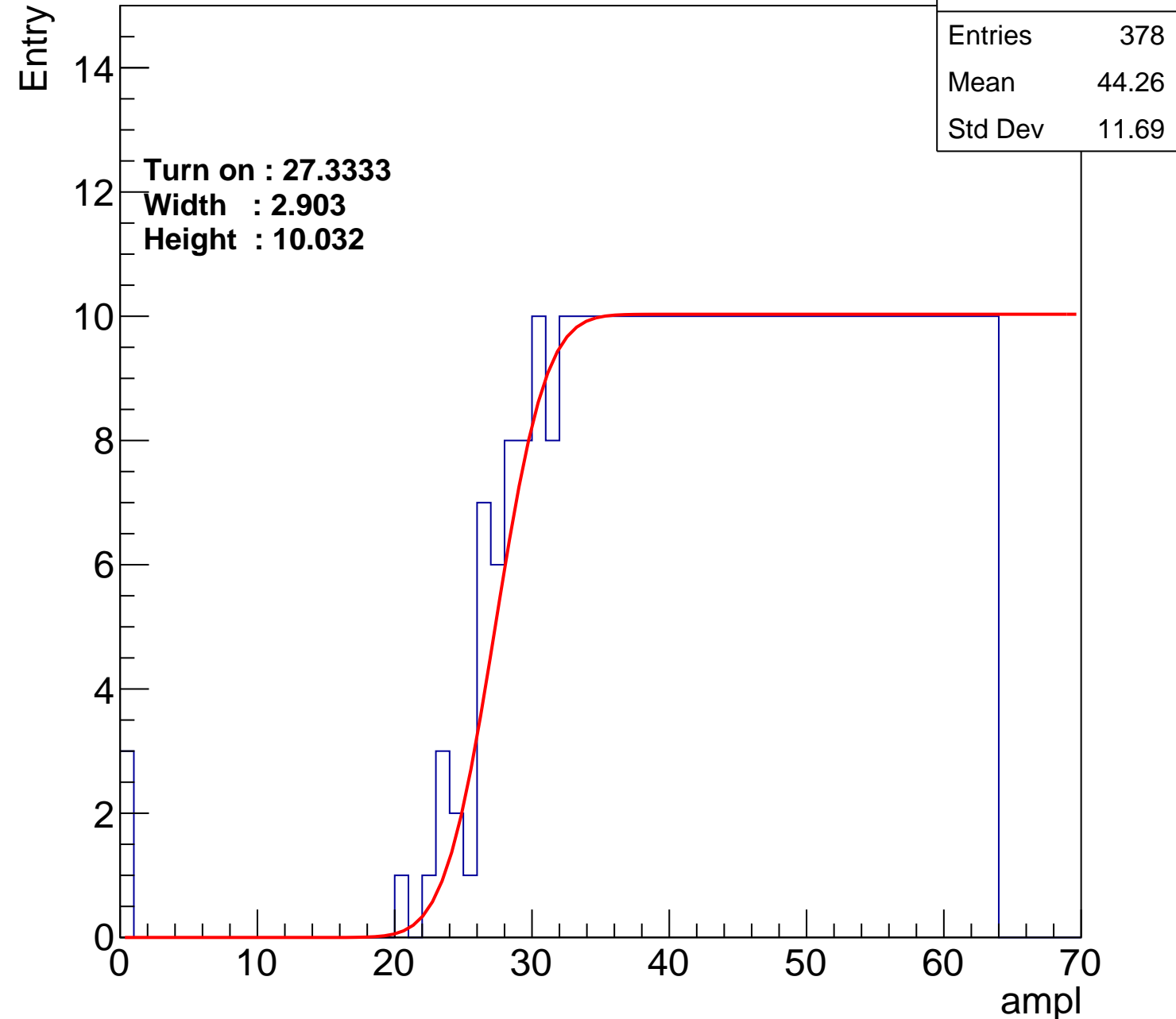
Width : 2.903

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.09
Std Dev	10.89

Turn on : 27.8693

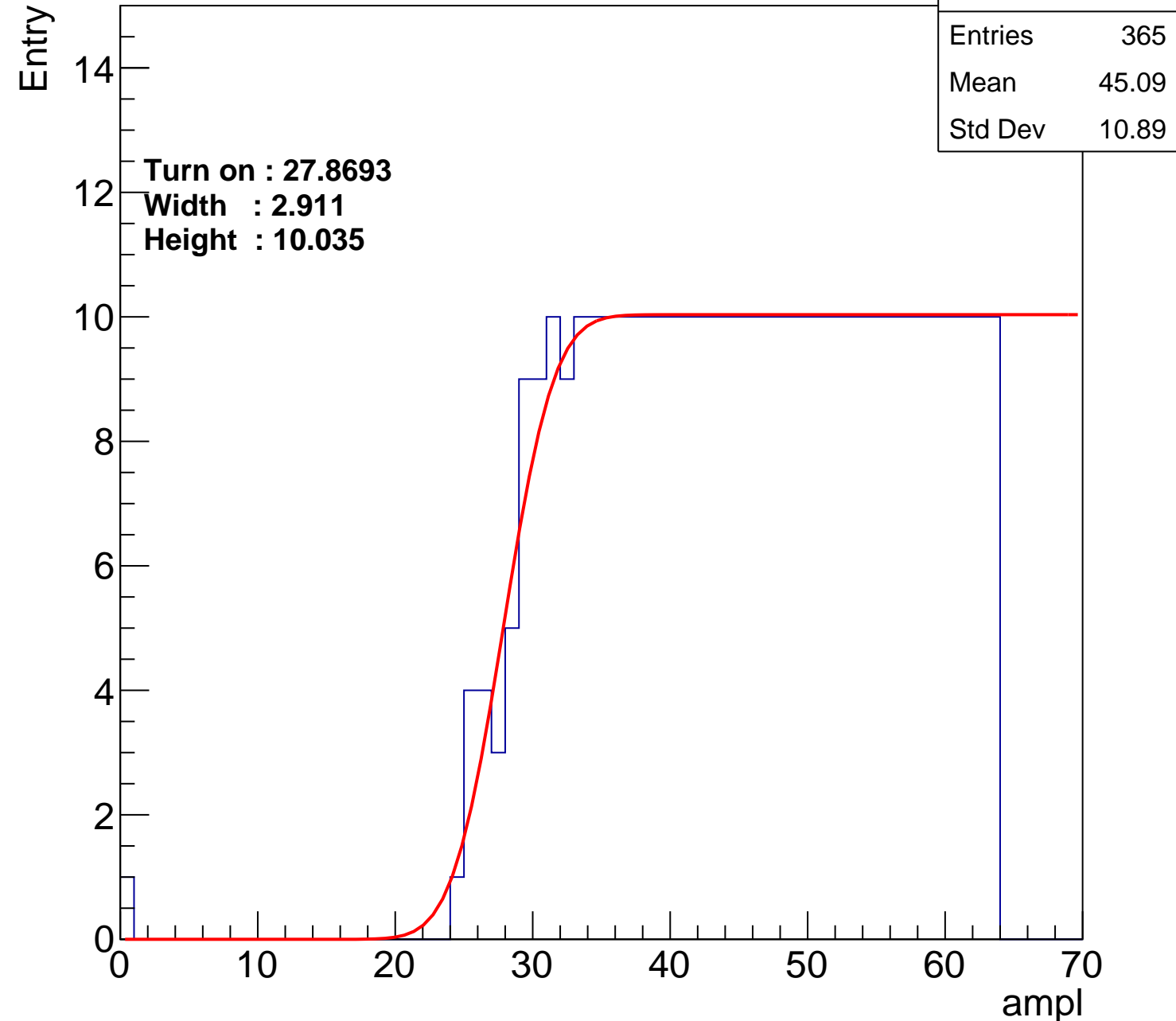
Width : 2.911

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.64
Std Dev	11.77

Turn on : 25.2380

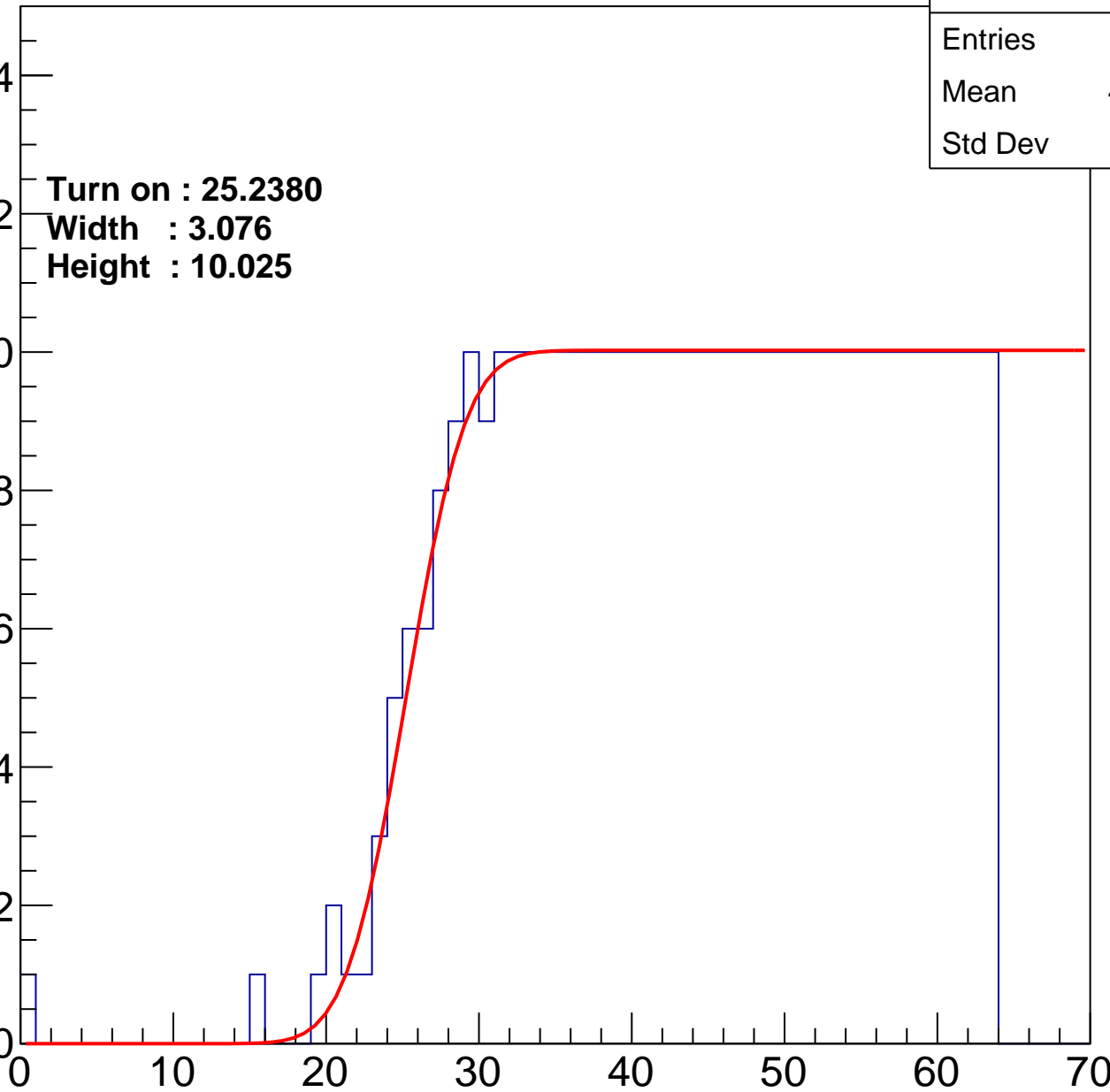
Width : 3.076

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.9
Std Dev	11.66

Turn on : 25.4421

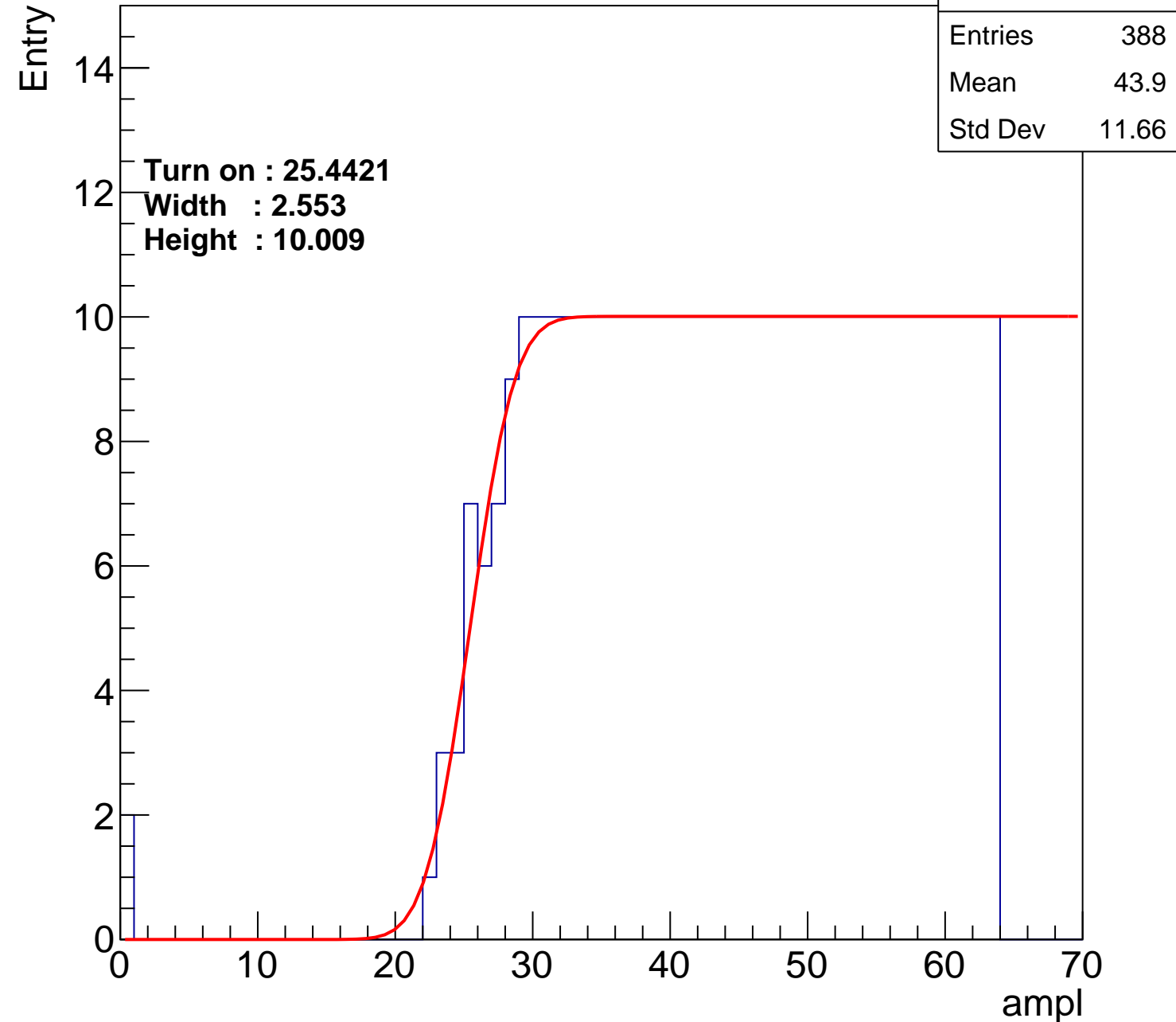
Width : 2.553

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 26.9225

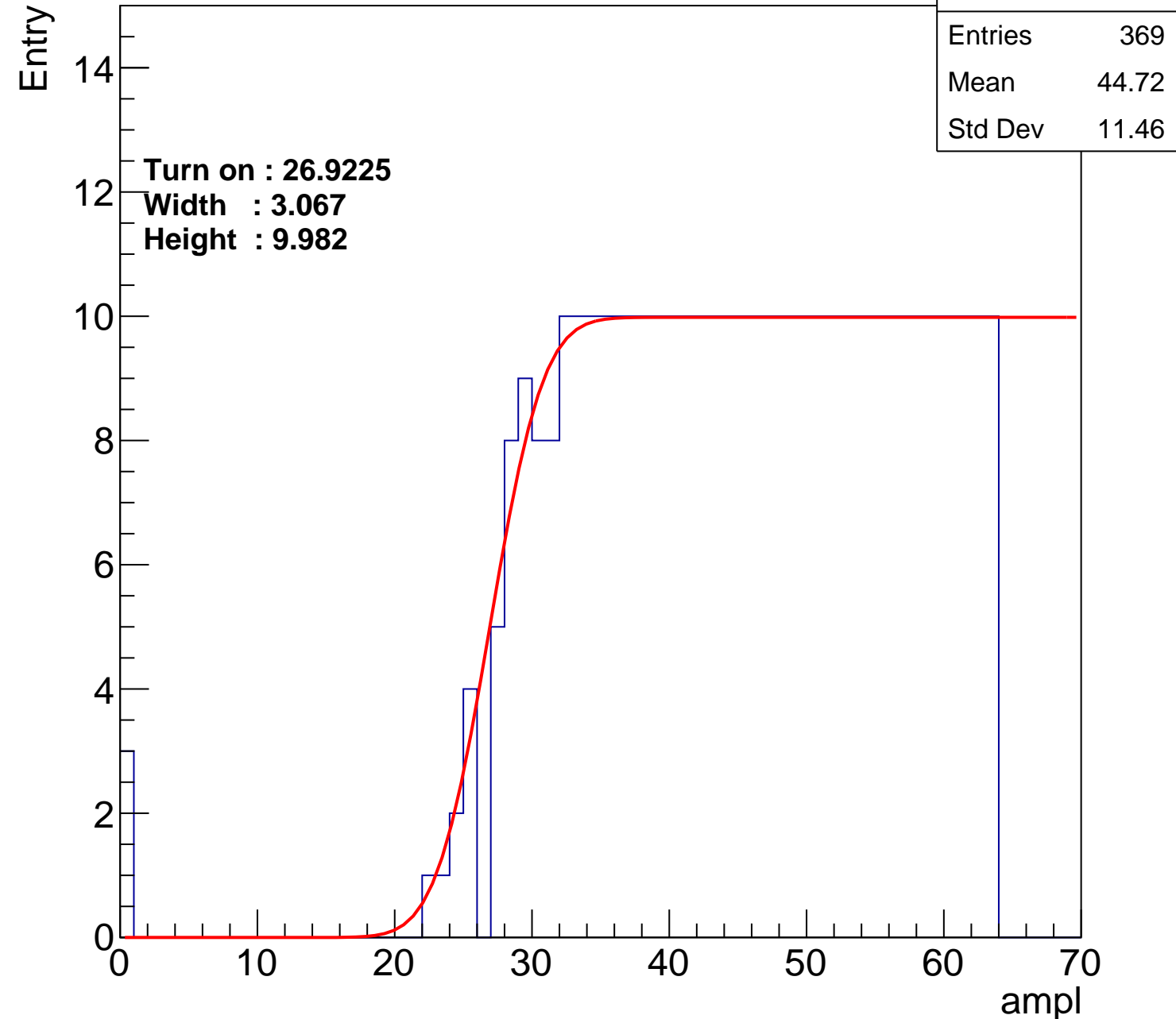
Width : 3.067

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch54

calib_packv5_042523_0143.root, FC#12, port B1

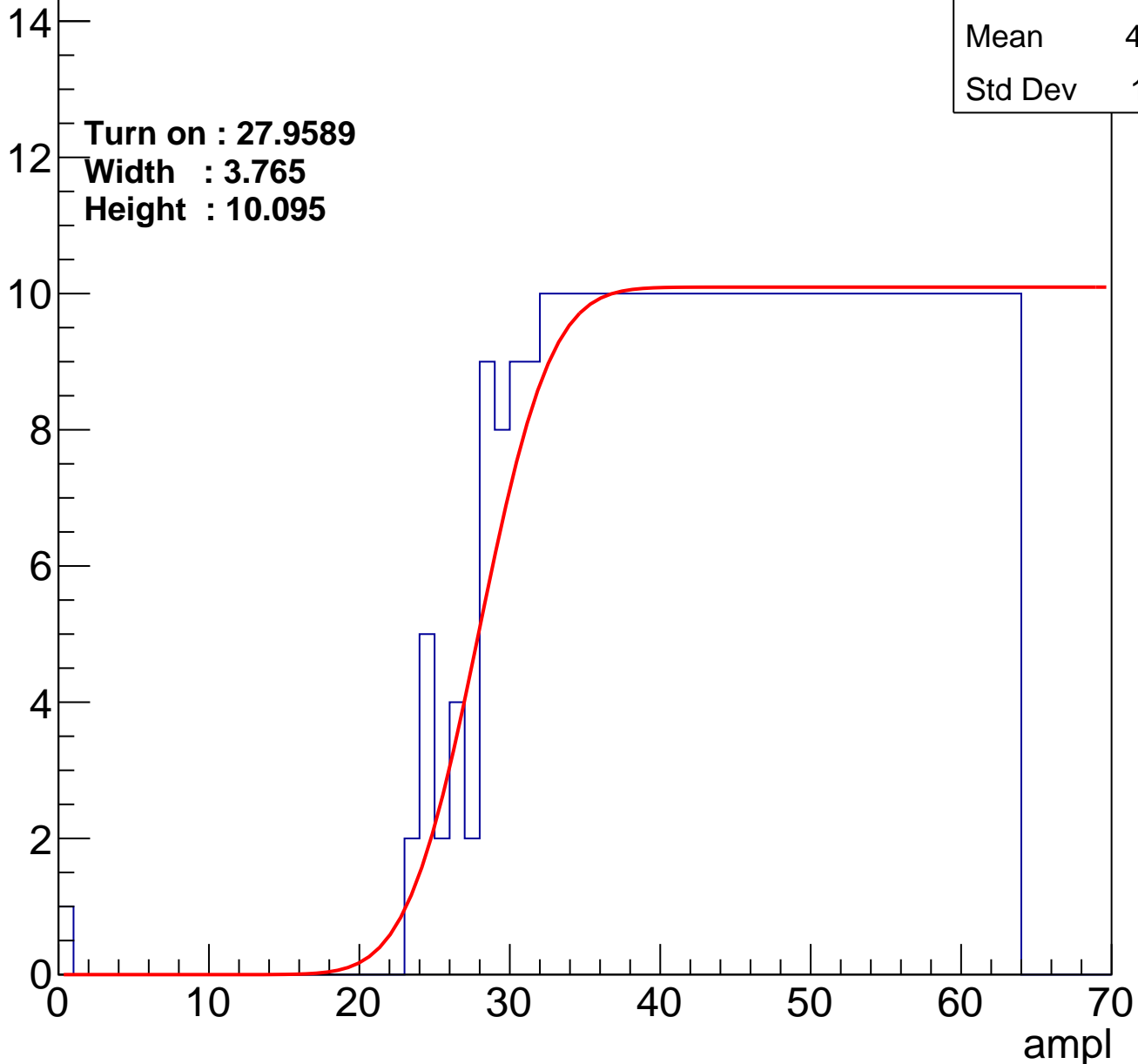
Entries	371
Mean	44.76
Std Dev	11.11

Turn on : 27.9589

Width : 3.765

Height : 10.095

Entry



B0L102S, U23-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.3757

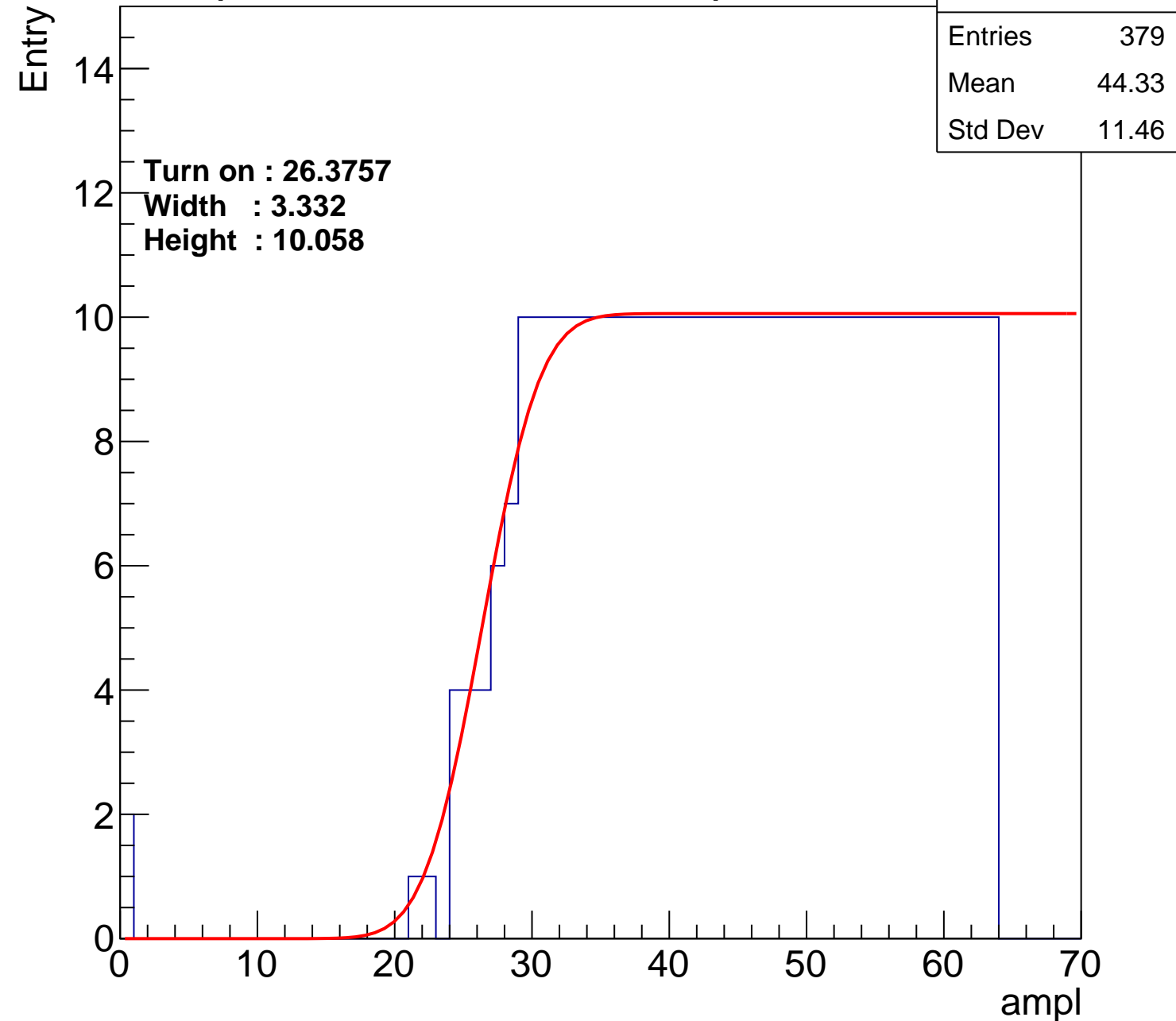
Width : 3.332

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.12
Std Dev	11.58

Turn on : 26.2771

Width : 2.912

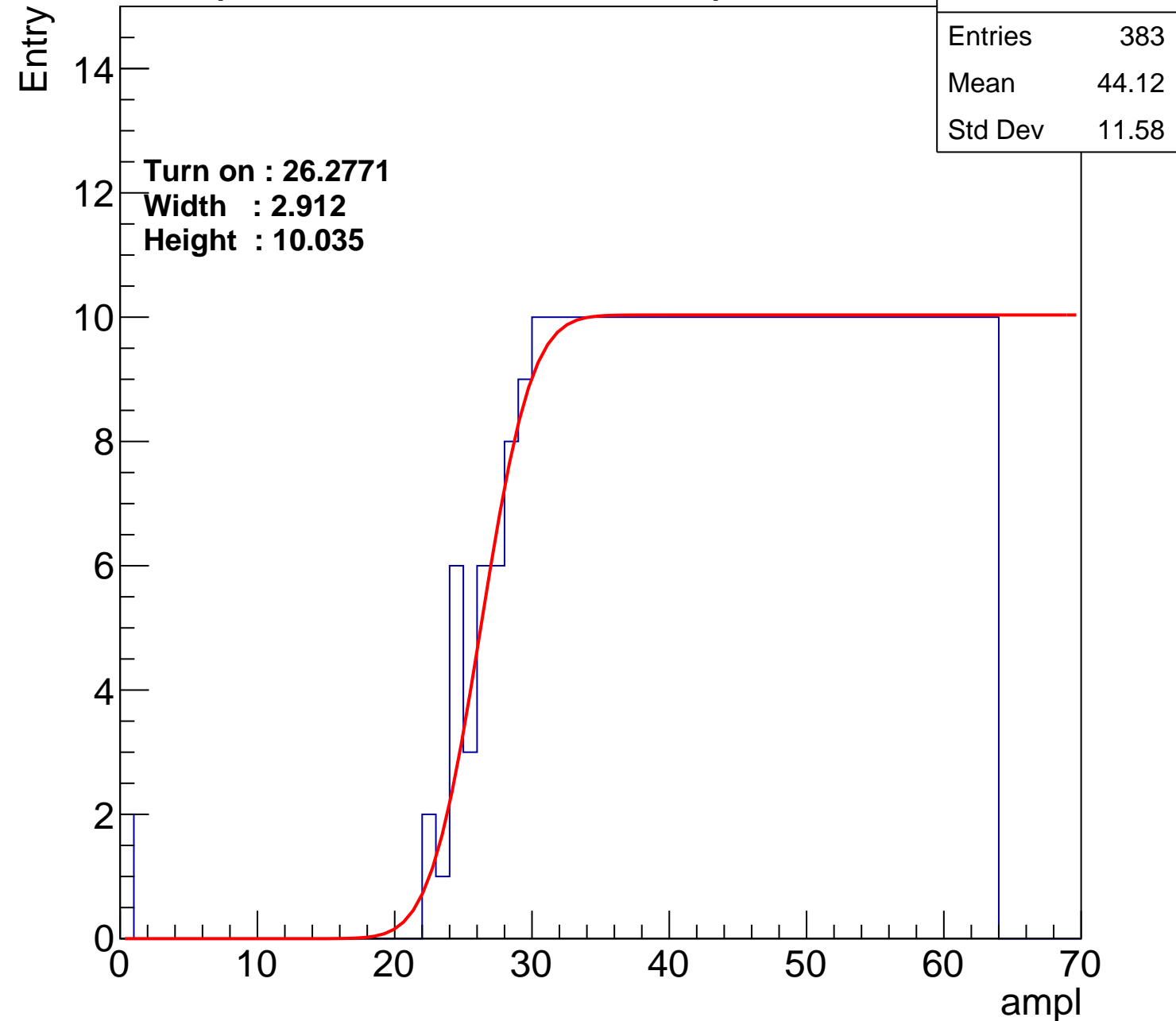
Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U23-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 27.3848

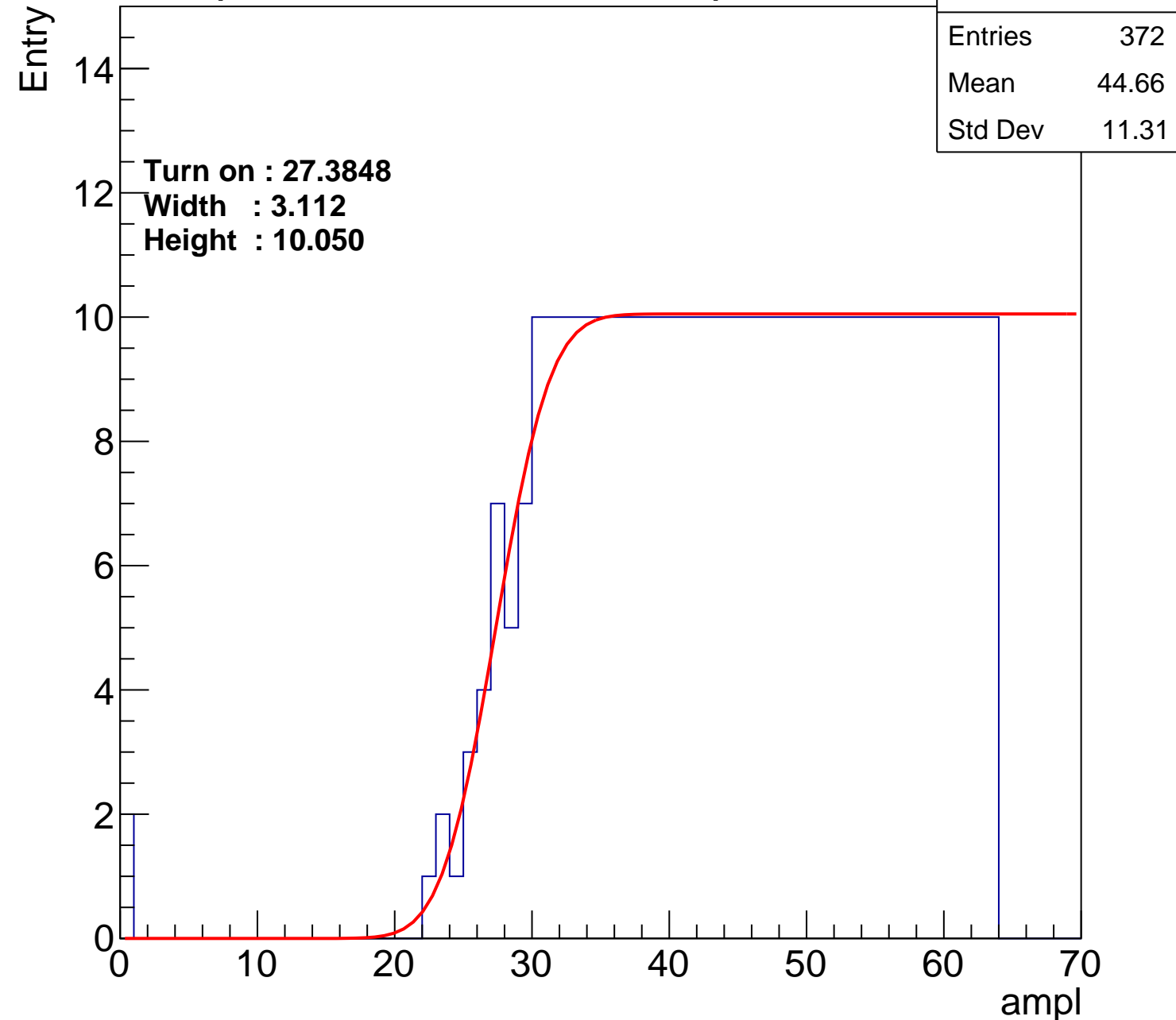
Width : 3.112

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.42
Std Dev	11.29

Turn on : 26.9584

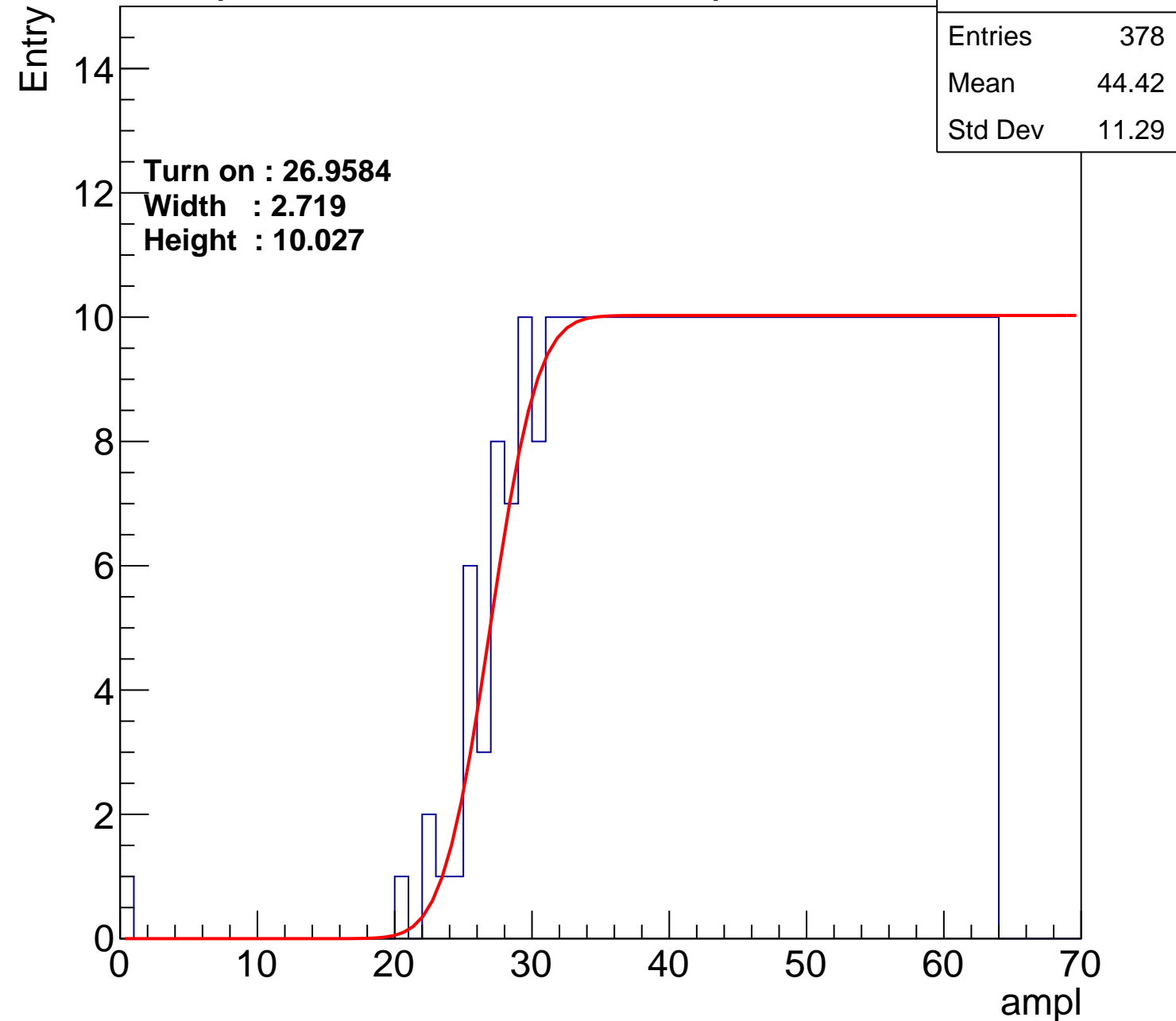
Width : 2.719

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch59

calib_packv5_042523_0143.root, FC#12, port B1

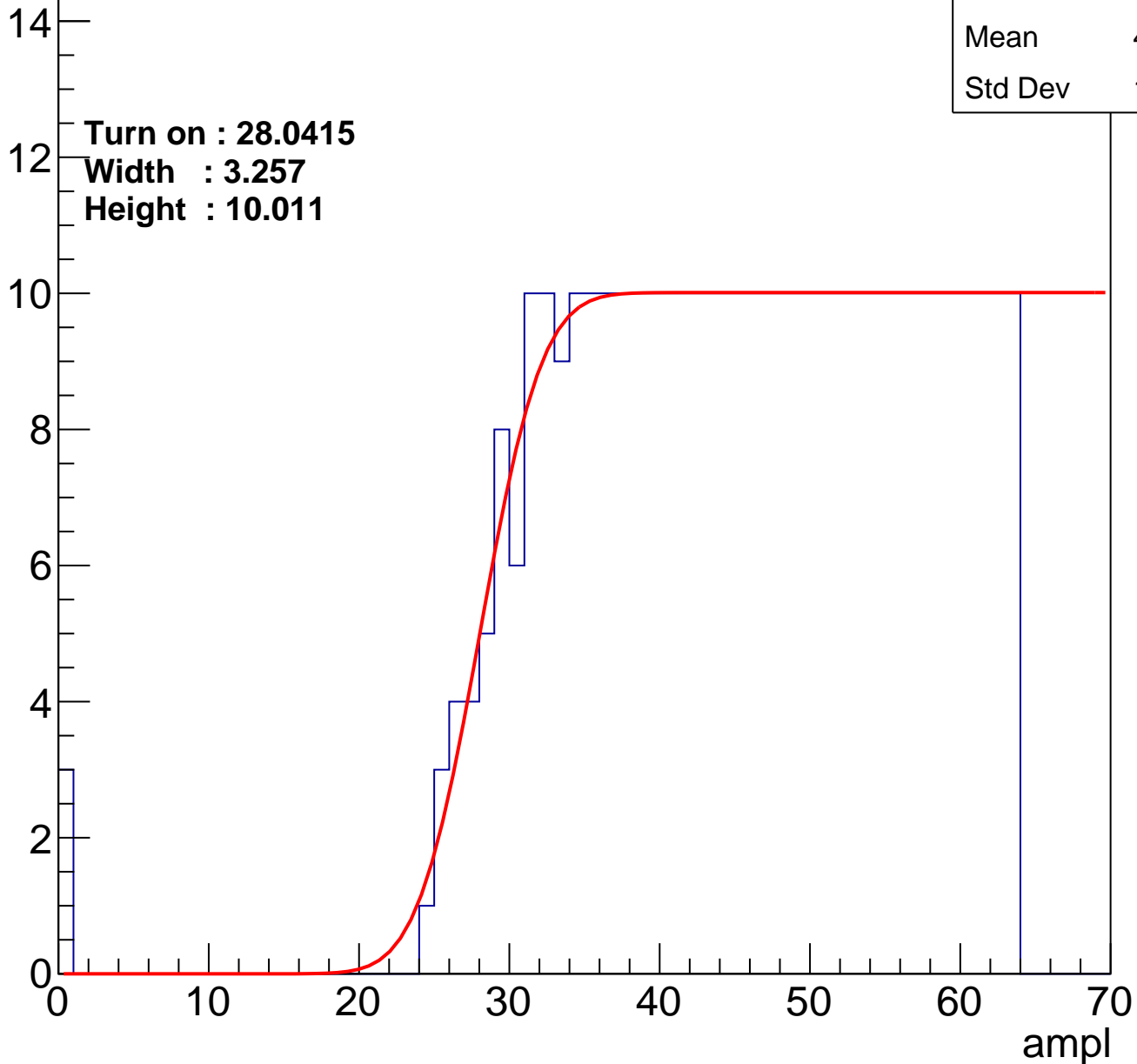
Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 28.0415

Width : 3.257

Height : 10.011

Entry



B0L102S, U23-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.0943

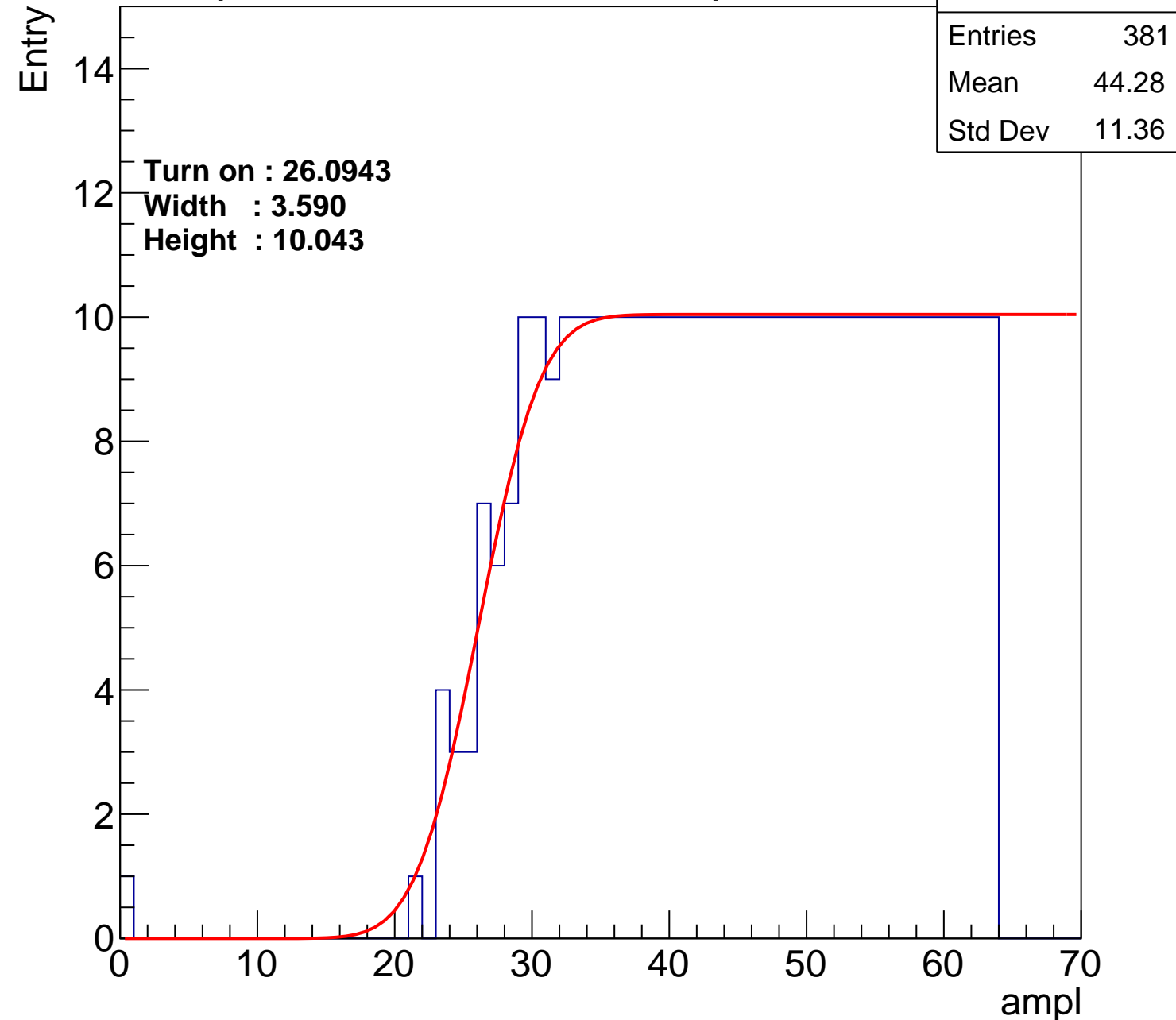
Width : 3.590

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.4426

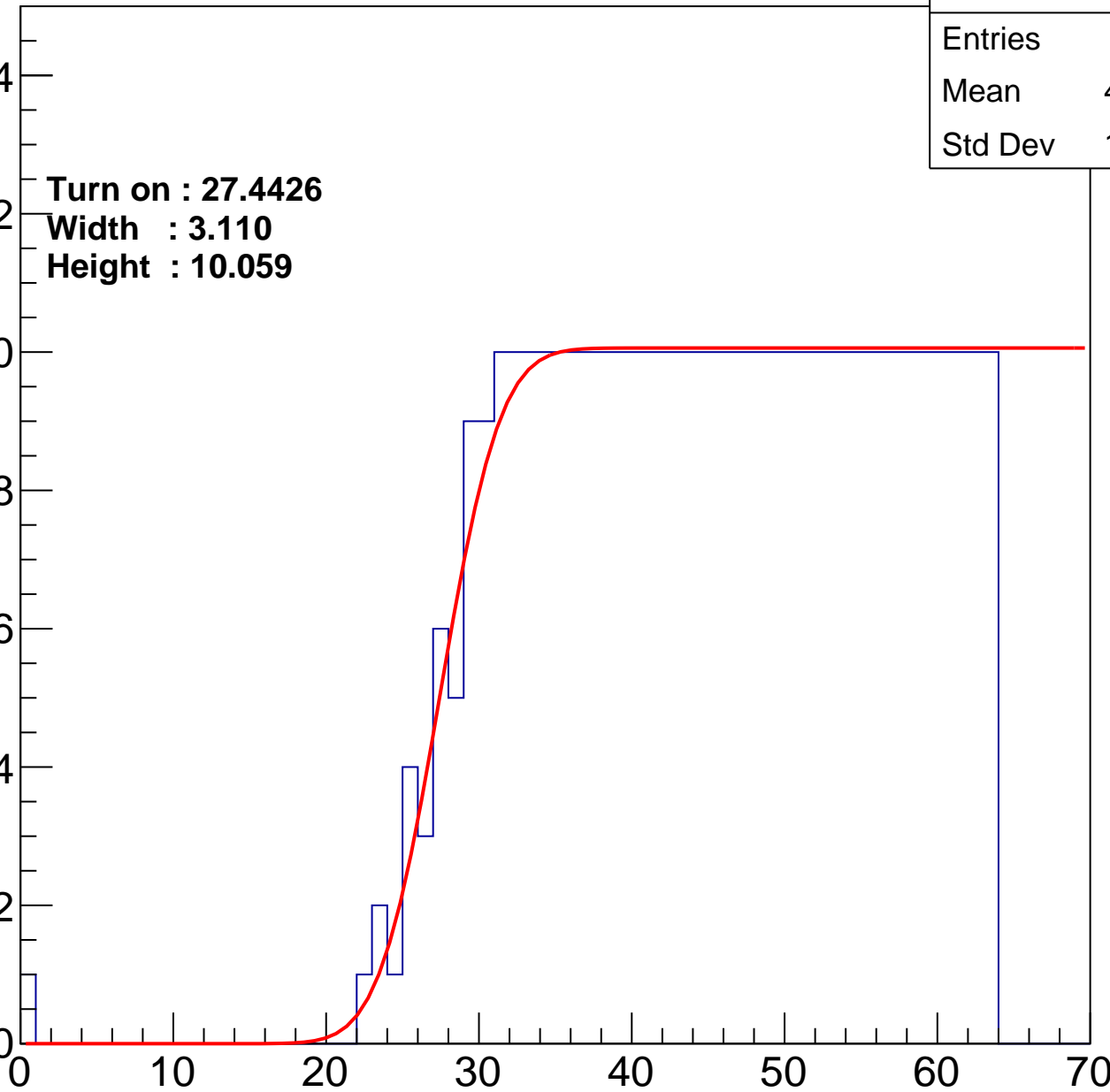
Width : 3.110

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.95
Std Dev	11.94

Turn on : 25.7125

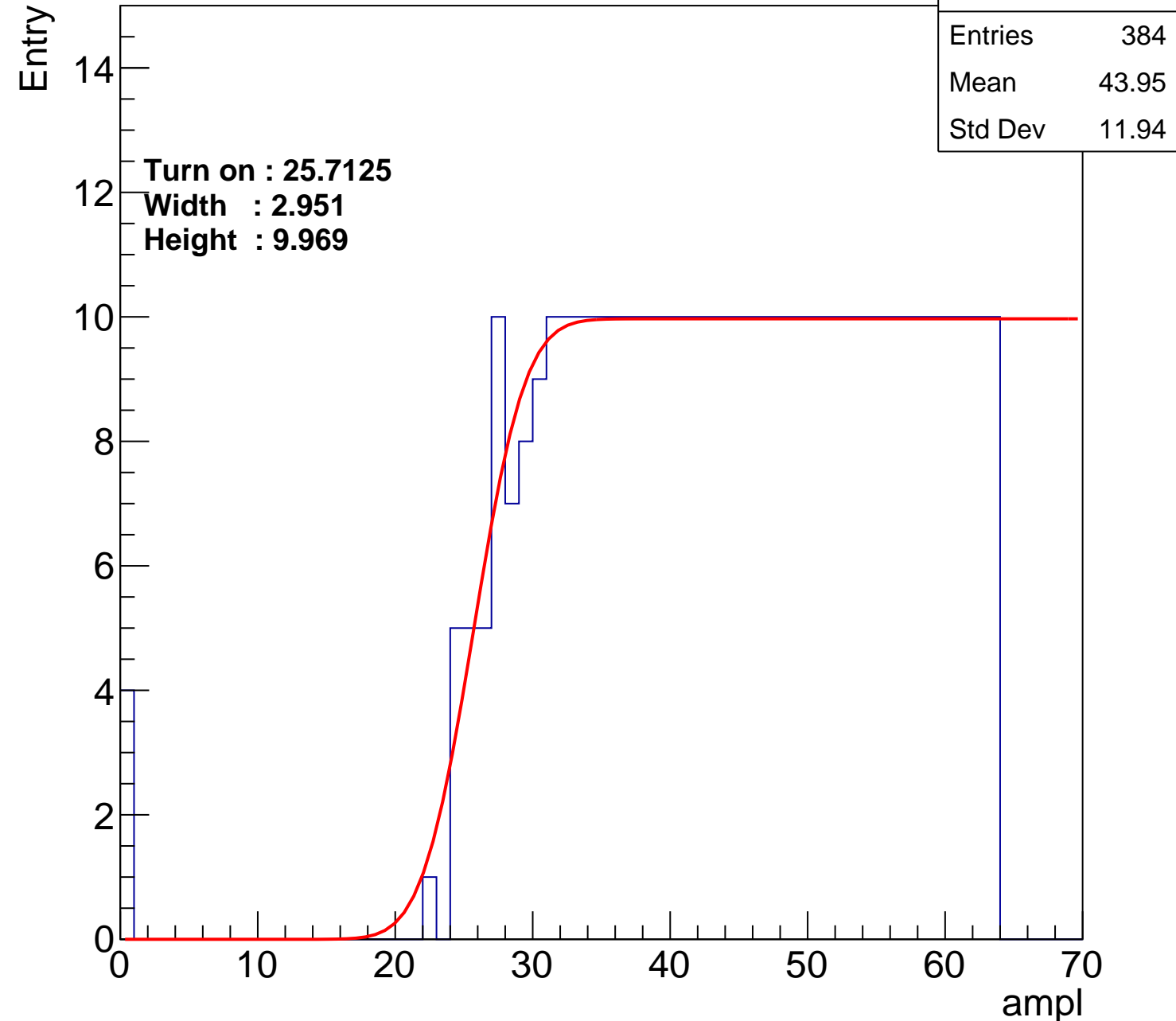
Width : 2.951

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.19
Std Dev	11.19

Turn on : 28.7269

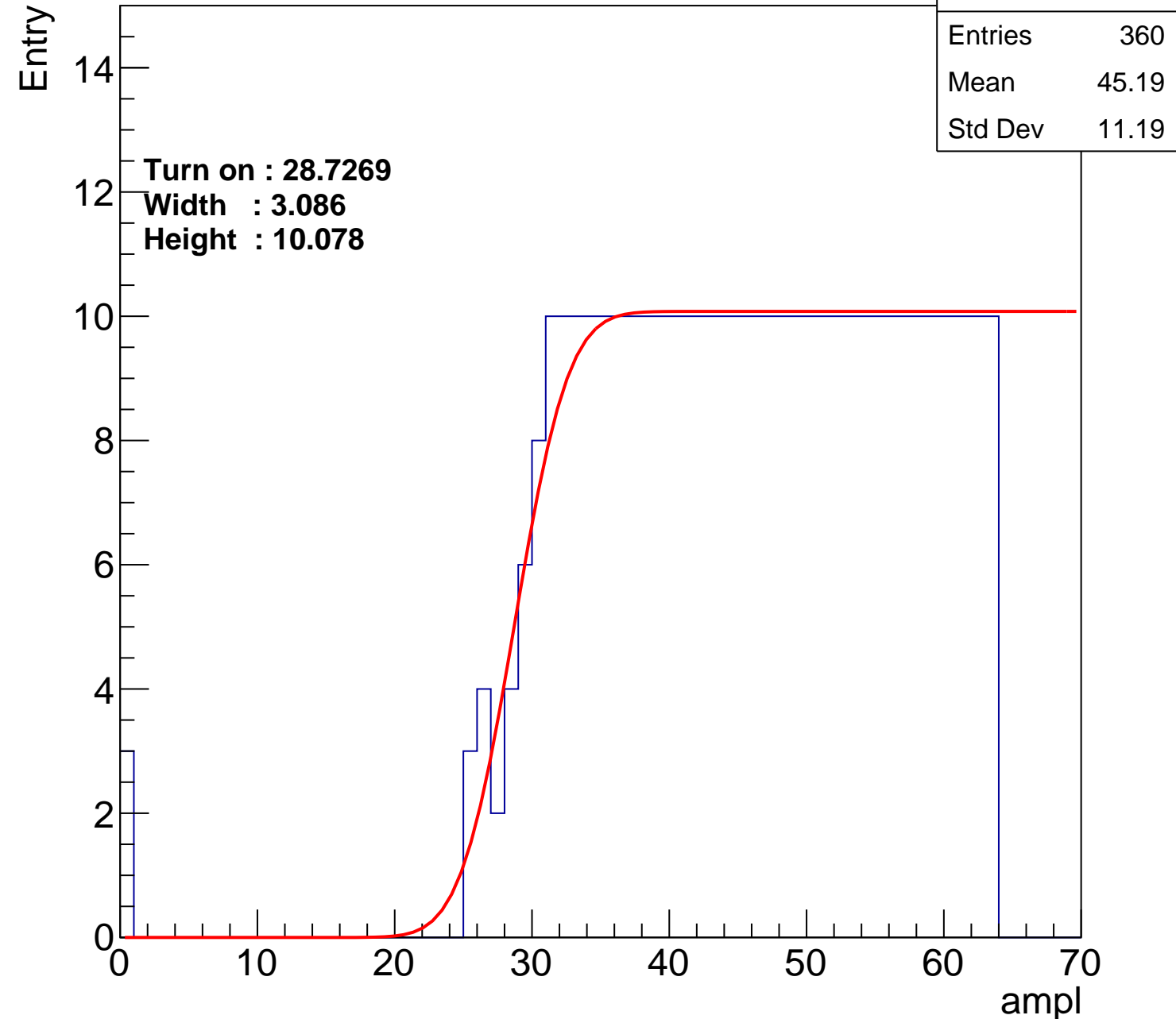
Width : 3.086

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.92
Std Dev	11.71

Turn on : 26.7266

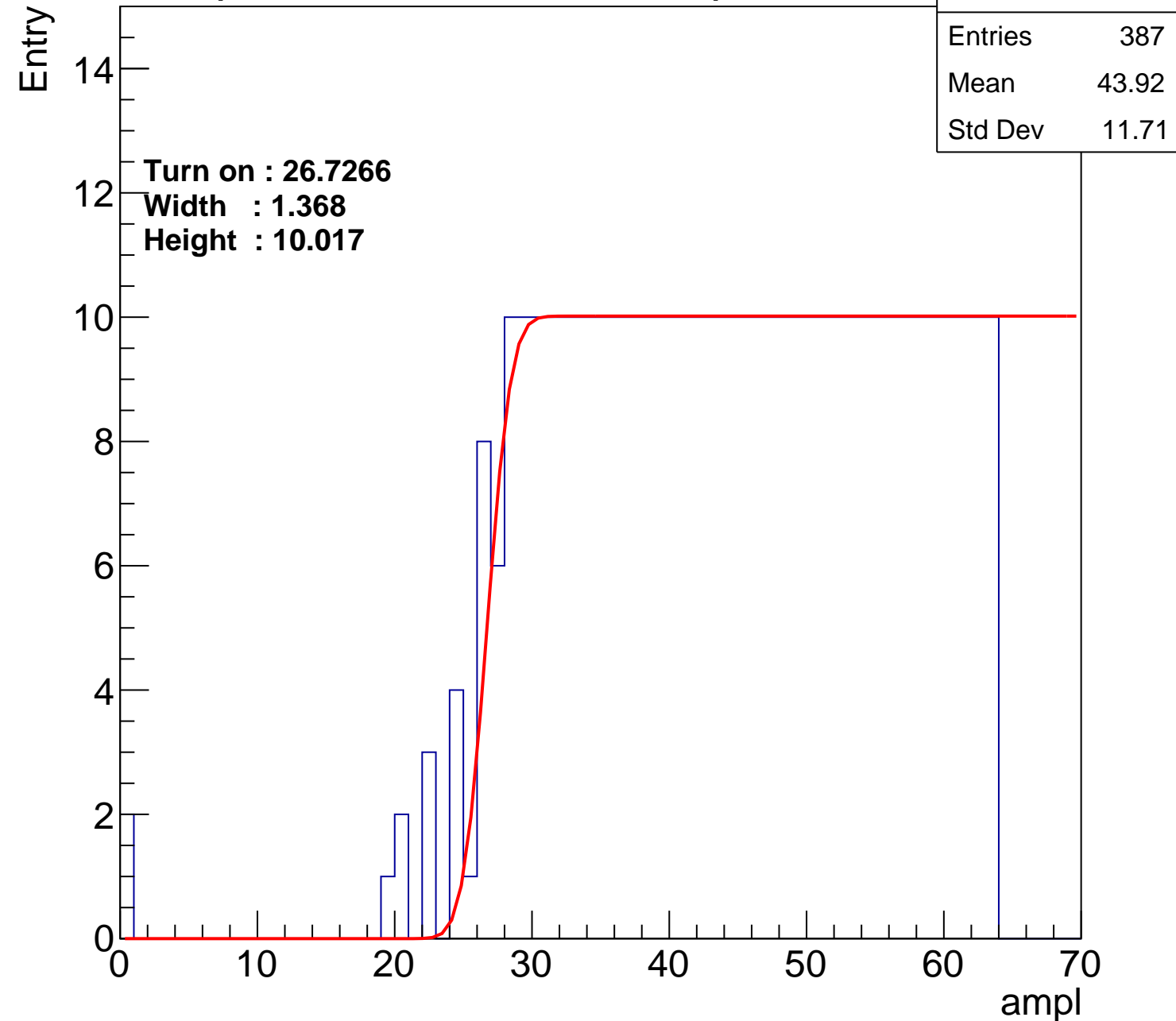
Width : 1.368

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.3565

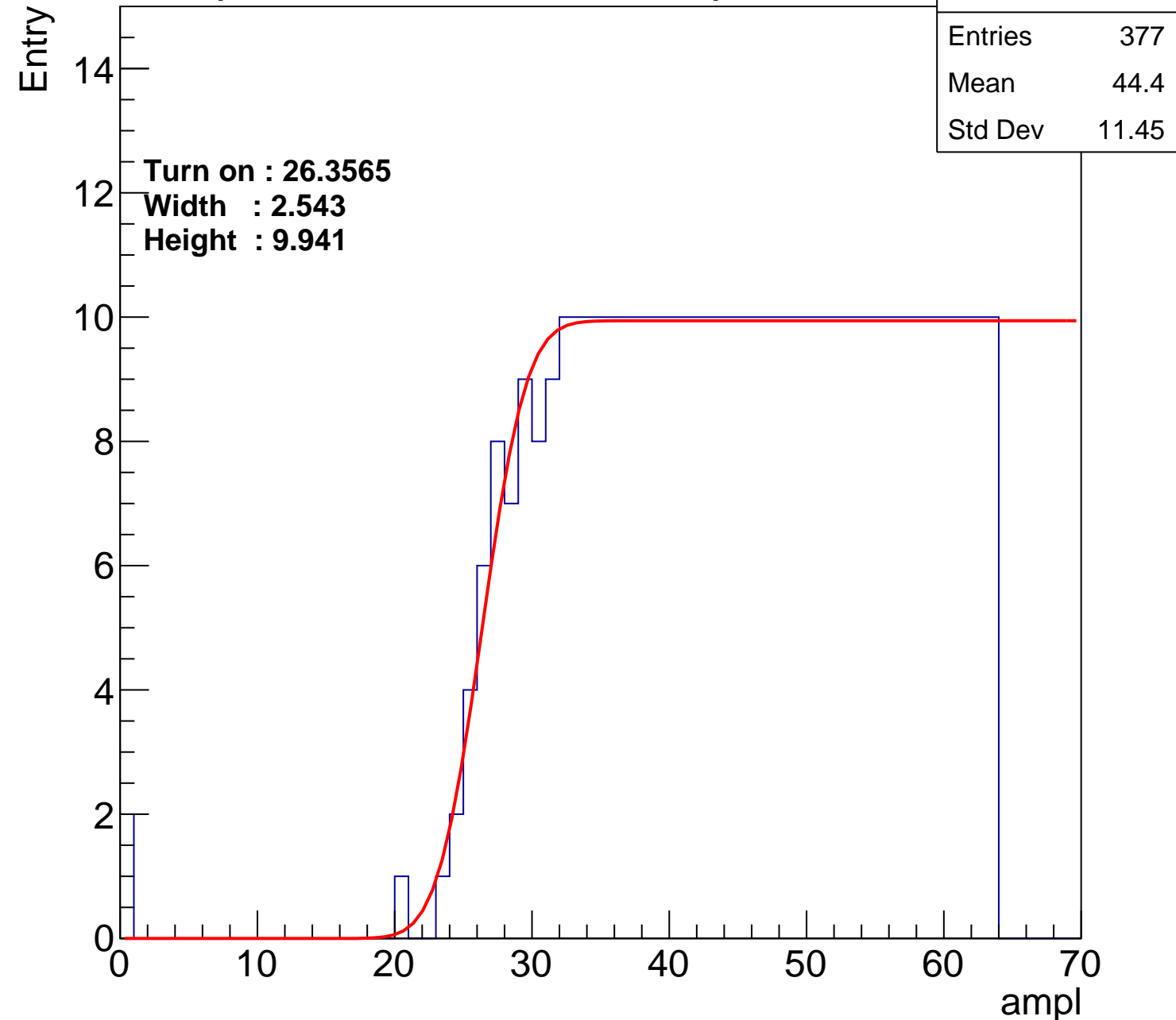
Width : 2.543

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch66

calib_packv5_042523_0143.root, FC#12, port B1

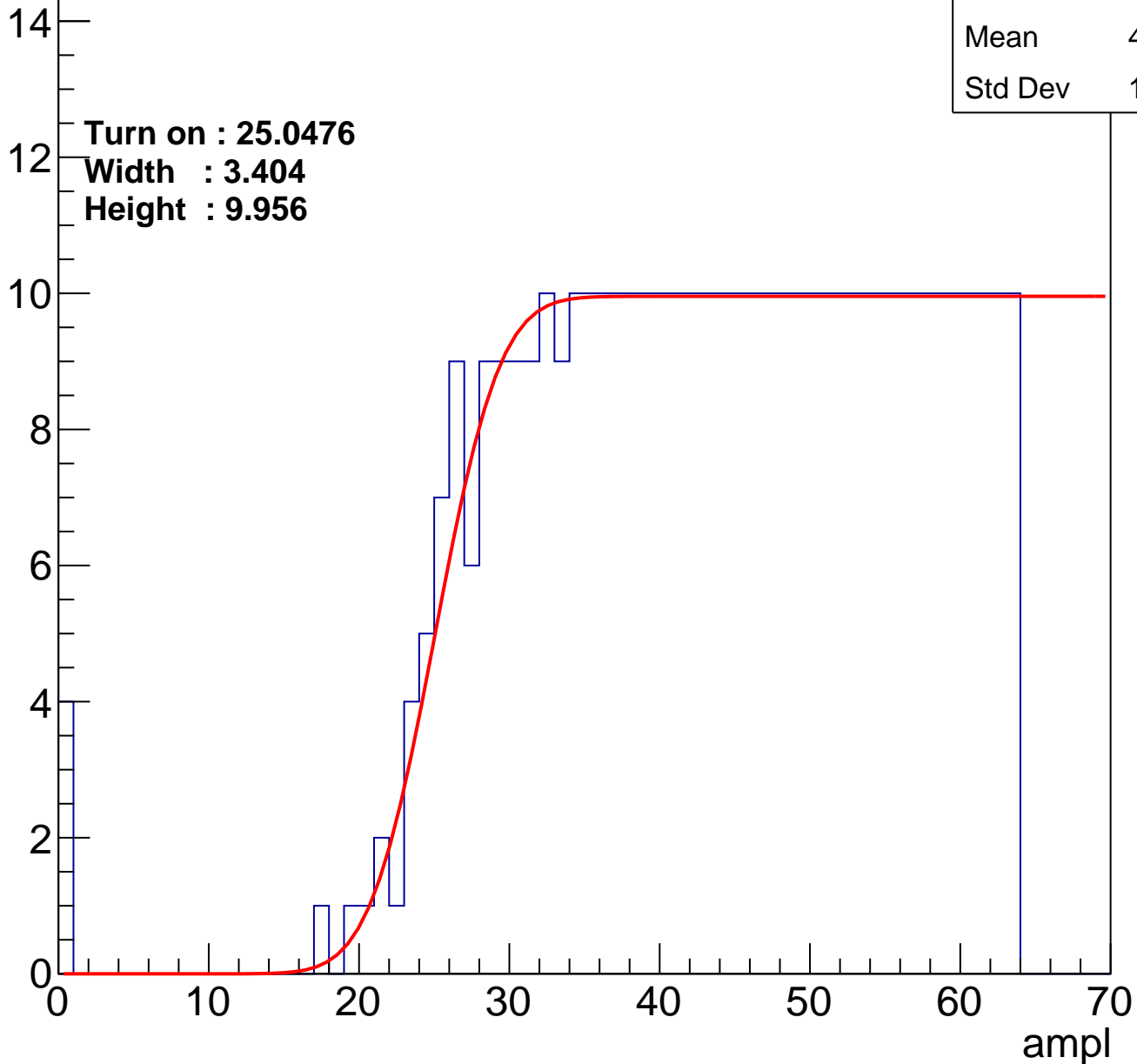
Entries	396
Mean	43.26
Std Dev	12.37

Turn on : 25.0476

Width : 3.404

Height : 9.956

Entry



B0L102S, U23-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.07
Std Dev	10.92

Turn on : 26.9146

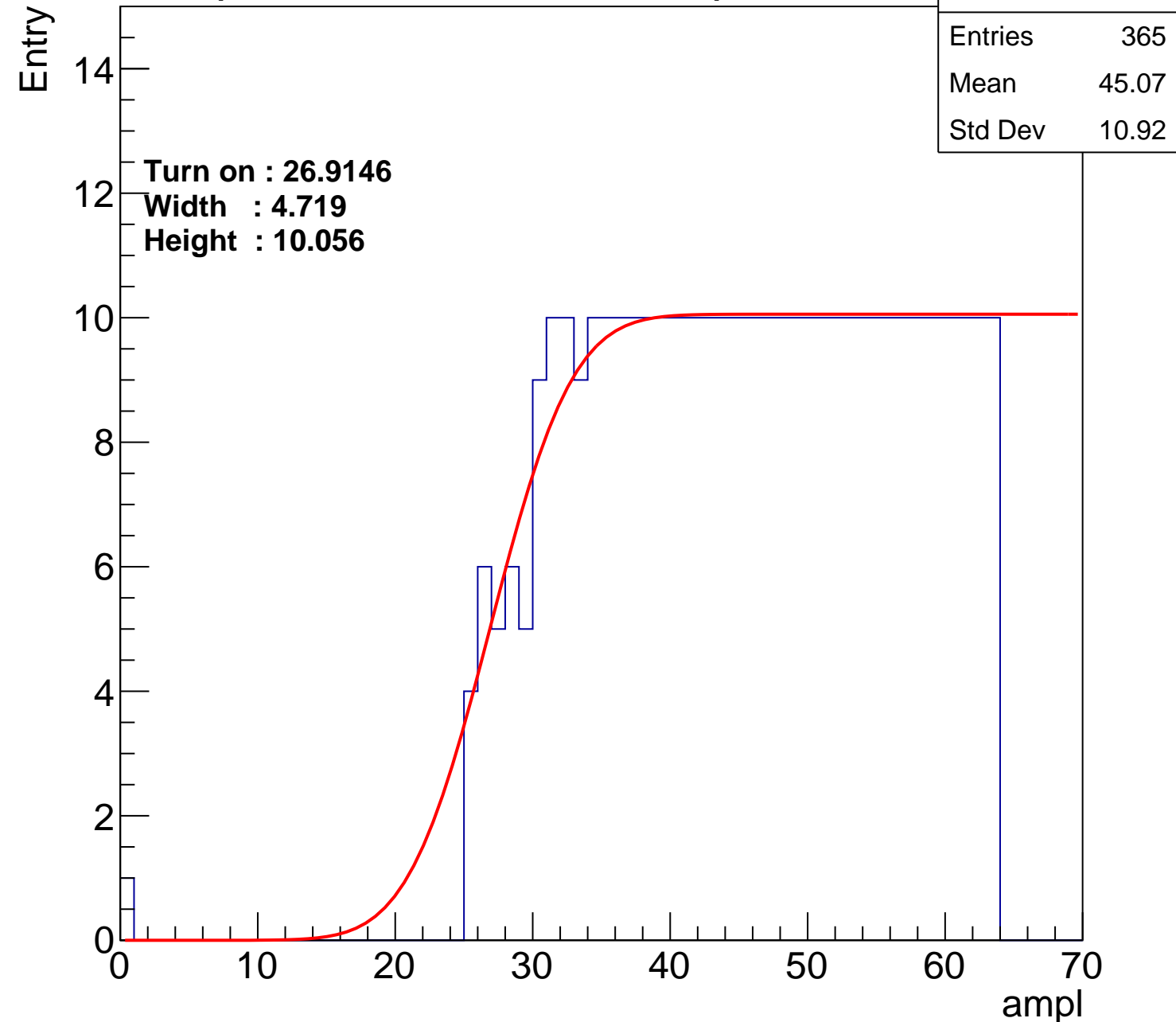
Width : 4.719

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.63
Std Dev	12.24

Turn on : 25.8622

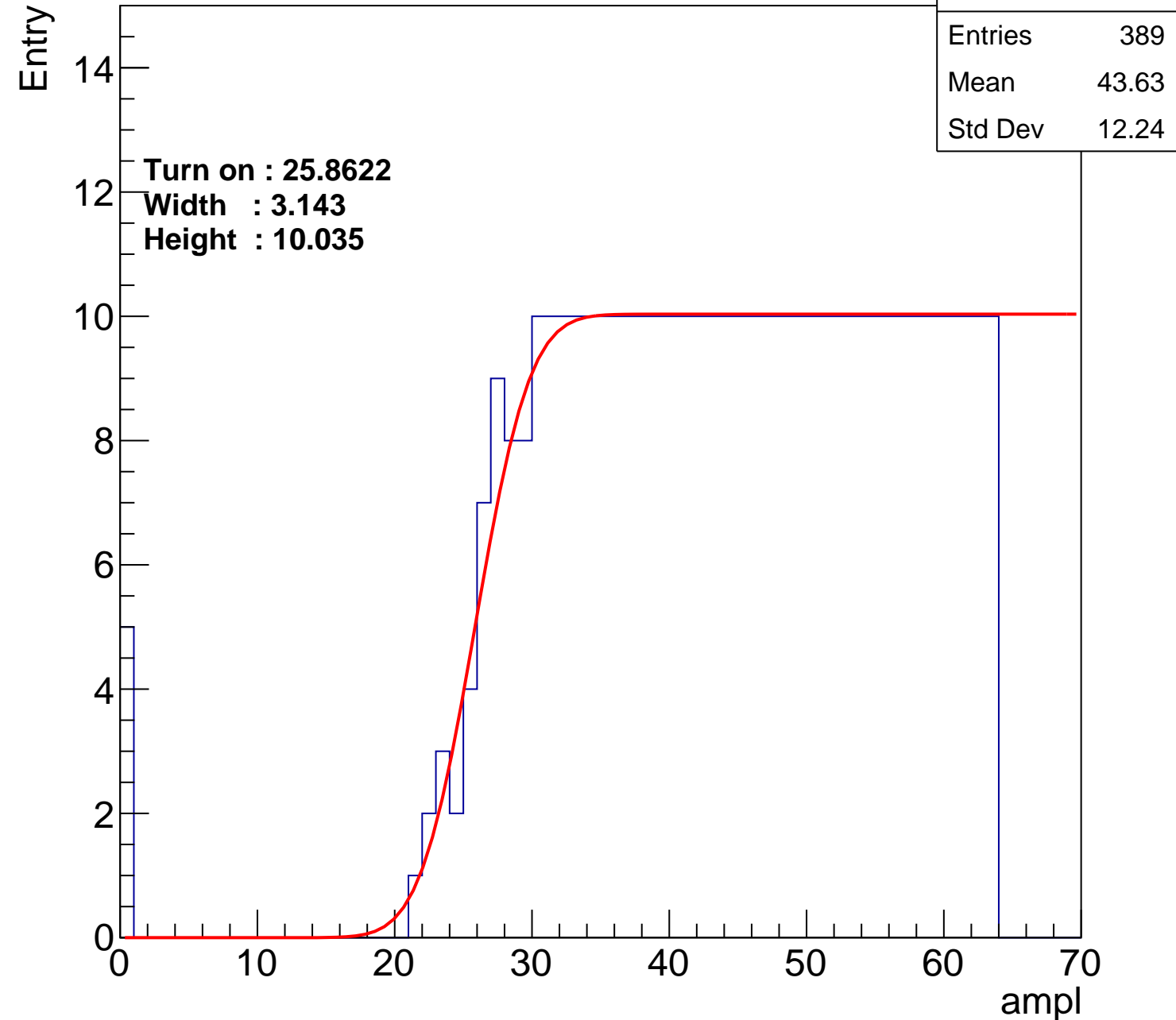
Width : 3.143

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.3389

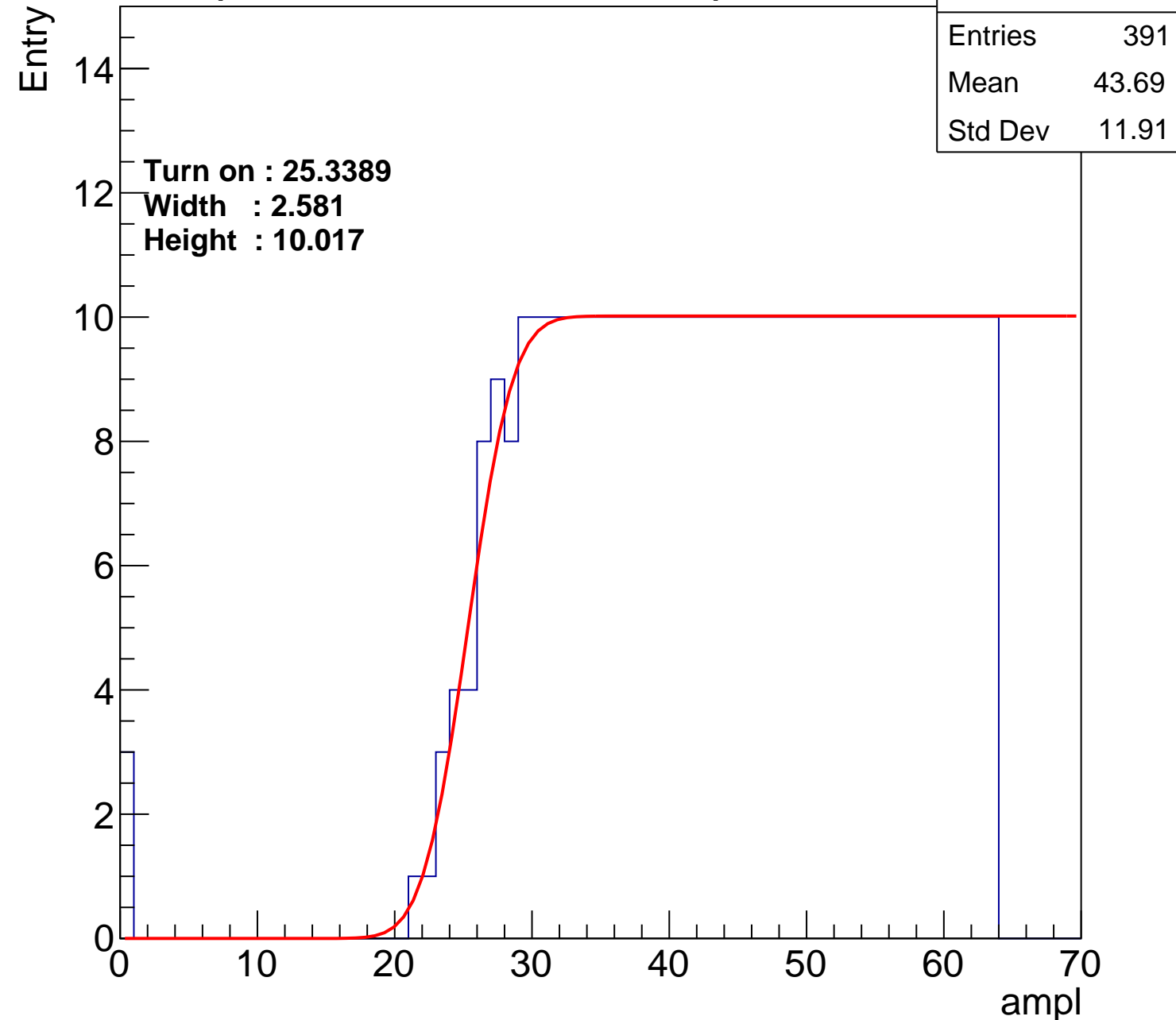
Width : 2.581

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 26.9129

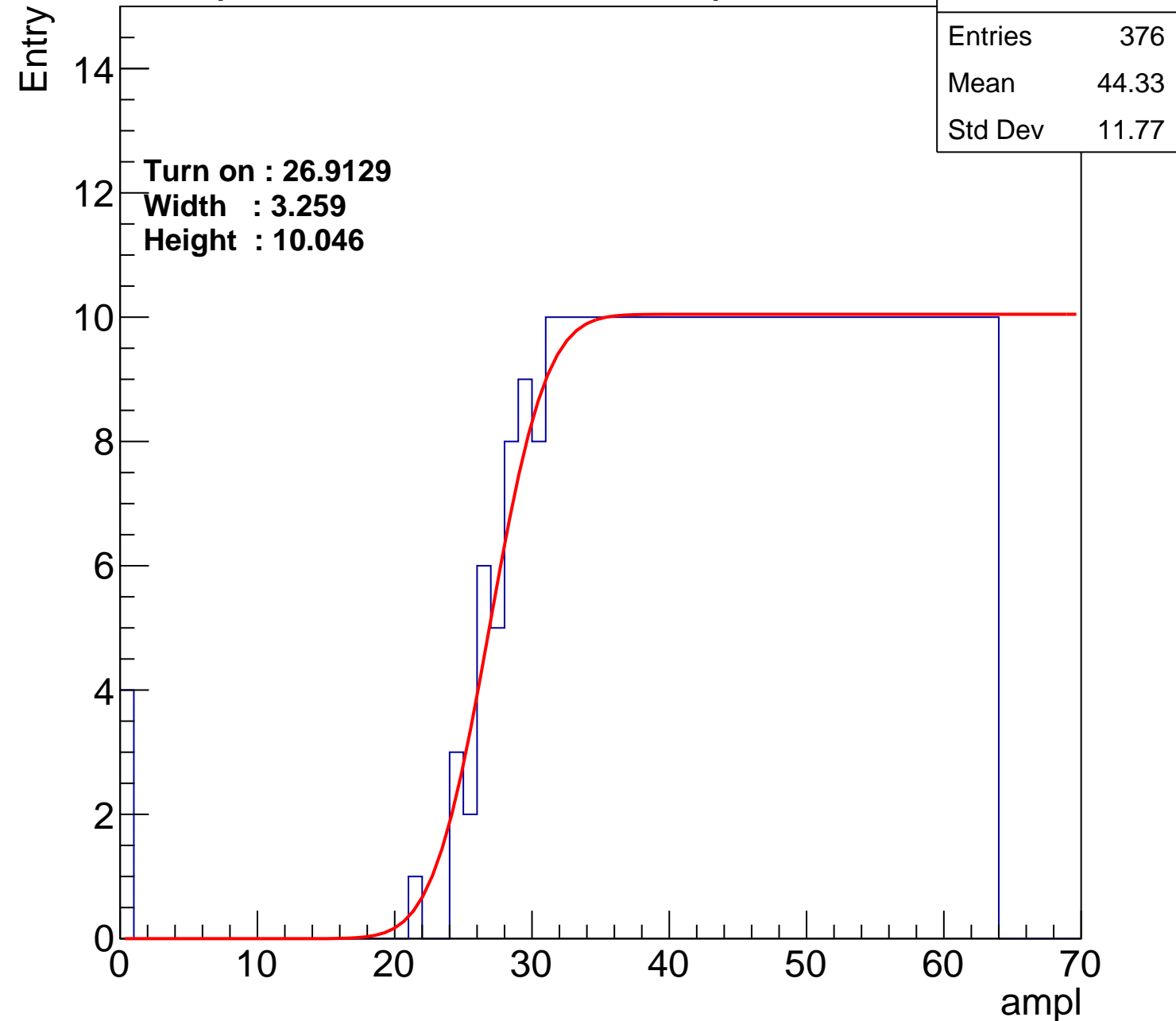
Width : 3.259

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.27
Std Dev	10.97

Turn on : 28.3168

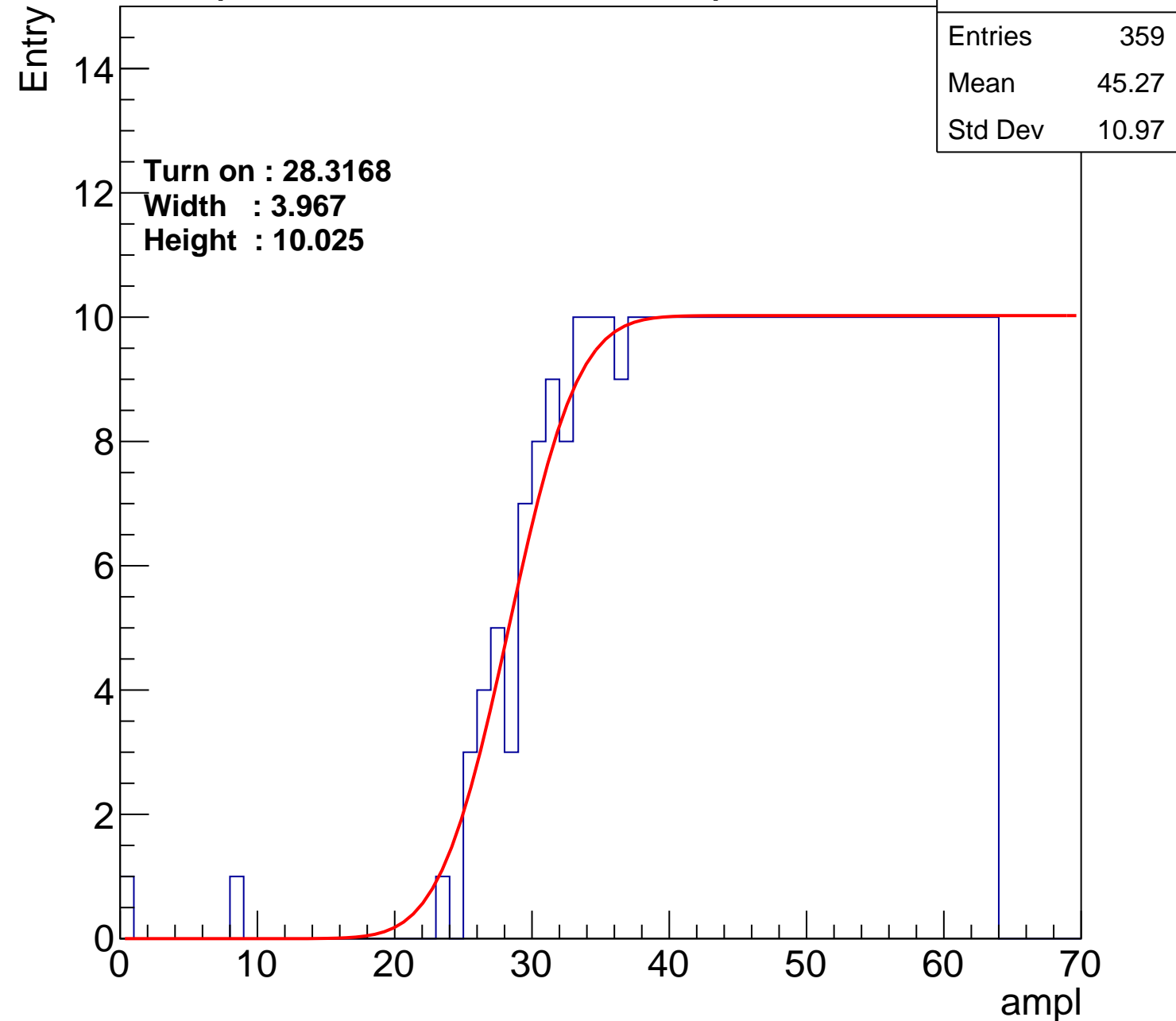
Width : 3.967

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	43.06
Std Dev	12.28

Turn on : 23.7187

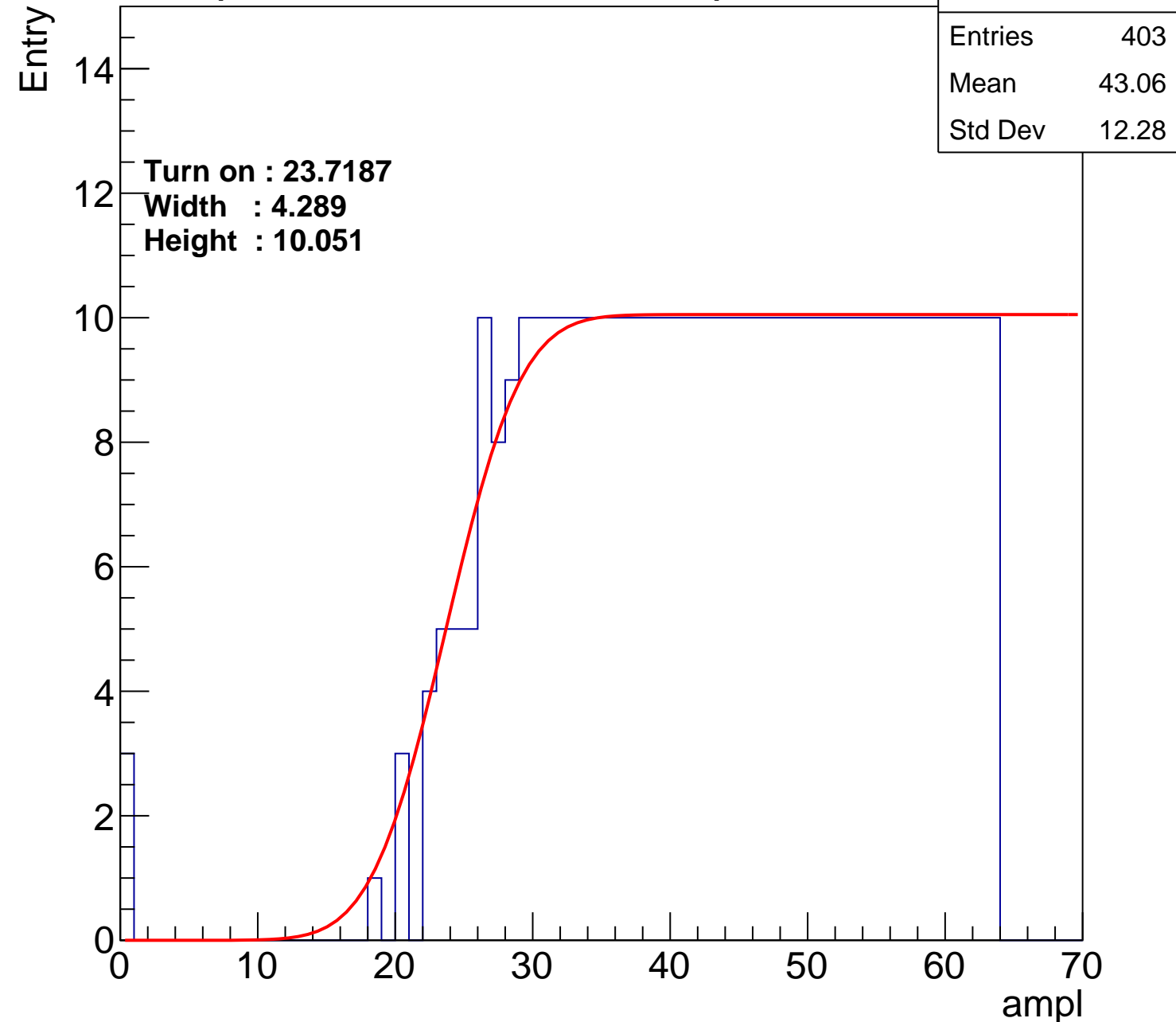
Width : 4.289

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.37
Std Dev	11.64

Turn on : 27.3228

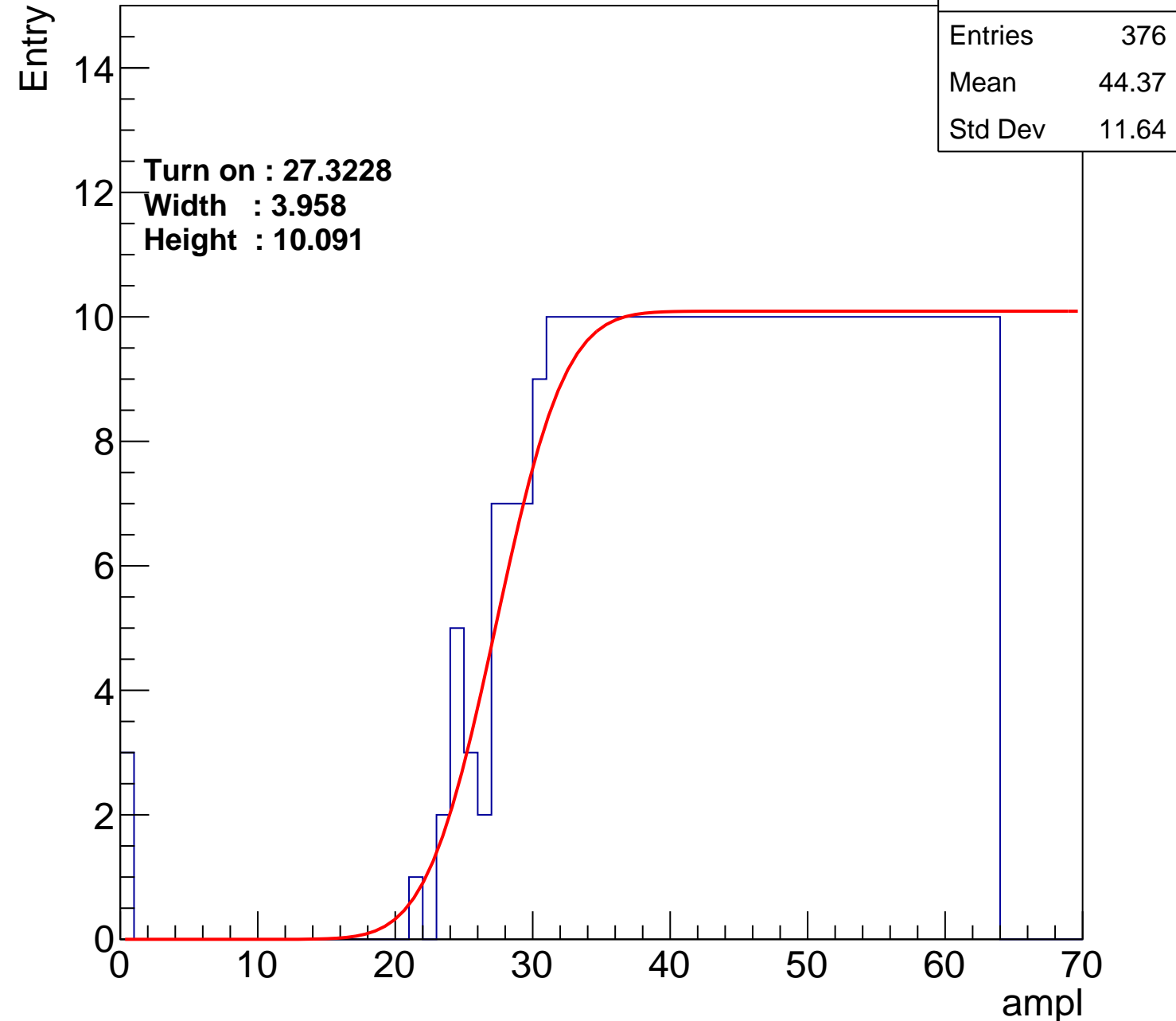
Width : 3.958

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch74

calib_packv5_042523_0143.root, FC#12, port B1

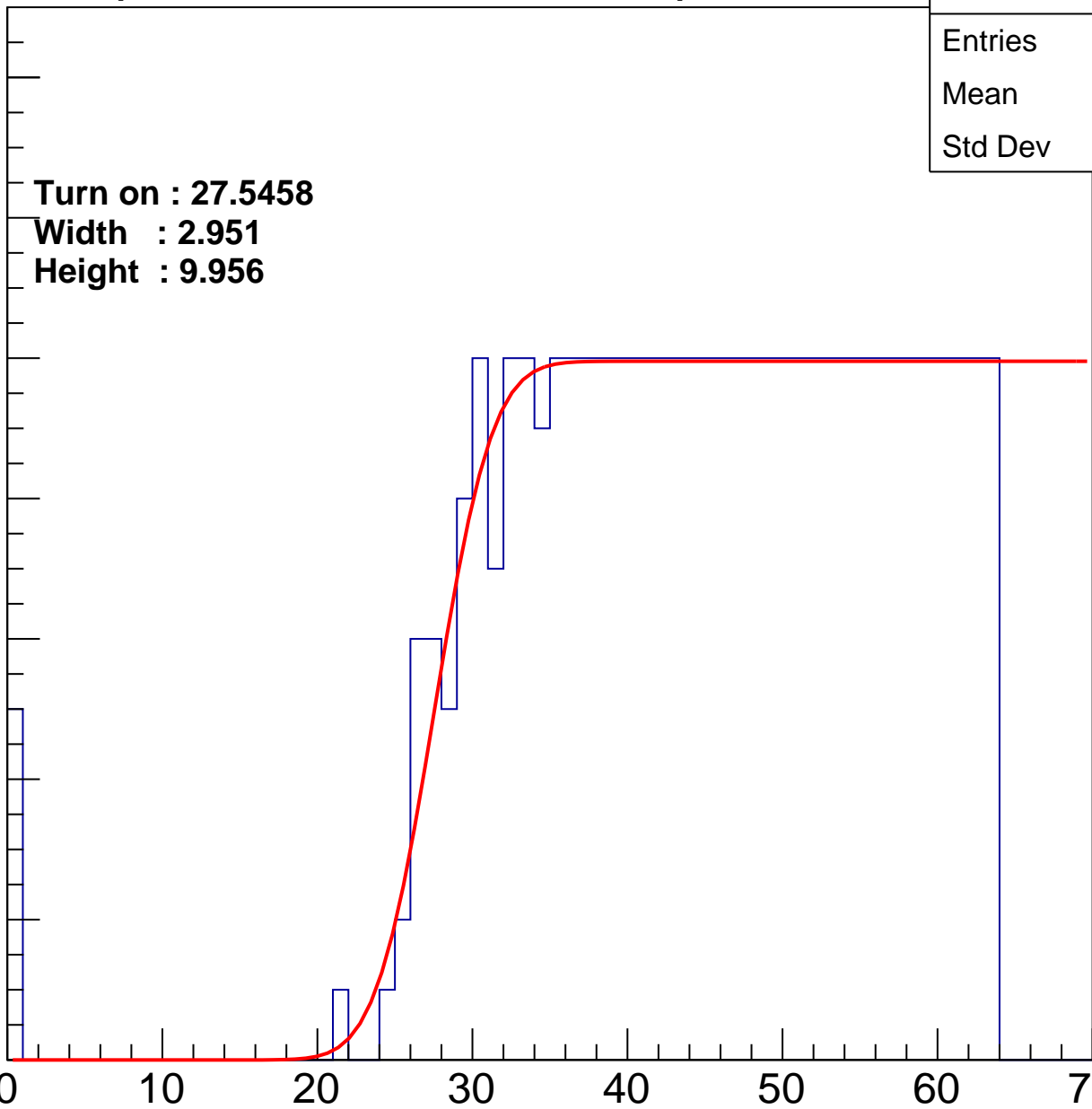
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5458
Width : 2.951
Height : 9.956

Entries	370
Mean	44.51
Std Dev	11.88

ampl



B0L102S, U23-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.46
Std Dev	11.57

Turn on : 26.9741

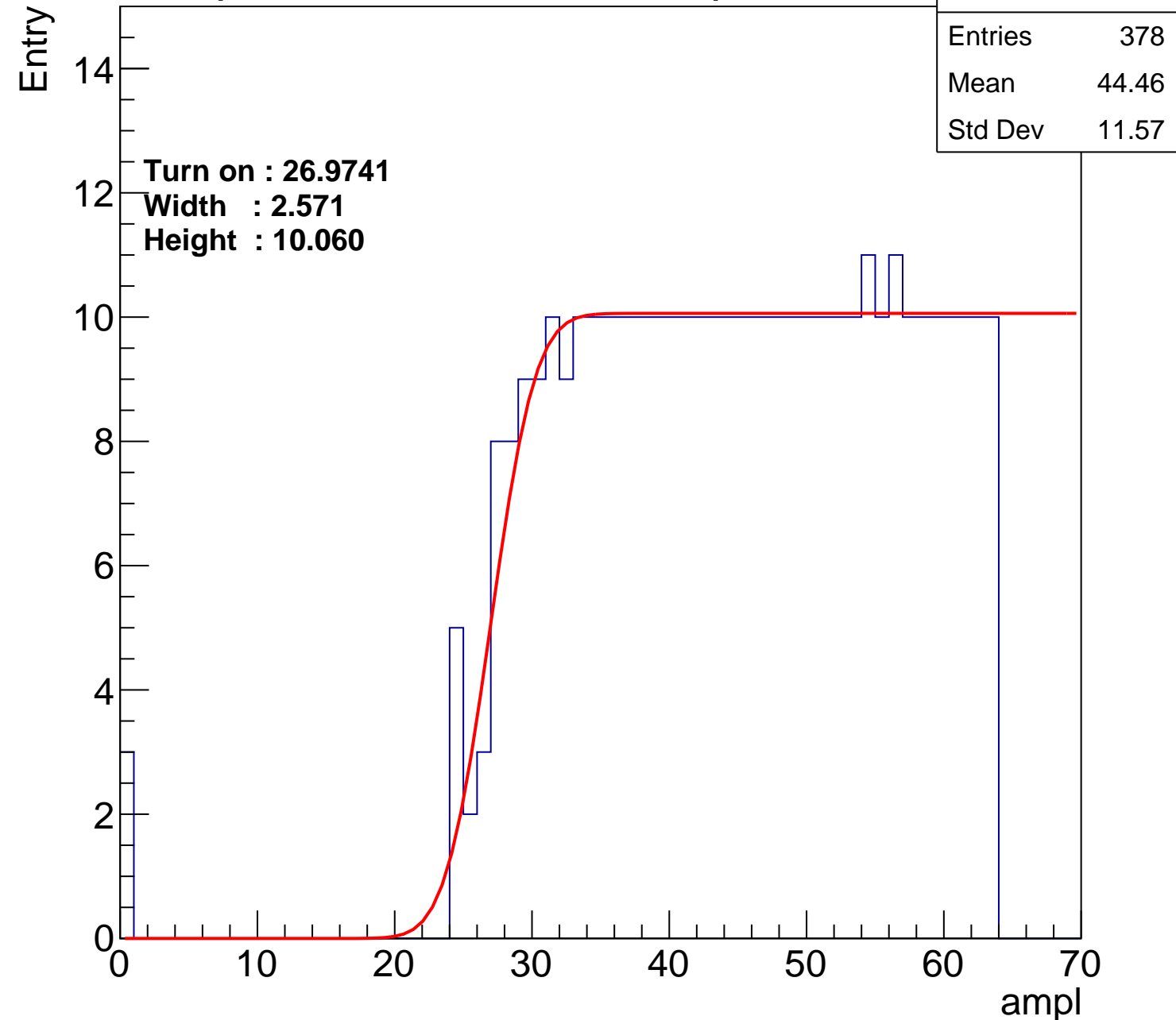
Width : 2.571

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 27.6237

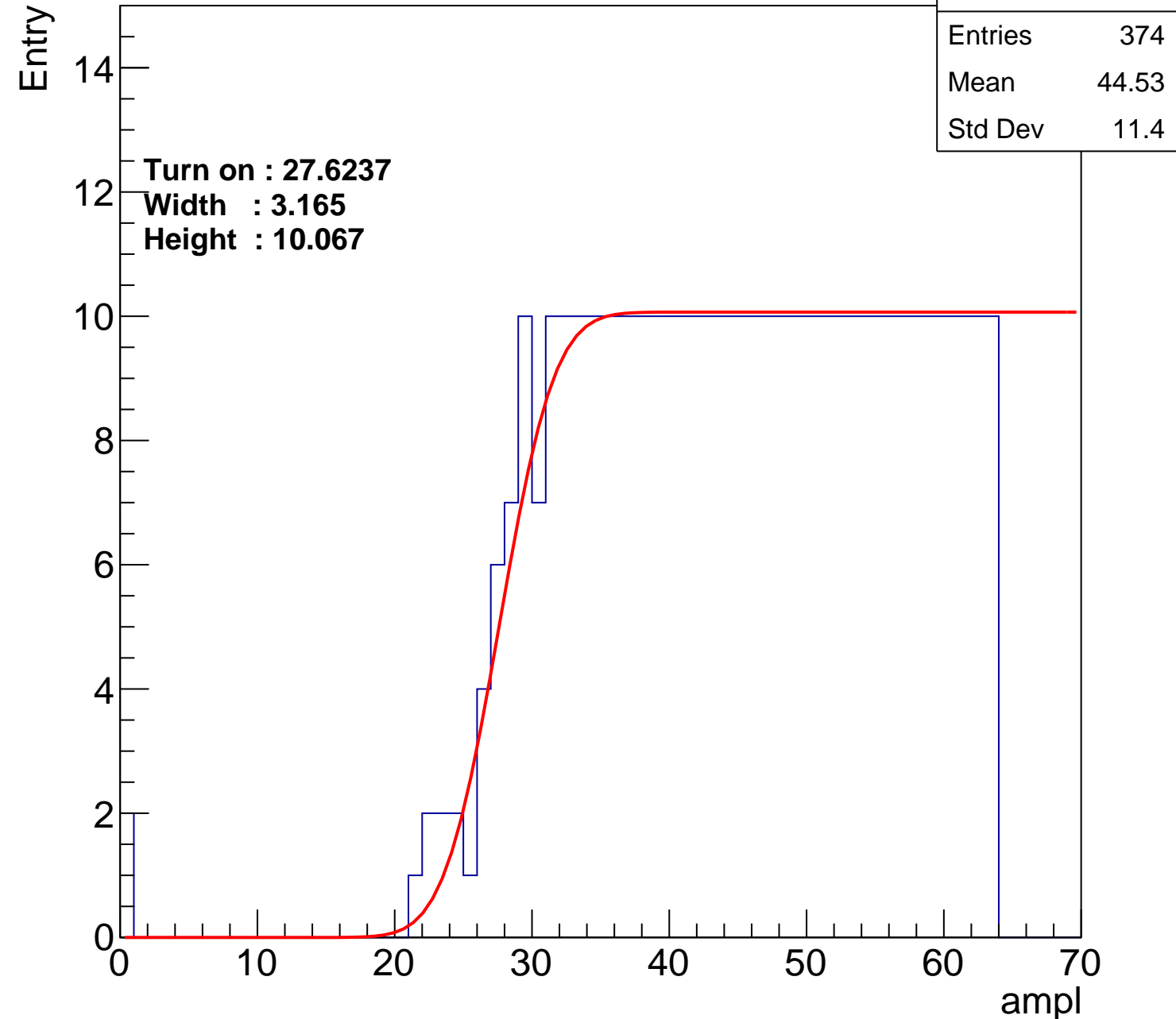
Width : 3.165

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.22
Std Dev	11.84

Turn on : 26.2964

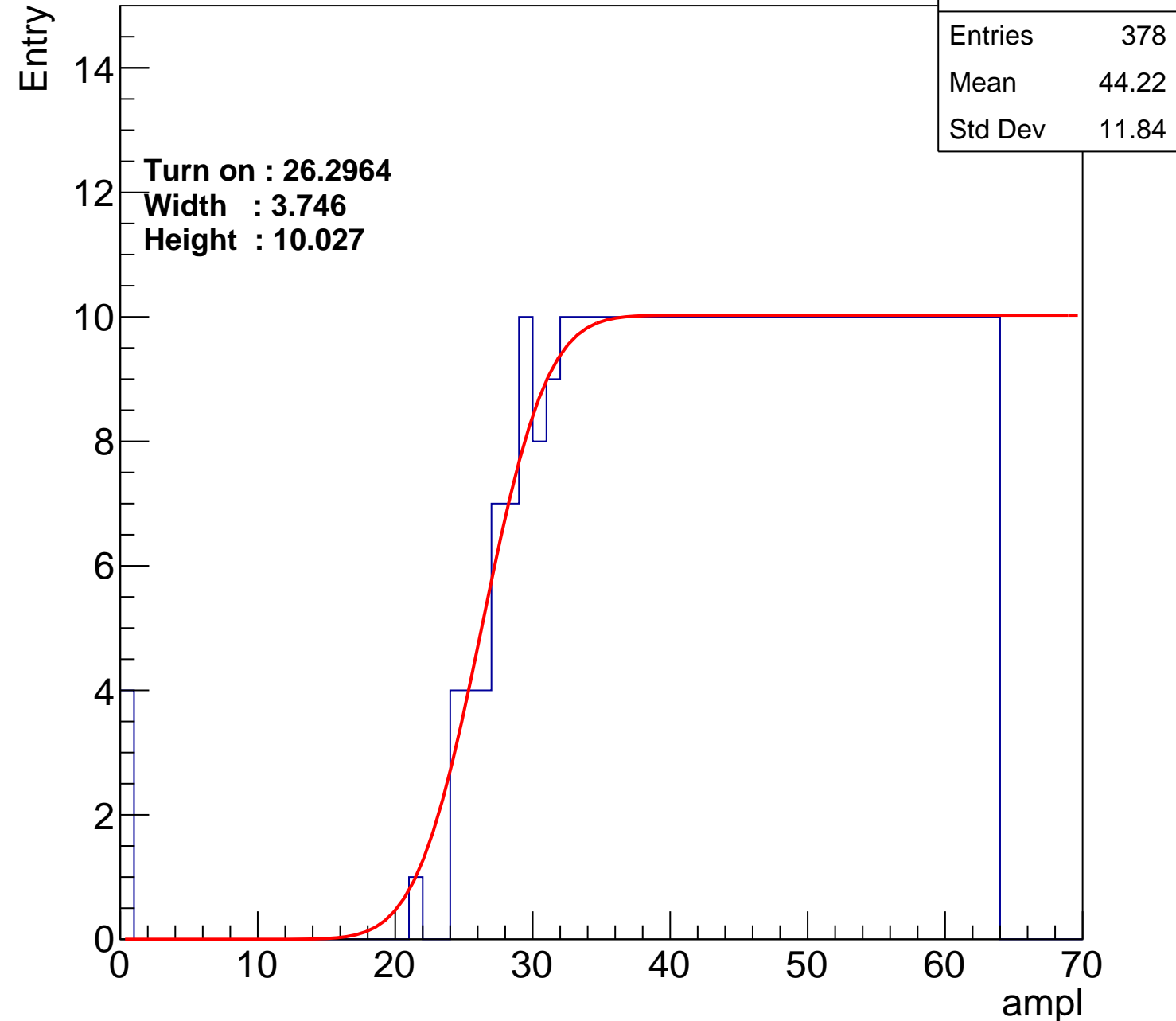
Width : 3.746

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.29
Std Dev	12.12

Turn on : 24.6117

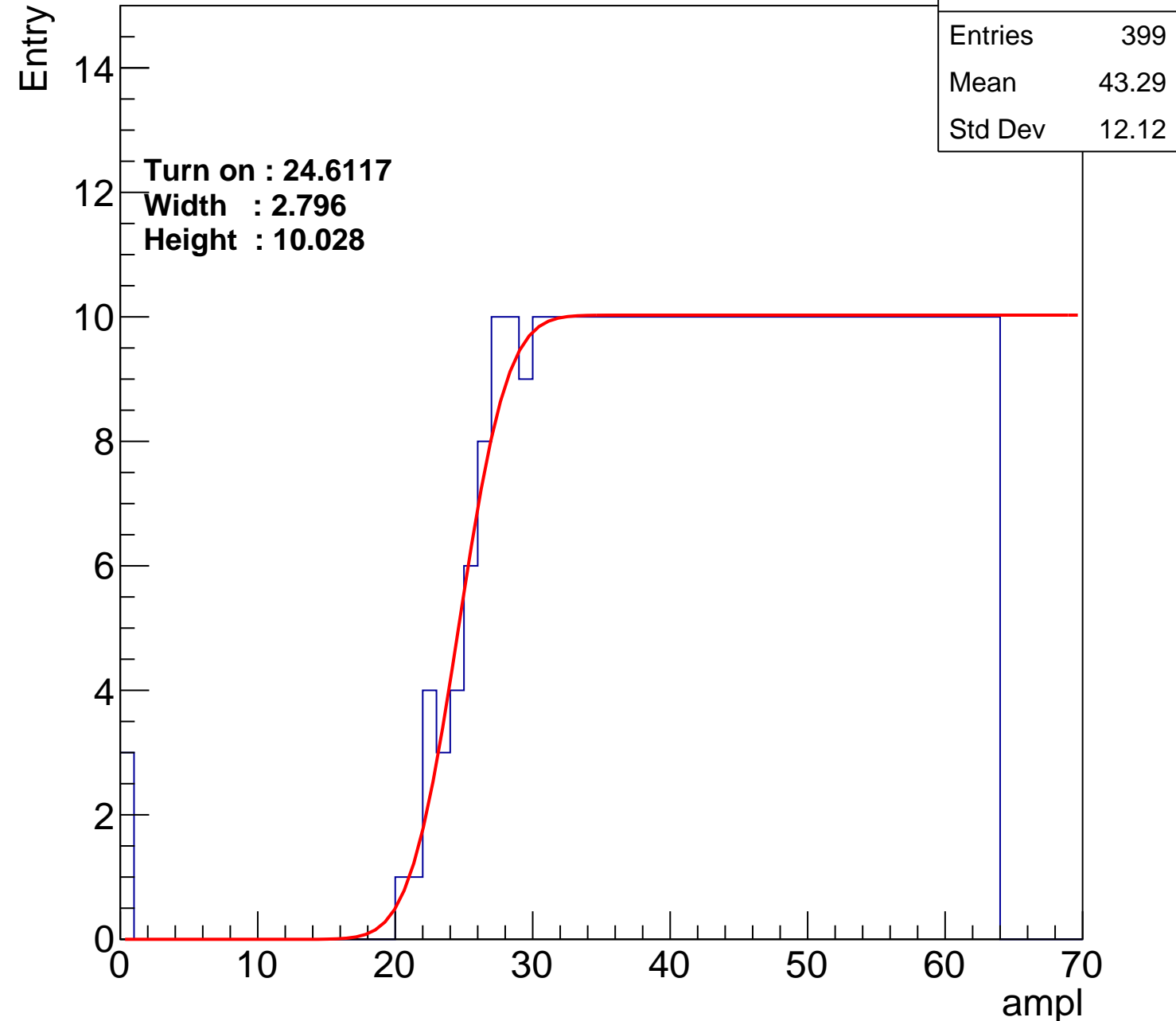
Width : 2.796

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.14
Std Dev	12.58

Turn on : 24.8568

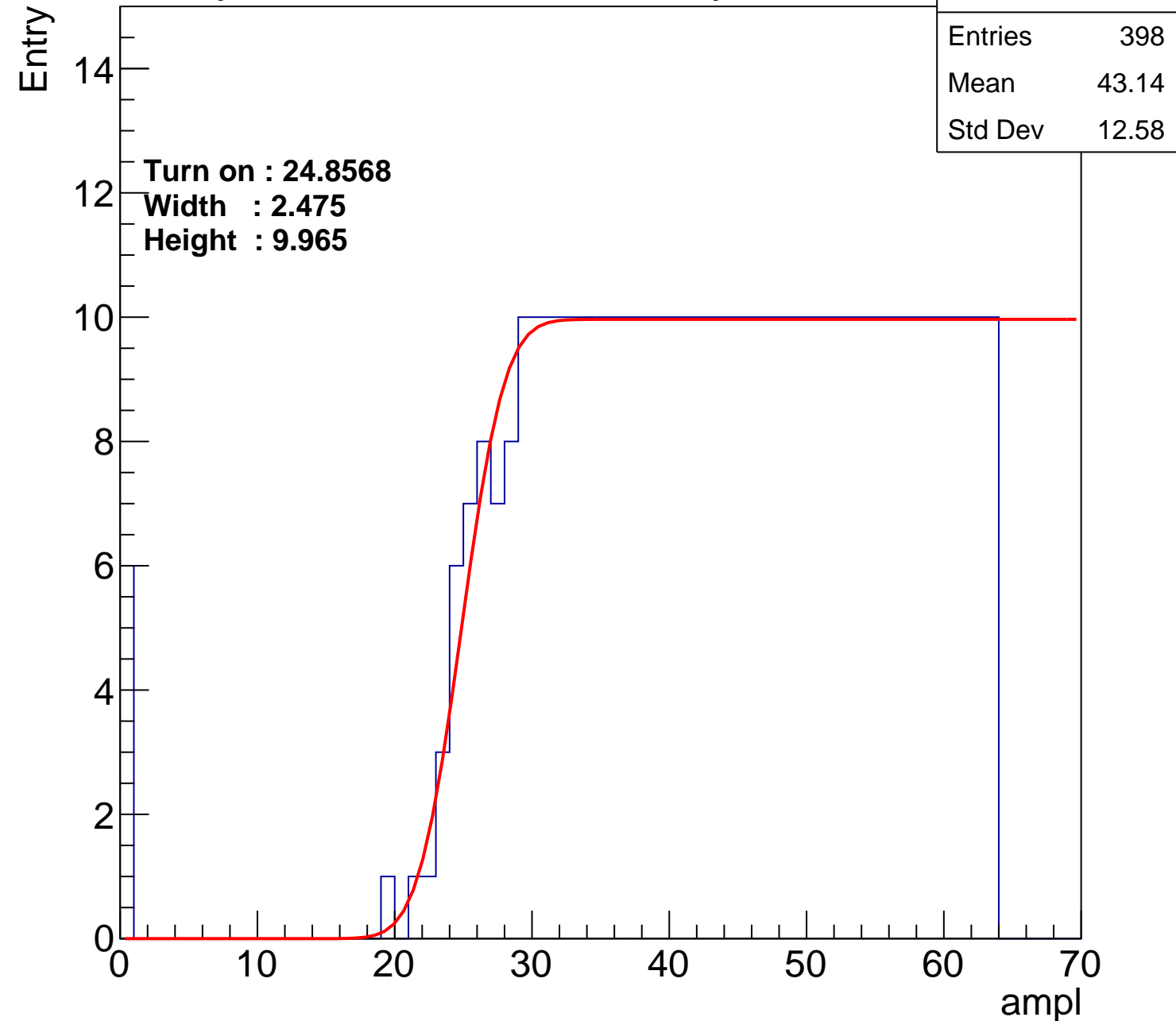
Width : 2.475

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.31
Std Dev	11.55

Turn on : 26.3636

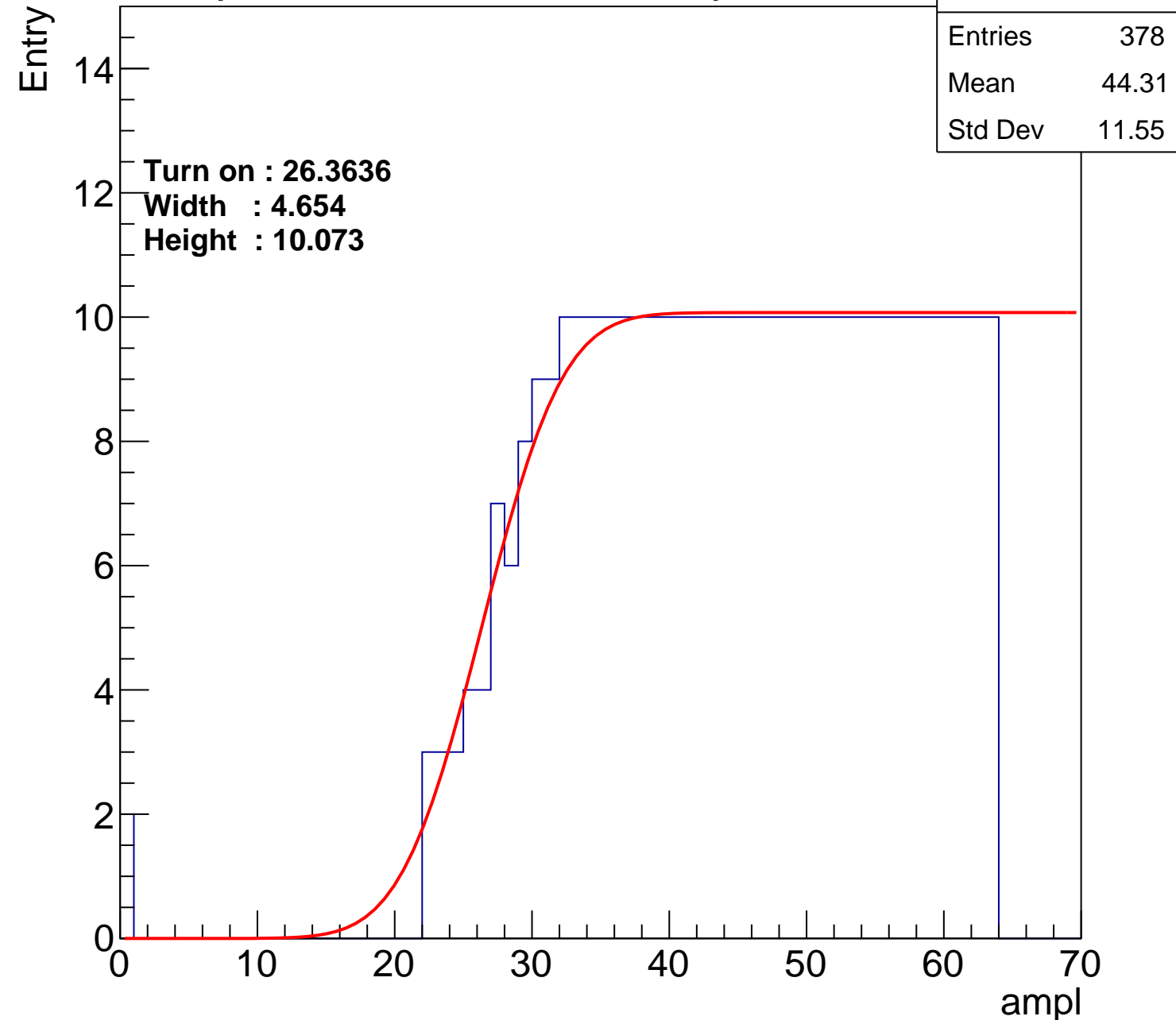
Width : 4.654

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.96
Std Dev	11.14

Turn on : 28.0551

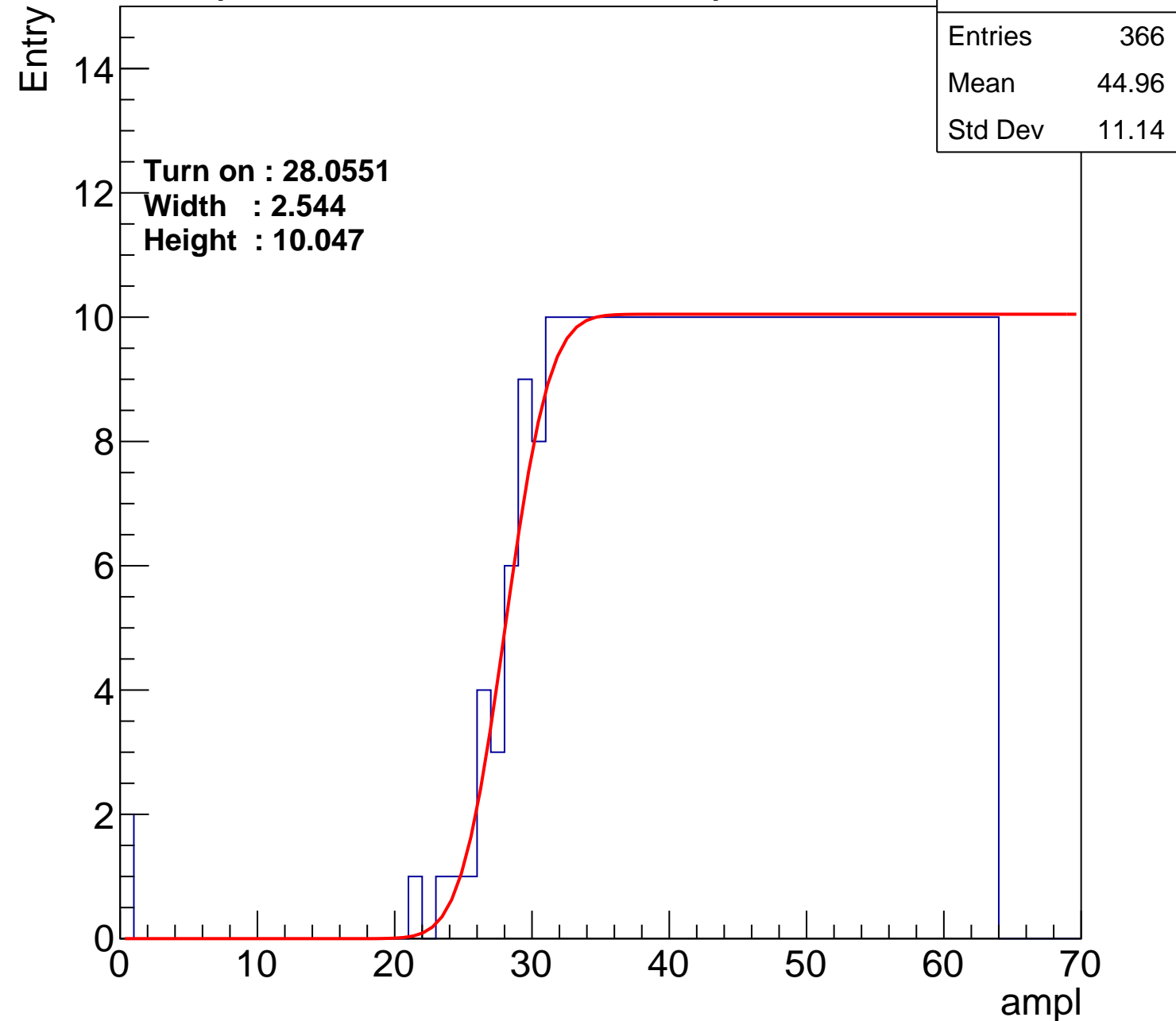
Width : 2.544

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#12, port B1

calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.1990
Width : 4.221
Height : 10.076



B0L102S, U23-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.8768

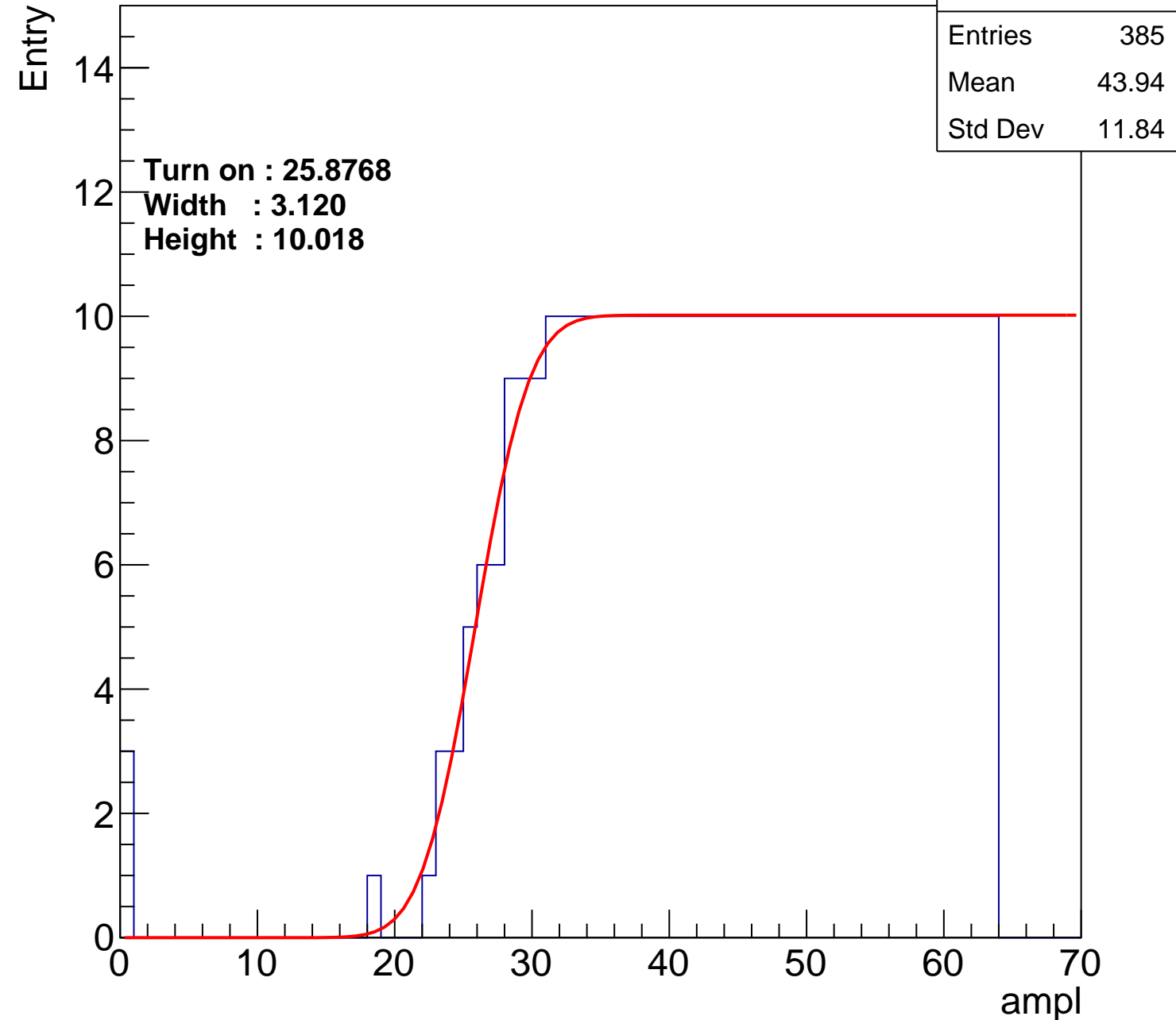
Width : 3.120

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.67
Std Dev	11.94

Turn on : 24.3425

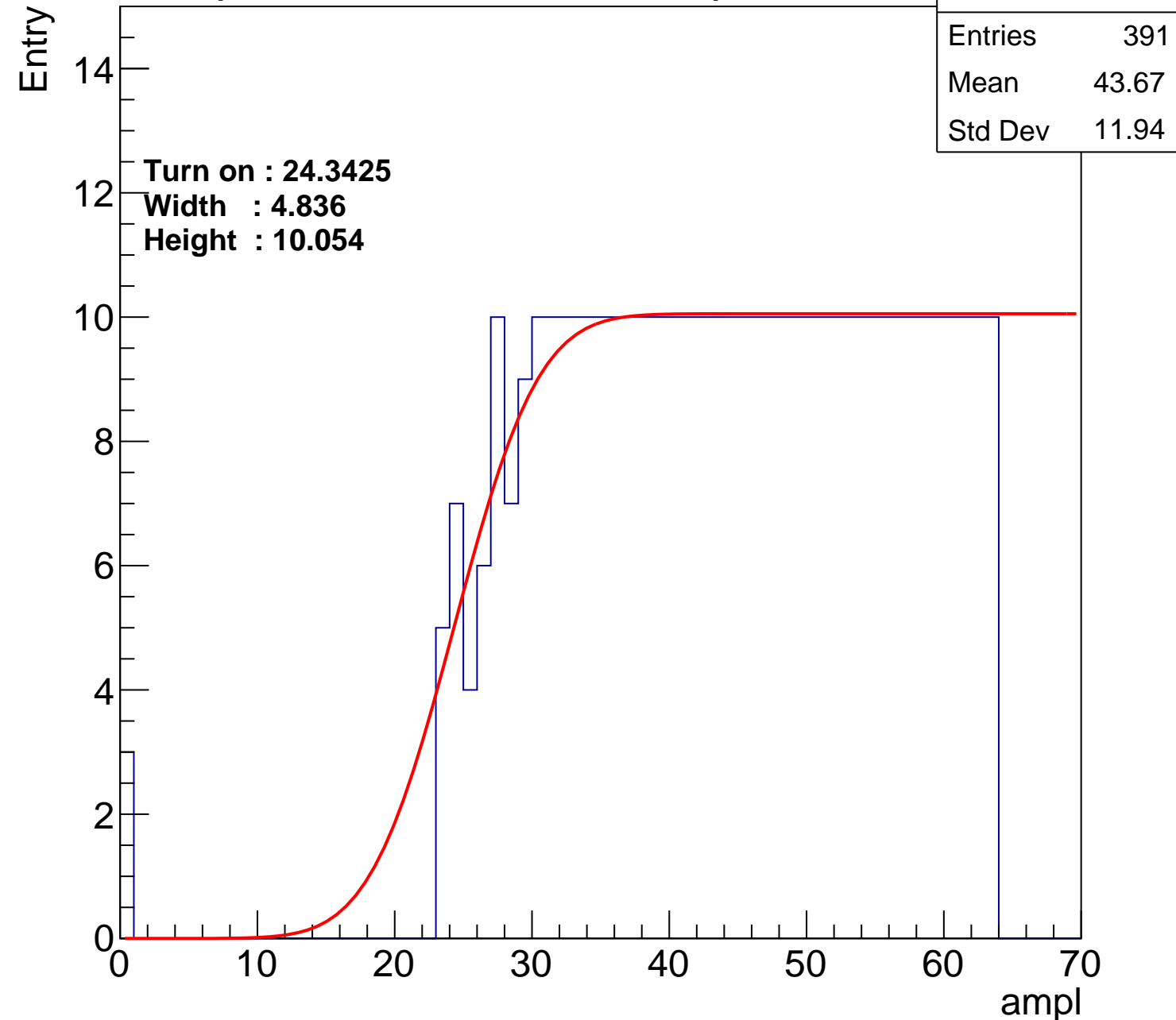
Width : 4.836

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.97
Std Dev	11.91

Turn on : 25.9059

Width : 2.561

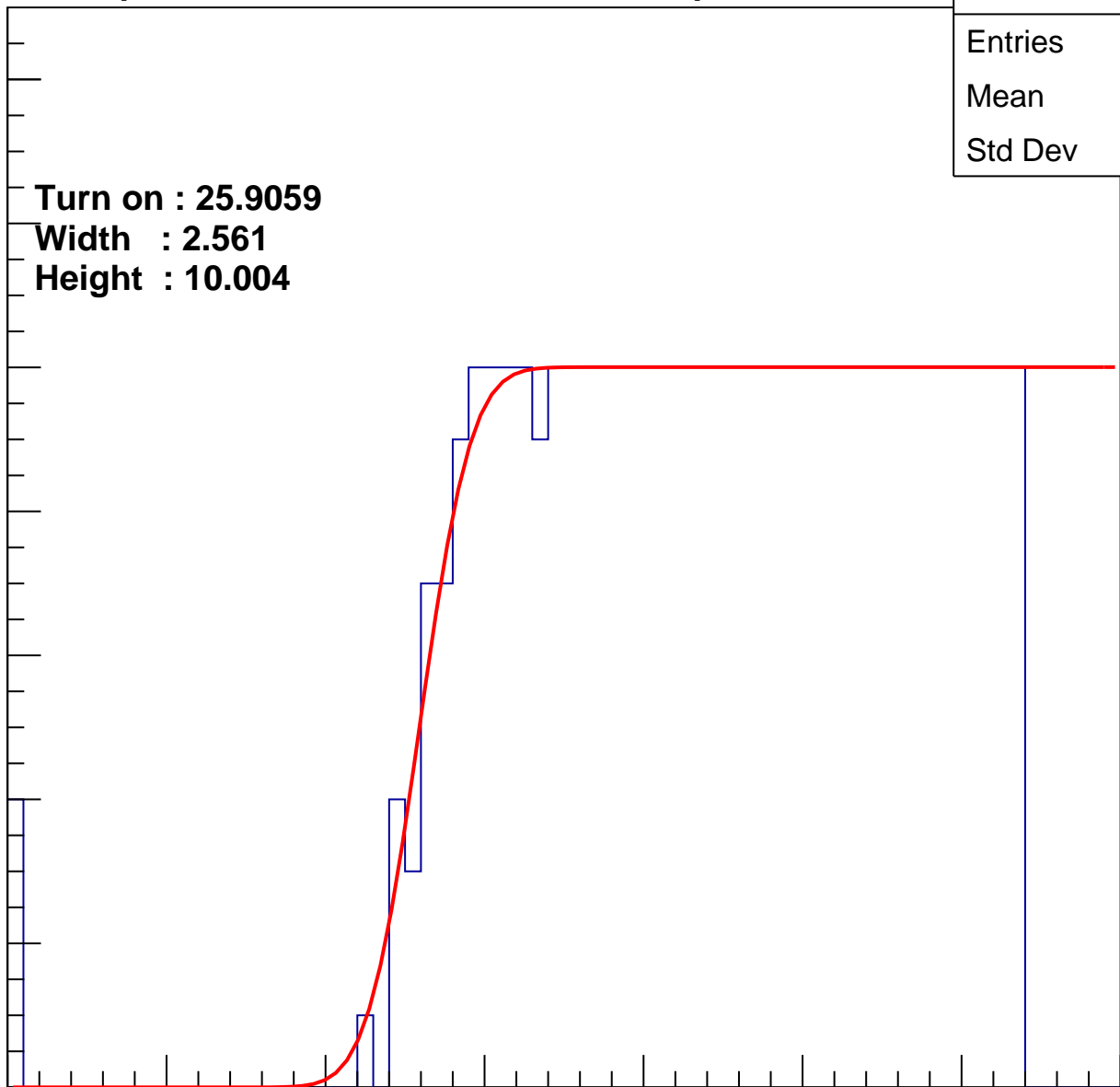
Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U23-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.51
Std Dev	11.92

Turn on : 24.9252

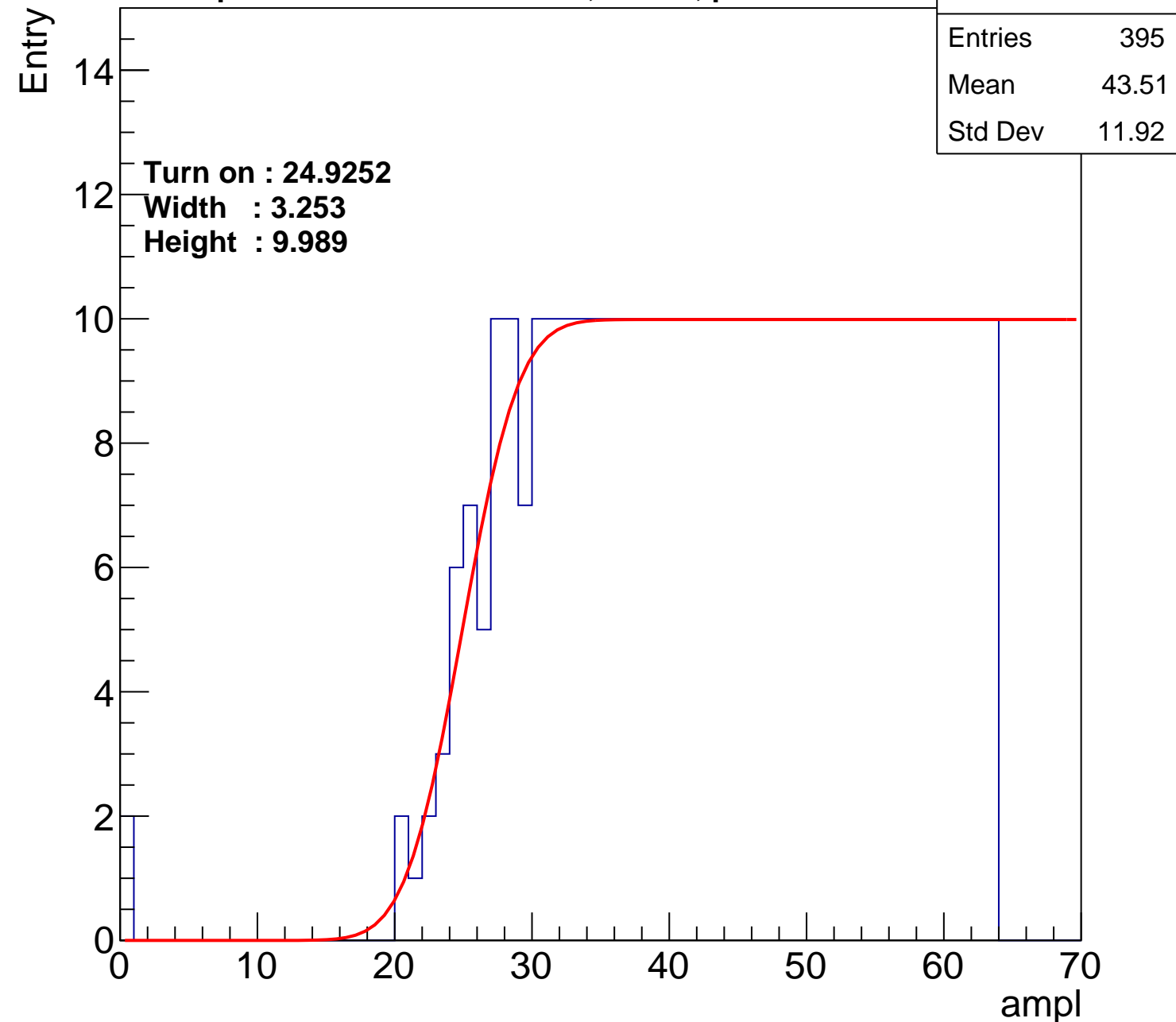
Width : 3.253

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.2970

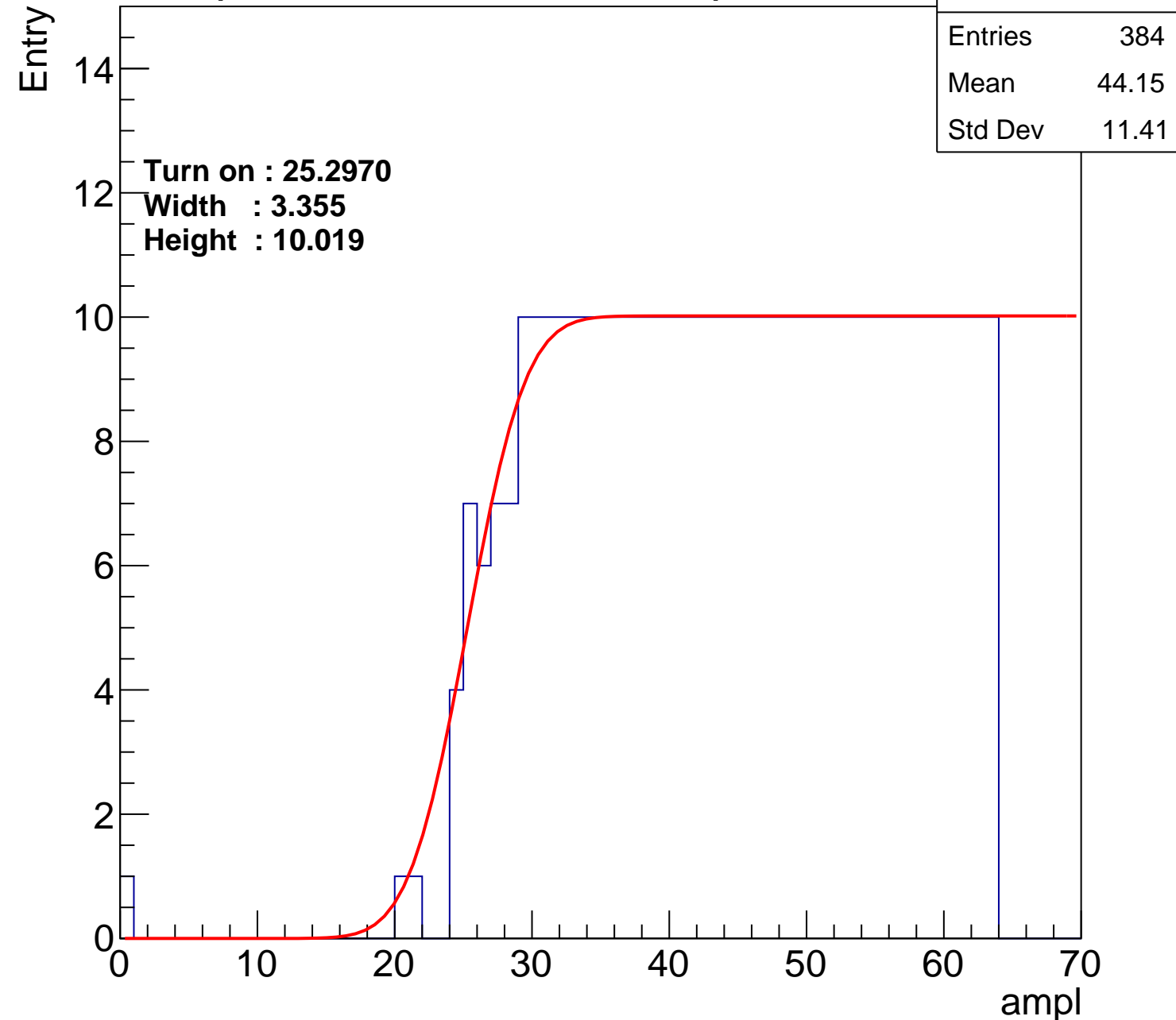
Width : 3.355

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch88

calib_packv5_042523_0143.root, FC#12, port B1

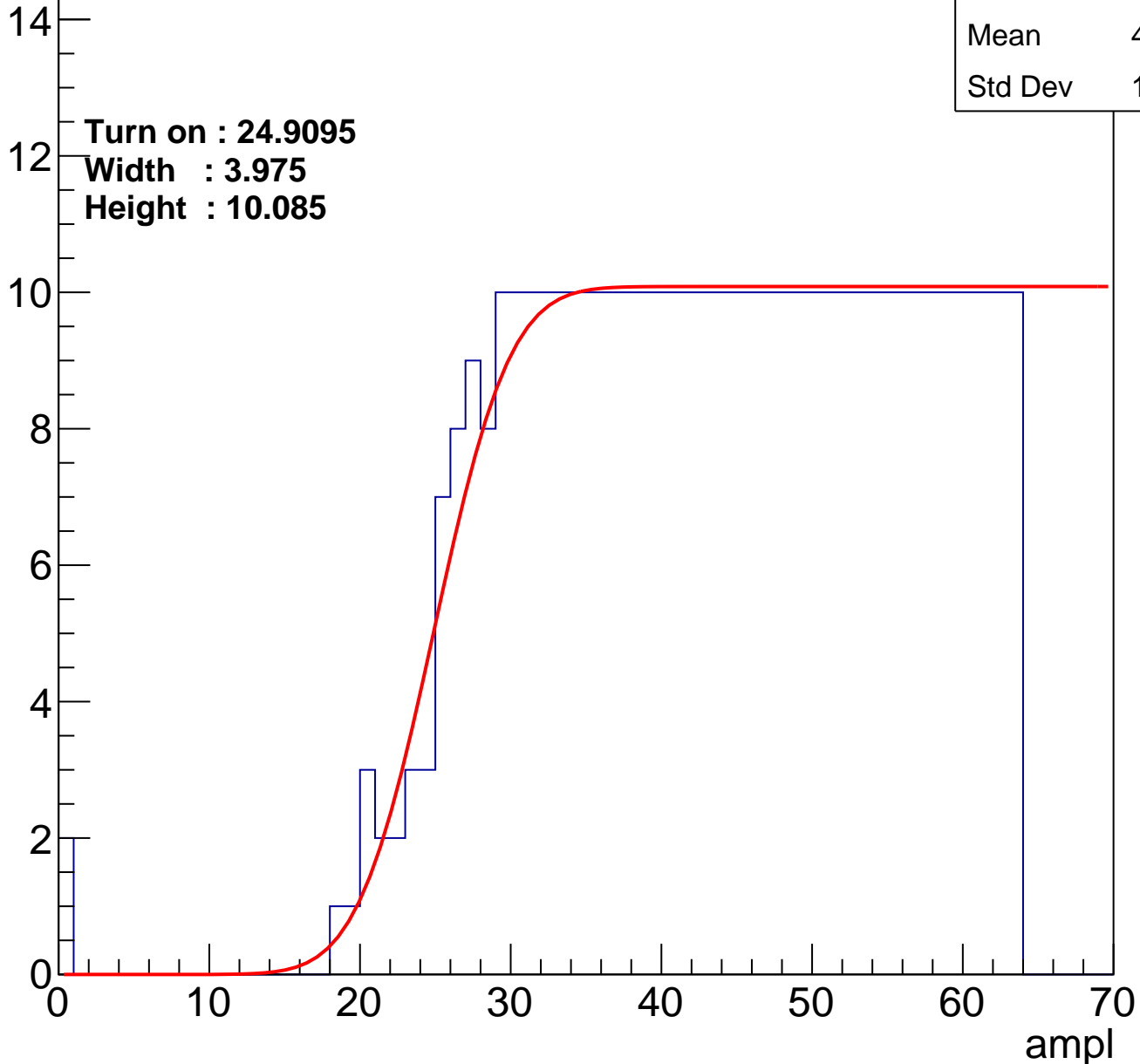
Entries	399
Mean	43.29
Std Dev	12.07

Turn on : 24.9095

Width : 3.975

Height : 10.085

Entry



B0L102S, U23-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.37
Std Dev	11.63

Turn on : 27.1498

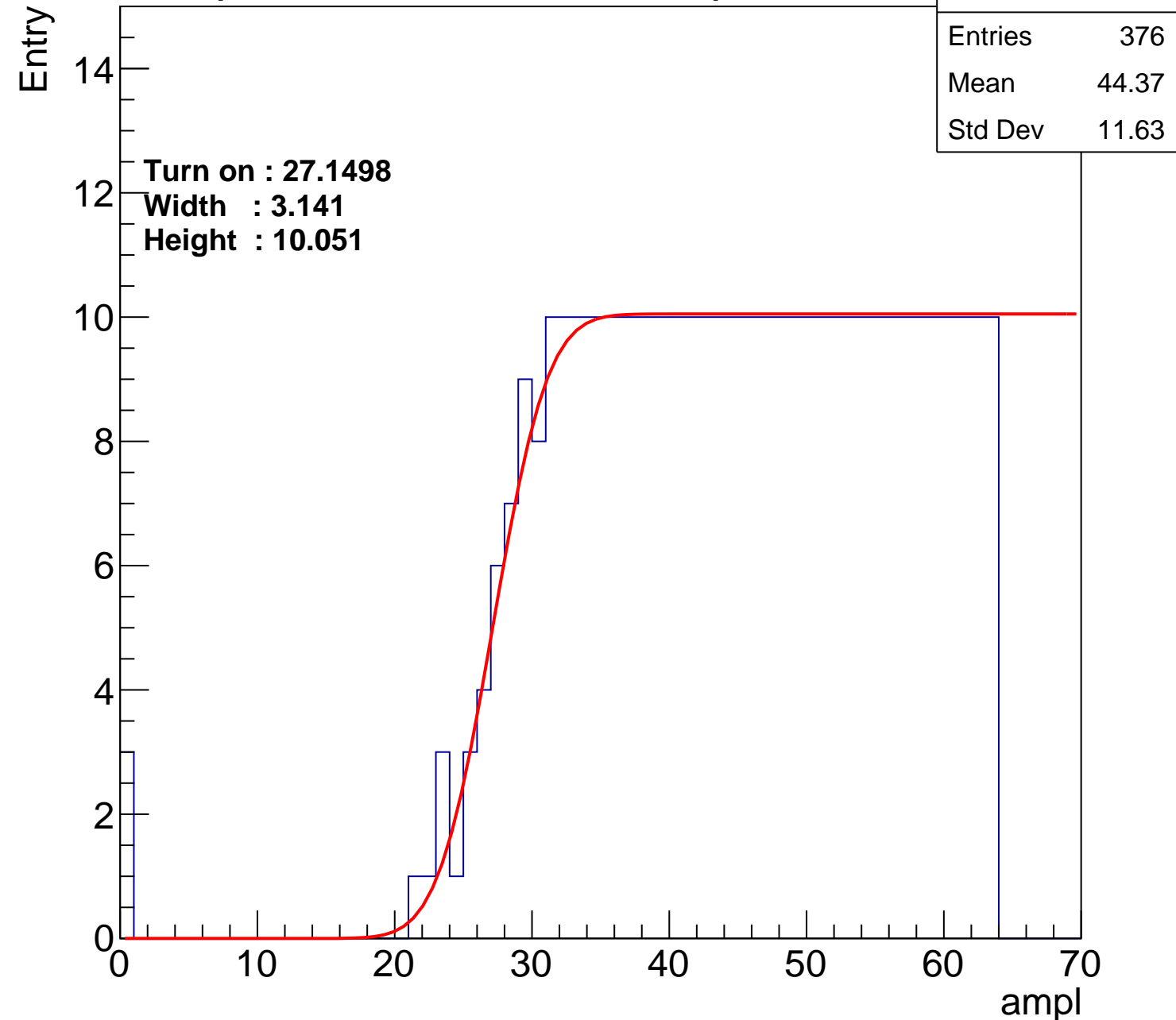
Width : 3.141

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch90

calib_packv5_042523_0143.root, FC#12, port B1

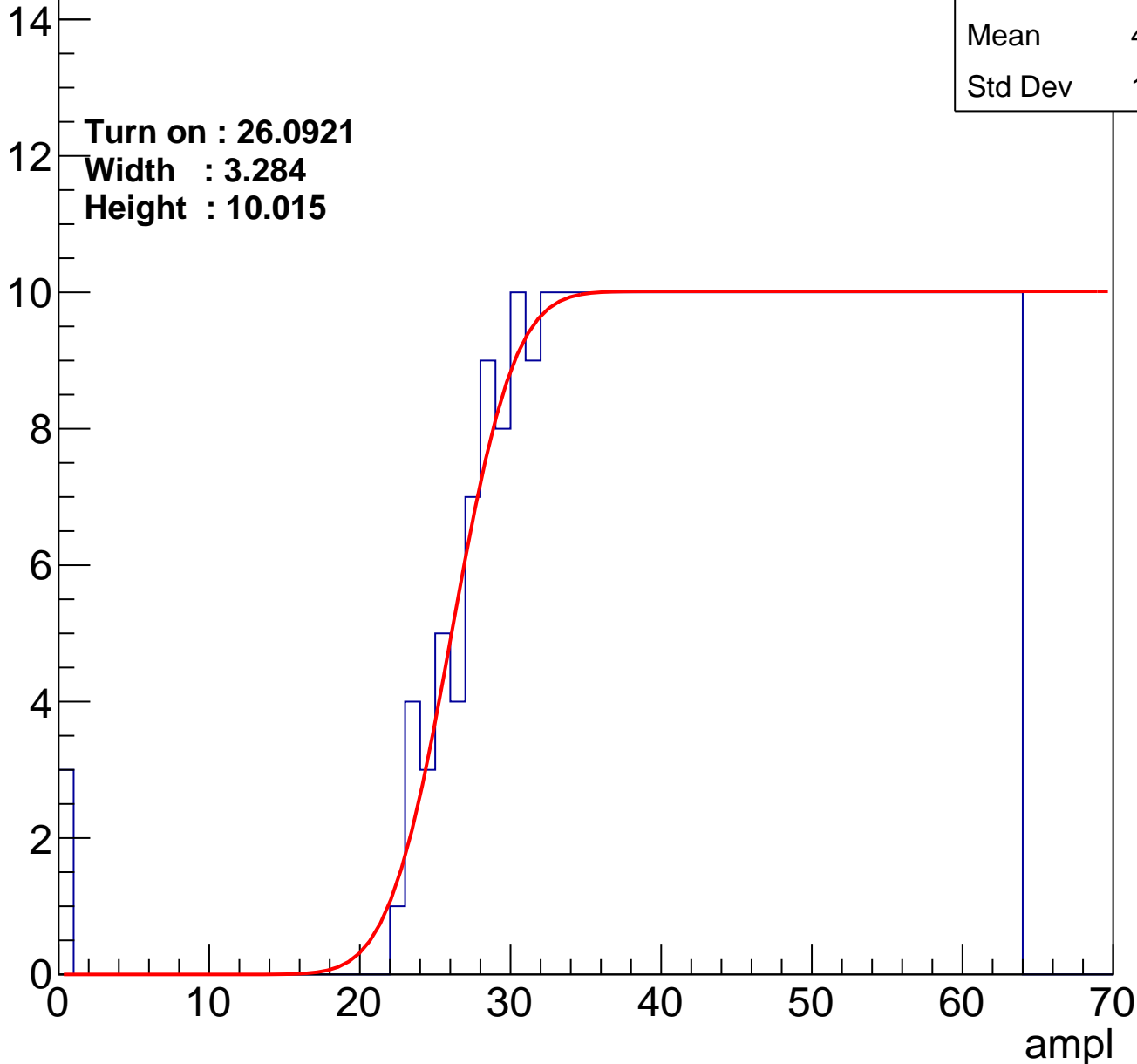
Entries	383
Mean	44.04
Std Dev	11.78

Turn on : 26.0921

Width : 3.284

Height : 10.015

Entry



B0L102S, U23-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.5415

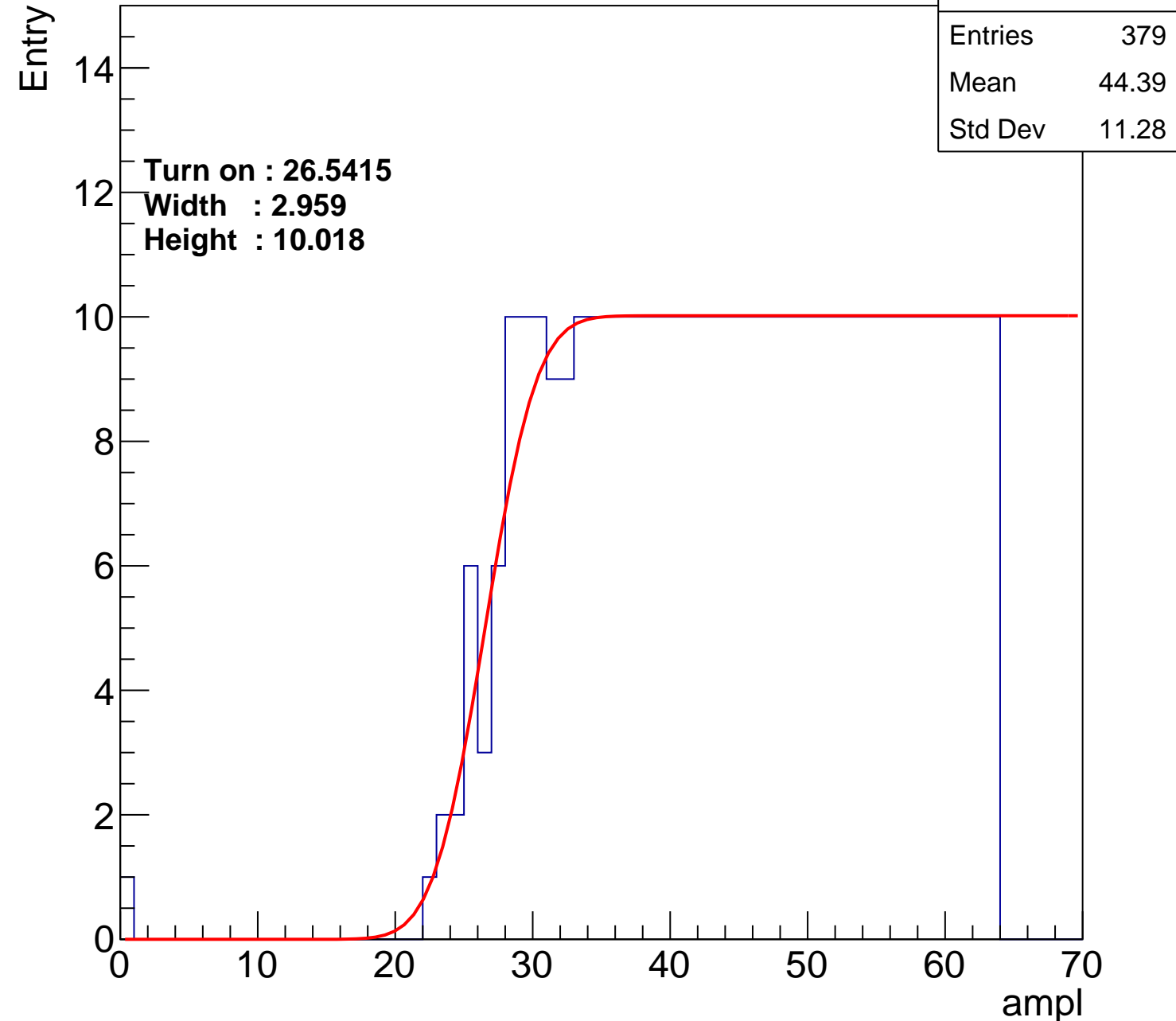
Width : 2.959

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.98
Std Dev	11.77

Turn on : 26.0131

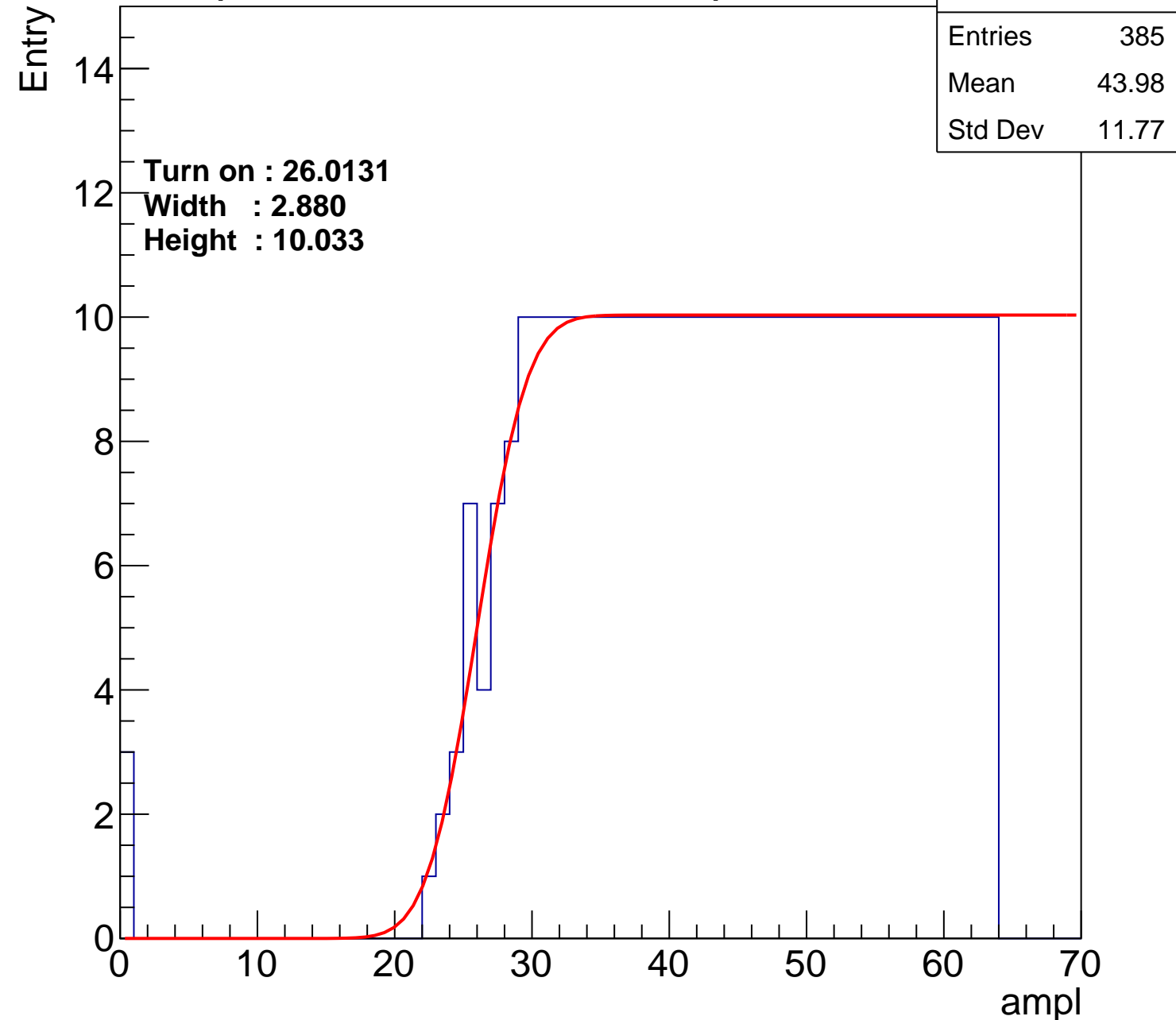
Width : 2.880

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 26.8372

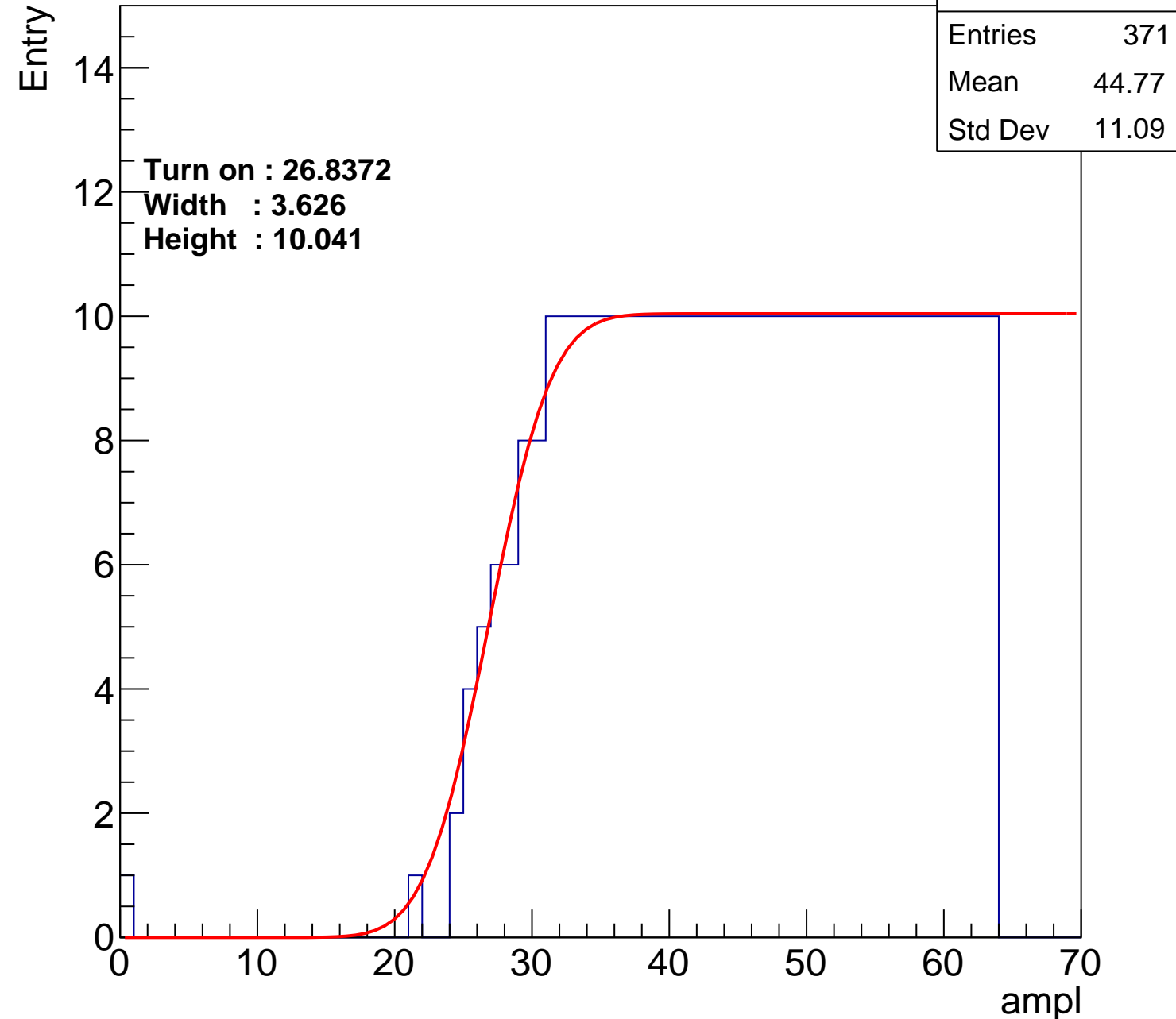
Width : 3.626

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.75
Std Dev	11.83

Turn on : 25.8605

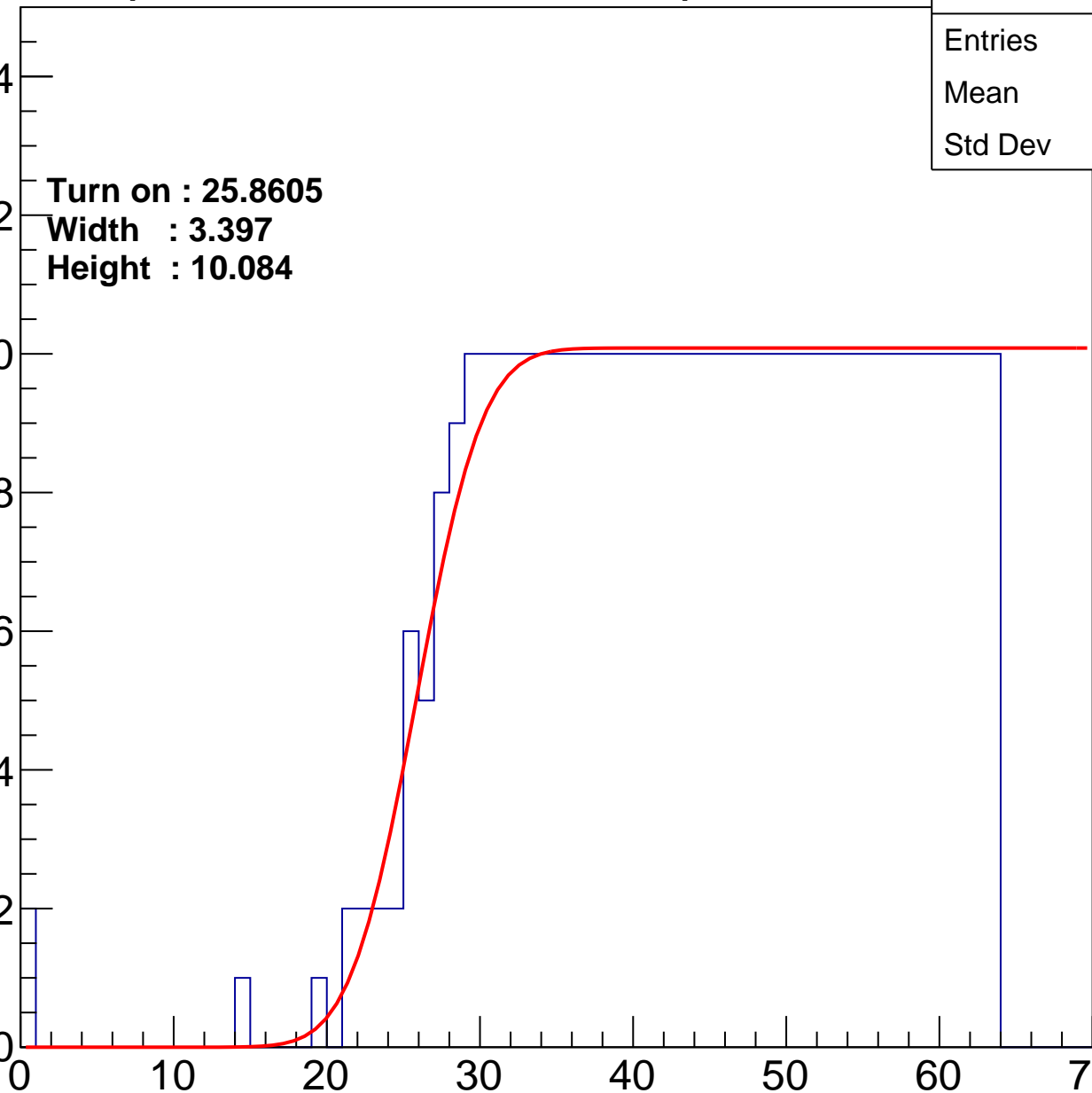
Width : 3.397

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch95

calib_packv5_042523_0143.root, FC#12, port B1

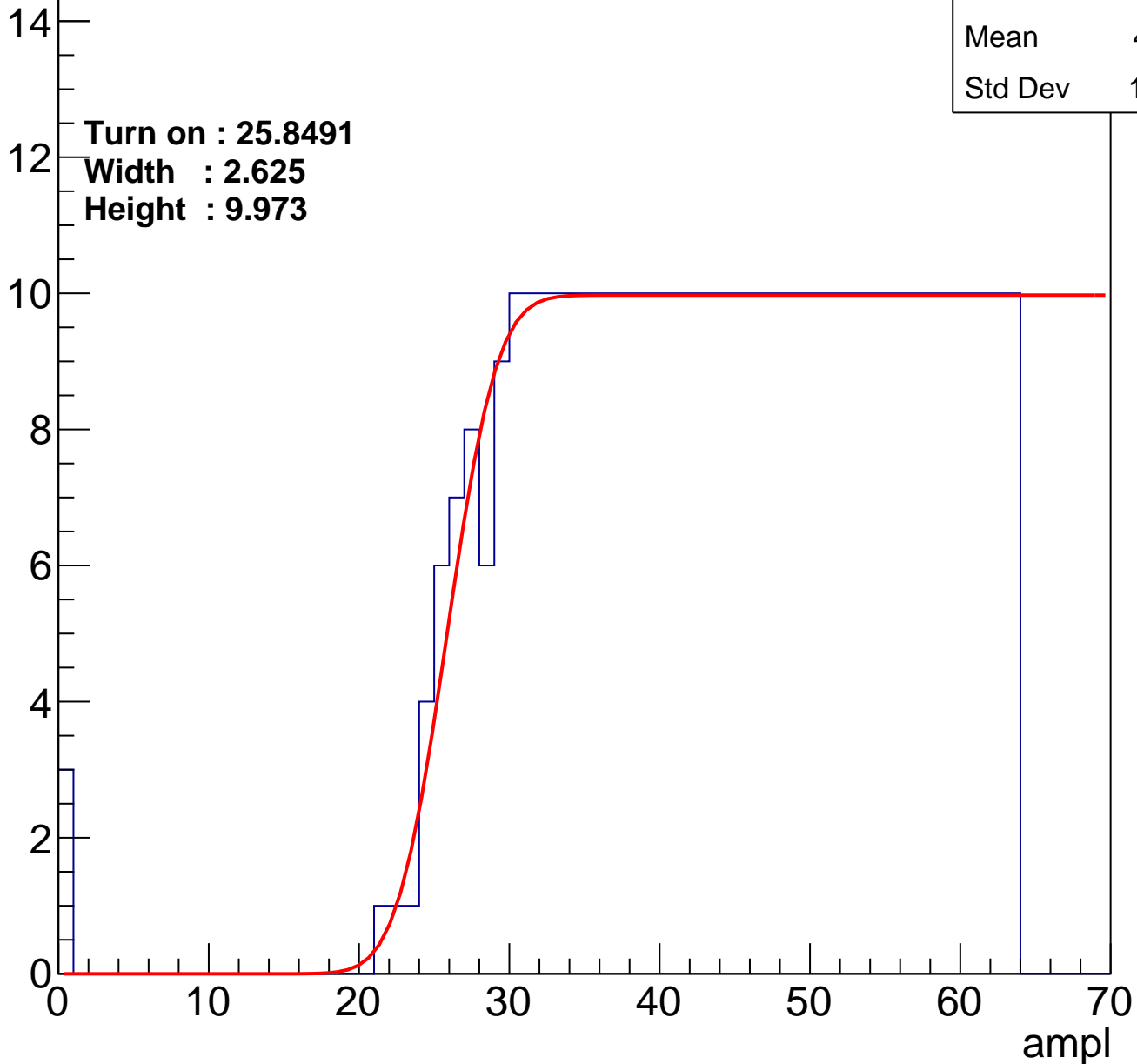
Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.8491

Width : 2.625

Height : 9.973

Entry



B0L102S, U23-ch96

calib_packv5_042523_0143.root, FC#12, port B1

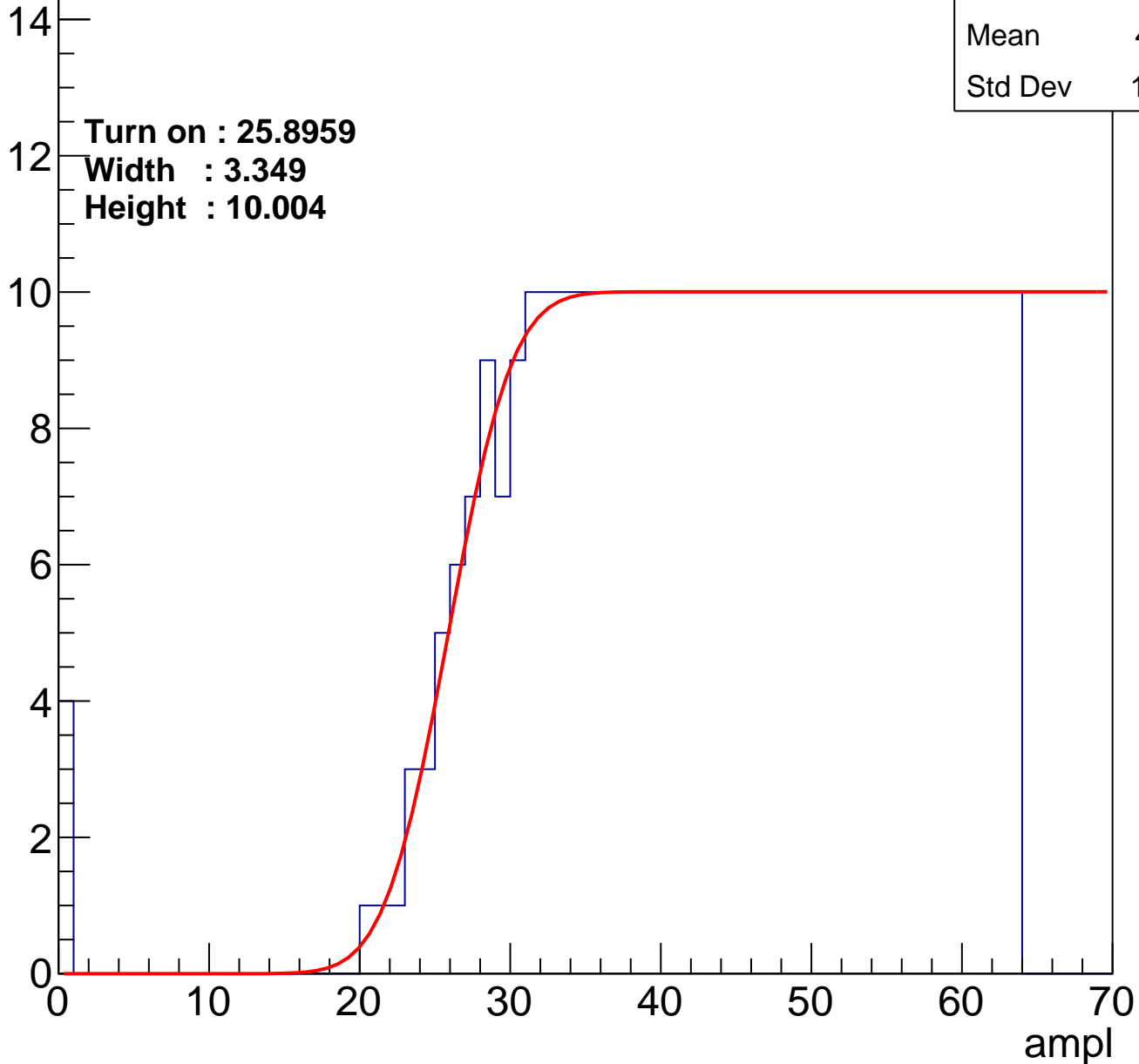
Entries	386
Mean	43.81
Std Dev	12.06

Turn on : 25.8959

Width : 3.349

Height : 10.004

Entry



B0L102S, U23-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.39
Std Dev	12.12

Turn on : 24.6651

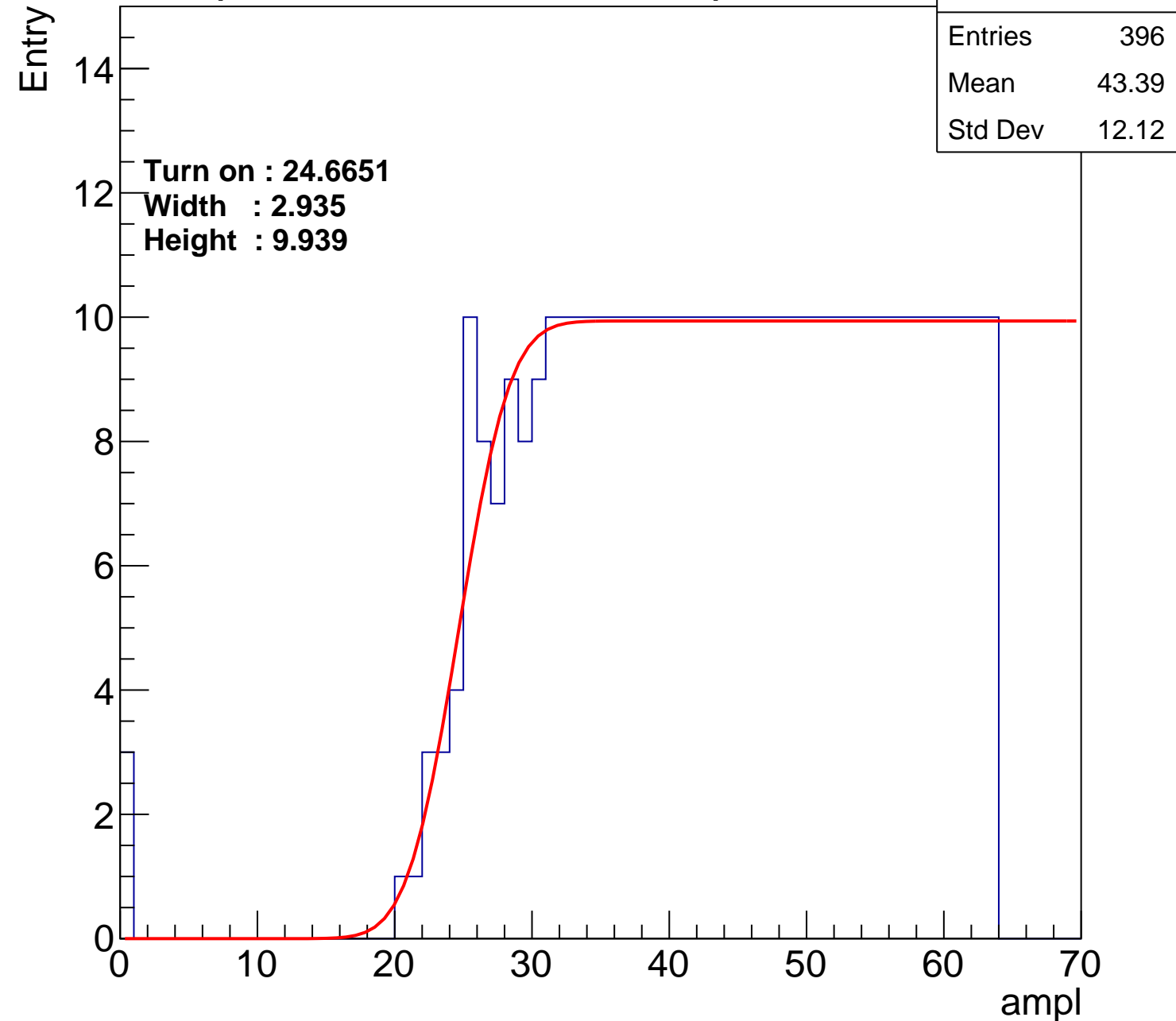
Width : 2.935

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.88
Std Dev	11.76

Turn on : 26.0419

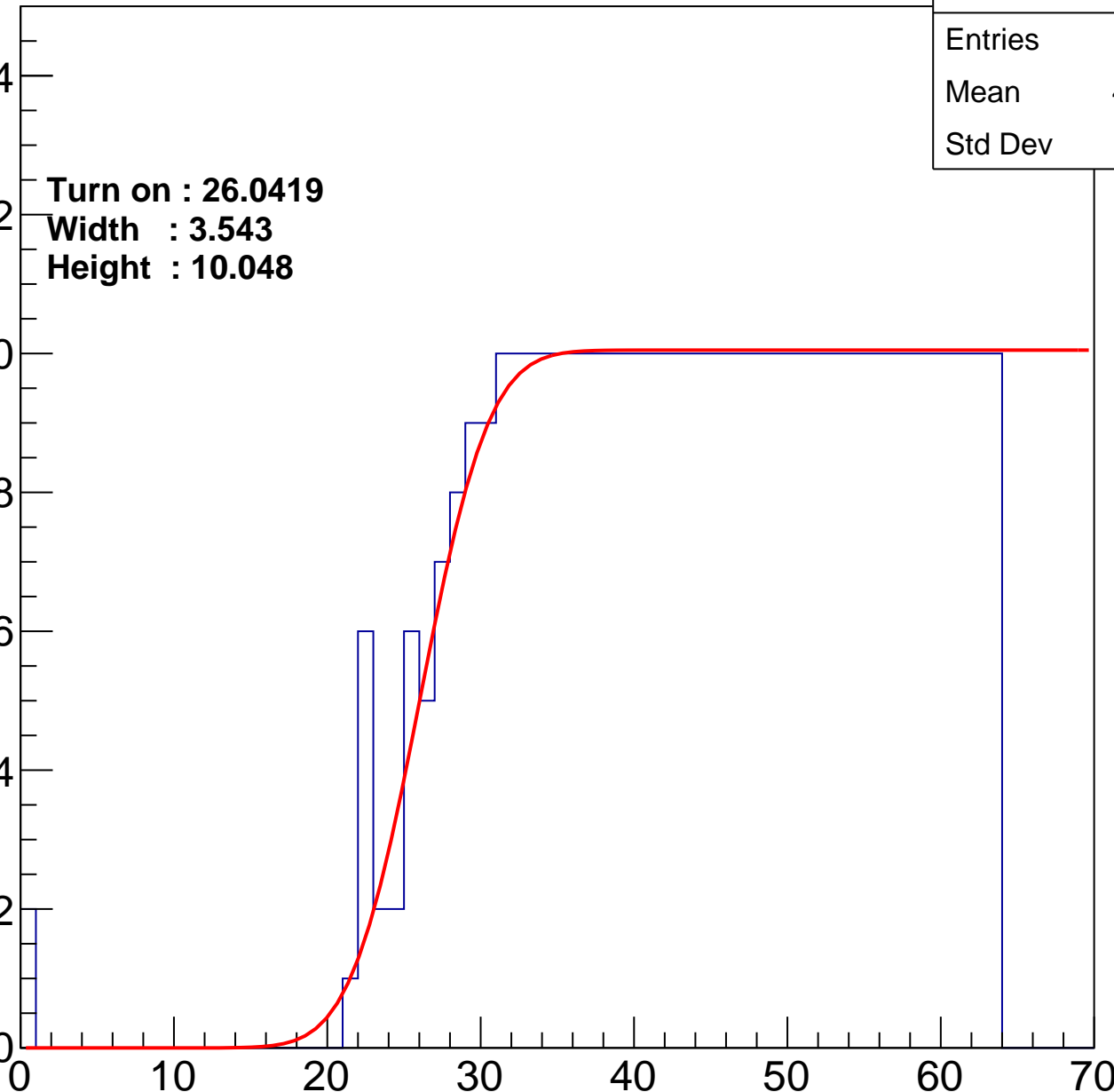
Width : 3.543

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch99

calib_packv5_042523_0143.root, FC#12, port B1

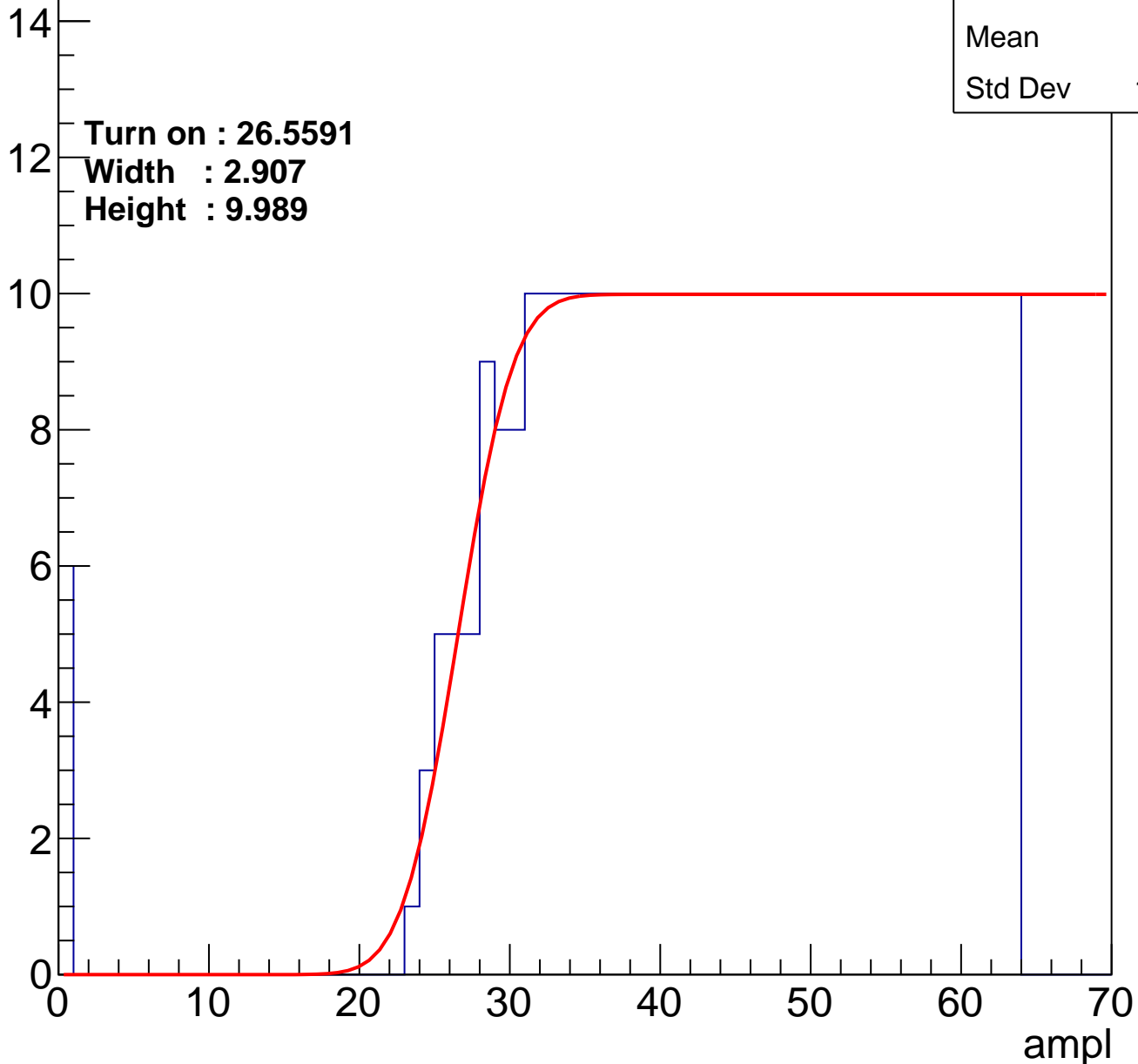
Entries	380
Mean	44
Std Dev	12.21

Turn on : 26.5591

Width : 2.907

Height : 9.989

Entry



B0L102S, U23-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.28
Std Dev	12.25

Turn on : 24.5744

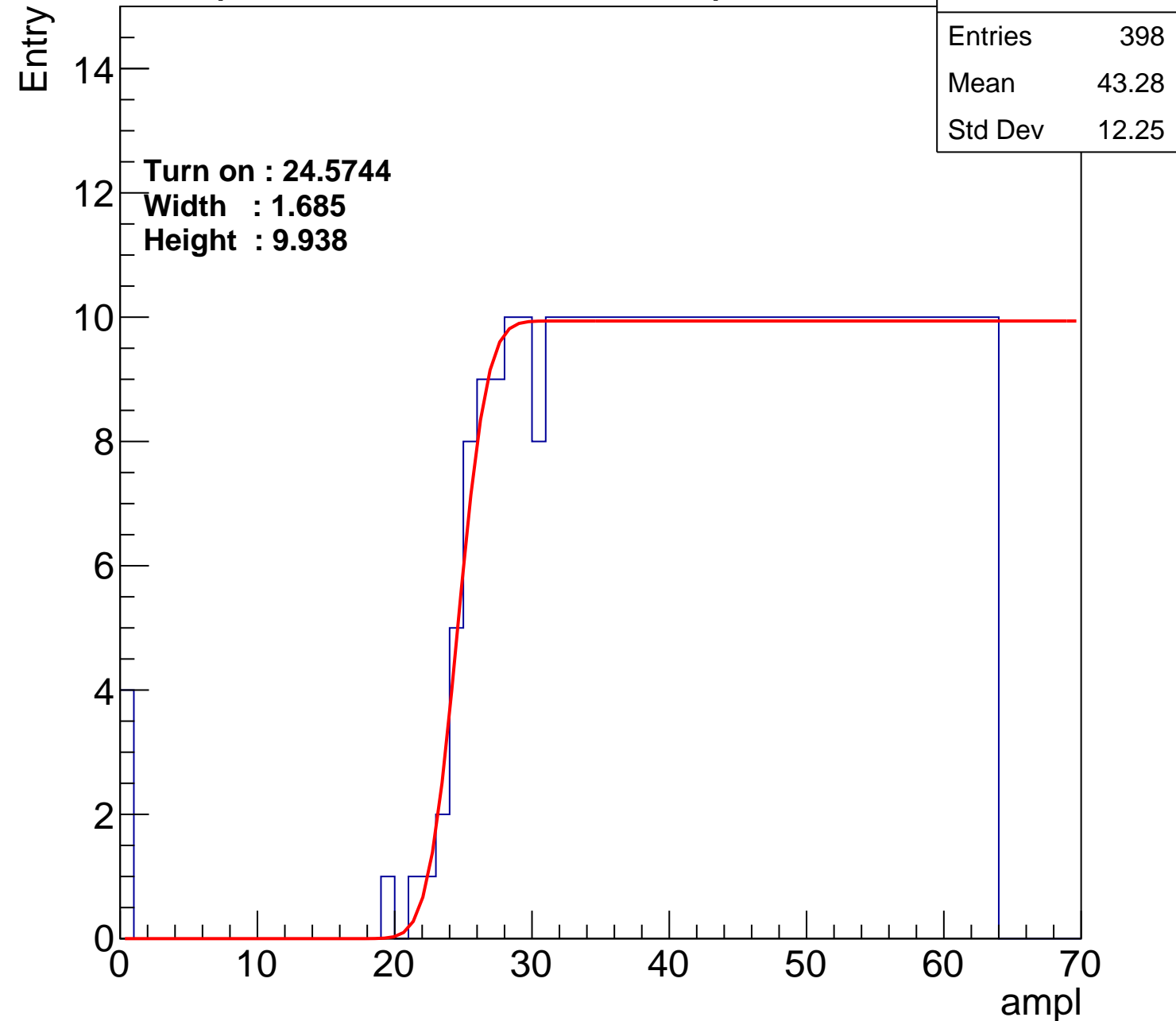
Width : 1.685

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 27.1573

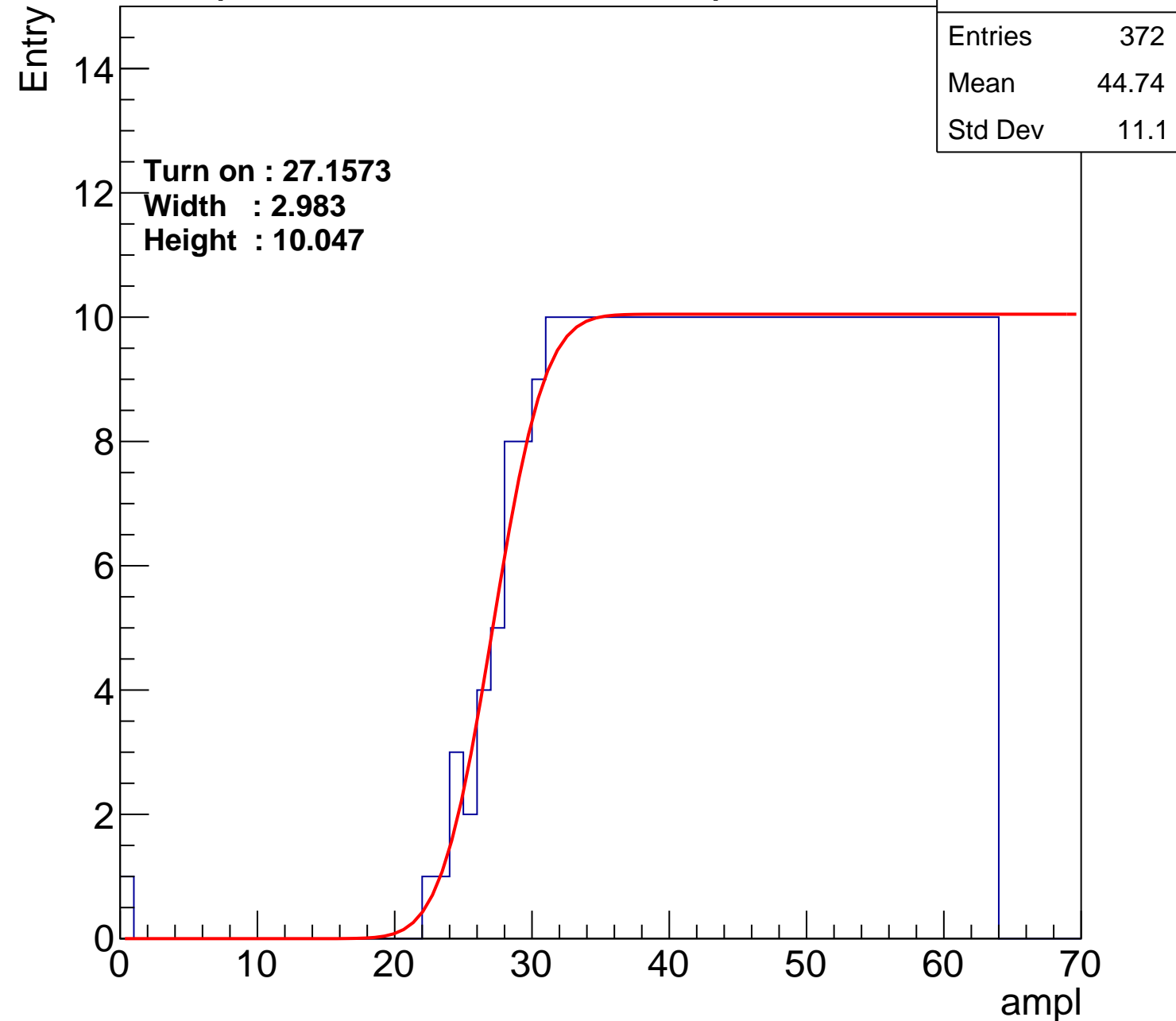
Width : 2.983

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.34
Std Dev	11.55

Turn on : 26.2248

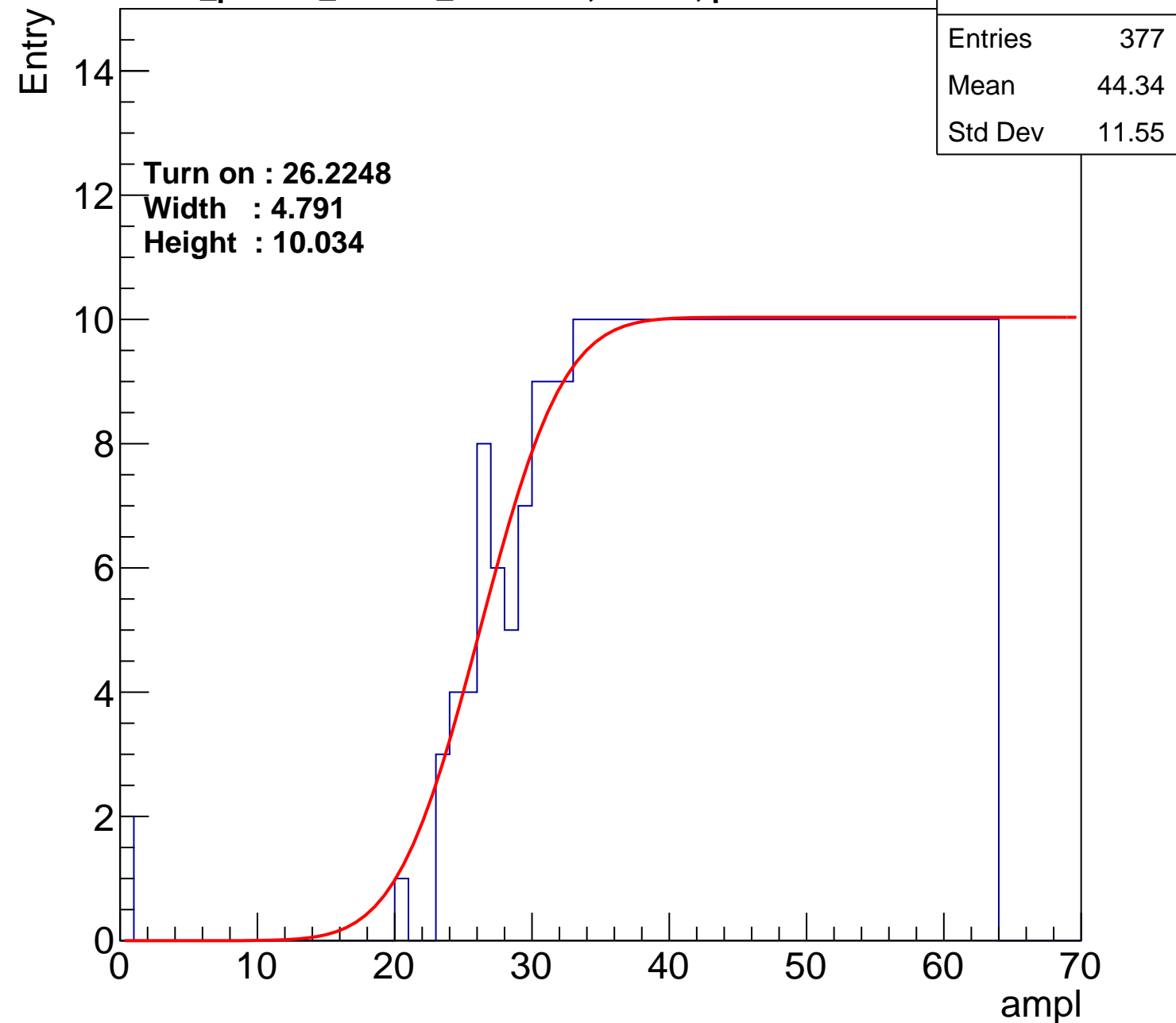
Width : 4.791

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.26
Std Dev	12.06

Turn on : 25.0906

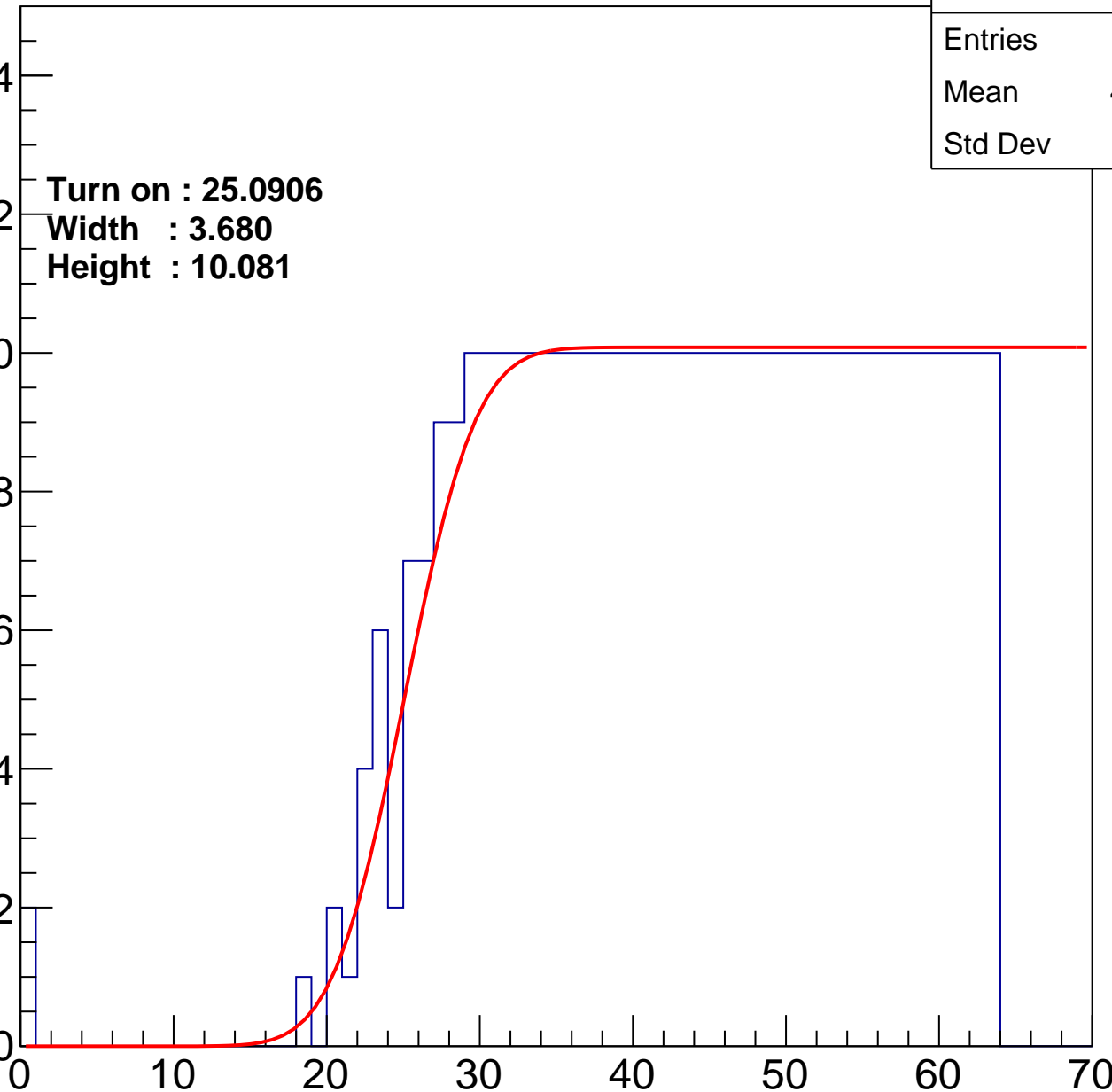
Width : 3.680

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.93
Std Dev	12.11

Turn on : 23.7761

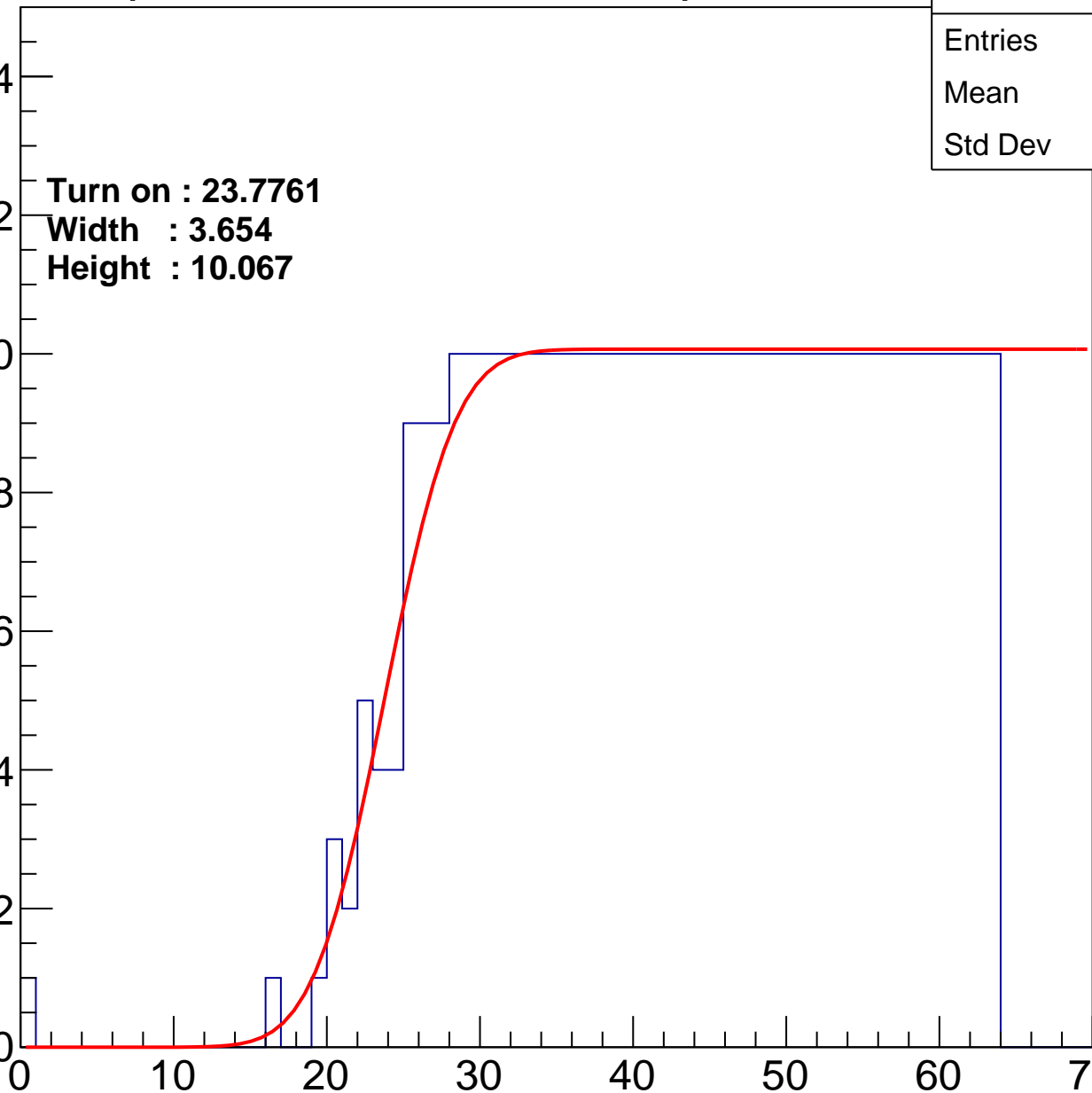
Width : 3.654

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.03
Std Dev	11.91

Turn on : 26.0636

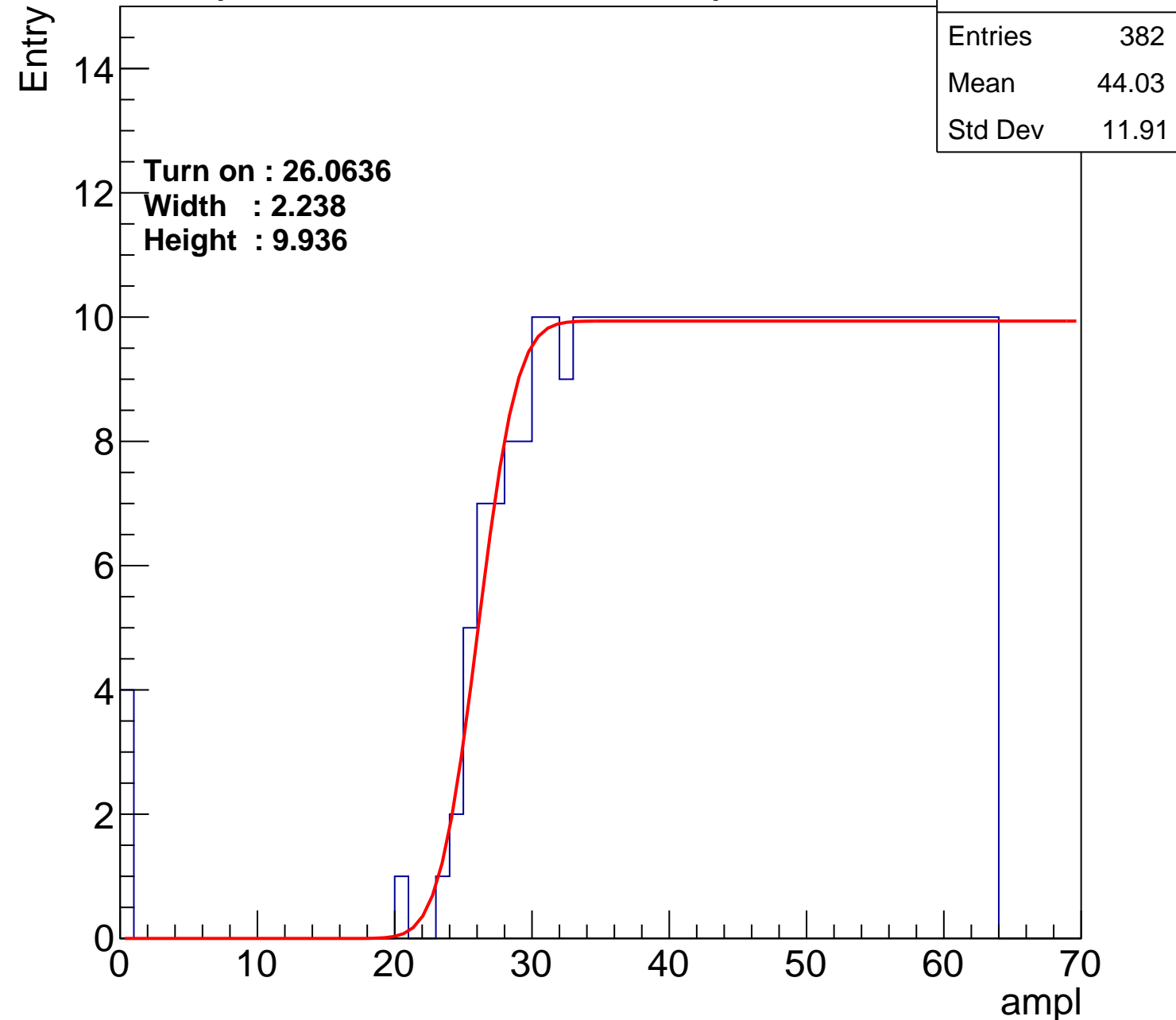
Width : 2.238

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.69
Std Dev	12.24

Turn on : 26.0318

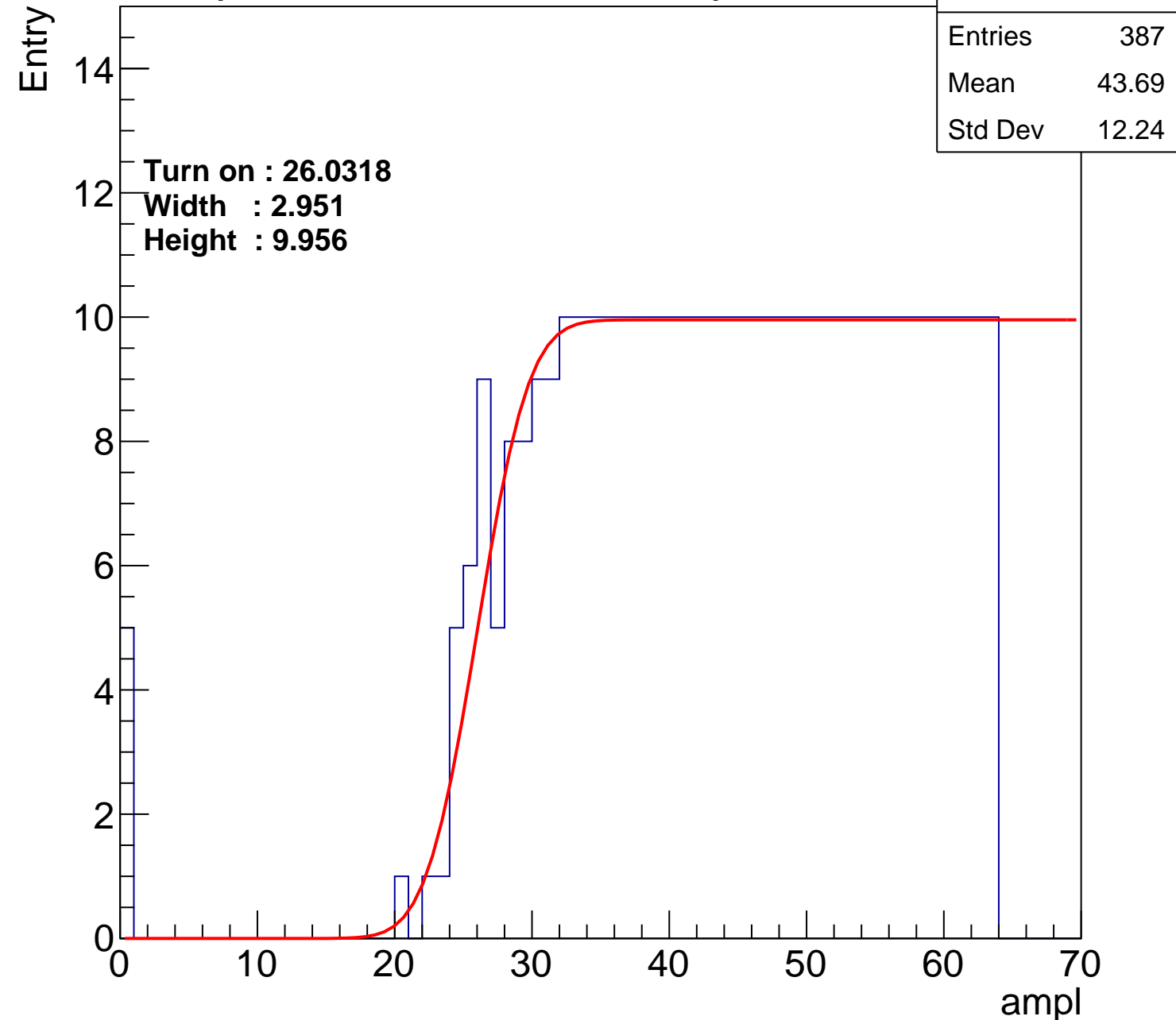
Width : 2.951

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.31
Std Dev	12.31

Turn on : 24.4898

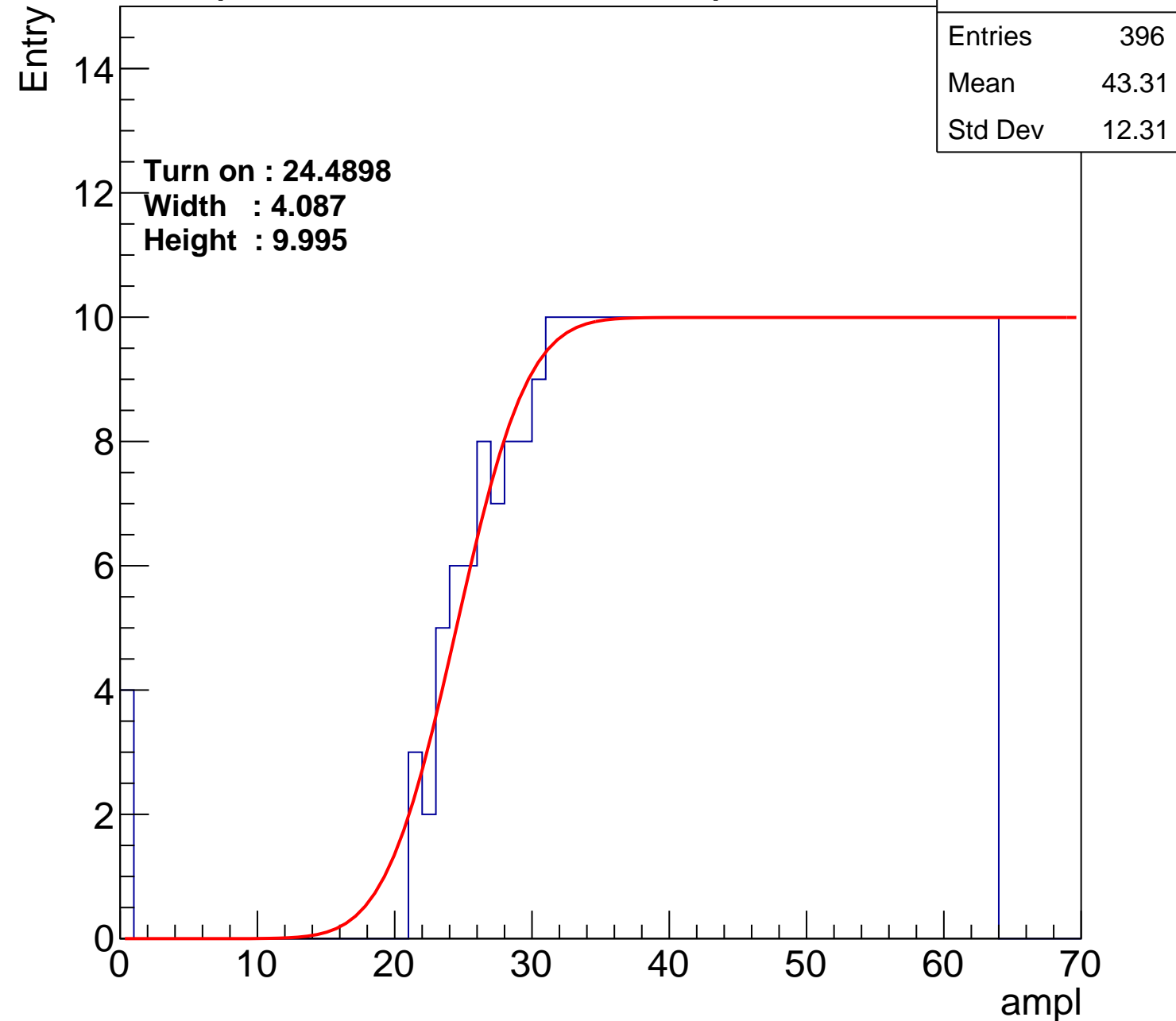
Width : 4.087

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.2176

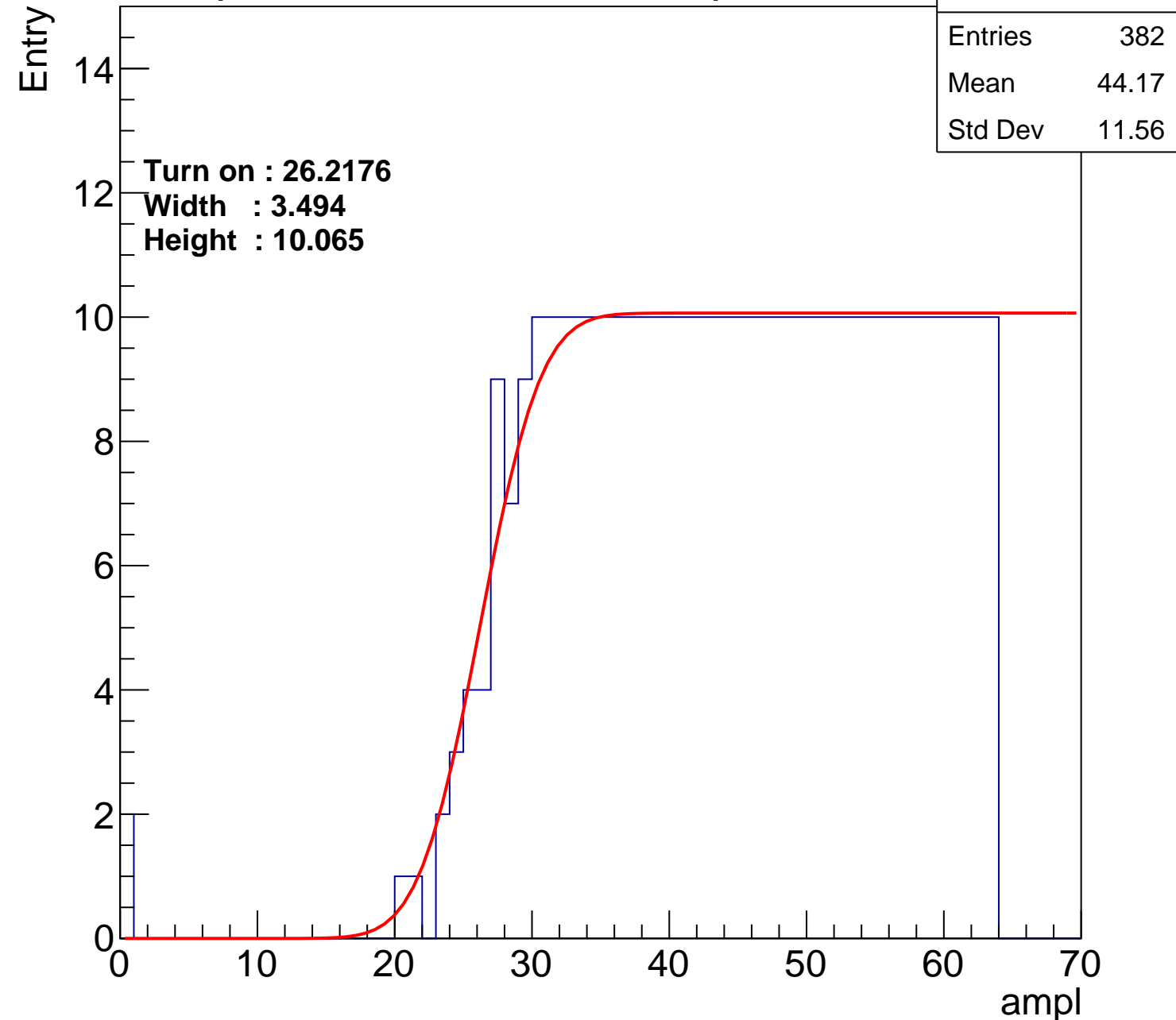
Width : 3.494

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.17
Std Dev	11.43

Turn on : 26.4390

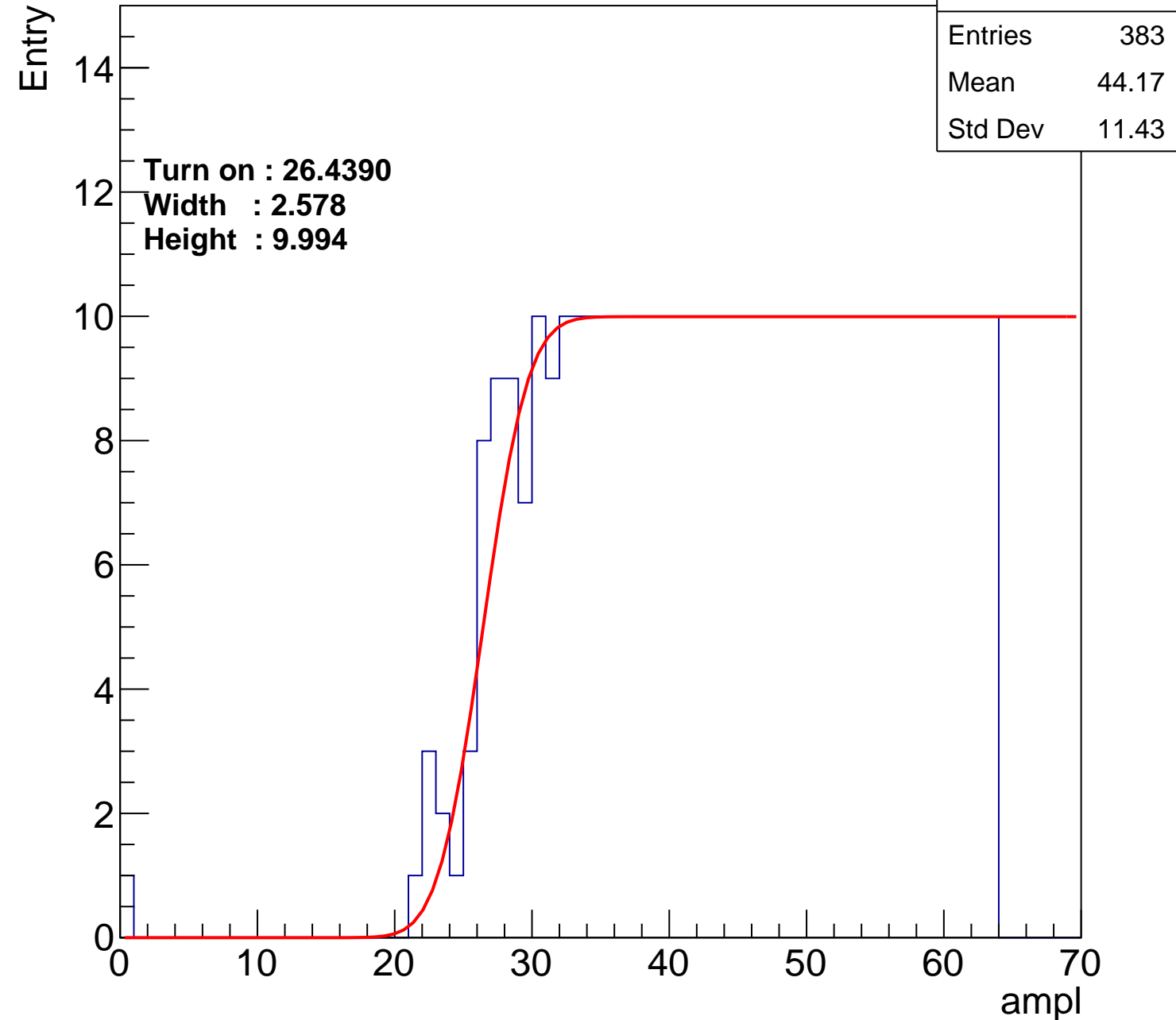
Width : 2.578

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch110

calib_packv5_042523_0143.root, FC#12, port B1

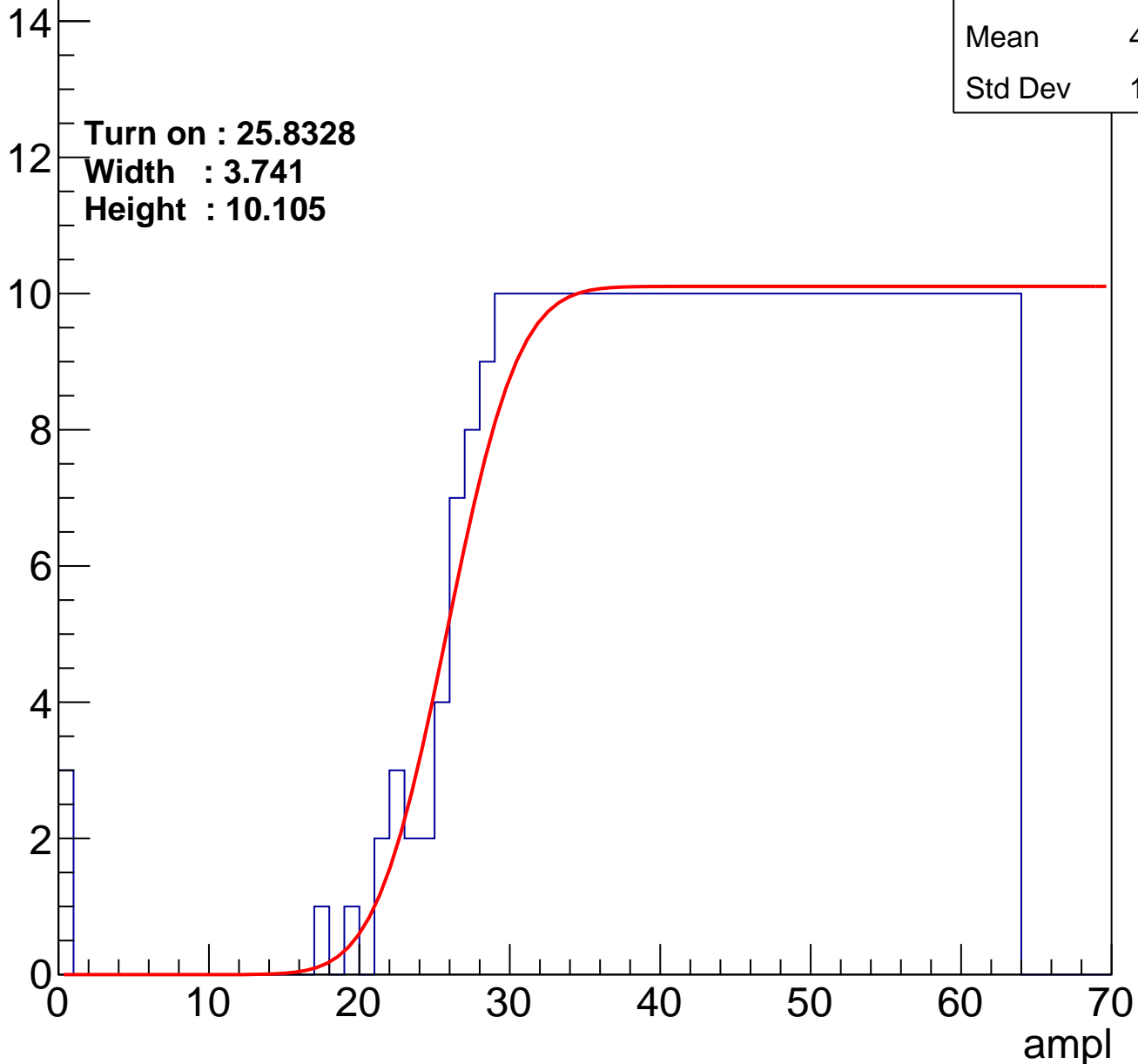
Entries	392
Mean	43.59
Std Dev	12.03

Turn on : 25.8328

Width : 3.741

Height : 10.105

Entry



B0L102S, U23-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.9479

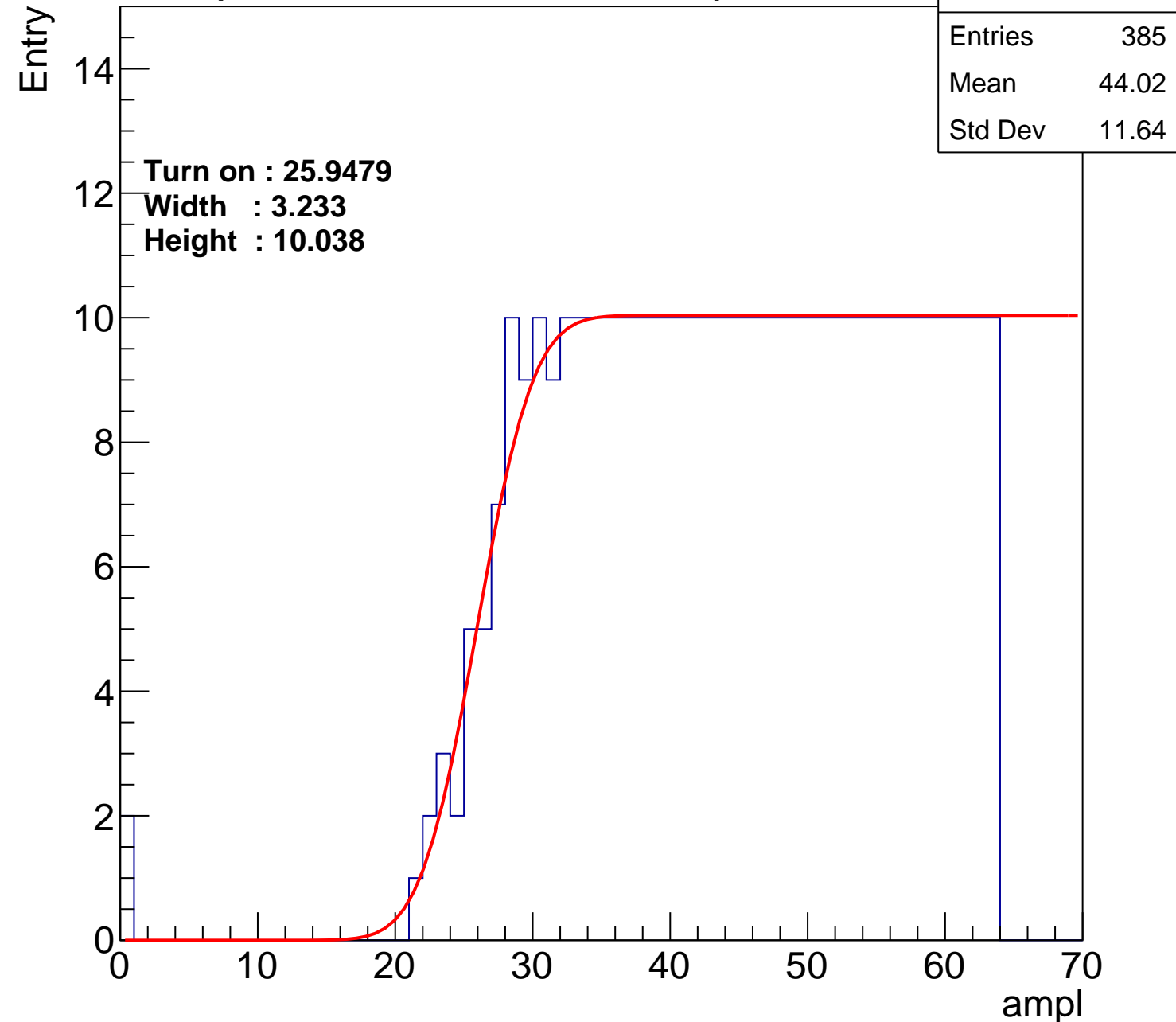
Width : 3.233

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.84
Std Dev	11.89

Turn on : 25.9567

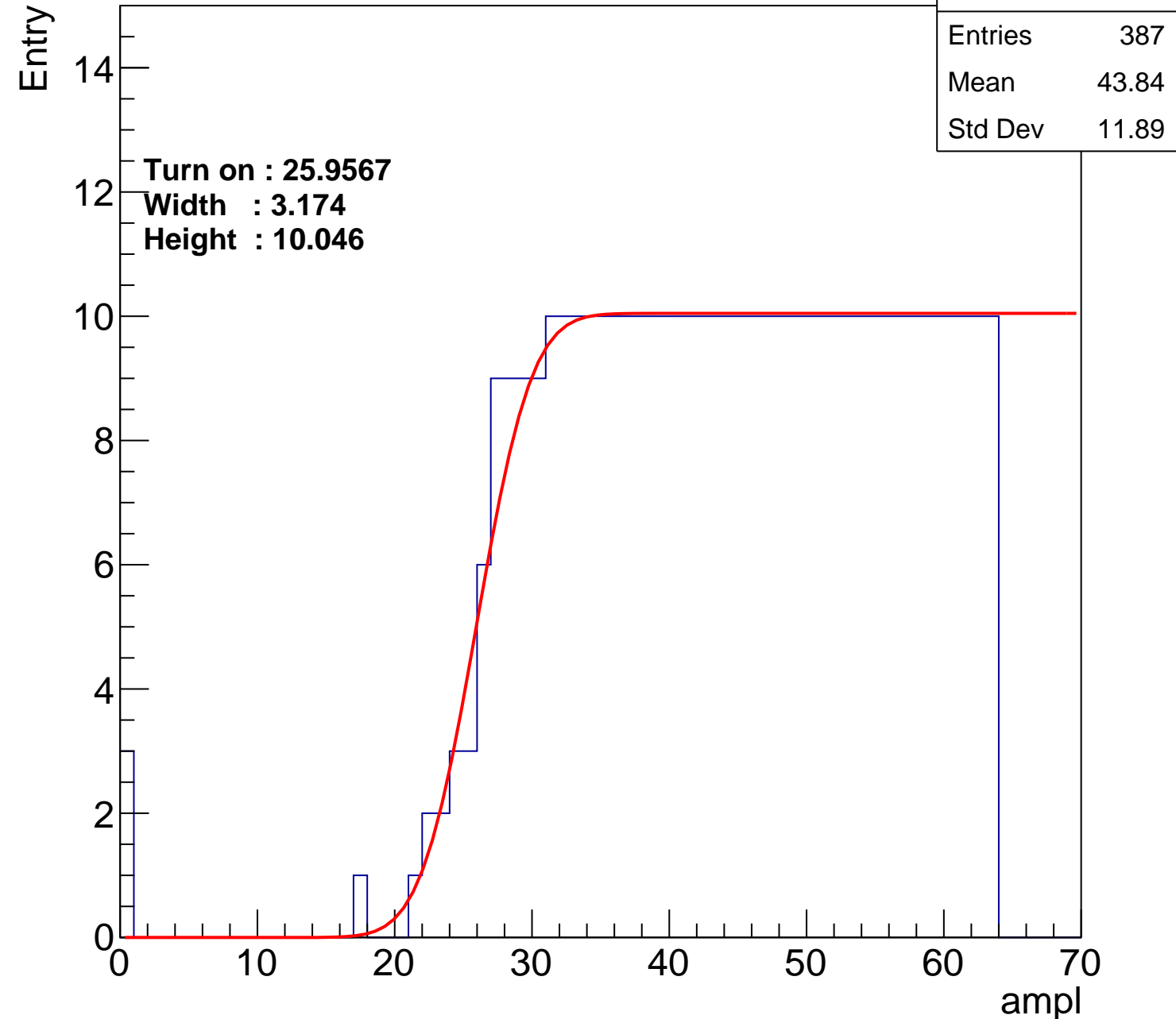
Width : 3.174

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch113

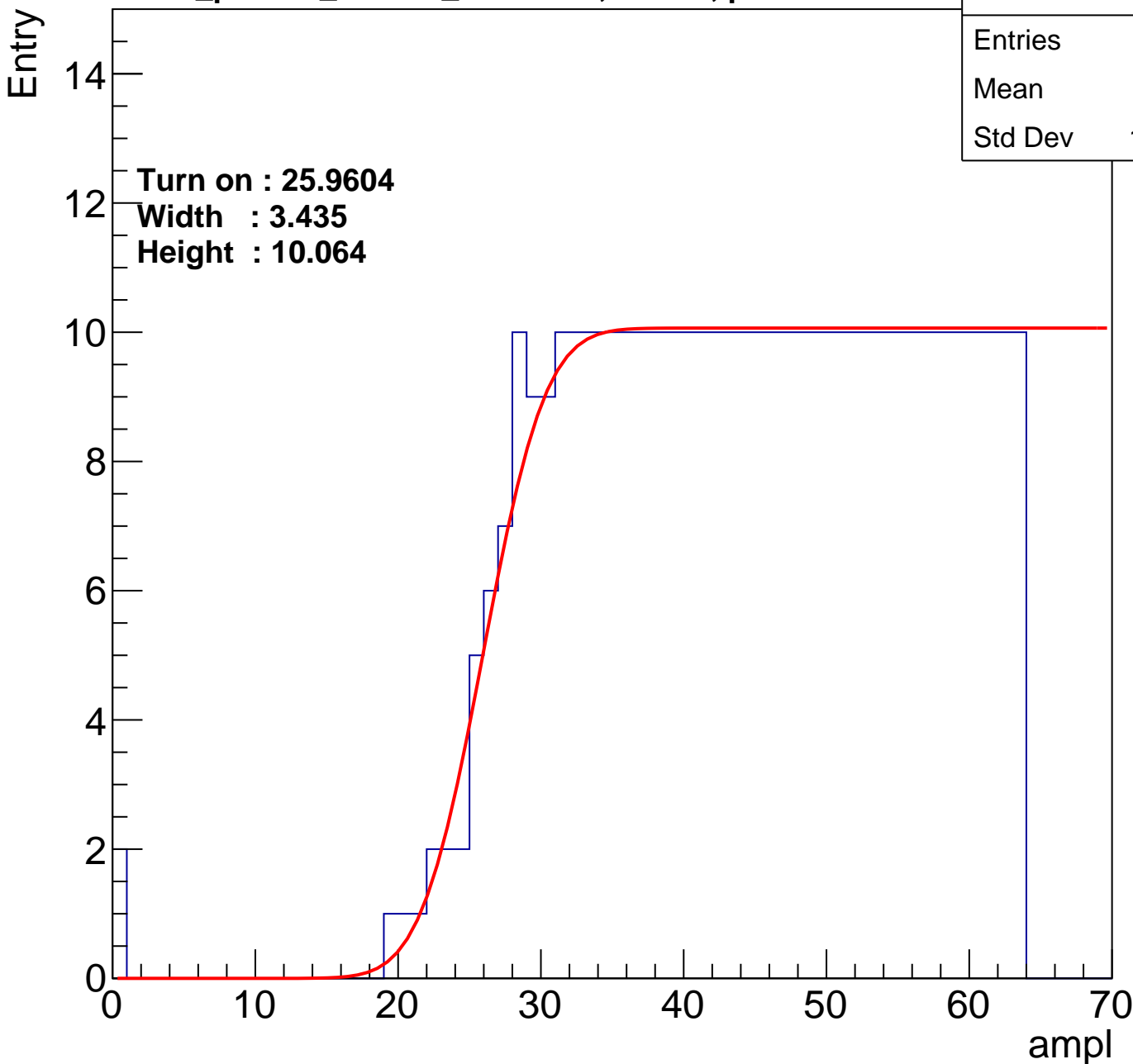
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.9604

Width : 3.435

Height : 10.064

Entries	387
Mean	43.9
Std Dev	11.73



B0L102S, U23-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.89
Std Dev	11.98

Turn on : 26.1475

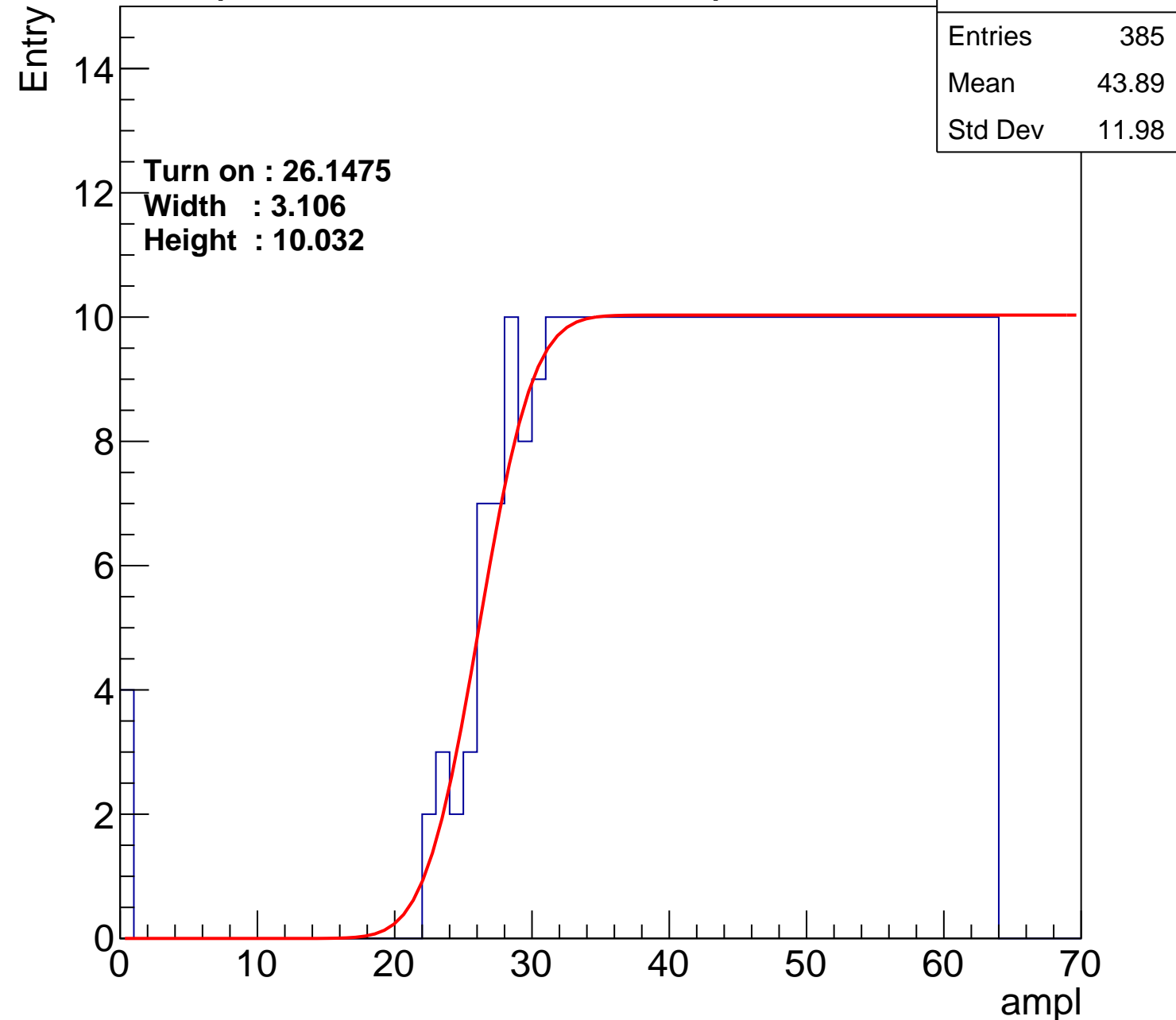
Width : 3.106

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.44
Std Dev	11.76

Turn on : 27.7181

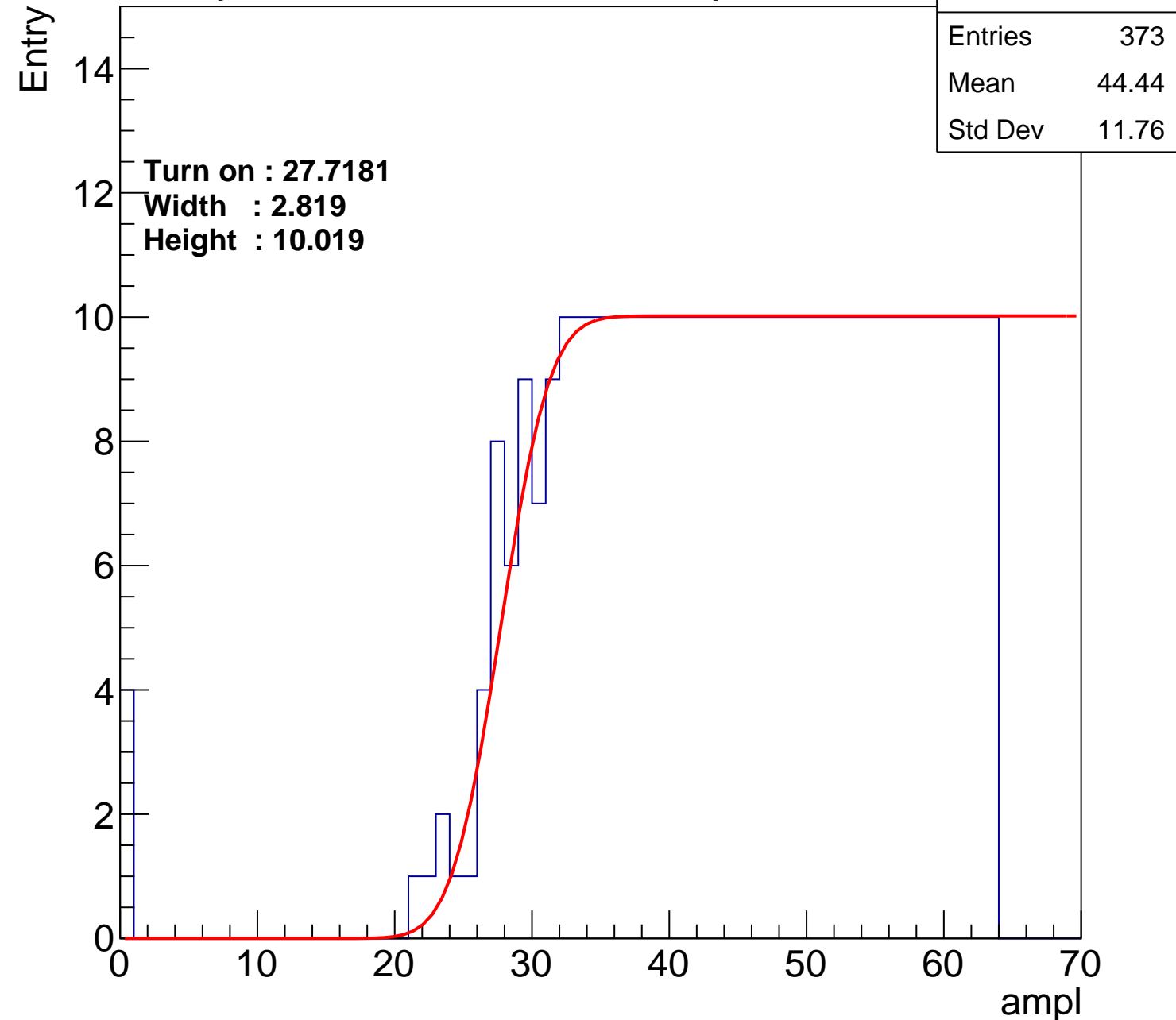
Width : 2.819

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.66
Std Dev	11.73

Turn on : 24.6158

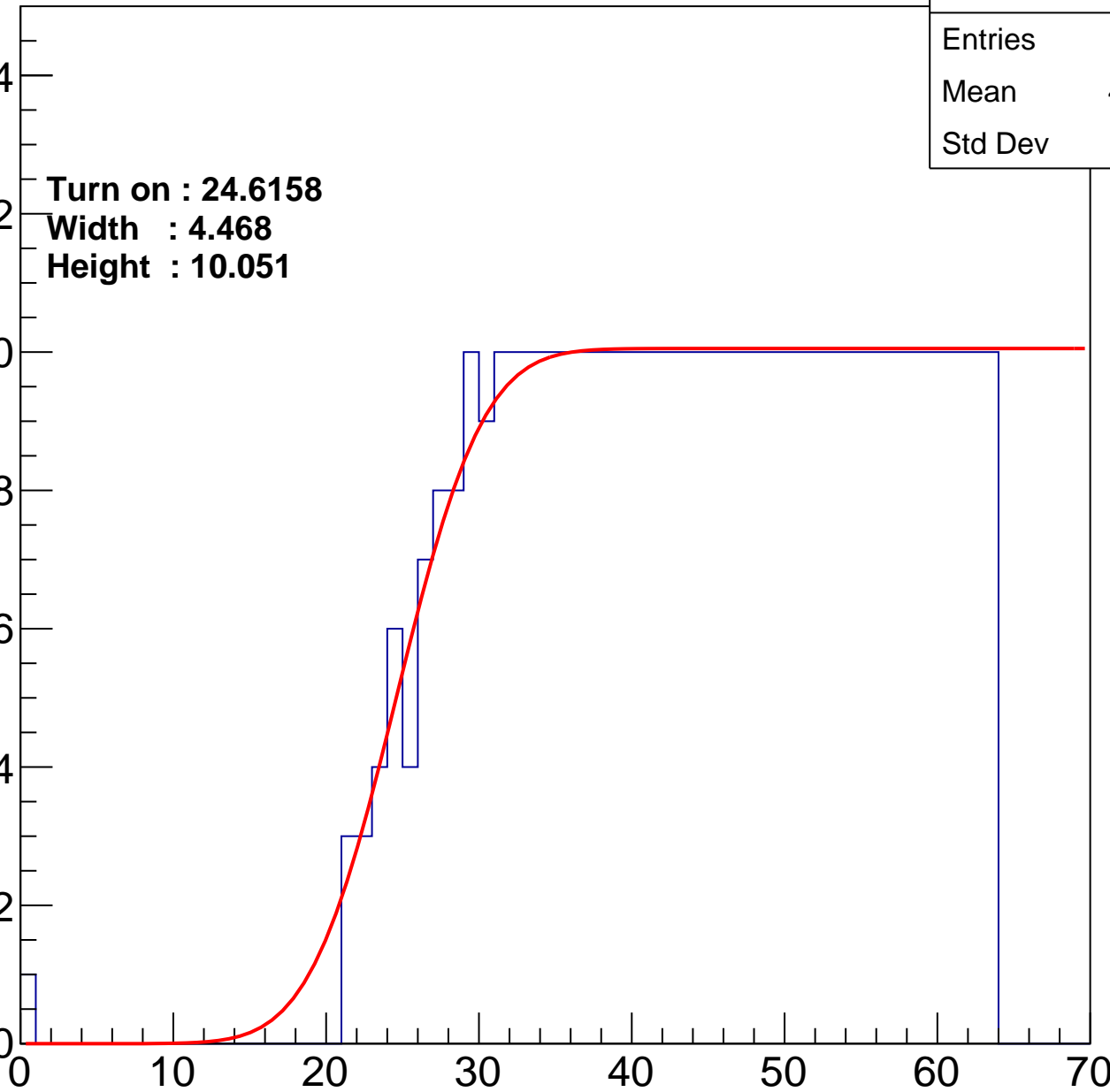
Width : 4.468

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.87
Std Dev	11.22

Turn on : 28.2074

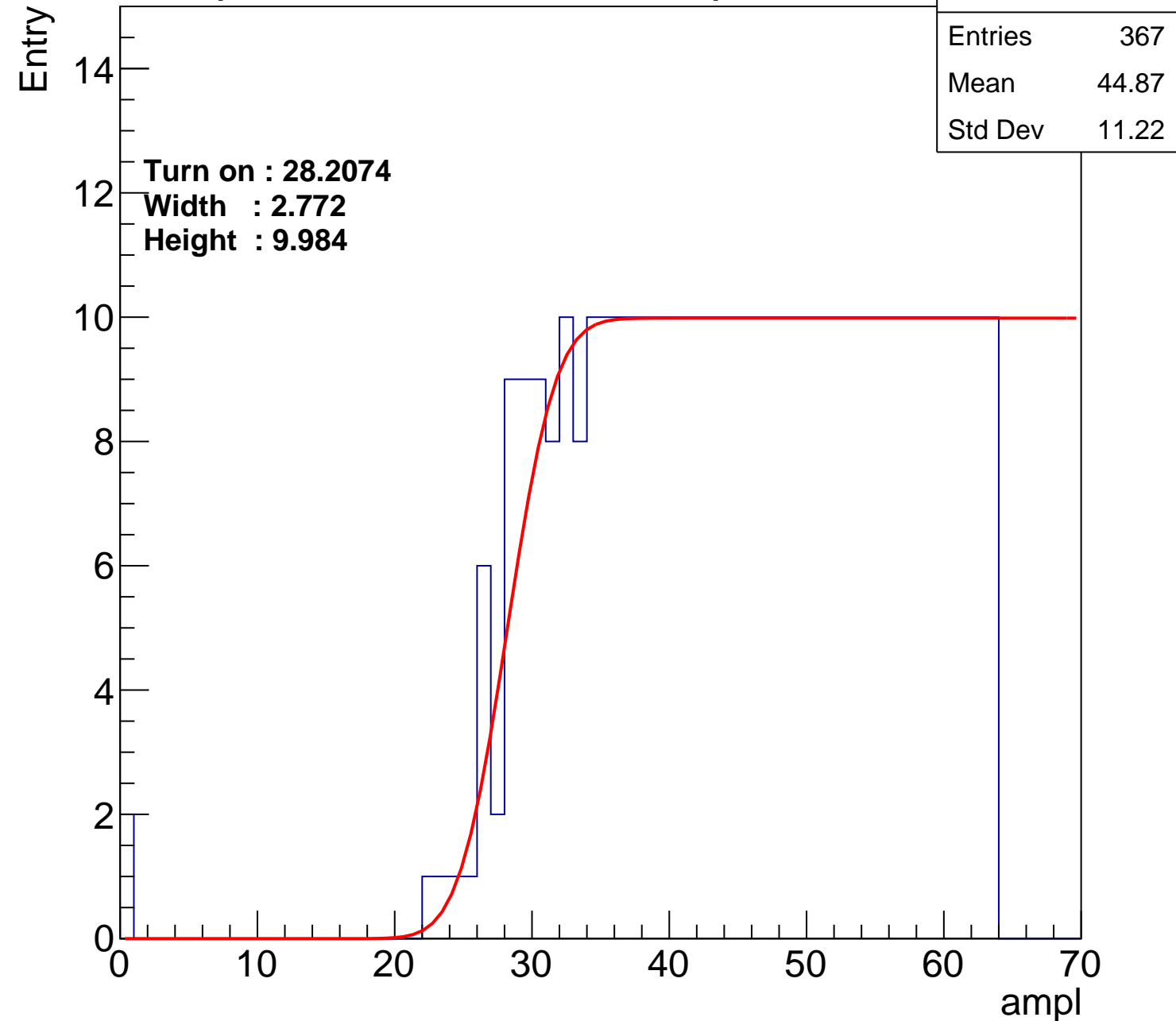
Width : 2.772

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.8959

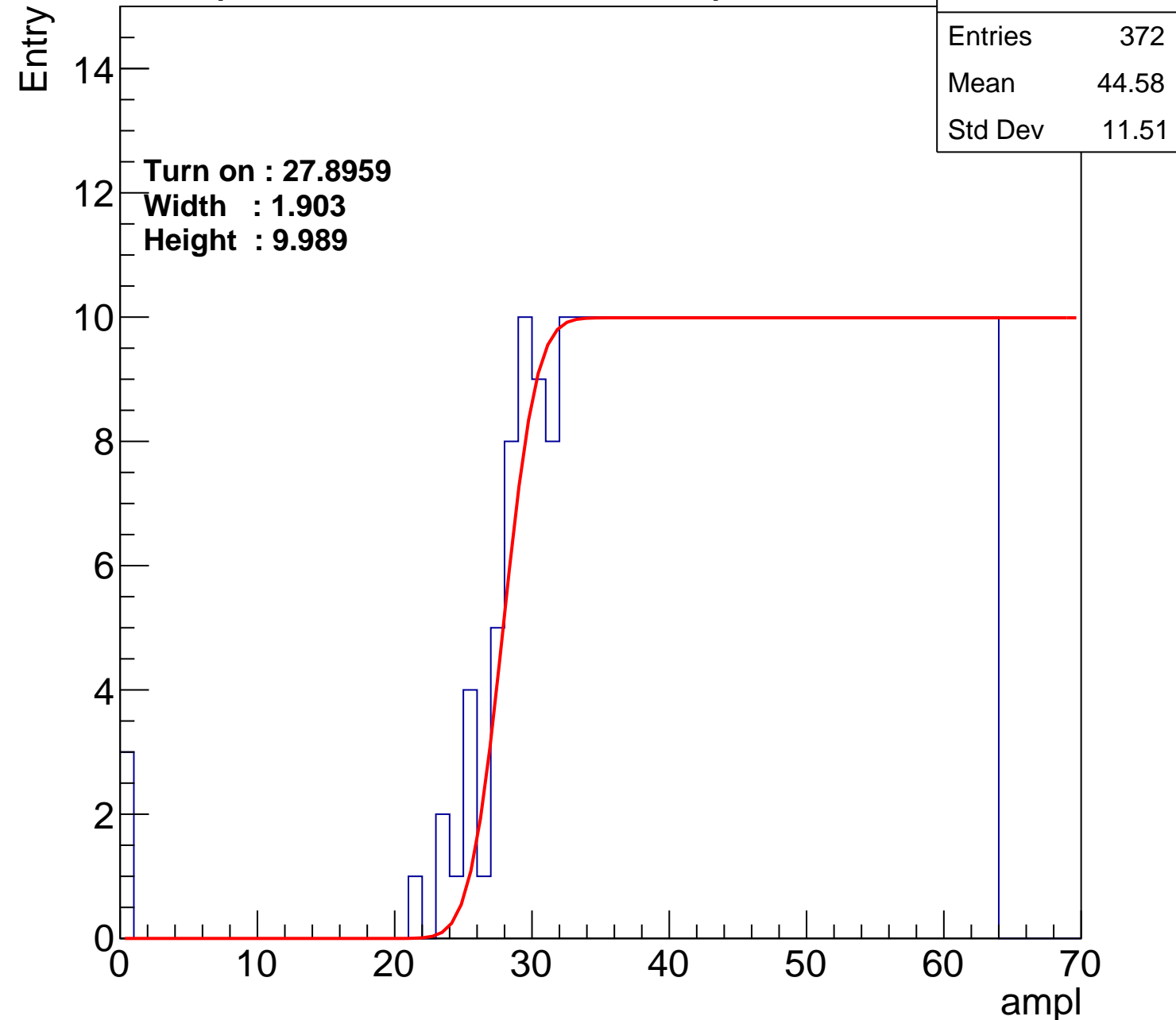
Width : 1.903

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch119

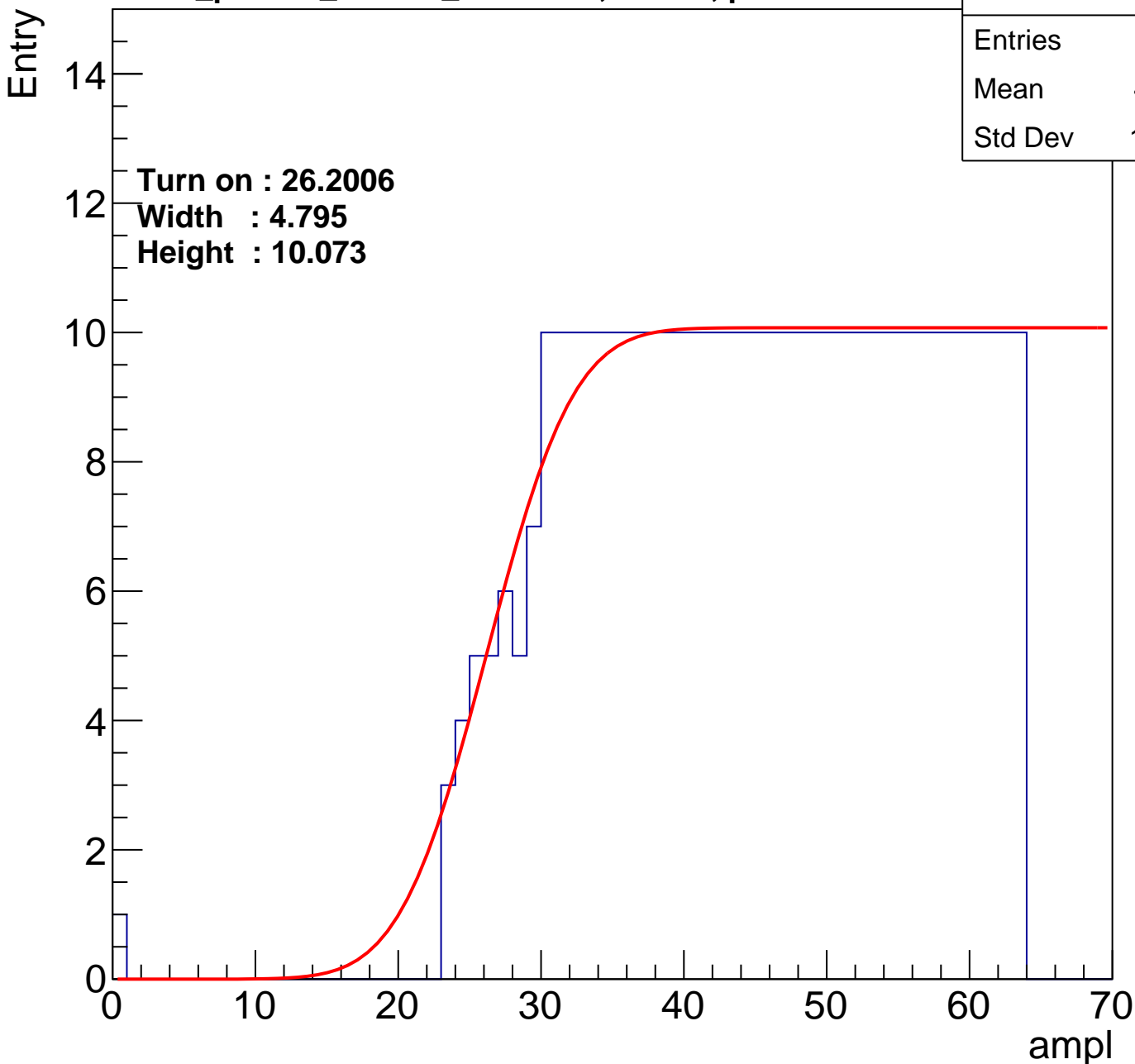
calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.51
Std Dev	11.25

Turn on : 26.2006

Width : 4.795

Height : 10.073



B0L102S, U23-ch120

calib_packv5_042523_0143.root, FC#12, port B1

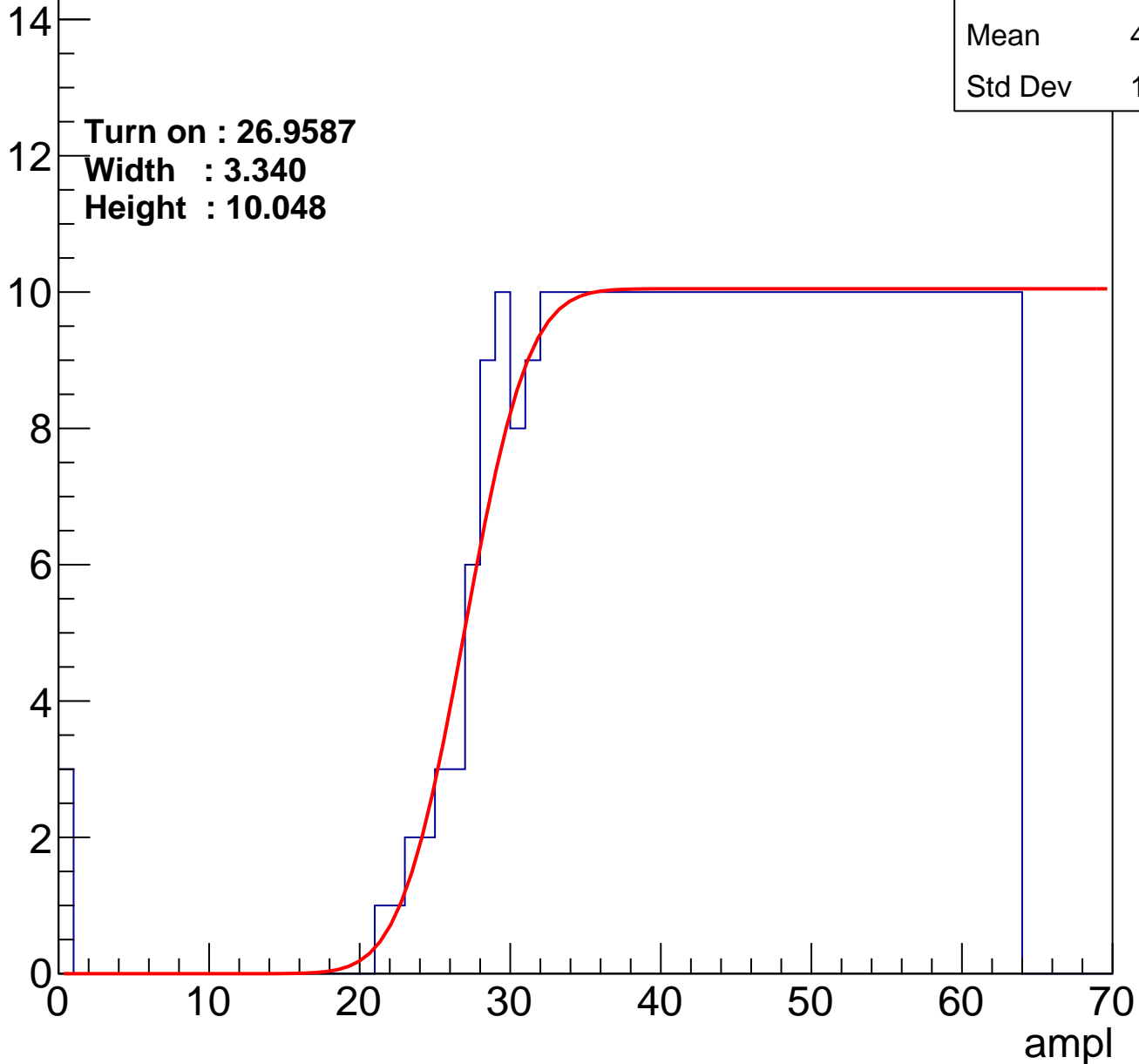
Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.9587

Width : 3.340

Height : 10.048

Entry



B0L102S, U23-ch121

calib_packv5_042523_0143.root, FC#12, port B1

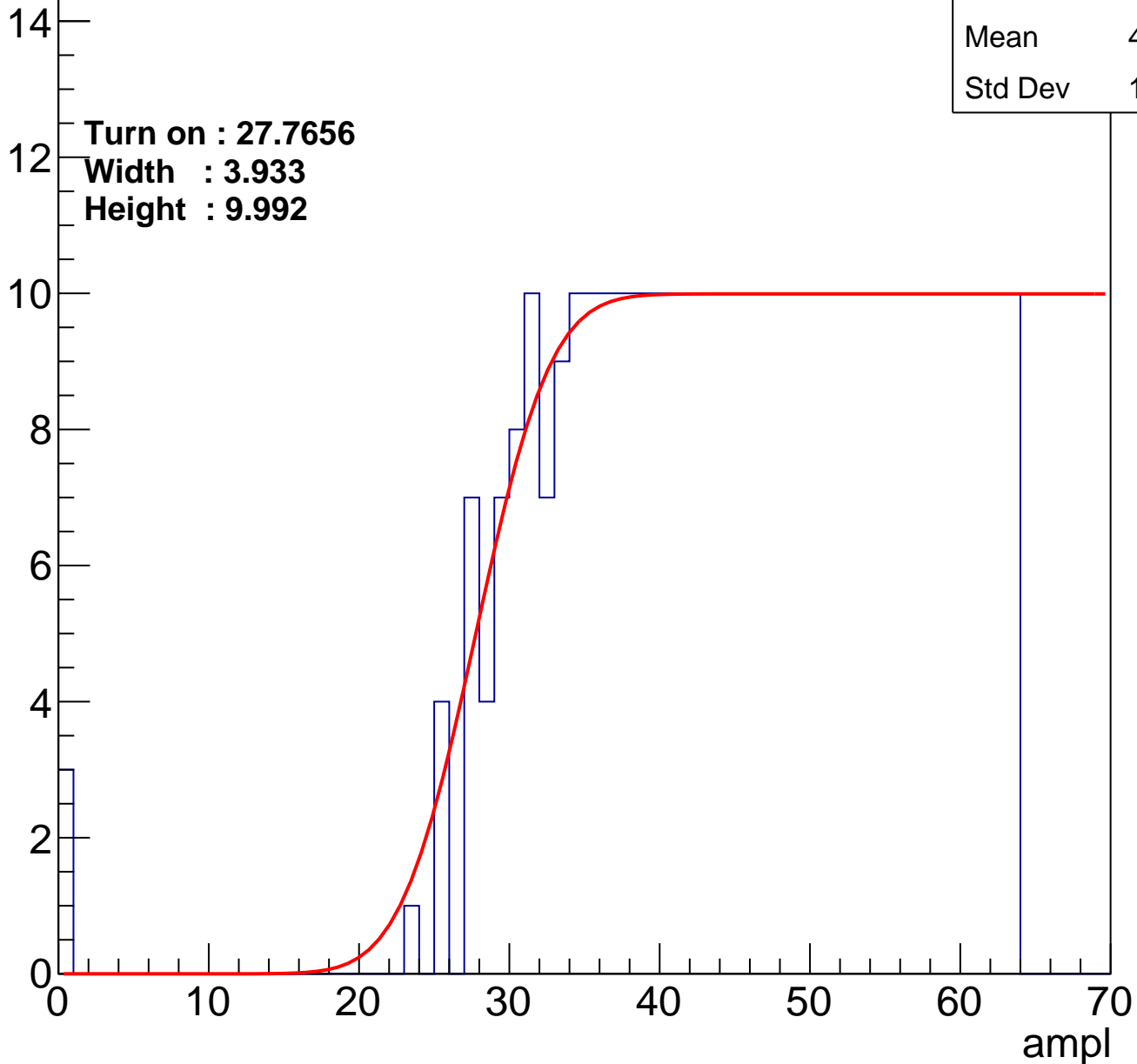
Entries	360
Mean	45.13
Std Dev	11.27

Turn on : 27.7656

Width : 3.933

Height : 9.992

Entry



B0L102S, U23-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	43.97
Std Dev	12.55

Turn on : 27.8482

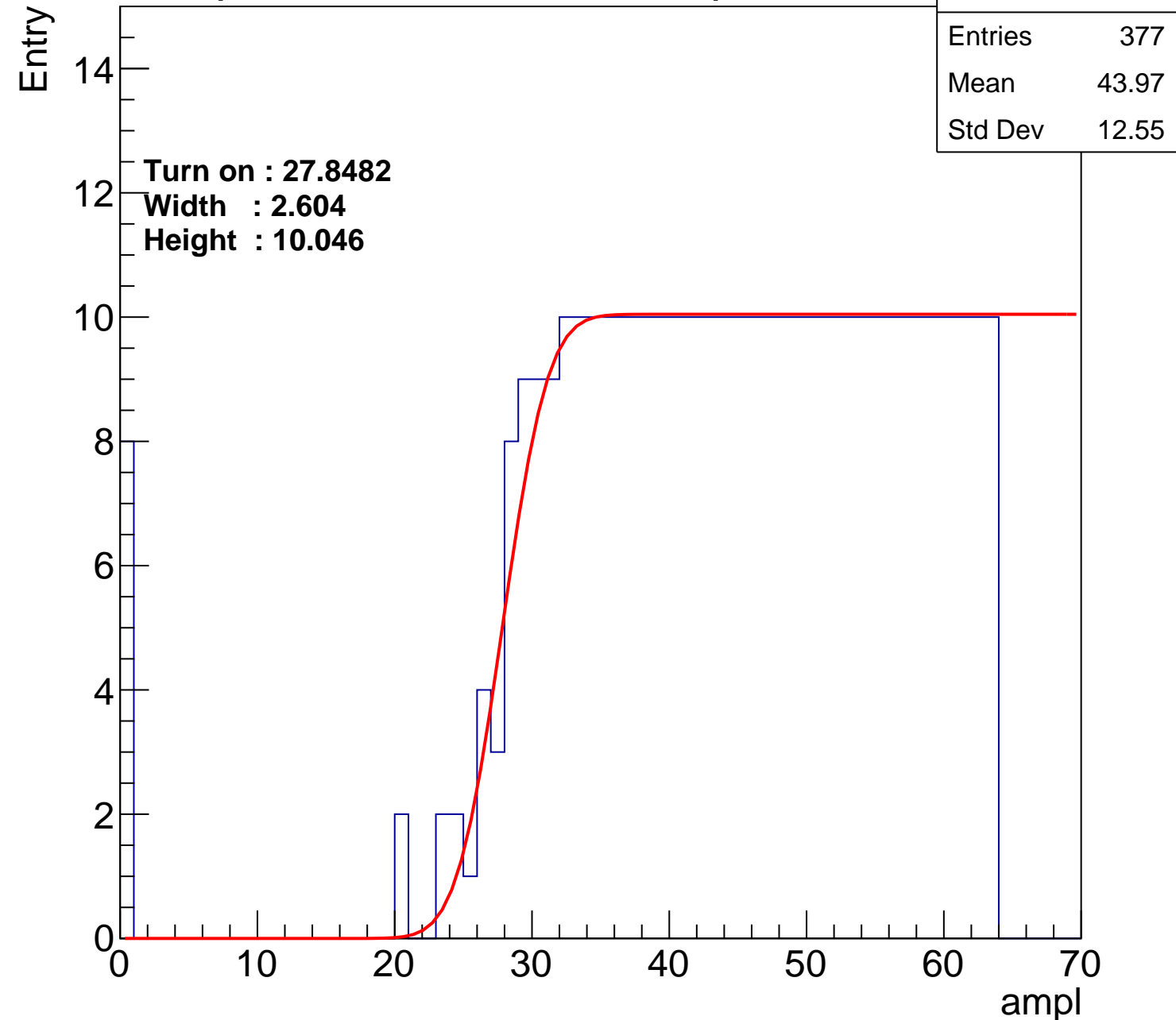
Width : 2.604

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch123

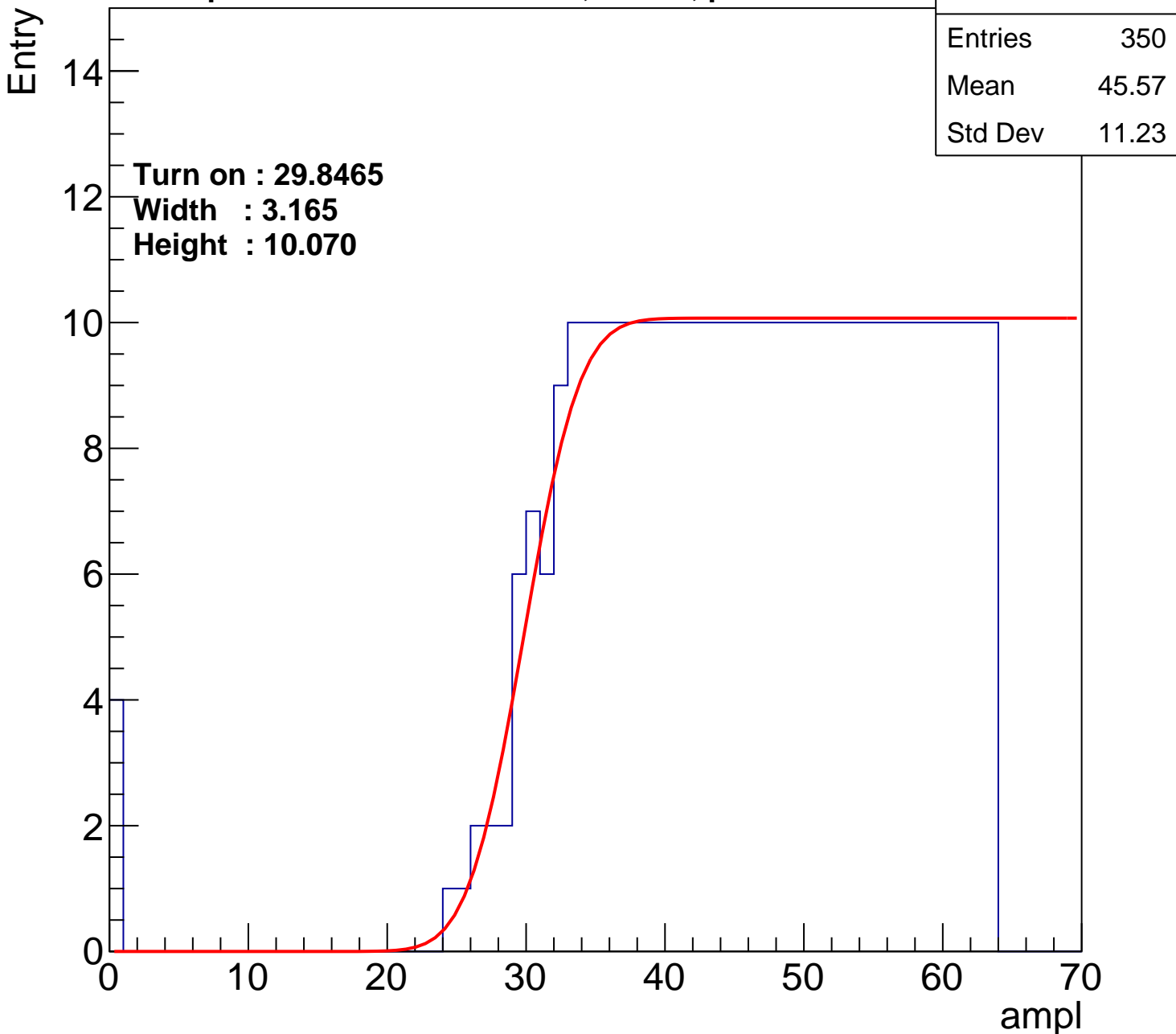
calib_packv5_042523_0143.root, FC#12, port B1

Entries	350
Mean	45.57
Std Dev	11.23

Turn on : 29.8465

Width : 3.165

Height : 10.070



B0L102S, U23-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.51
Std Dev	12.16

Turn on : 24.9963

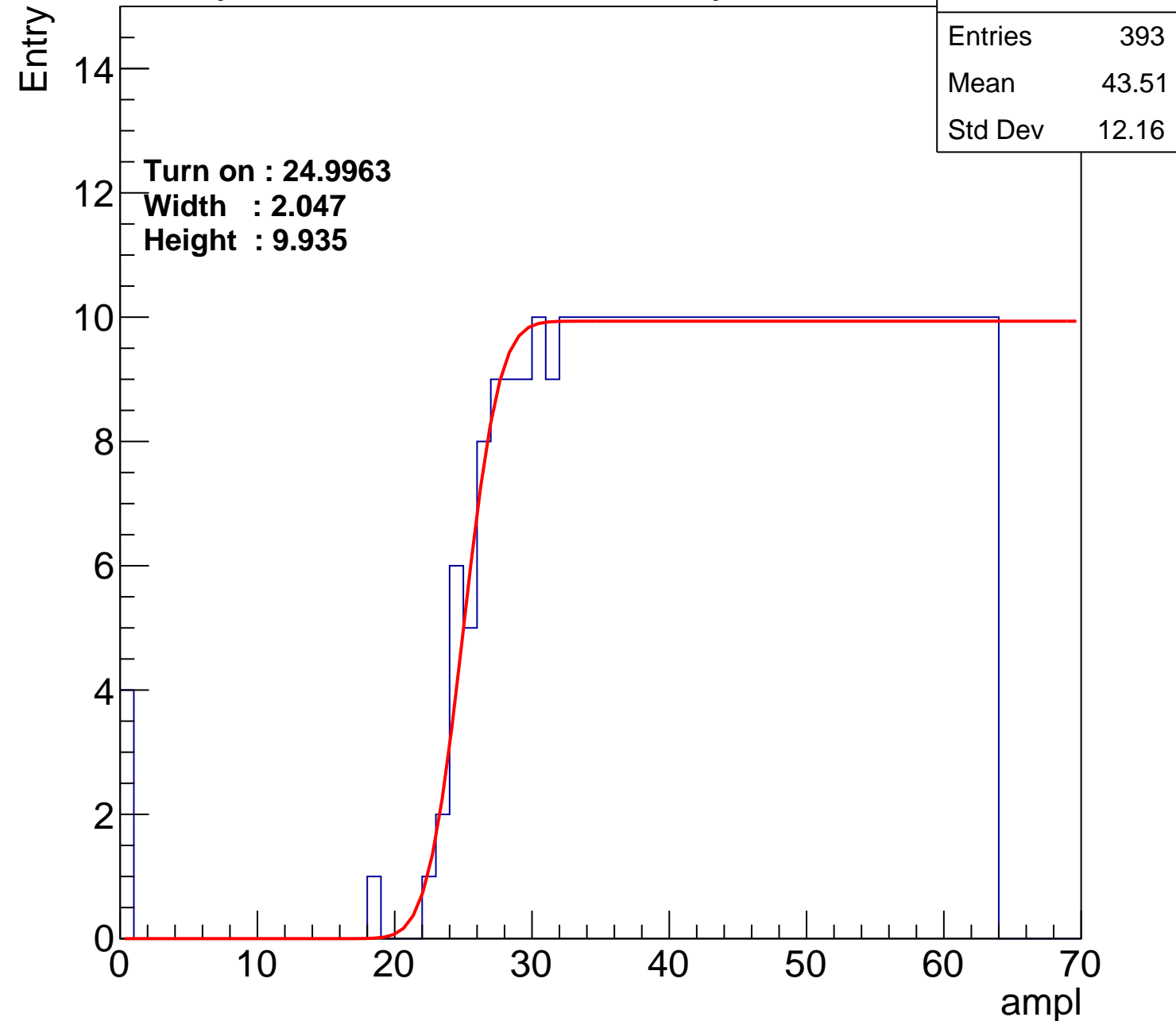
Width : 2.047

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.72
Std Dev	11.23

Turn on : 27.3543

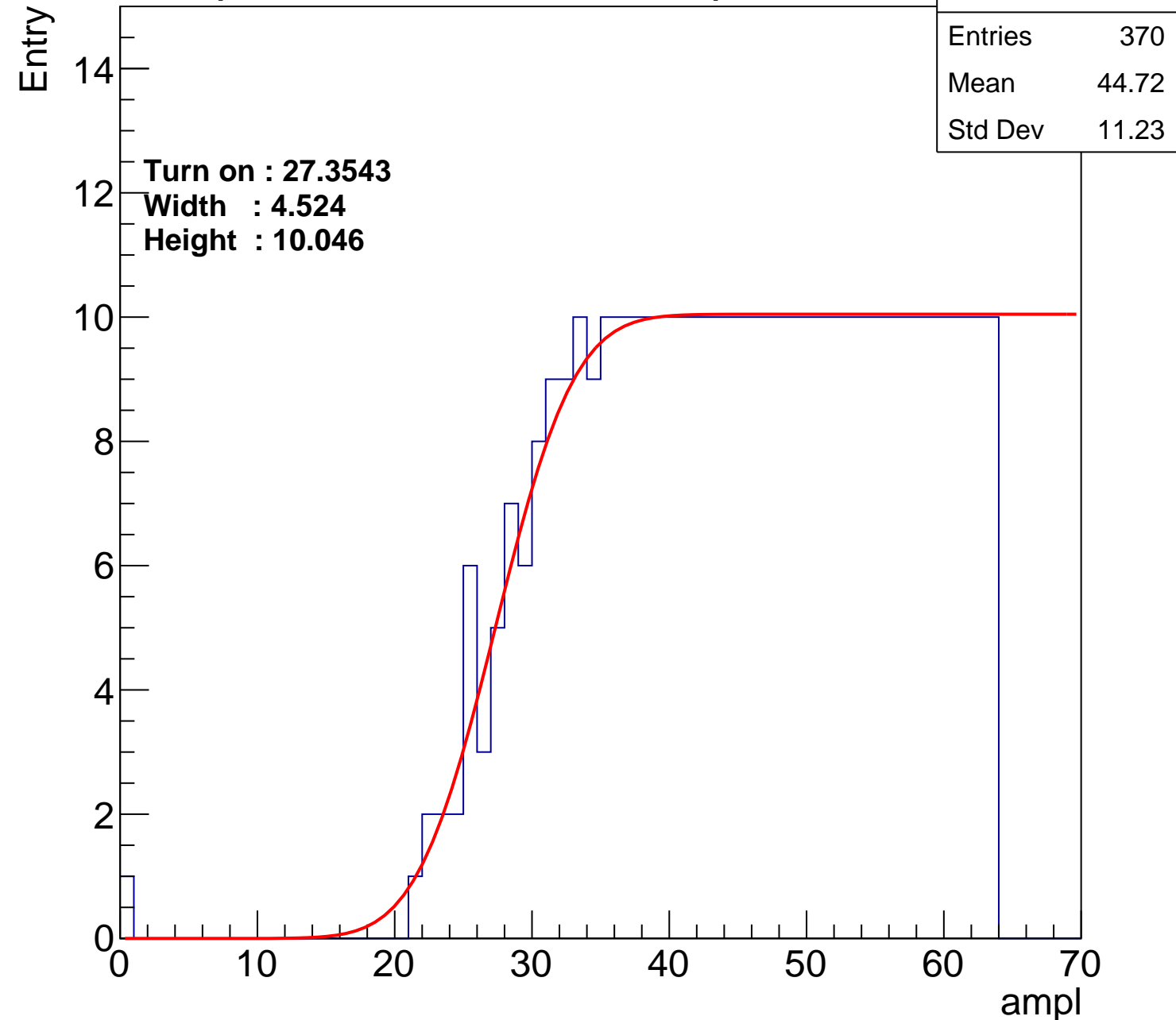
Width : 4.524

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.23
Std Dev	12.39

Turn on : 25.6072

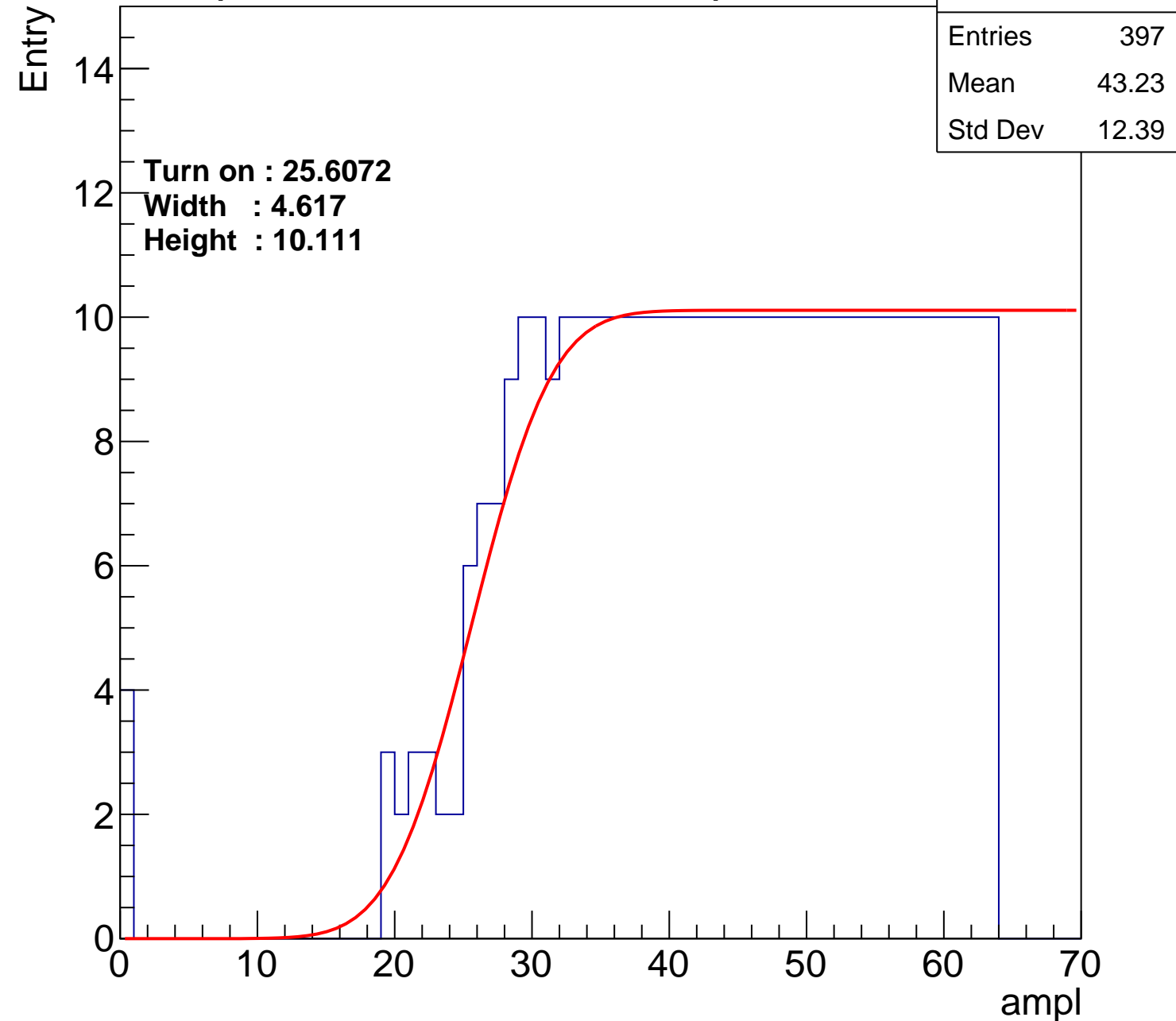
Width : 4.617

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.6
Std Dev	12.64

Turn on : 23.8001

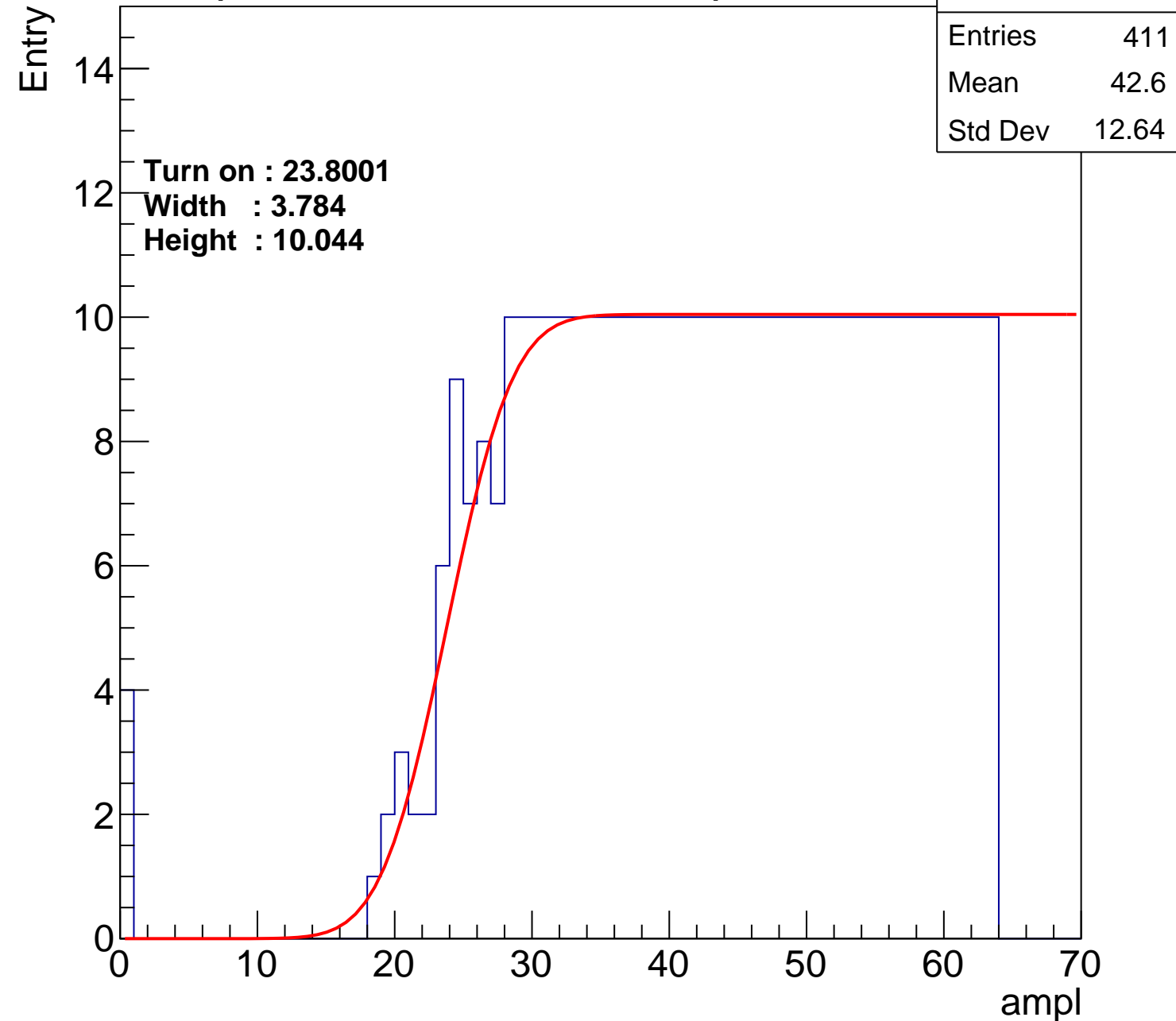
Width : 3.784

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U23-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.6
Std Dev	12.64

Turn on : 23.8001

Width : 3.784

Height : 10.044

Entry

