

B0L002S, U4-ch0, adc0

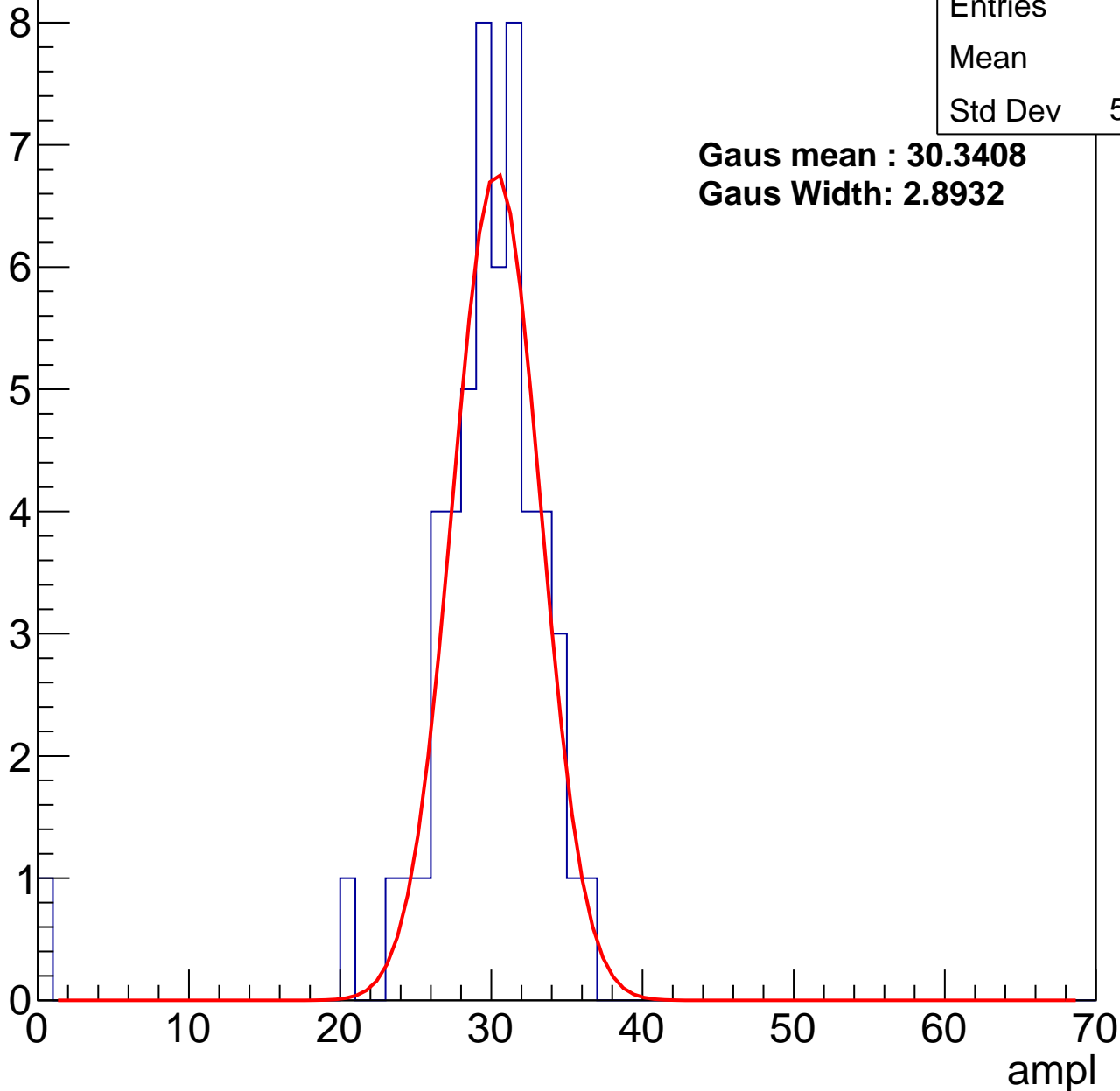
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	29
Std Dev	5.054

Gaus mean : 30.3408

Gaus Width: 2.8932



B0L002S, U4-ch0, adc1

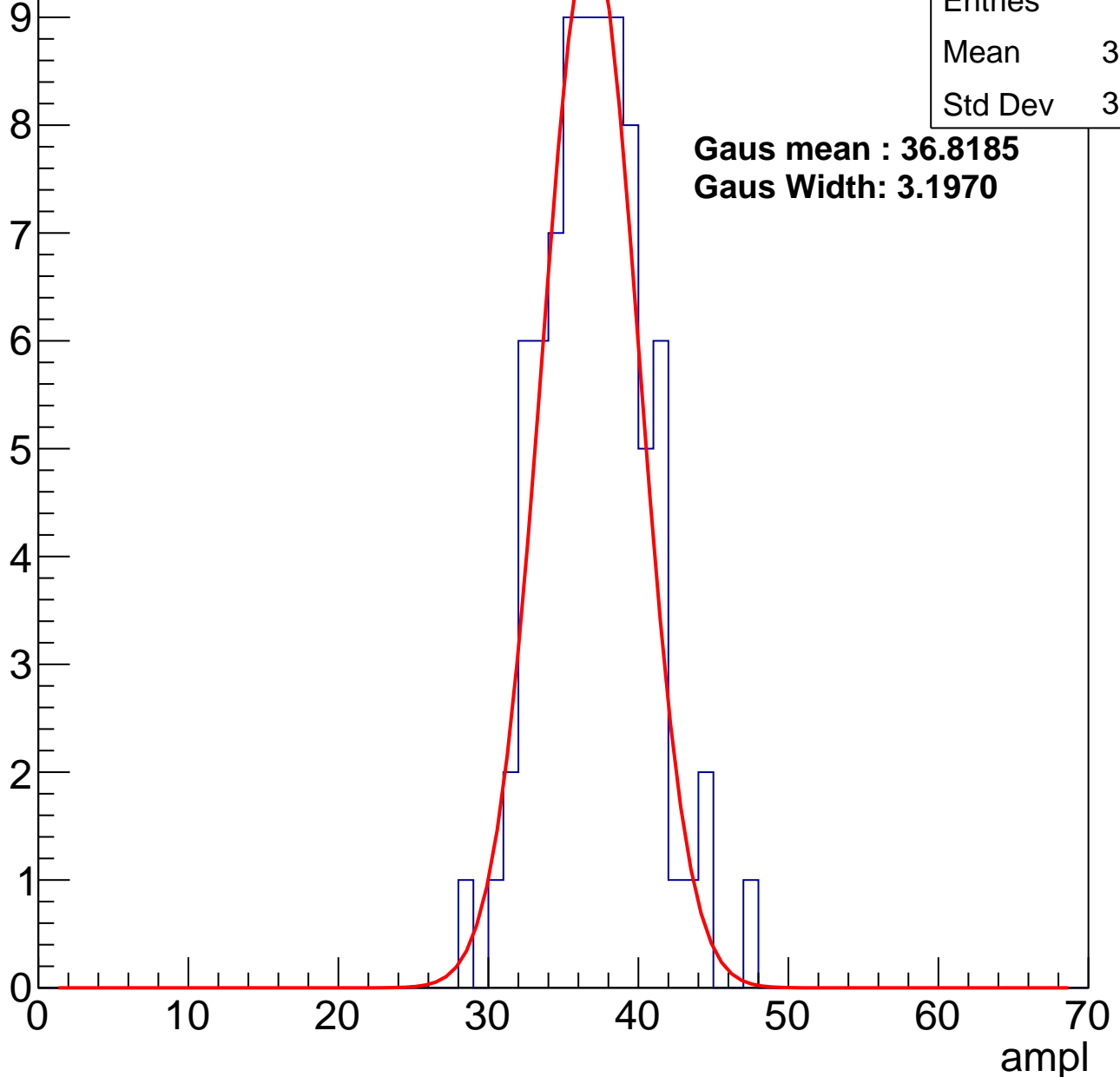
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	36.63
Std Dev	3.446

Gaus mean : 36.8185

Gaus Width: 3.1970



B0L002S, U4-ch0, adc2

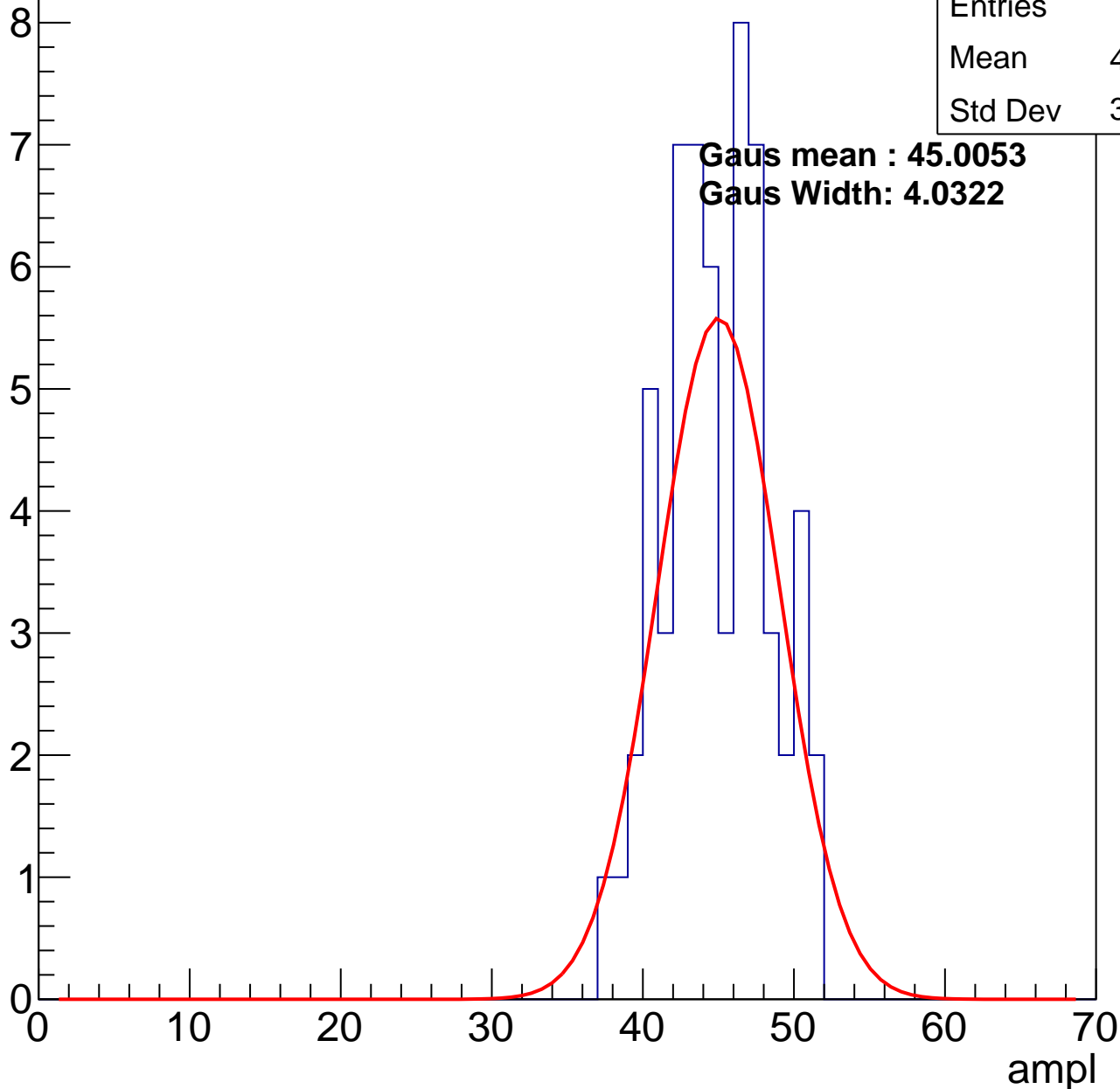
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.44
Std Dev	3.395

Gaus mean : 45.0053

Gaus Width: 4.0322

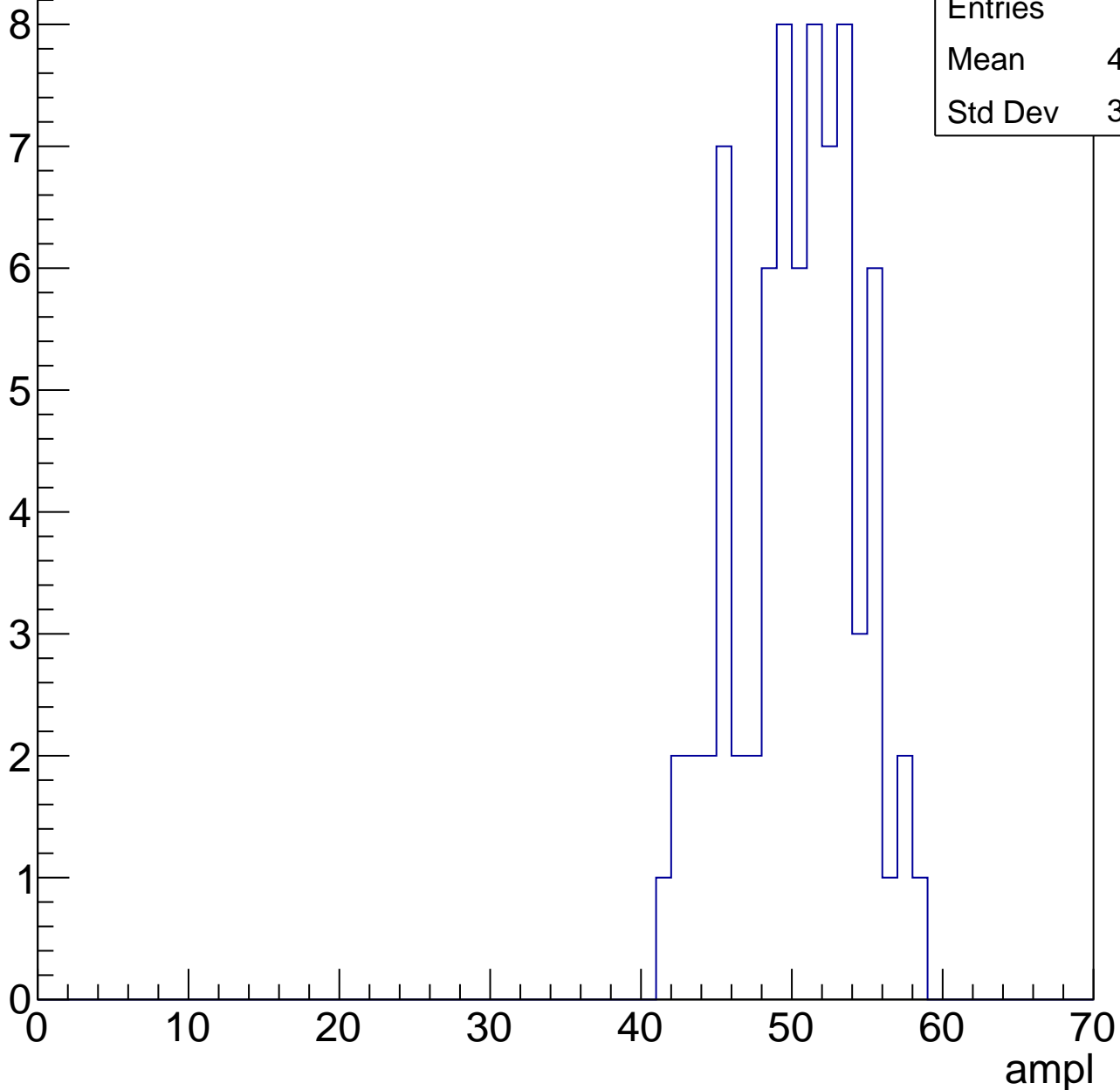


B0L002S, U4-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	49.95
Std Dev	3.925



B0L002S, U4-ch0, adc4

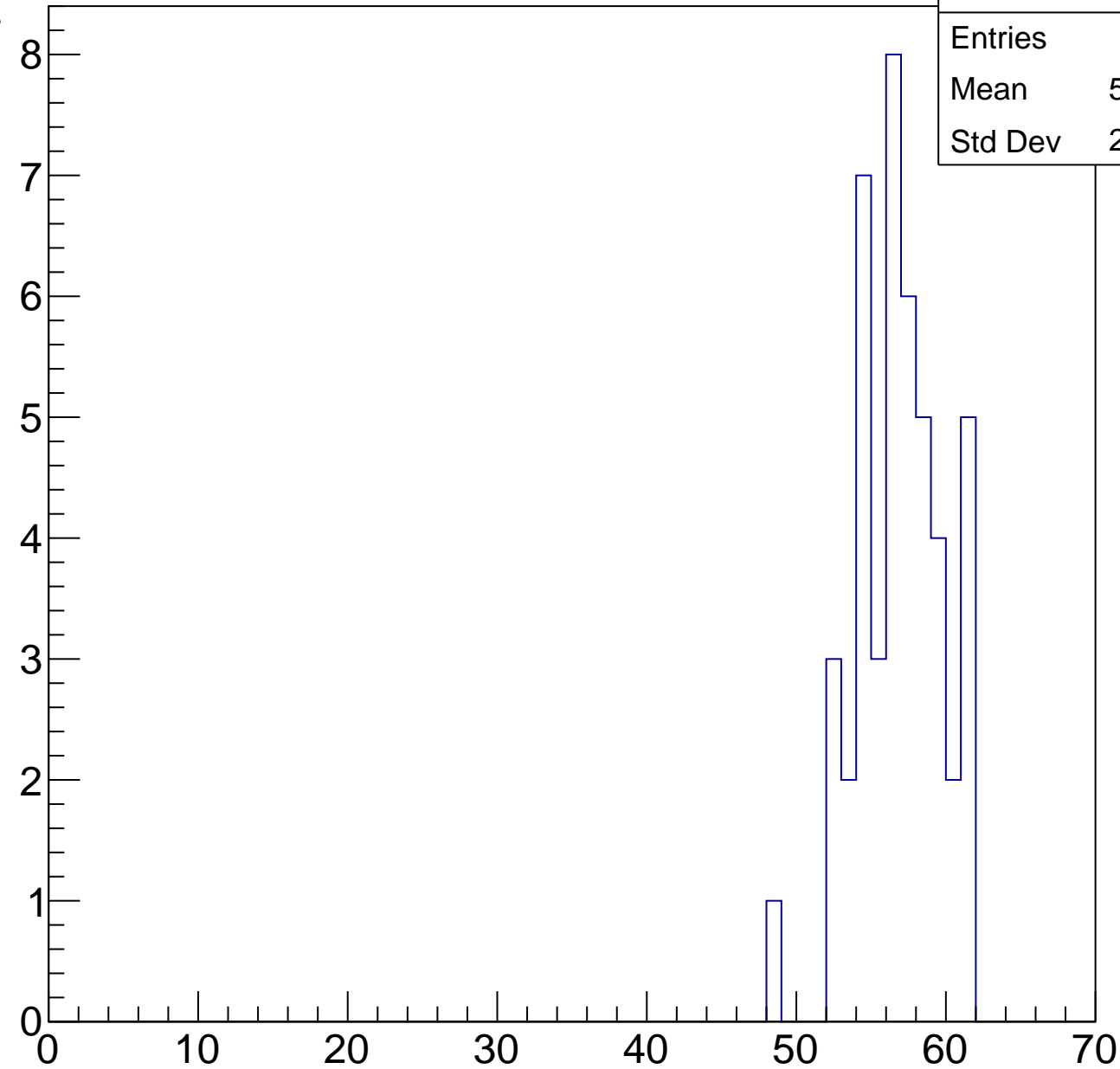
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	56.39
Std Dev	2.847

ampl



B0L002S, U4-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	47
Mean	59.21
Std Dev	8.966

Entry

10

8

6

4

2

0

0

10

20

30

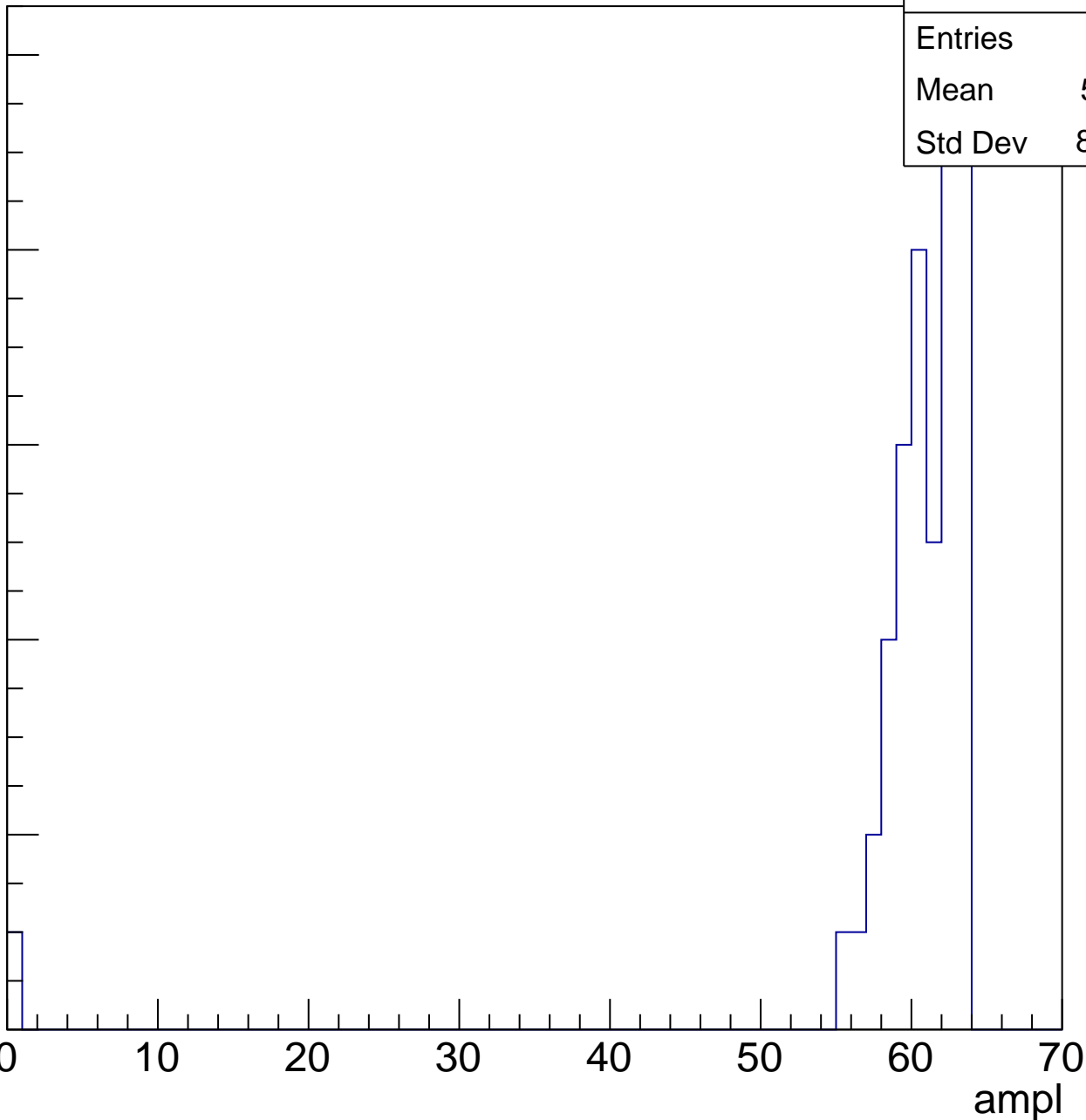
40

50

60

70

ampl



B0L002S, U4-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch1, adc0

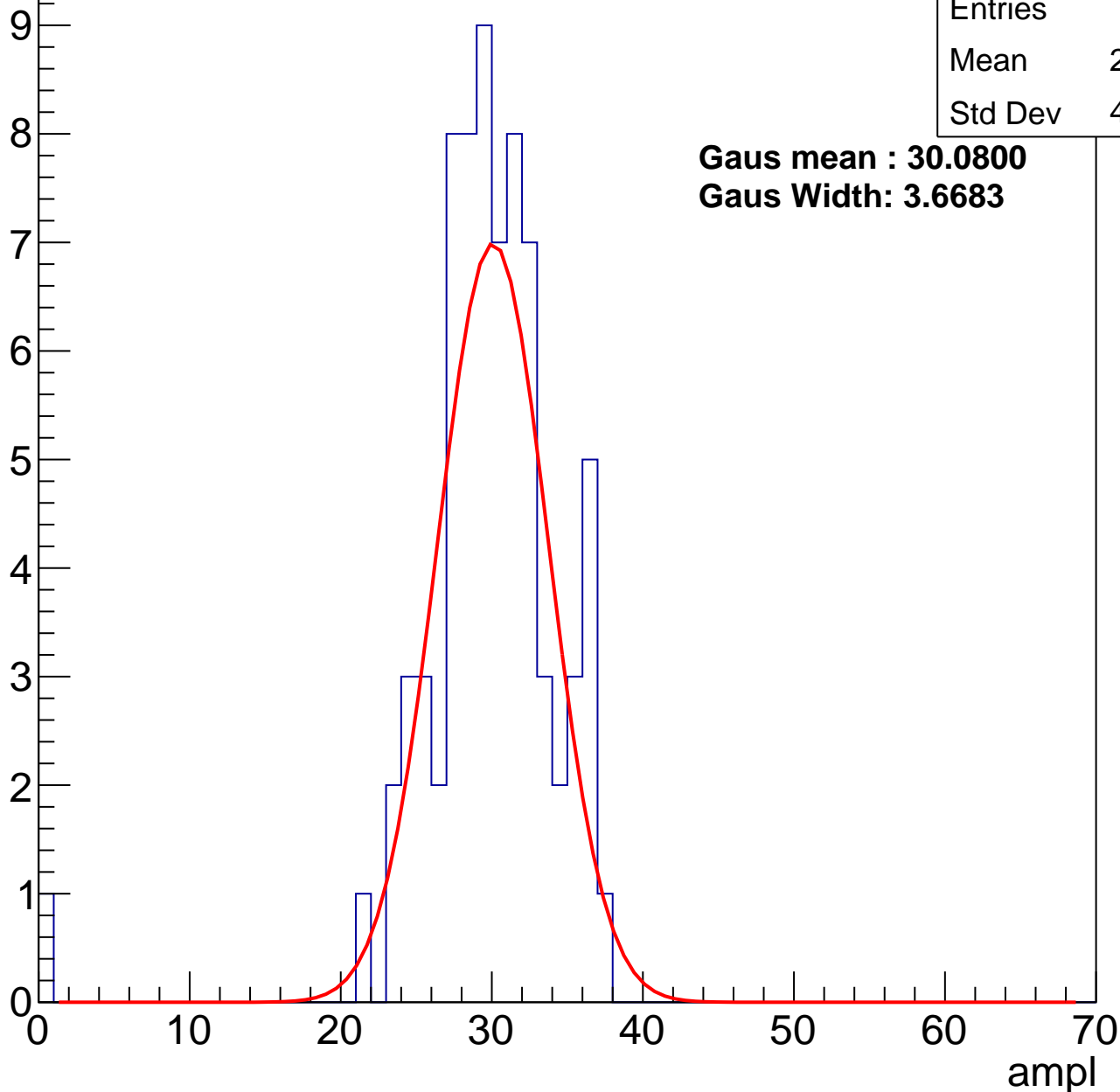
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.29
Std Dev	4.923

Gaus mean : 30.0800

Gaus Width: 3.6683



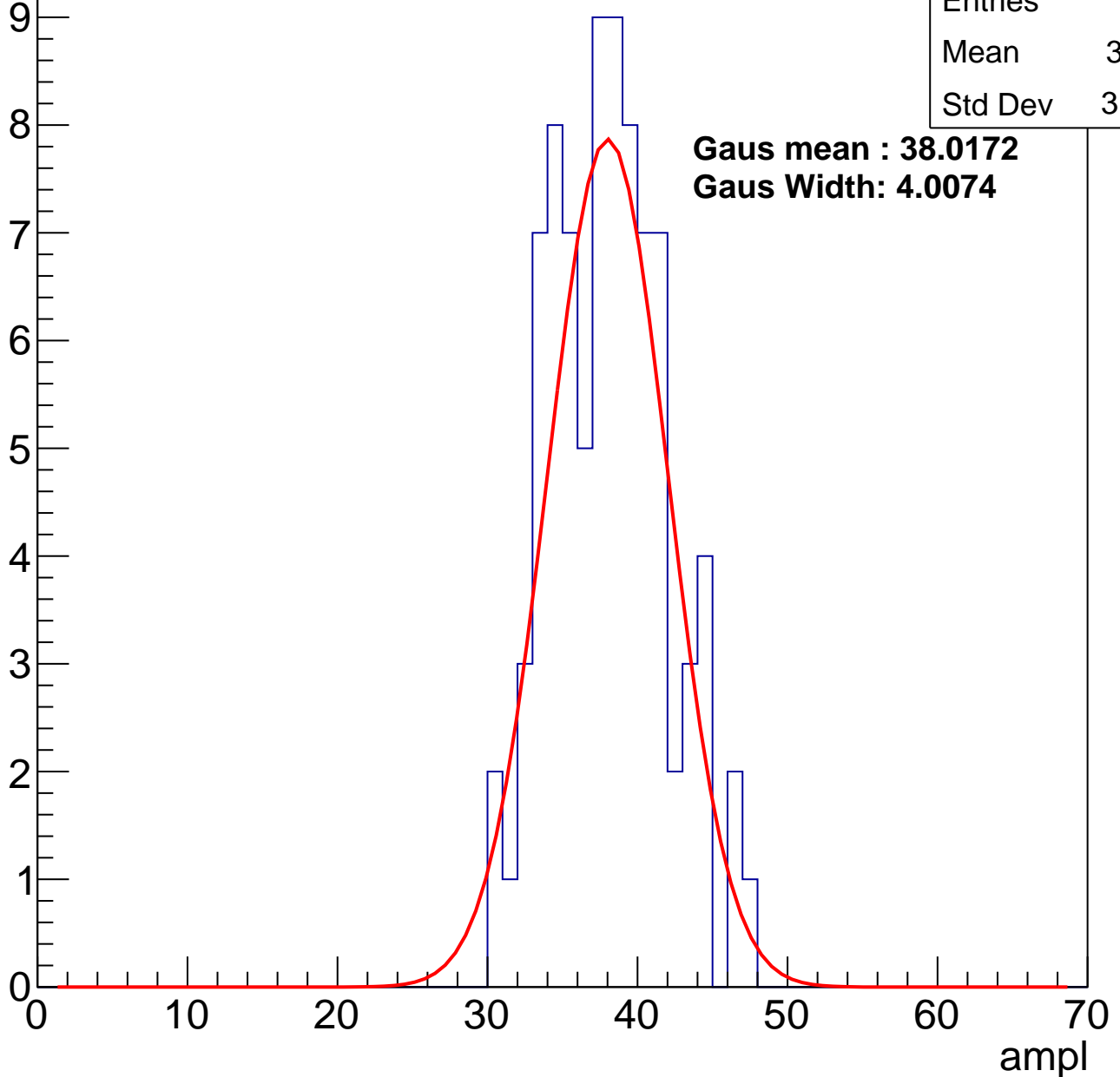
B0L002S, U4-ch1, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	37.61
Std Dev	3.798

Gaus mean : 38.0172
Gaus Width: 4.0074



B0L002S, U4-ch1, adc2

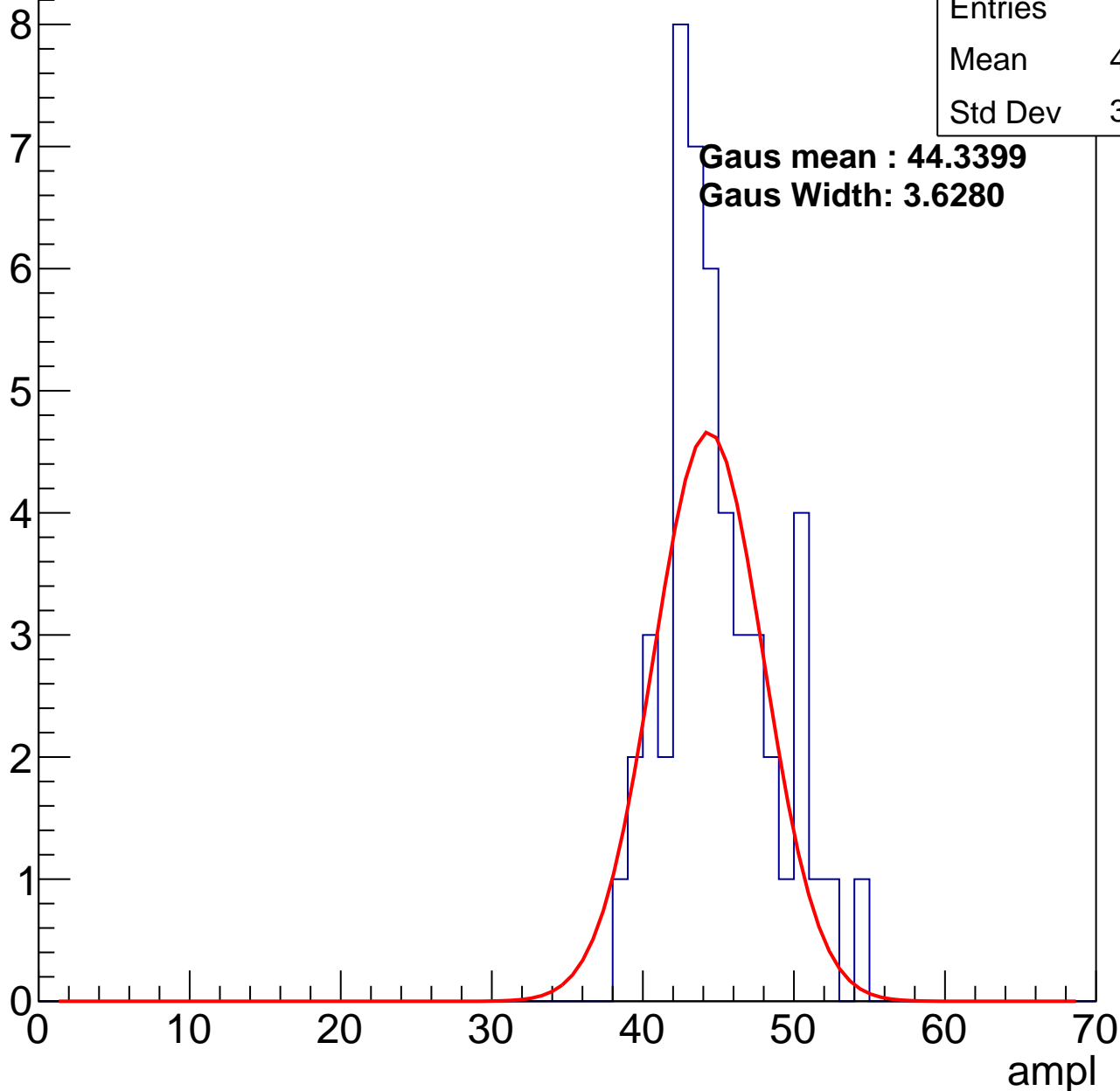
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	44.49
Std Dev	3.603

Gaus mean : 44.3399

Gaus Width: 3.6280

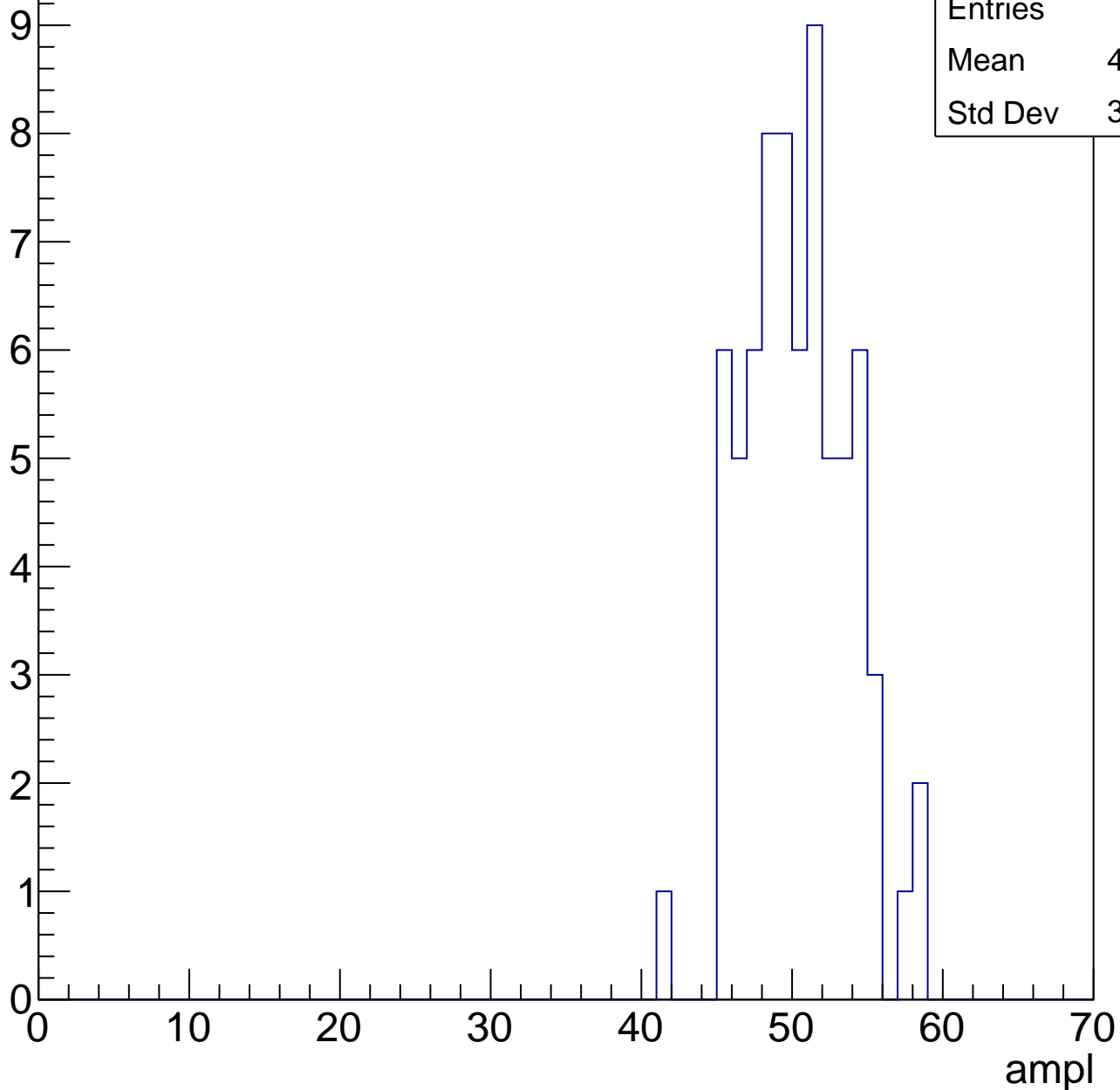


B0L002S, U4-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	49.93
Std Dev	3.412

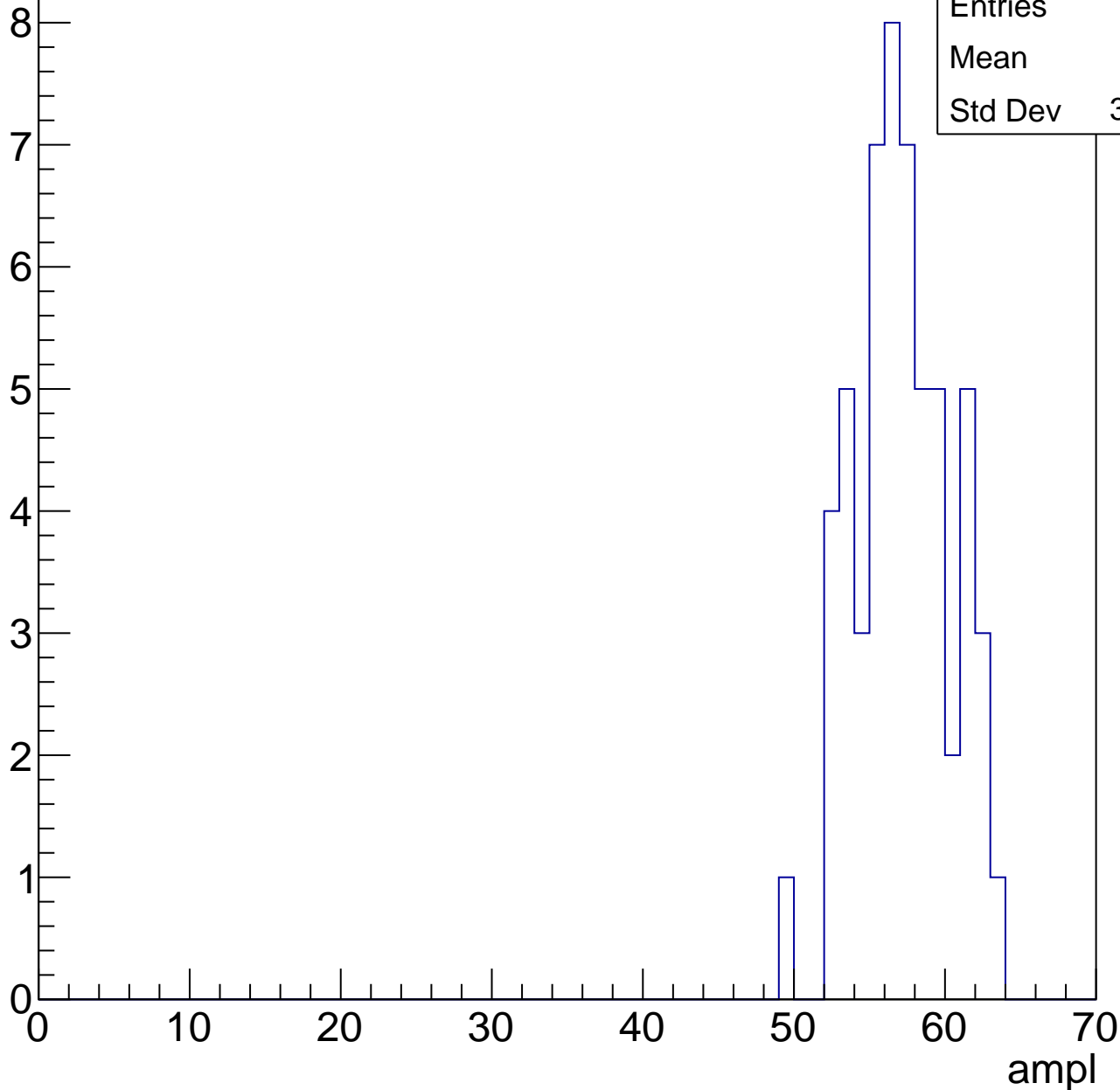


B0L002S, U4-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.7
Std Dev	3.093



B0L002S, U4-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

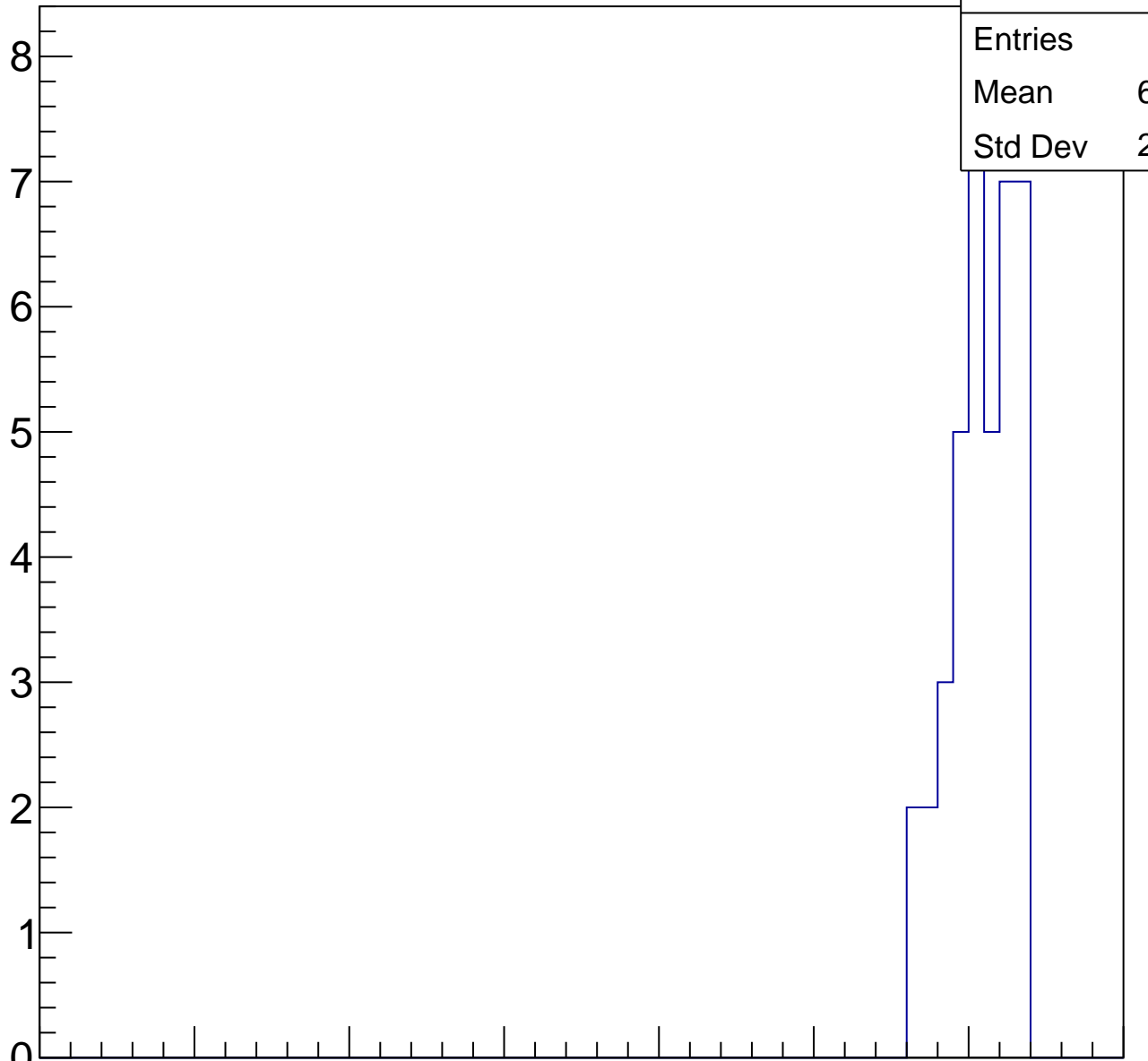
Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	60.38
Std Dev	2.008

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch2, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	30.88
Std Dev	5.061

Gaus mean : 32.1058

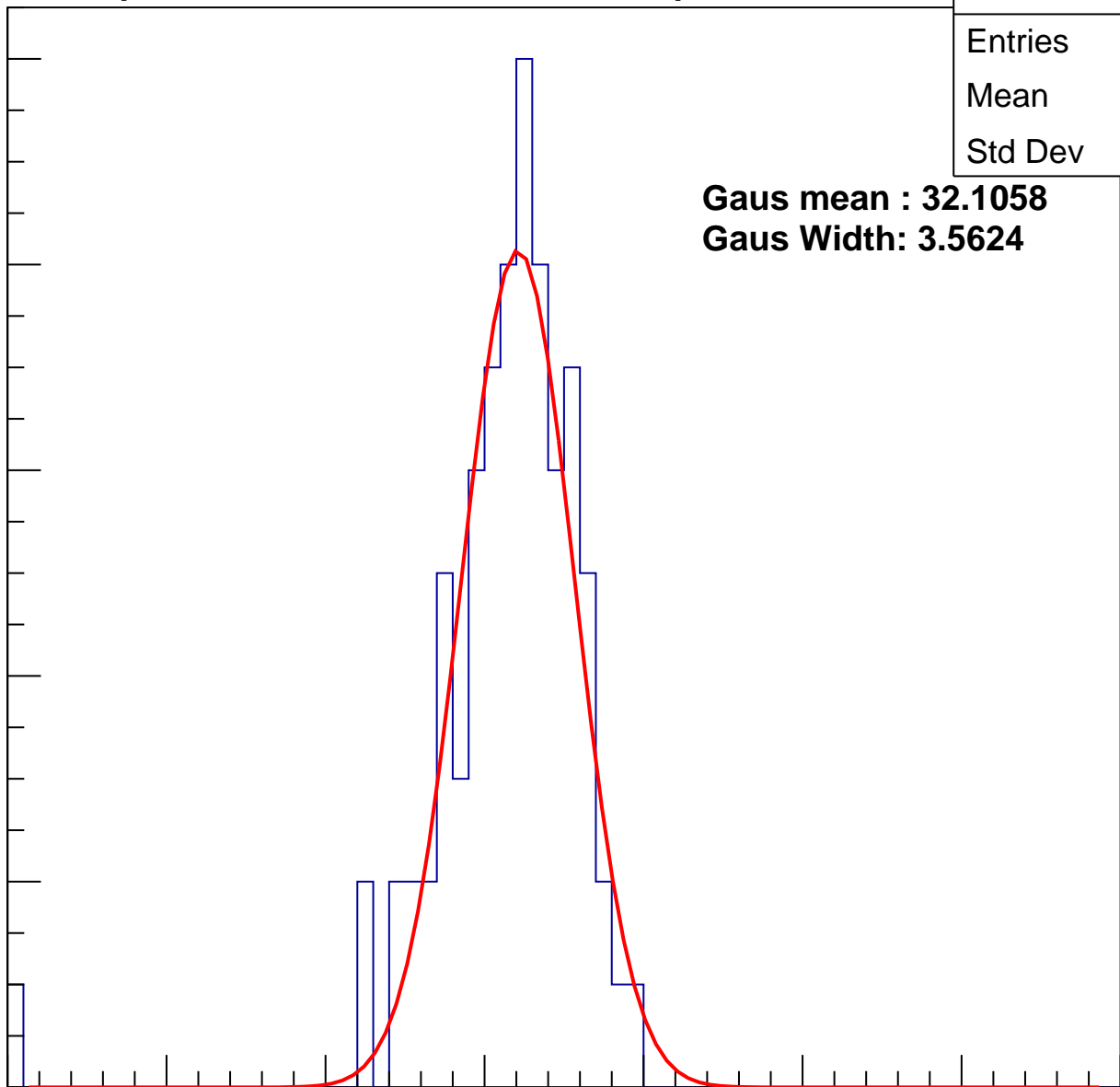
Gaus Width: 3.5624

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch2, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	39.57
Std Dev	3.192

Gaus mean : 40.1273

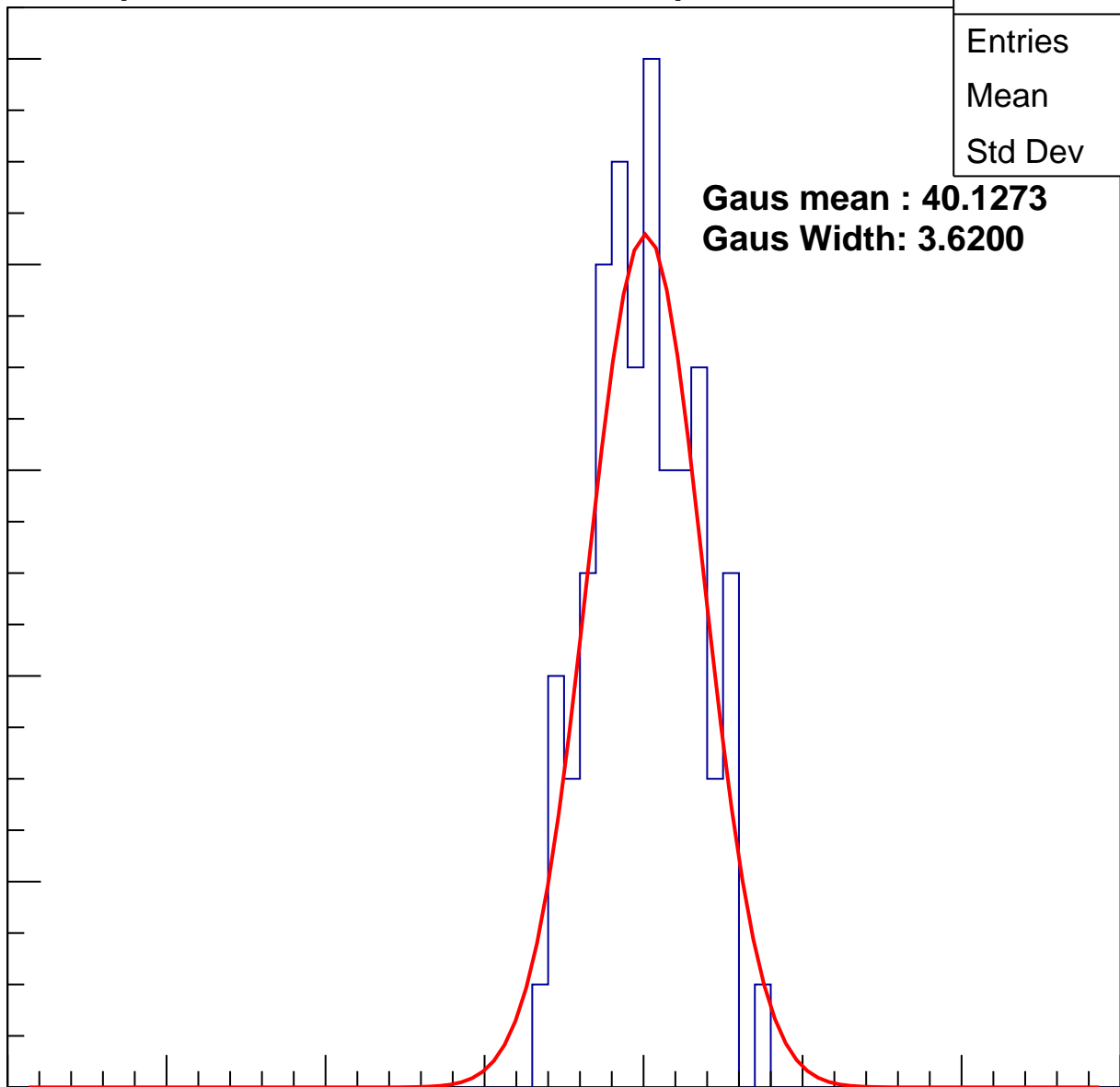
Gaus Width: 3.6200

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch2, adc2

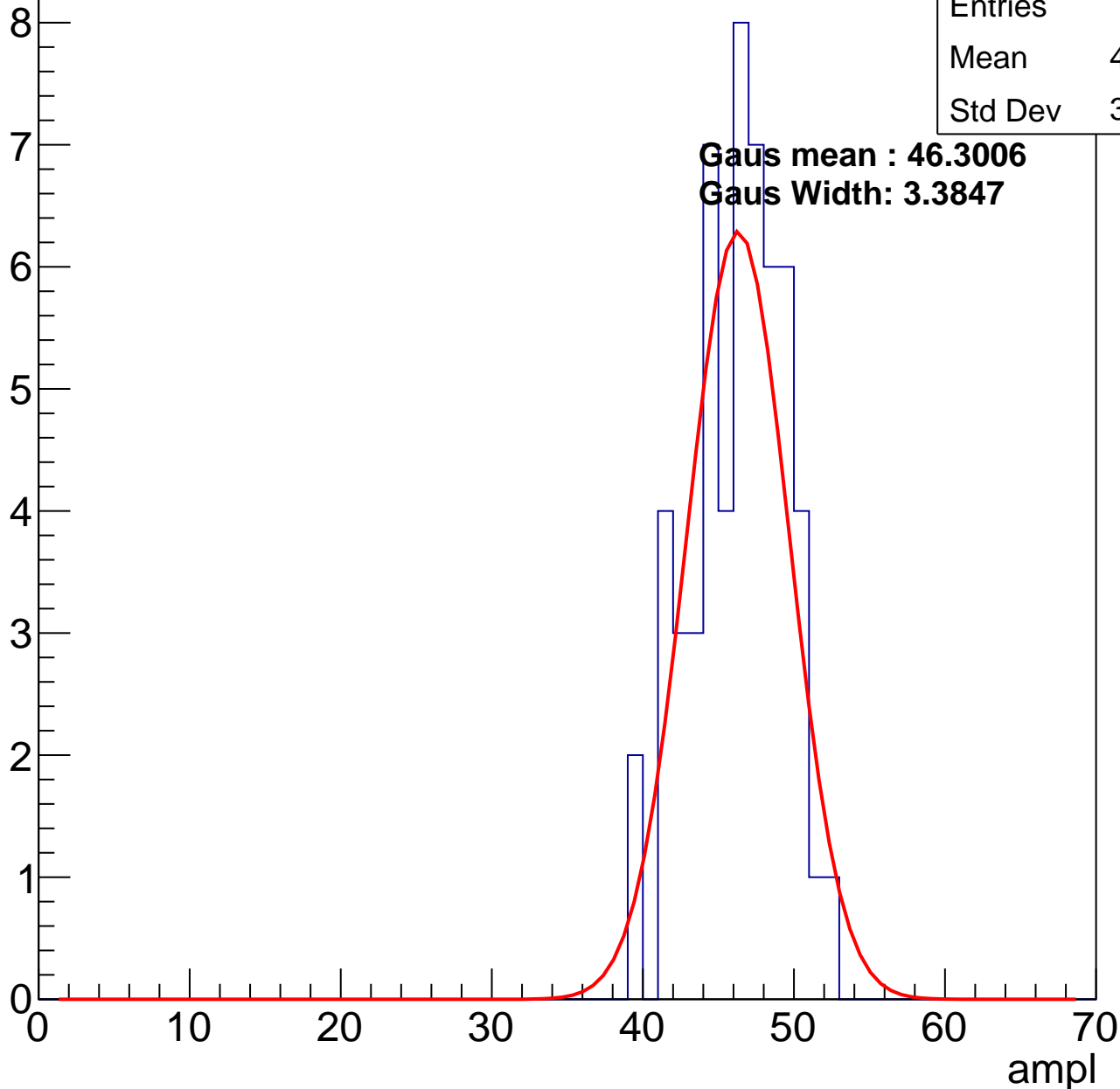
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	45.84
Std Dev	3.028

Gaus mean : 46.3006

Gaus Width: 3.3847

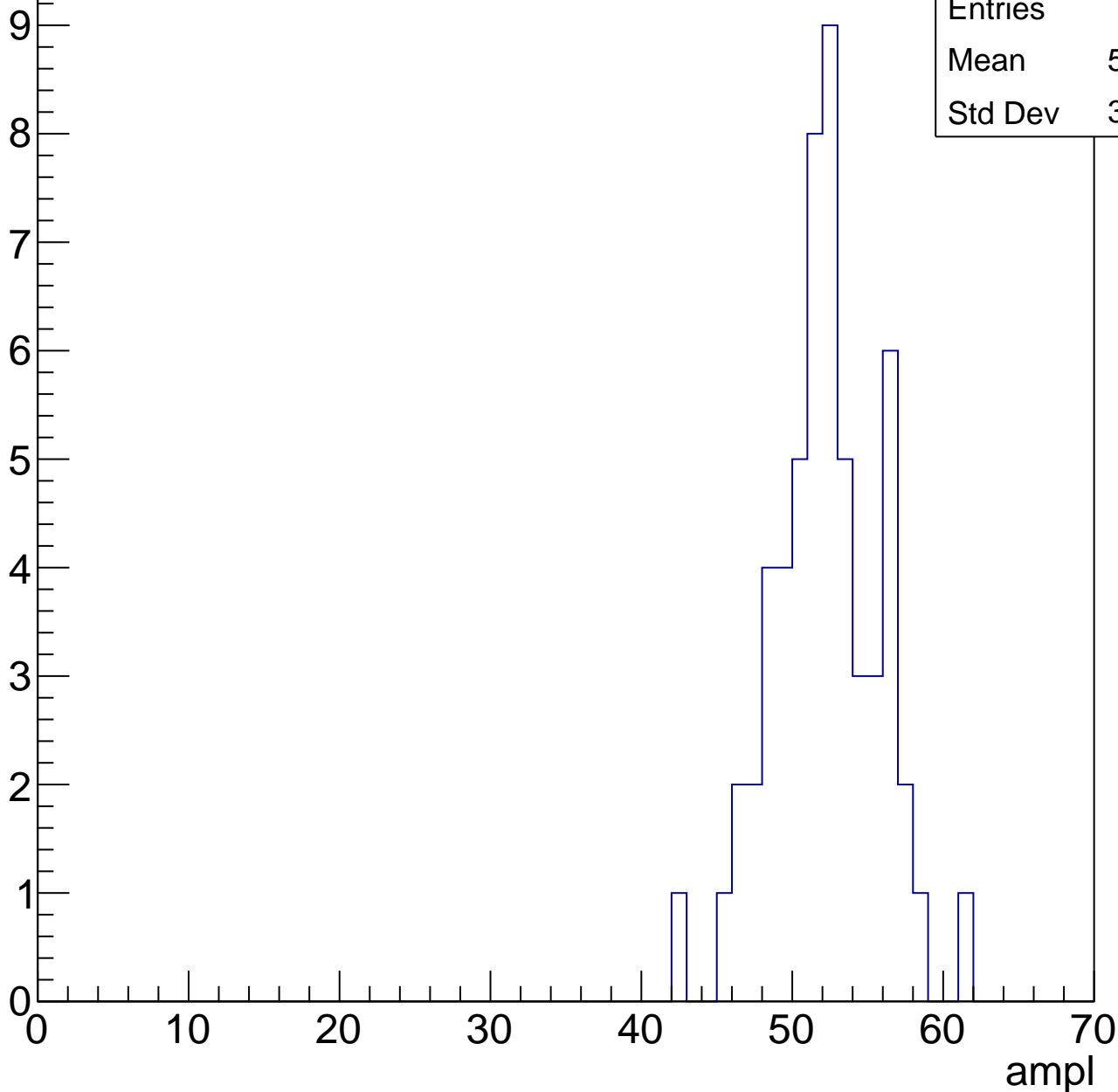


B0L002S, U4-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	51.72
Std Dev	3.508



B0L002S, U4-ch2, adc4

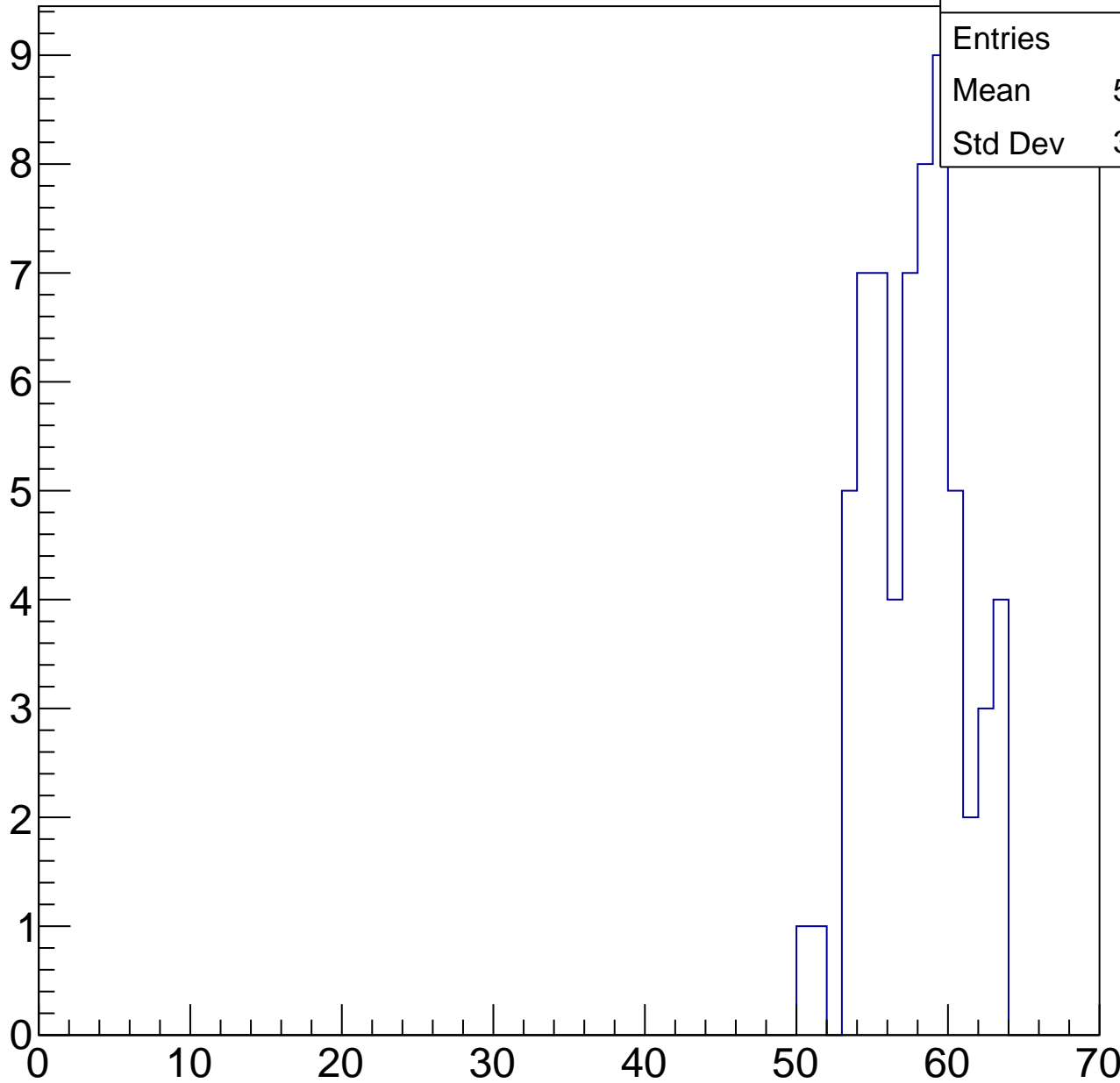
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	57.25
Std Dev	3.076

ampl

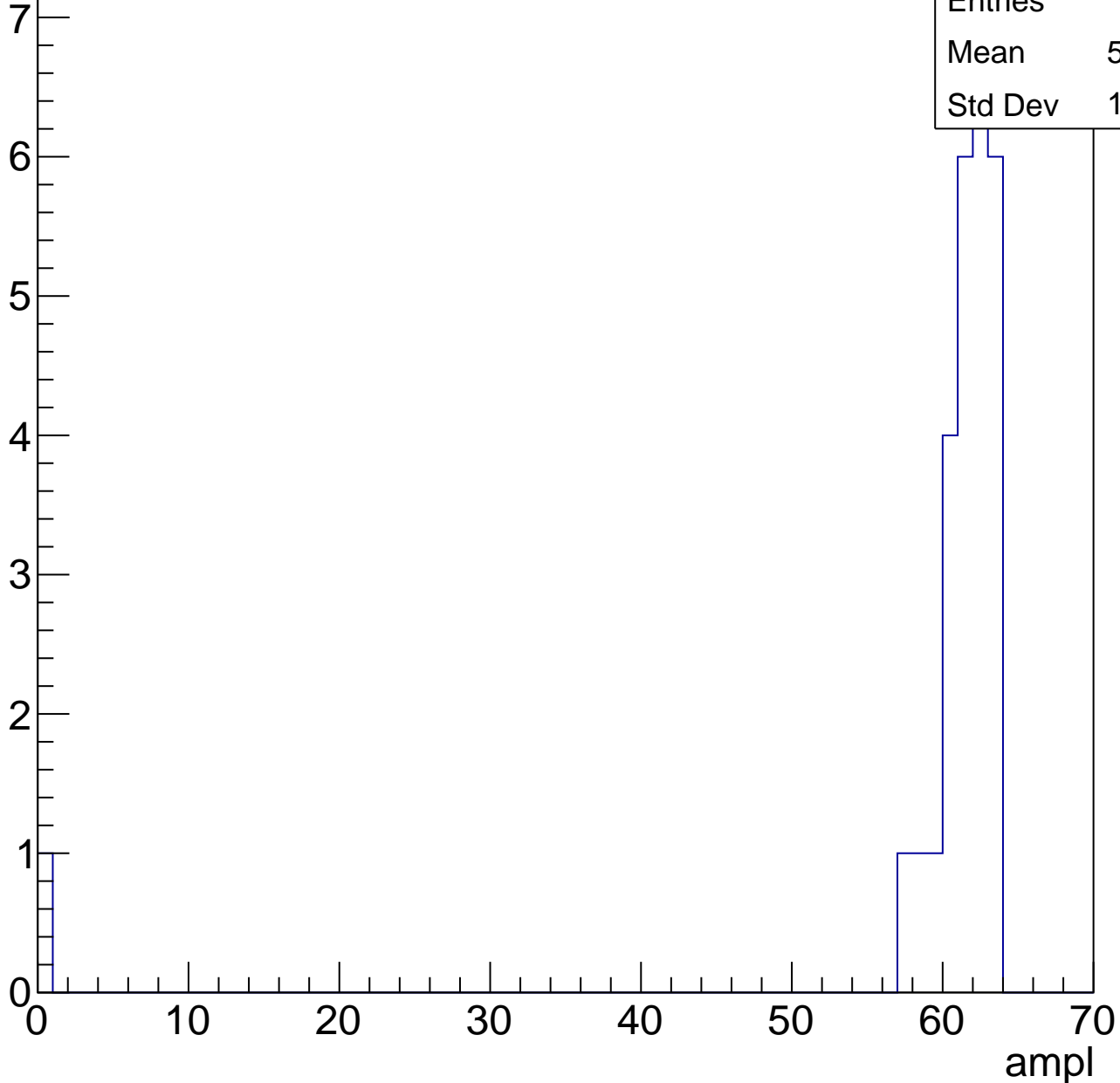


B0L002S, U4-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

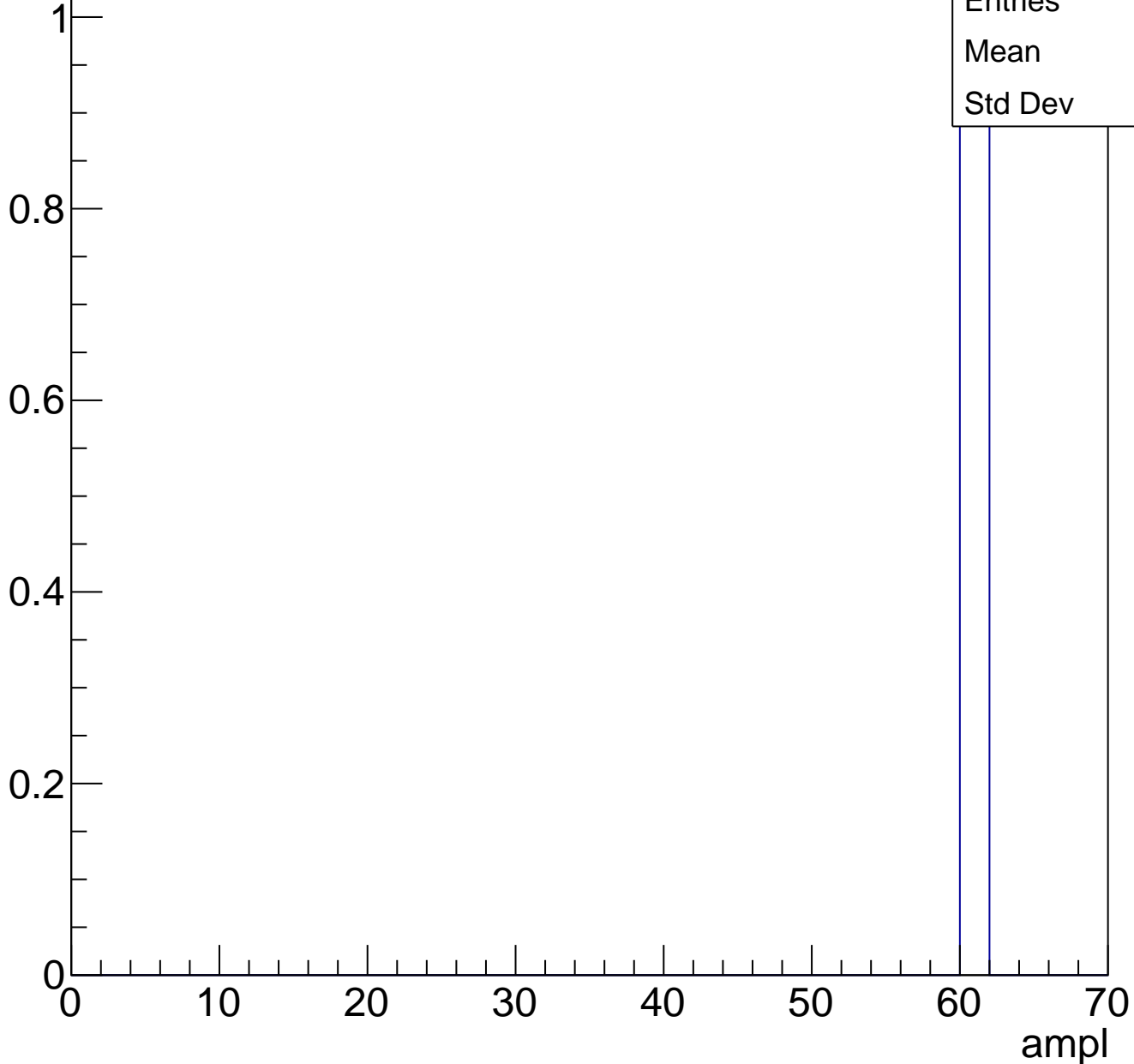
Entries	27
Mean	58.96
Std Dev	11.66



B0L002S, U4-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch3, adc0

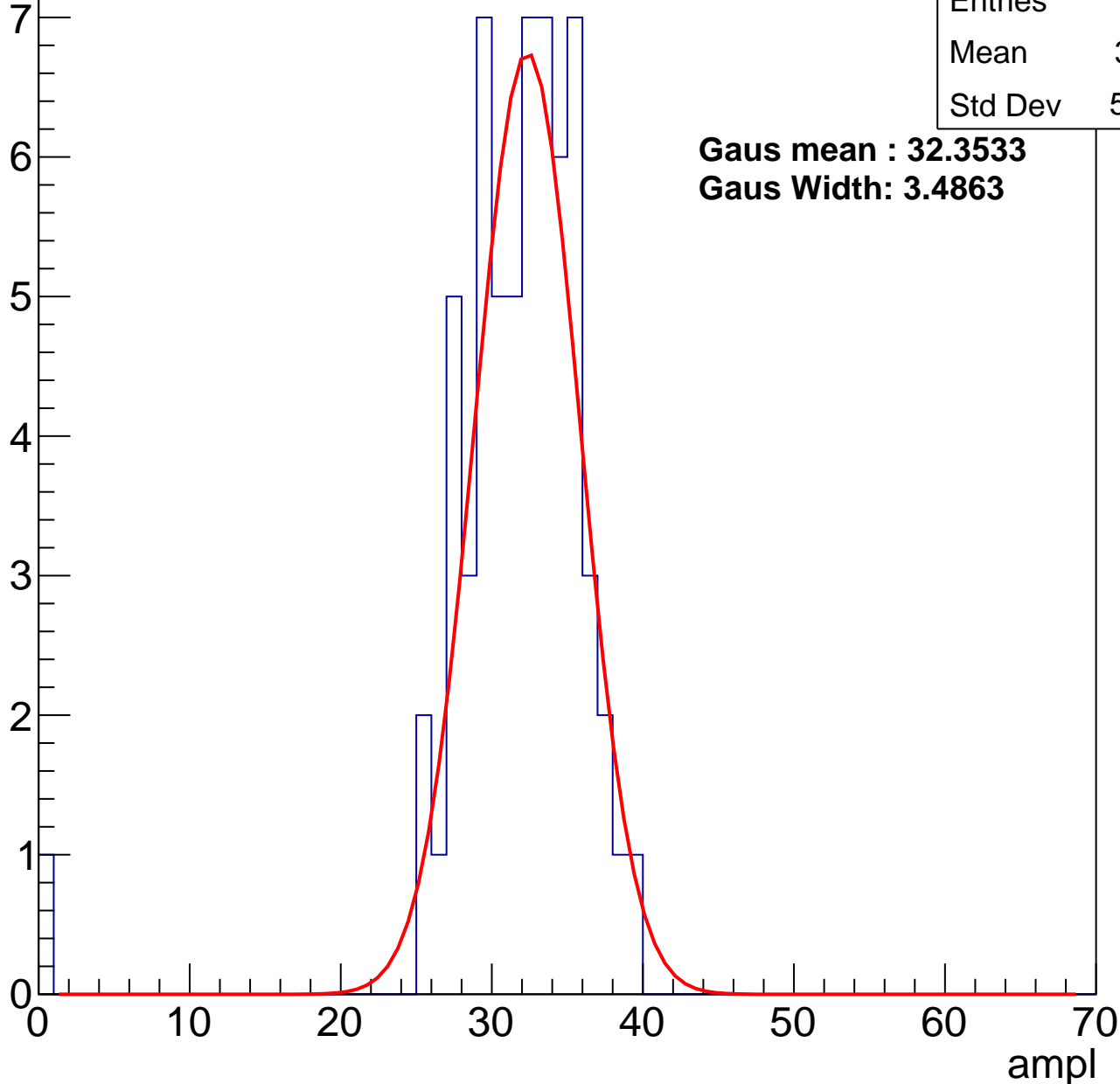
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	31.21
Std Dev	5.127

Gaus mean : 32.3533

Gaus Width: 3.4863



B0L002S, U4-ch3, adc1

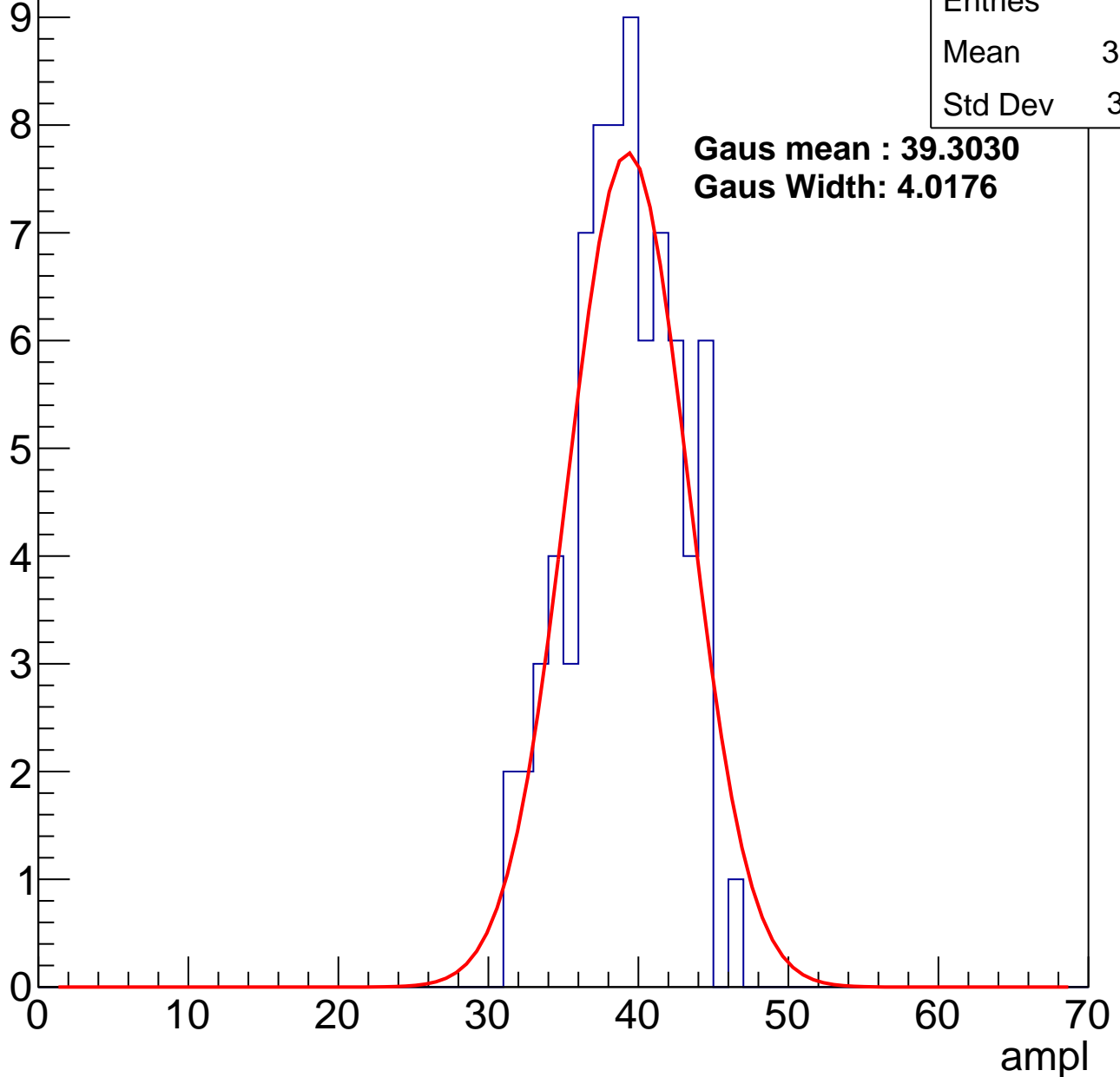
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	38.55
Std Dev	3.481

Gaus mean : 39.3030

Gaus Width: 4.0176



B0L002S, U4-ch3, adc2

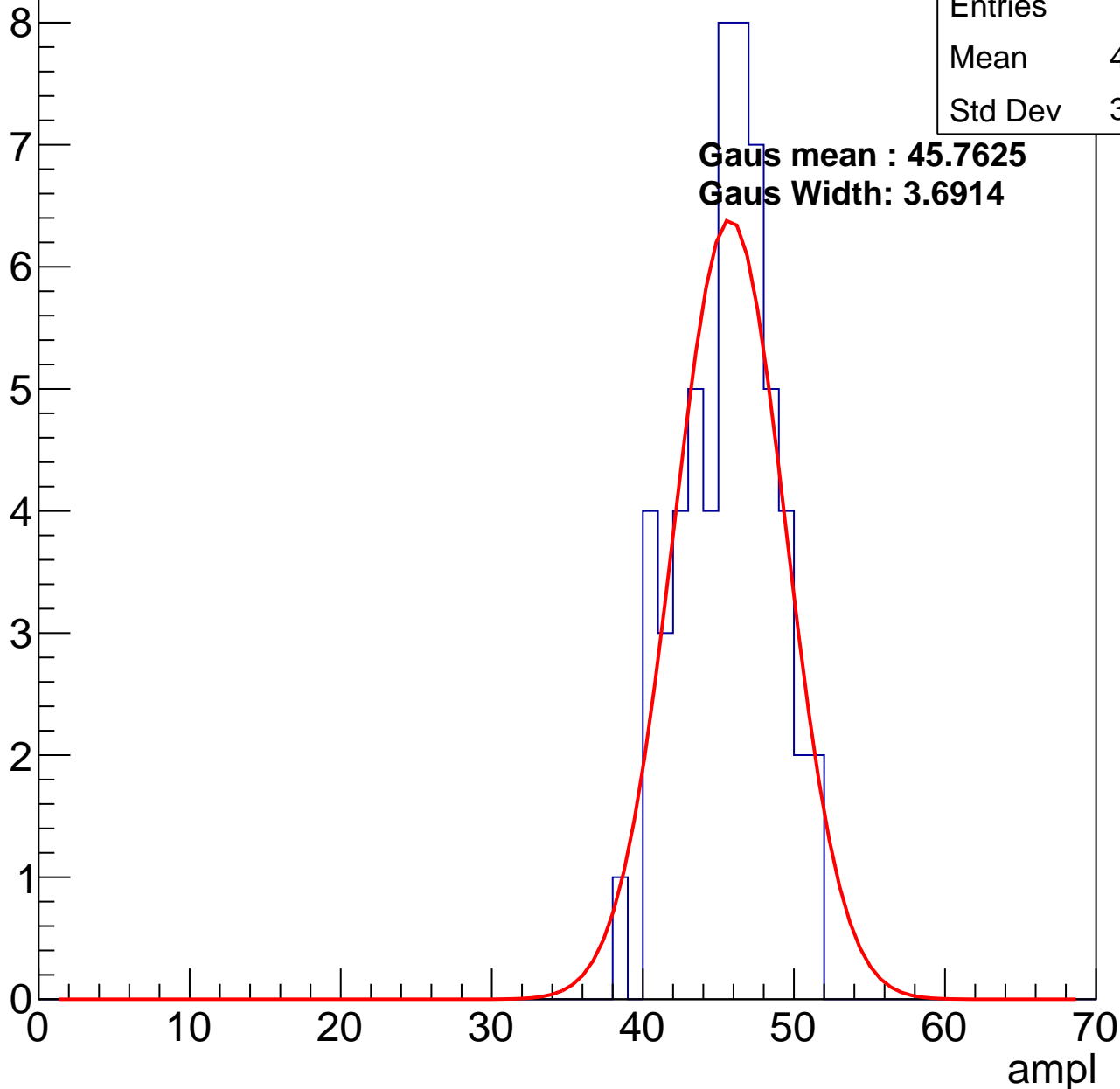
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.18
Std Dev	3.027

Gaus mean : 45.7625

Gaus Width: 3.6914

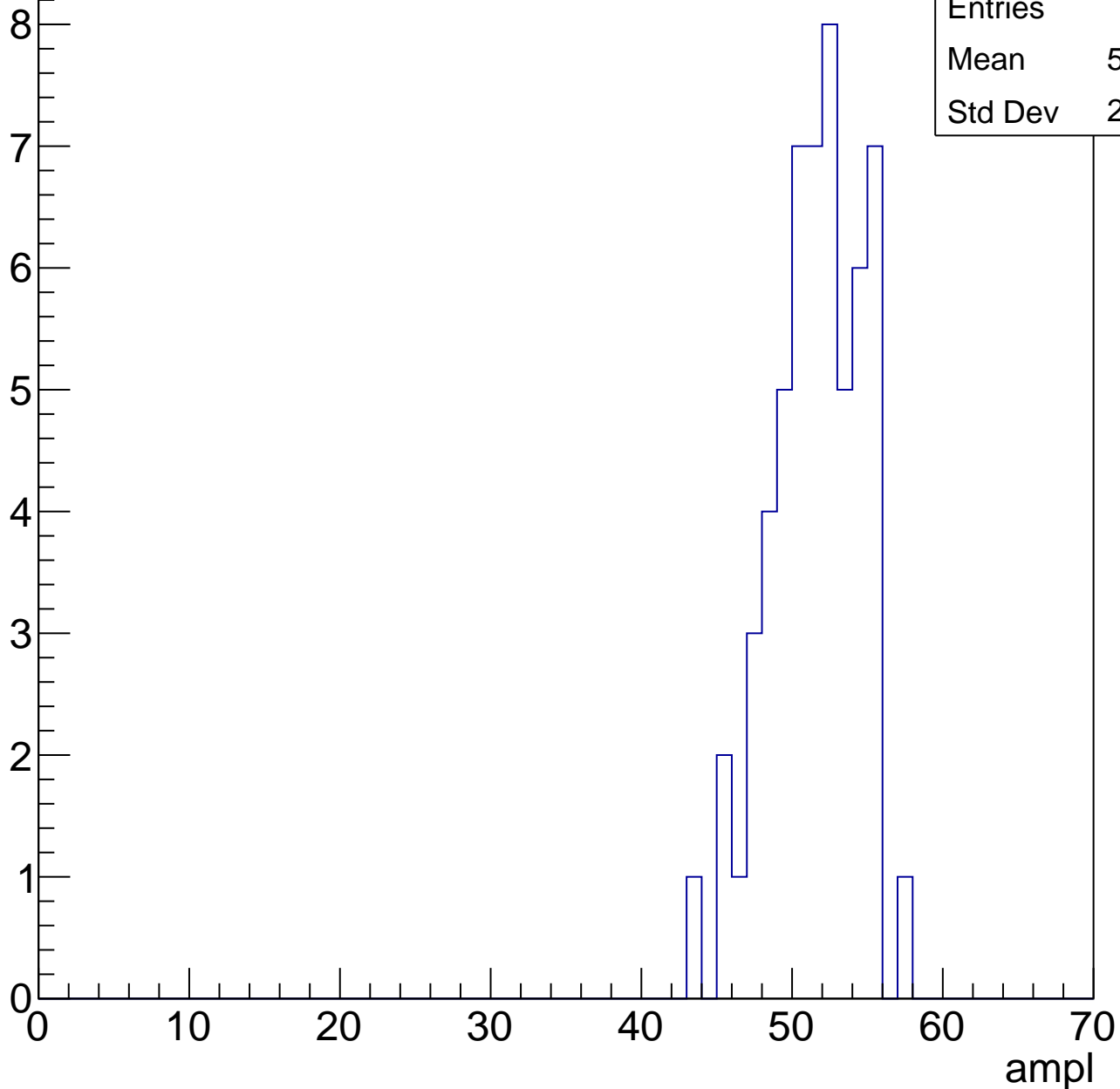


B0L002S, U4-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

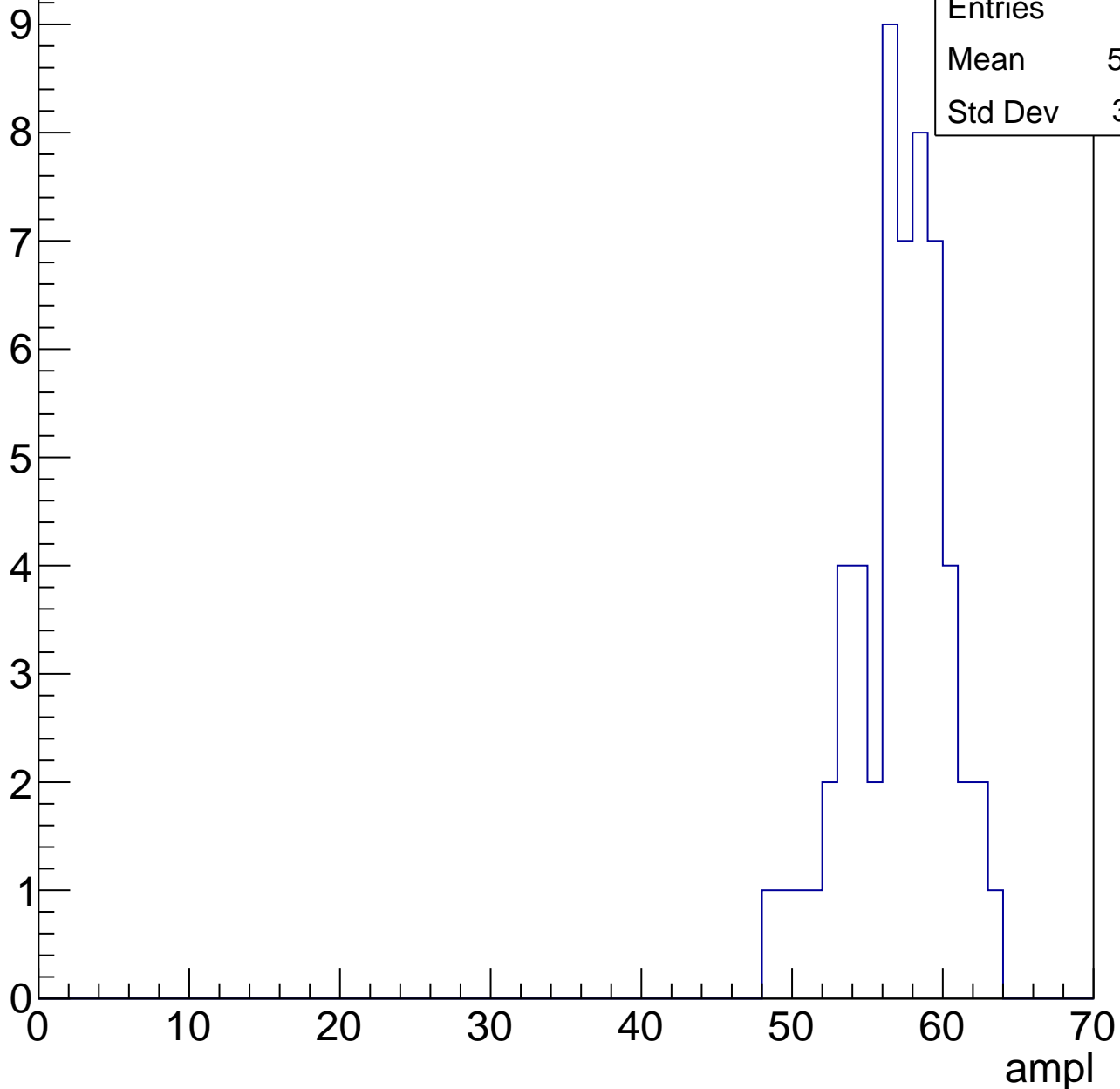
Entries	57
Mean	51.07
Std Dev	2.967



B0L002S, U4-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



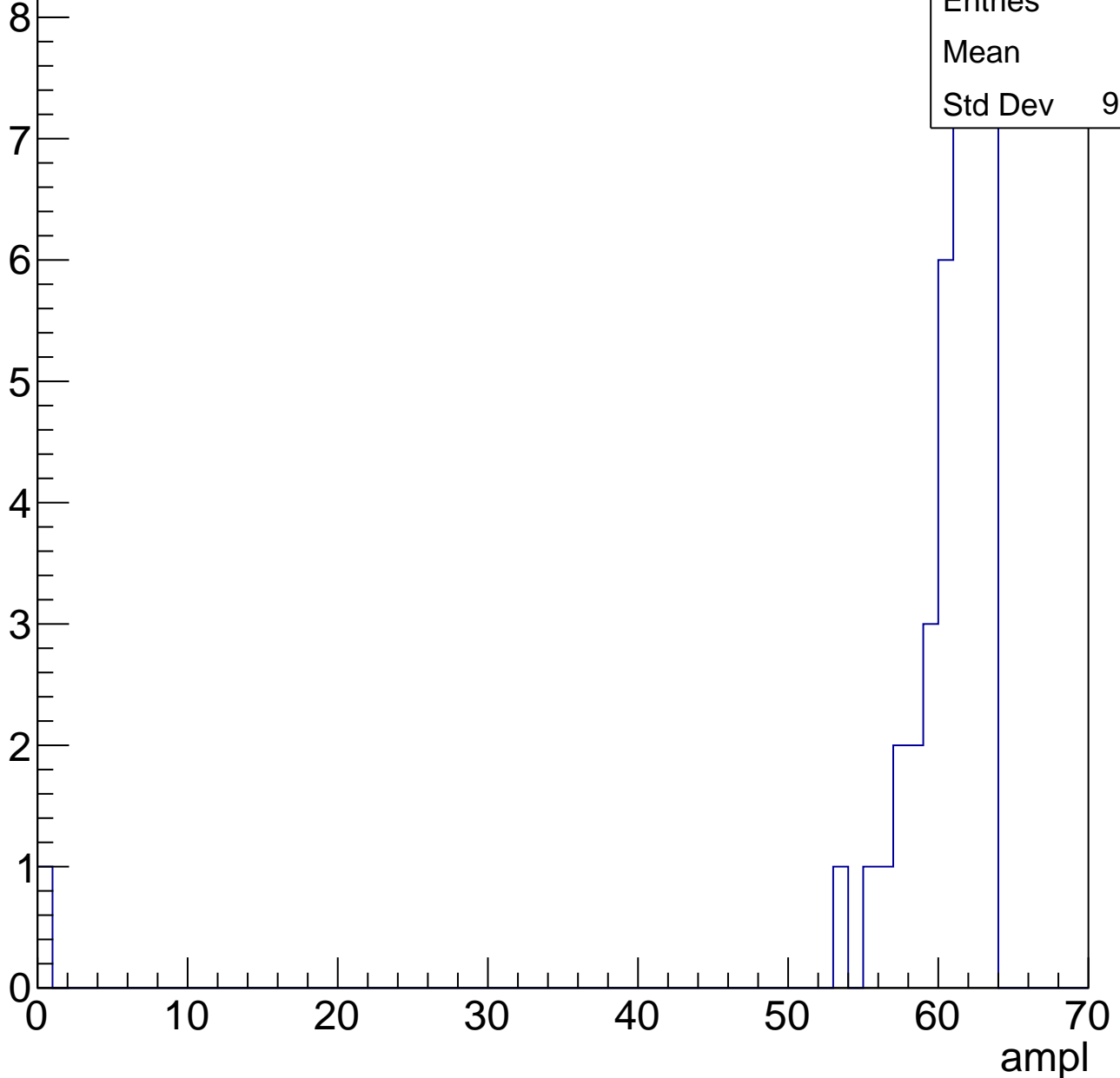
Entries	56
Mean	56.59
Std Dev	3.211

B0L002S, U4-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	59
Std Dev	9.615



B0L002S, U4-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch4, adc0

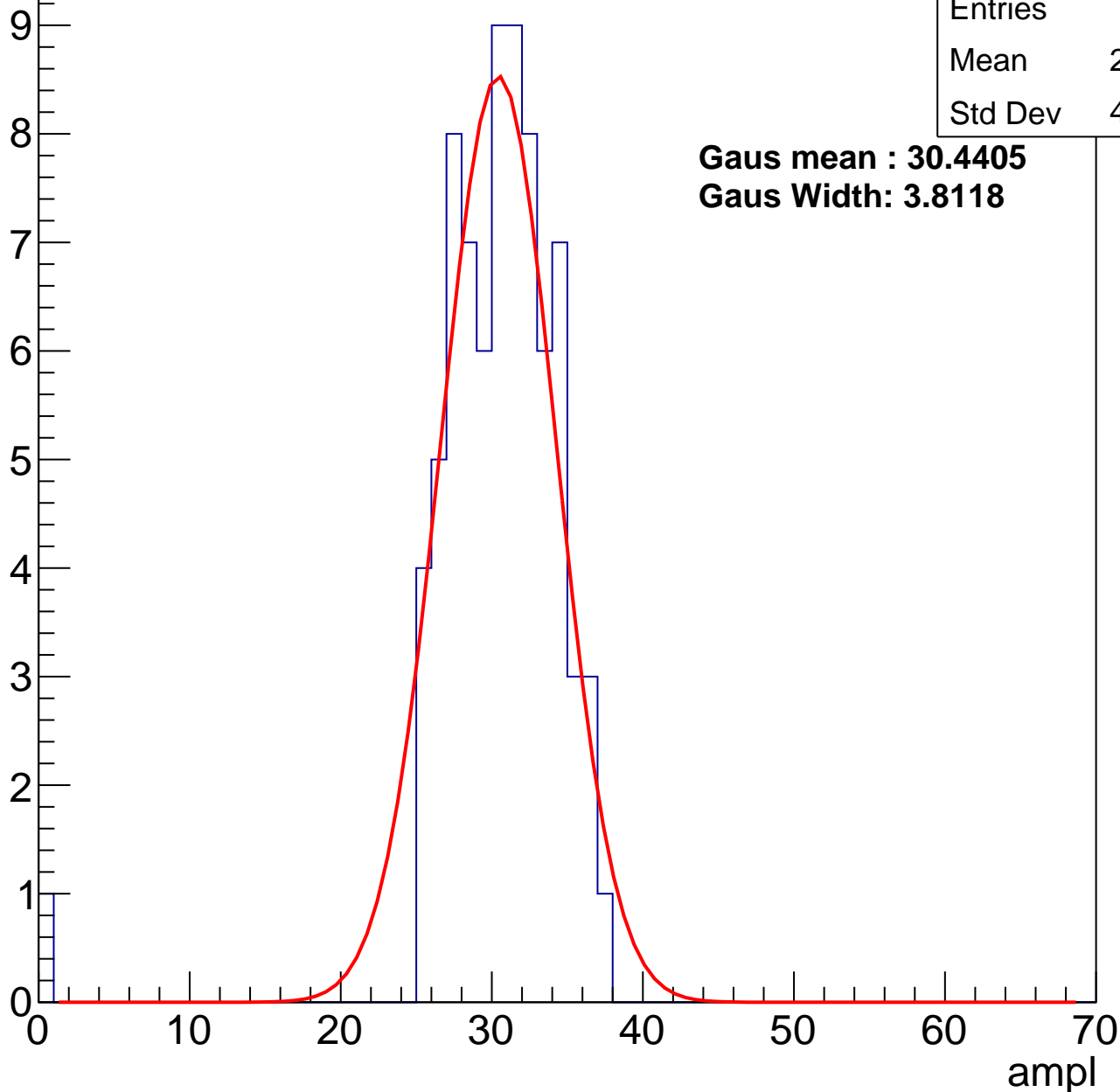
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.96
Std Dev	4.594

Gaus mean : 30.4405

Gaus Width: 3.8118



B0L002S, U4-ch4, adc1

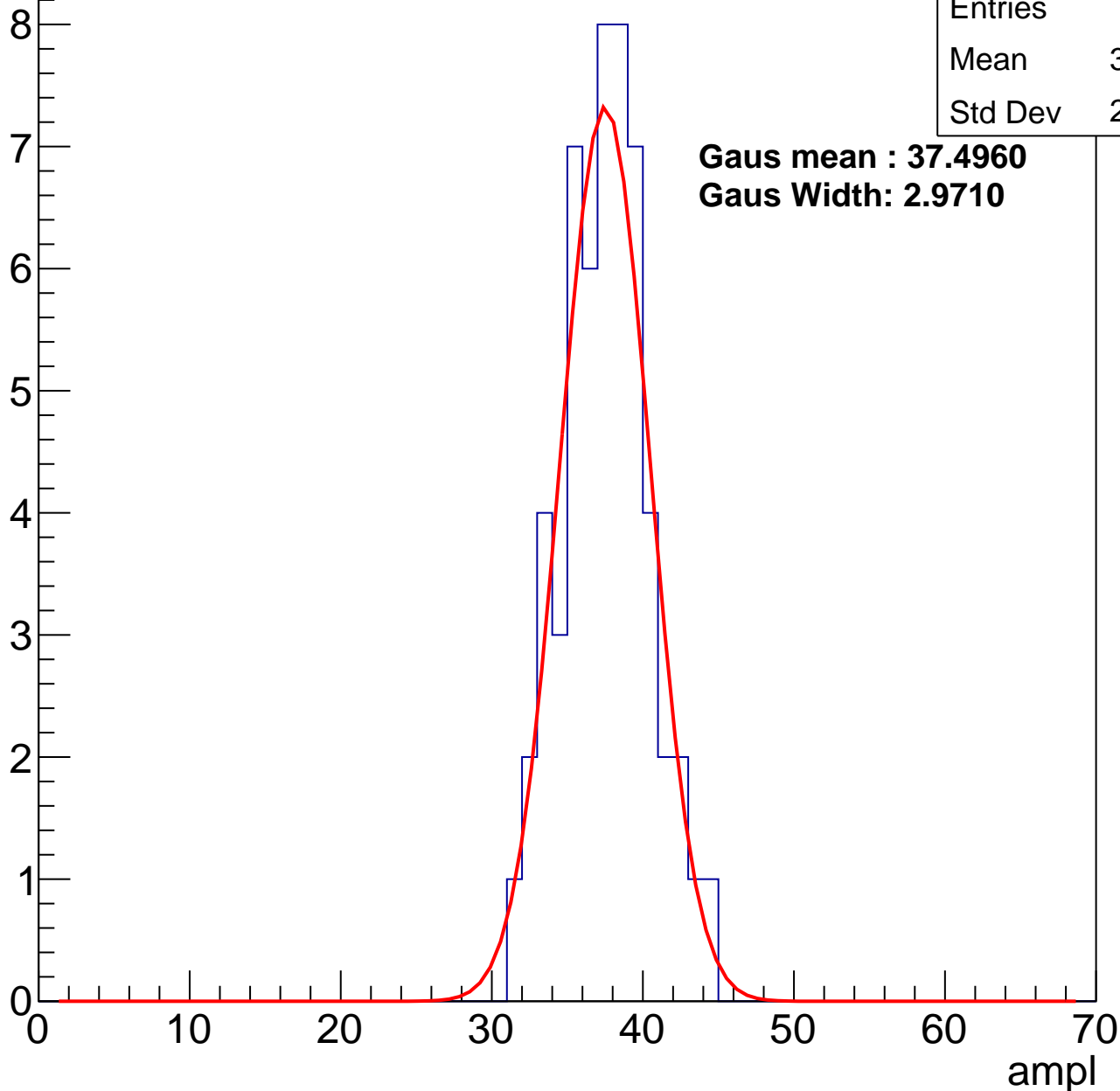
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.07
Std Dev	2.834

Gaus mean : 37.4960

Gaus Width: 2.9710



B0L002S, U4-ch4, adc2

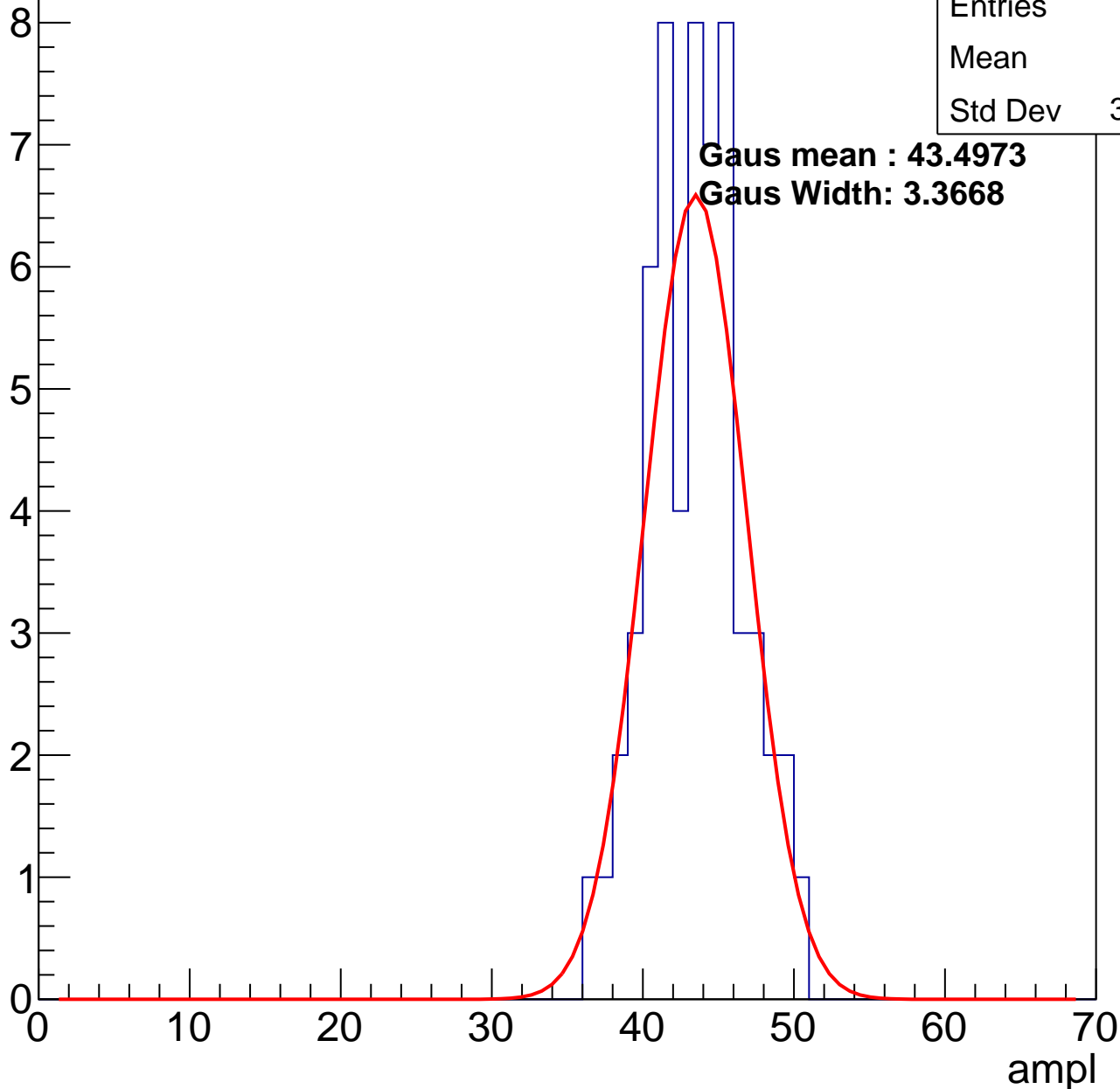
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	43
Std Dev	3.075

Gaus mean : 43.4973

Gaus Width: 3.3668

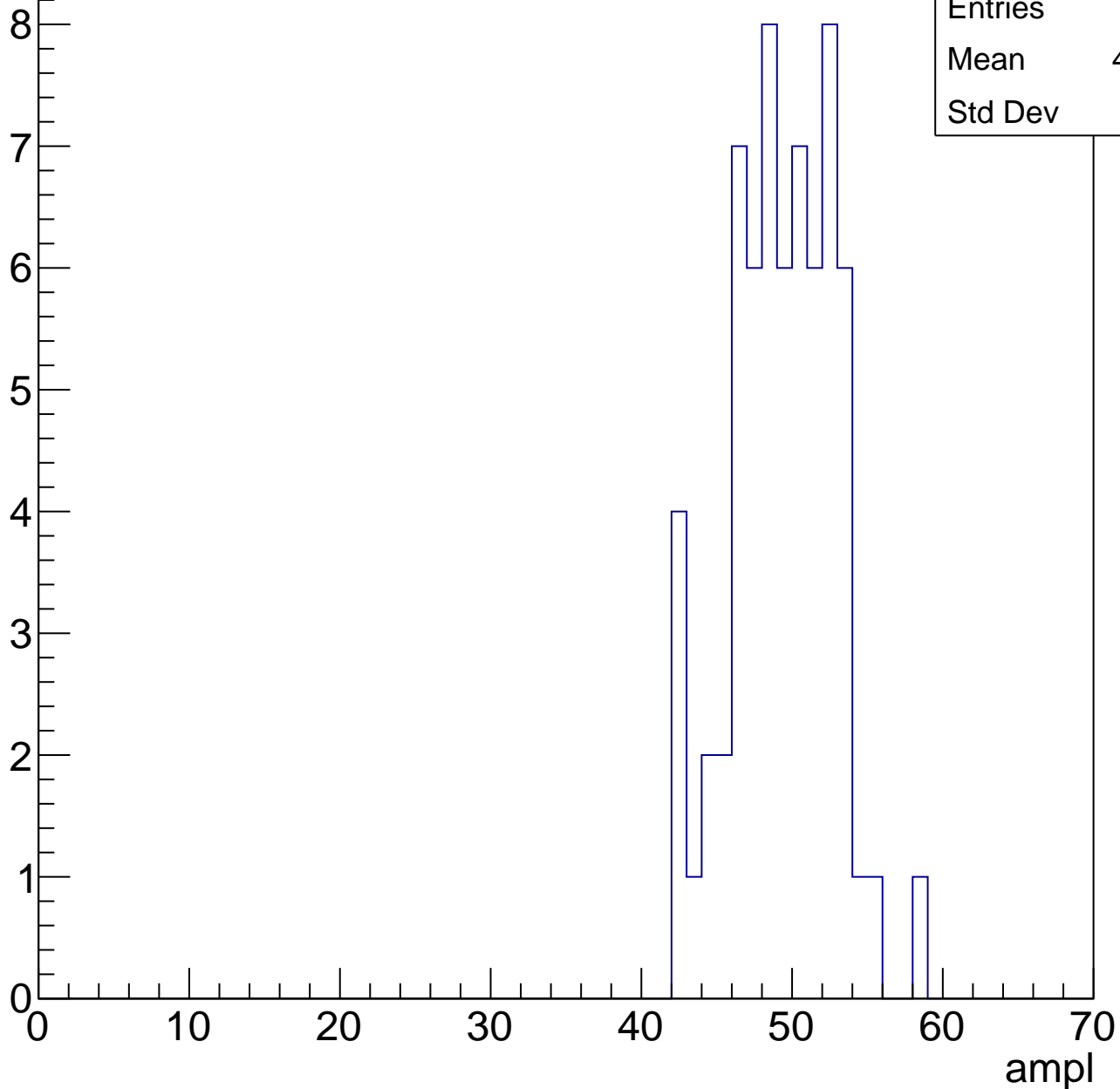


B0L002S, U4-ch4, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	48.91
Std Dev	3.37

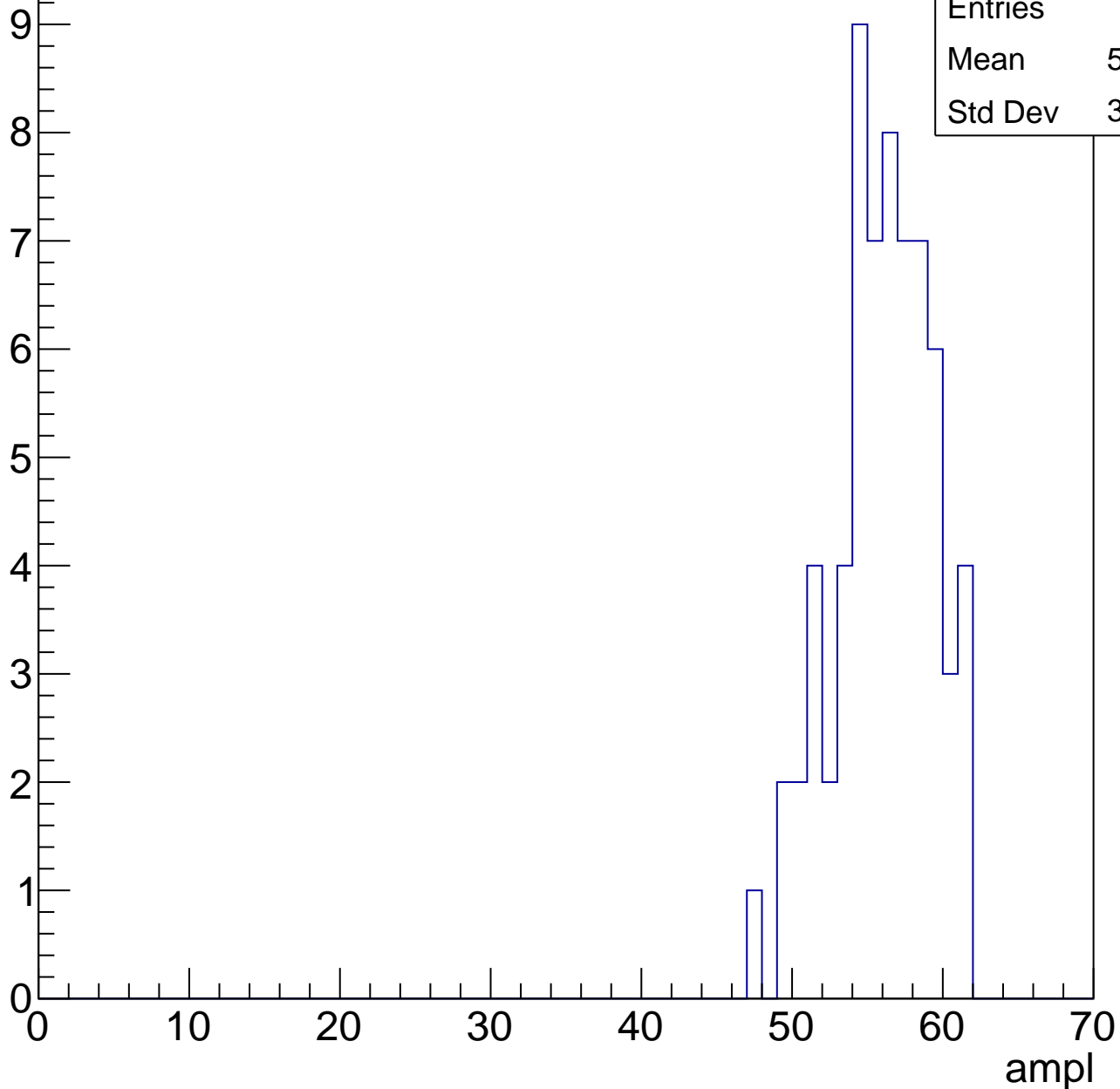


B0L002S, U4-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.56
Std Dev	3.225



B0L002S, U4-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

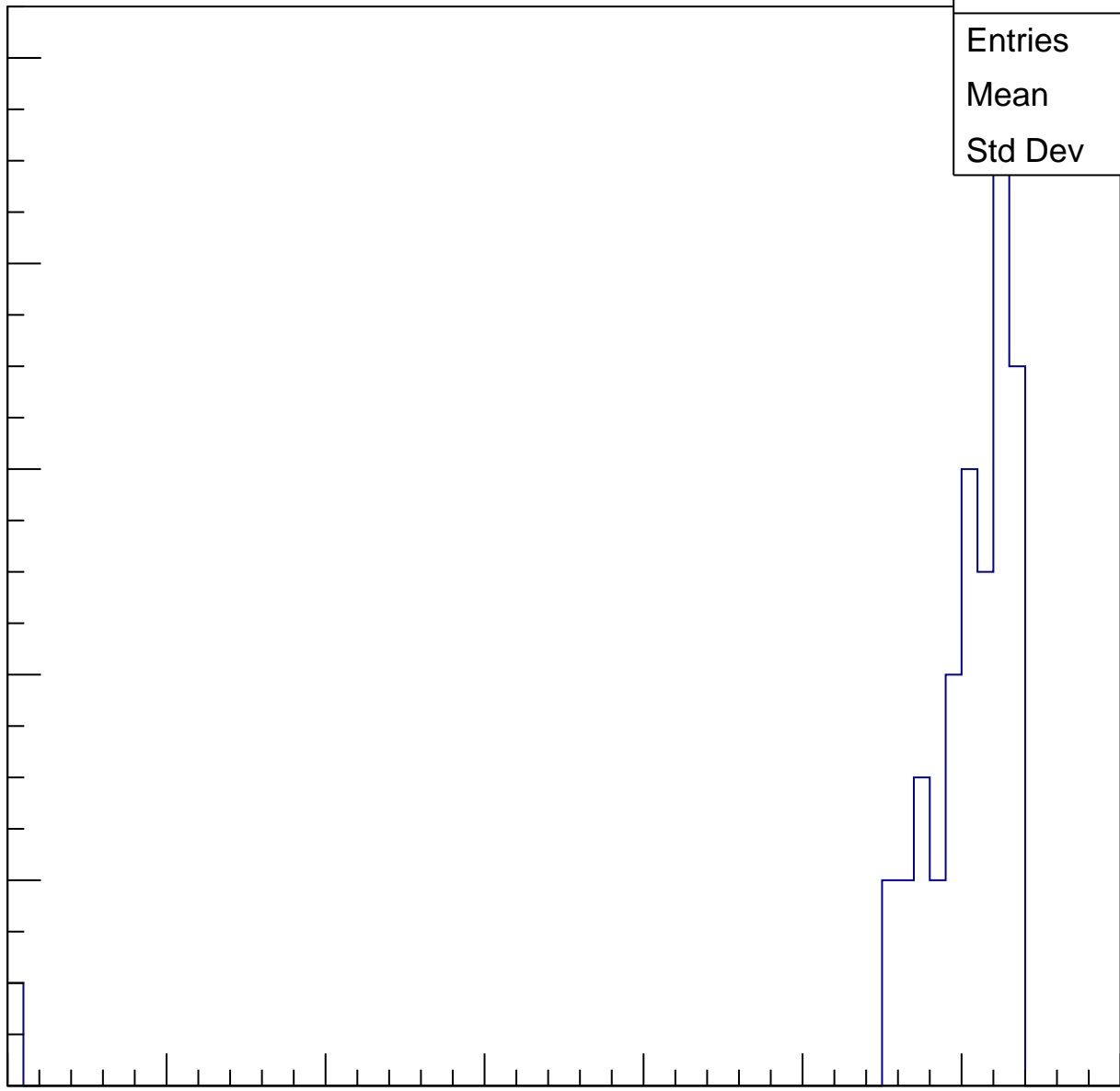
Entries	42
Mean	58.83
Std Dev	9.477

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch5, adc0

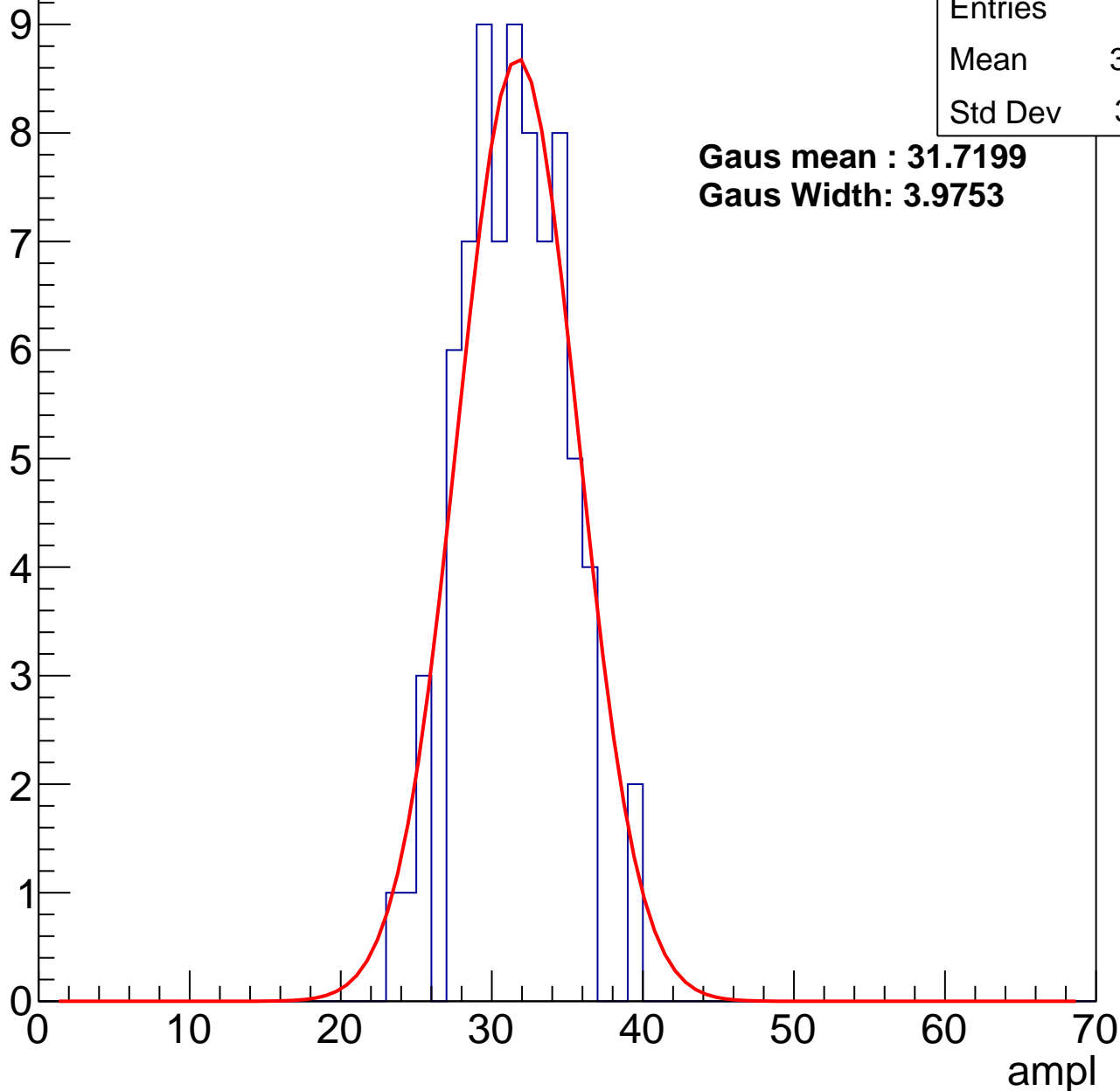
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.99
Std Dev	3.301

Gaus mean : 31.7199

Gaus Width: 3.9753



B0L002S, U4-ch5, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	38.1
Std Dev	2.873

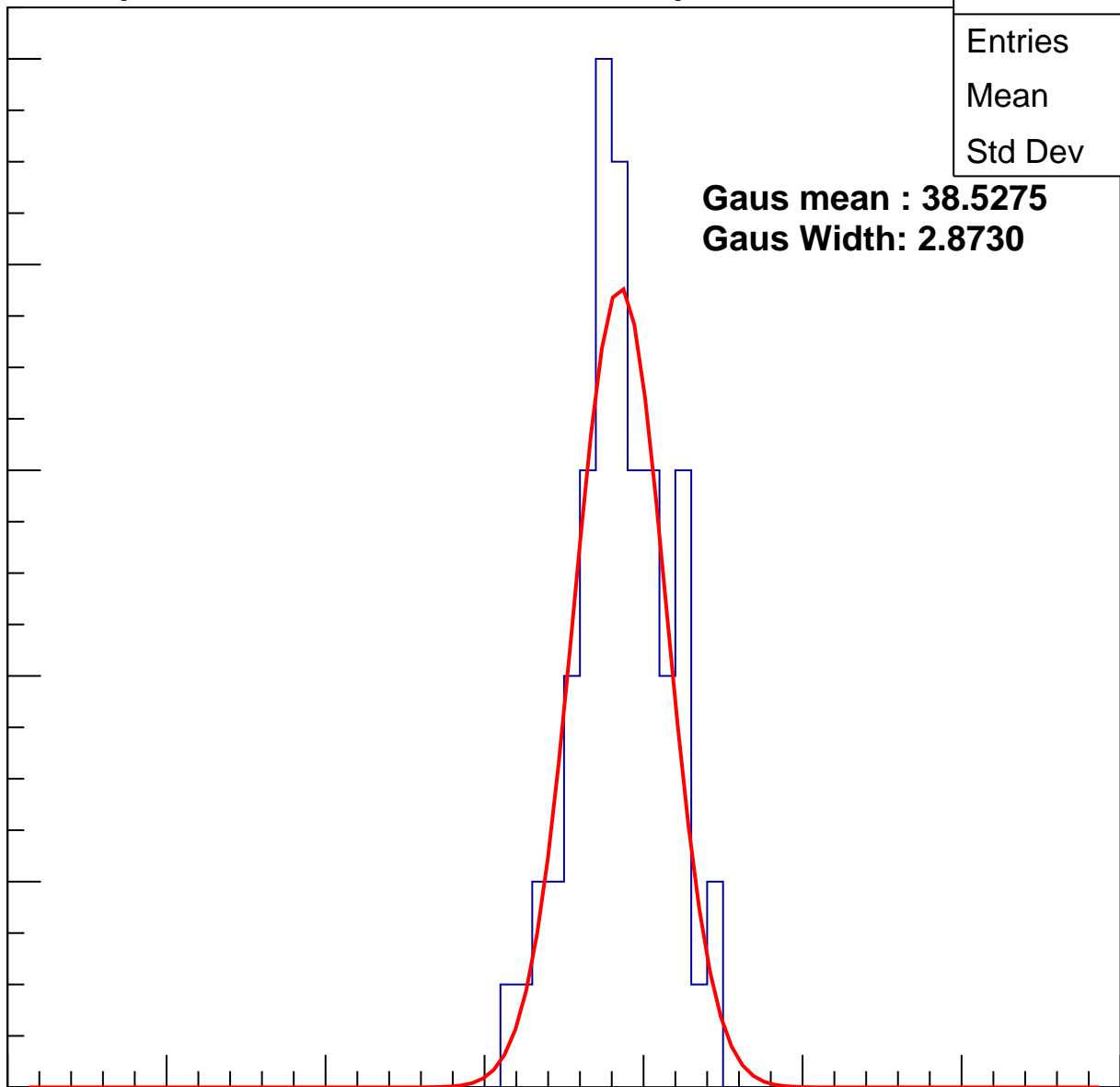
Gaus mean : 38.5275
Gaus Width: 2.8730

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch5, adc2

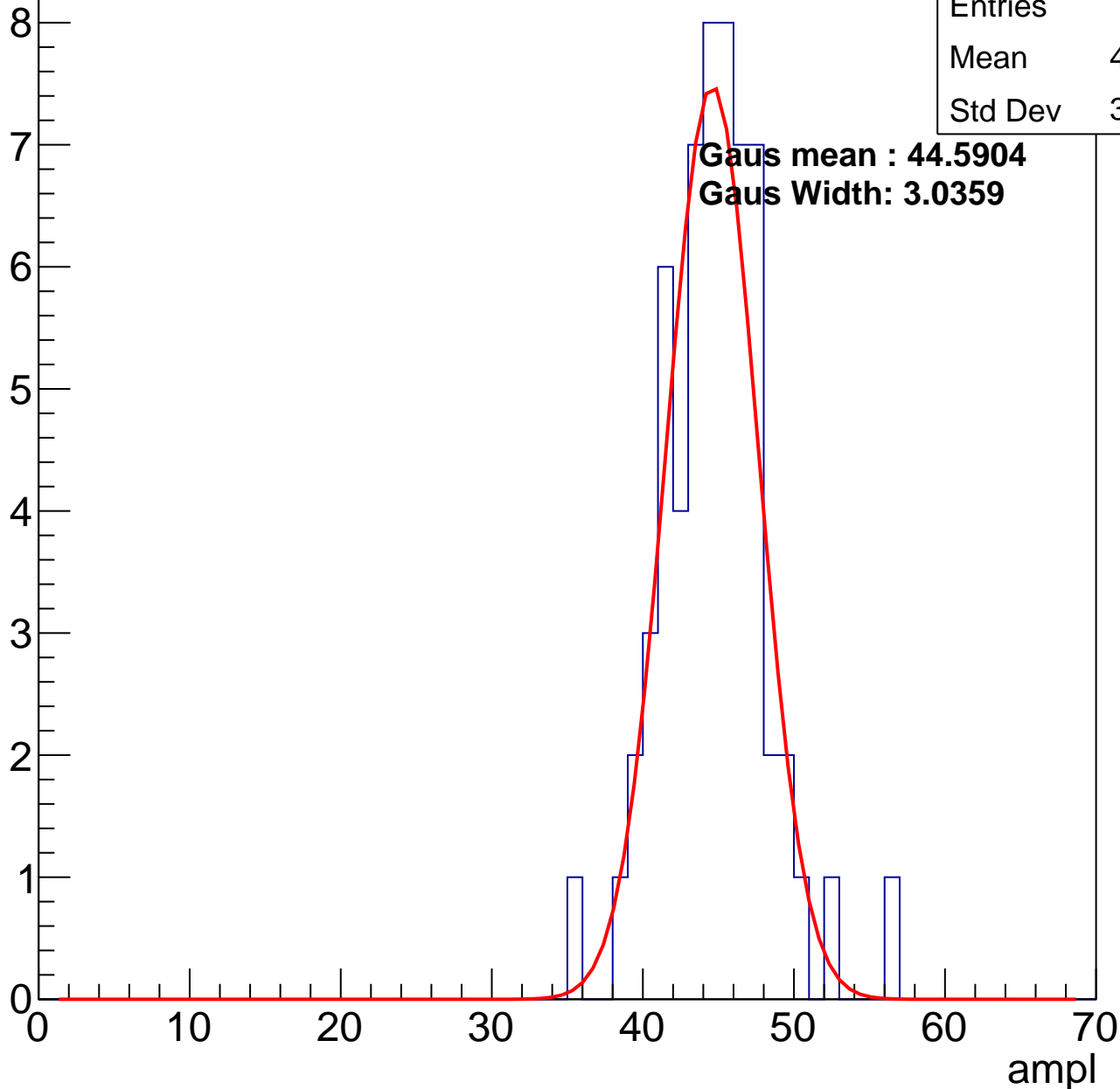
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.28
Std Dev	3.417

Gaus mean : 44.5904

Gaus Width: 3.0359



B0L002S, U4-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	50.96
Std Dev	3.515

Entry

10

8

6

4

2

0

0

10

20

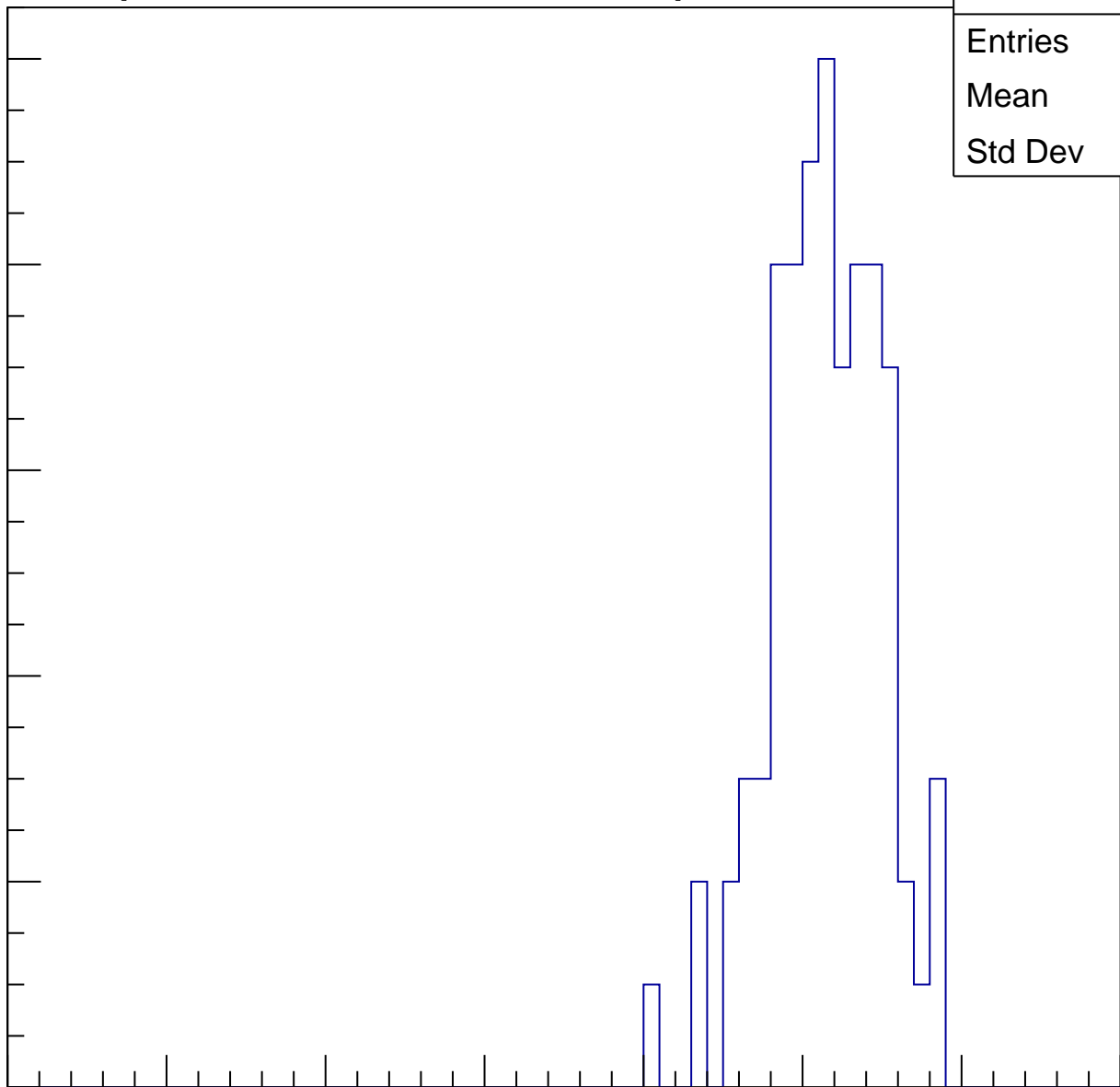
30

40

50

60

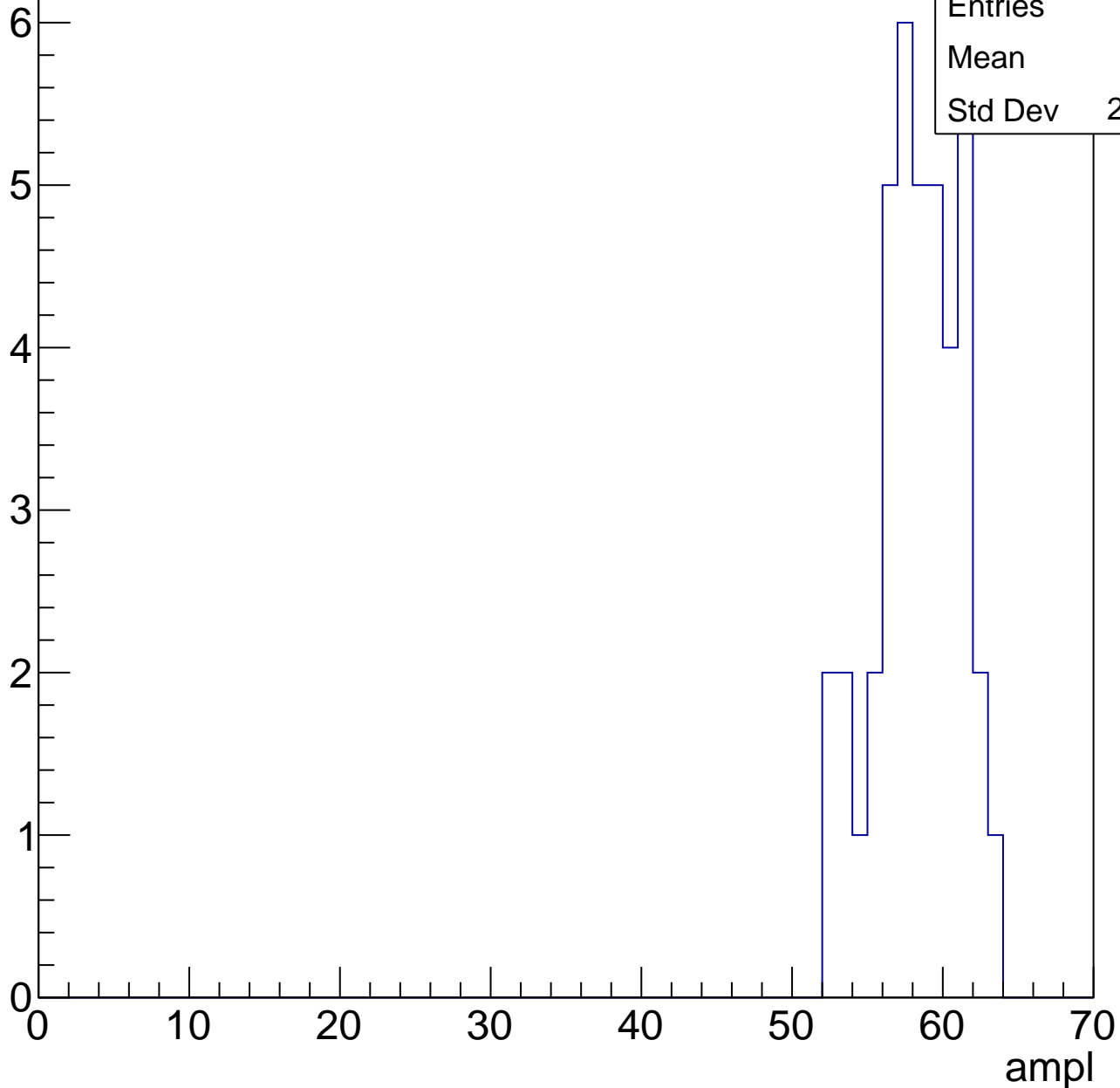
ampl



B0L002S, U4-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



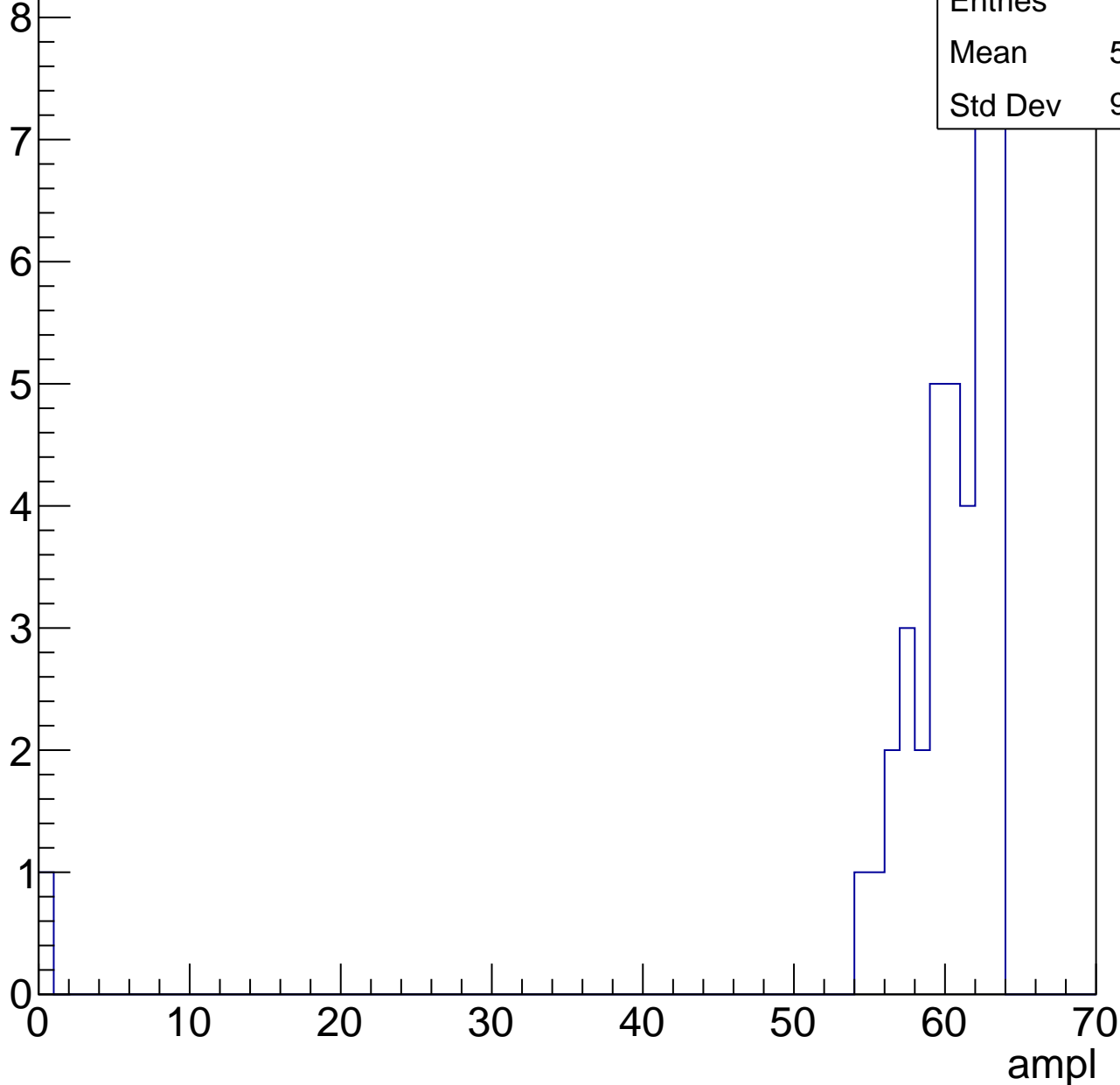
Entries	41
Mean	57.9
Std Dev	2.766

B0L002S, U4-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.67
Std Dev	9.709



B0L002S, U4-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch6, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	58
Mean	30.45
Std Dev	4.959

Gaus mean : 31.2789

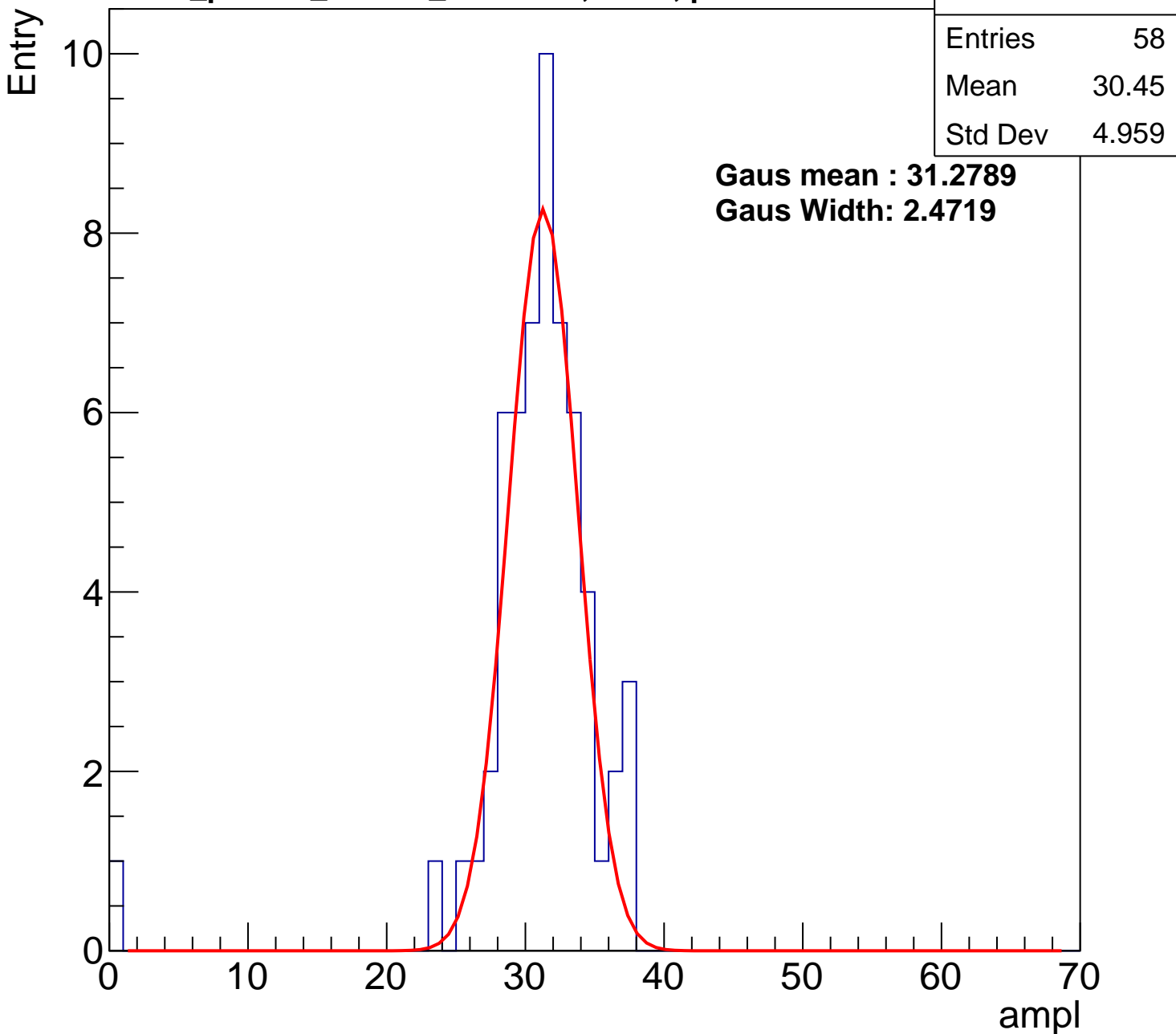
Gaus Width: 2.4719

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch6, adc1

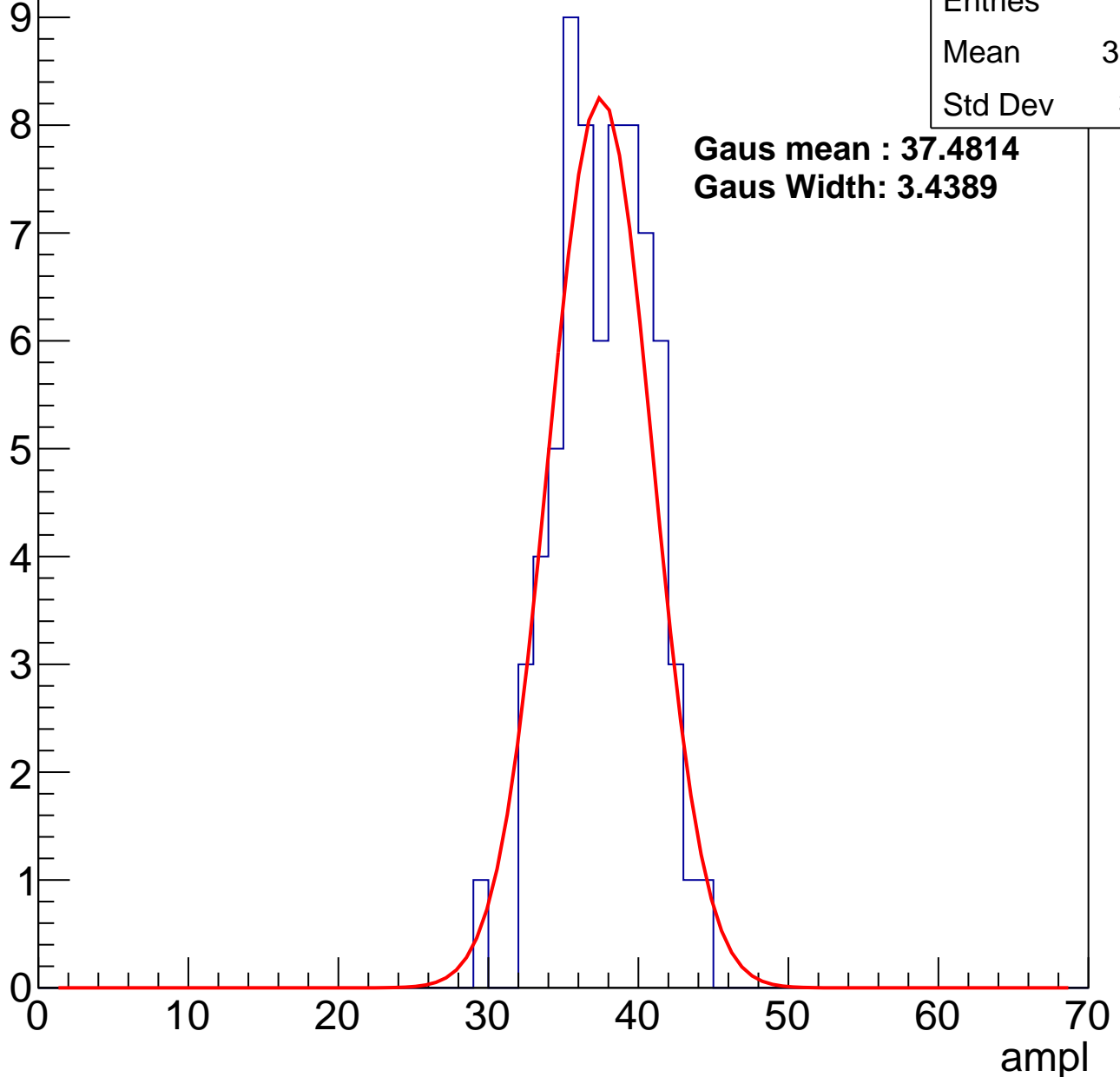
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.24
Std Dev	3.04

Gaus mean : 37.4814

Gaus Width: 3.4389



B0L002S, U4-ch6, adc2

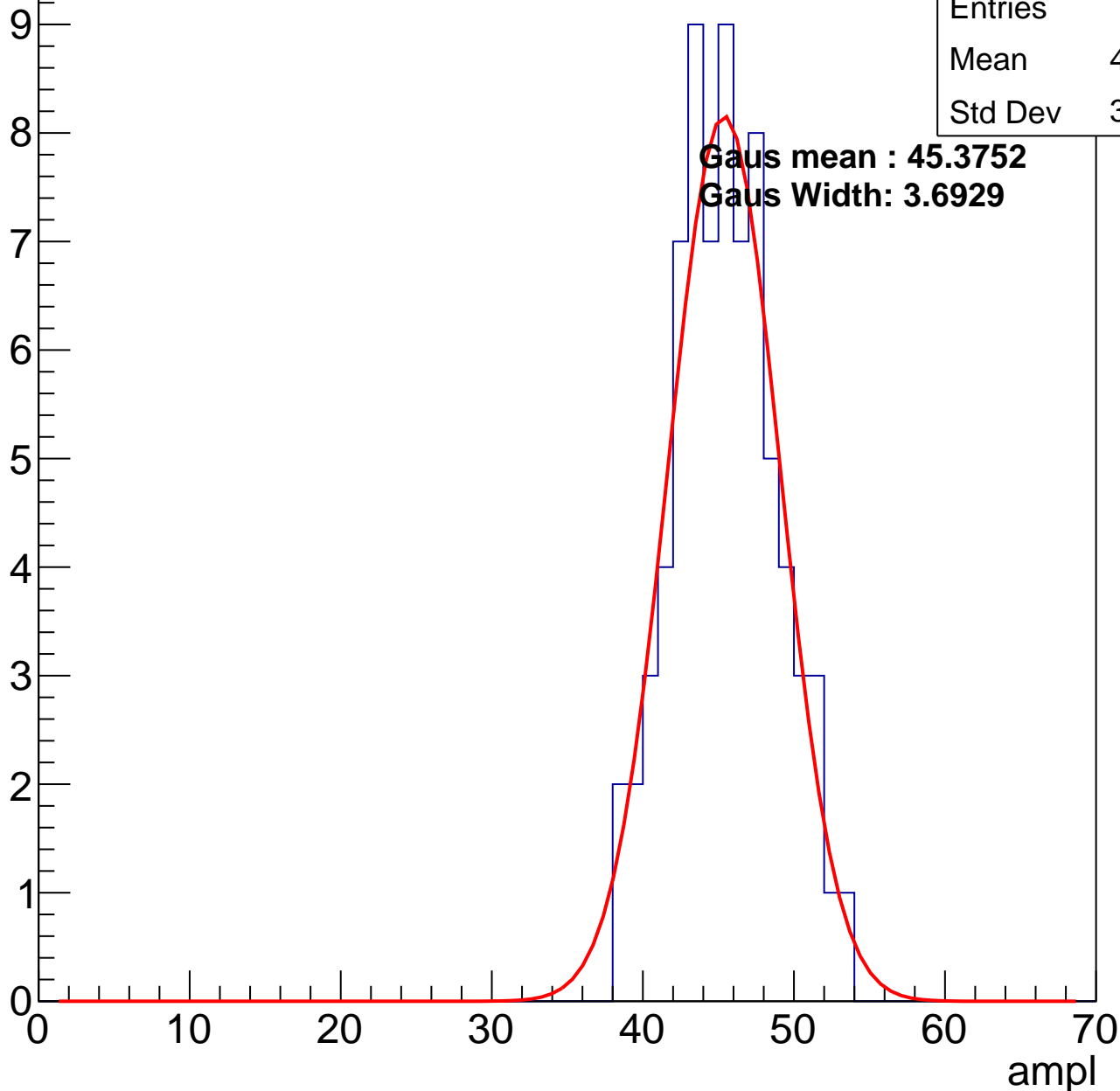
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.99
Std Dev	3.384

Gaus mean : 45.3752

Gaus Width: 3.6929

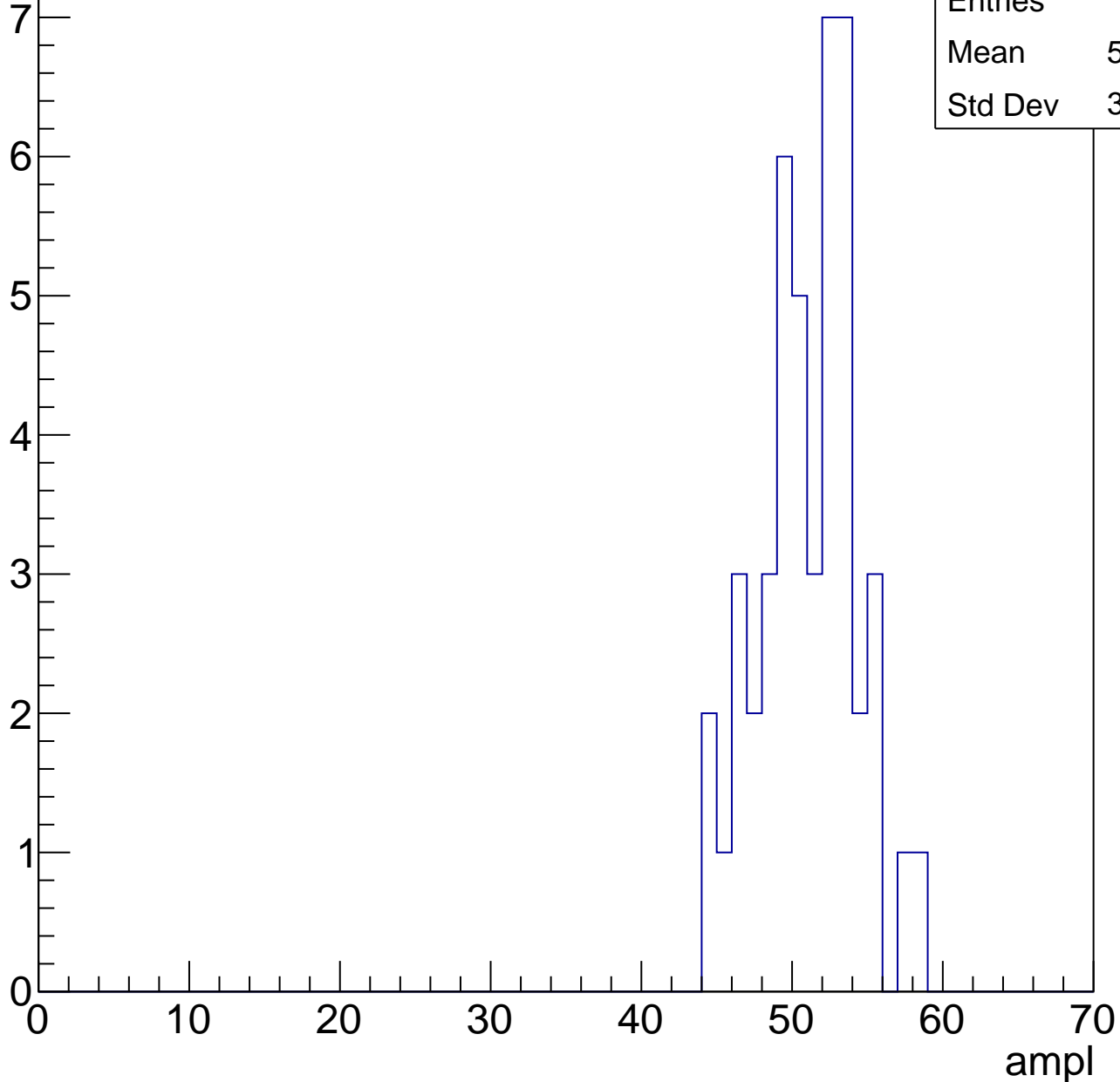


B0L002S, U4-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

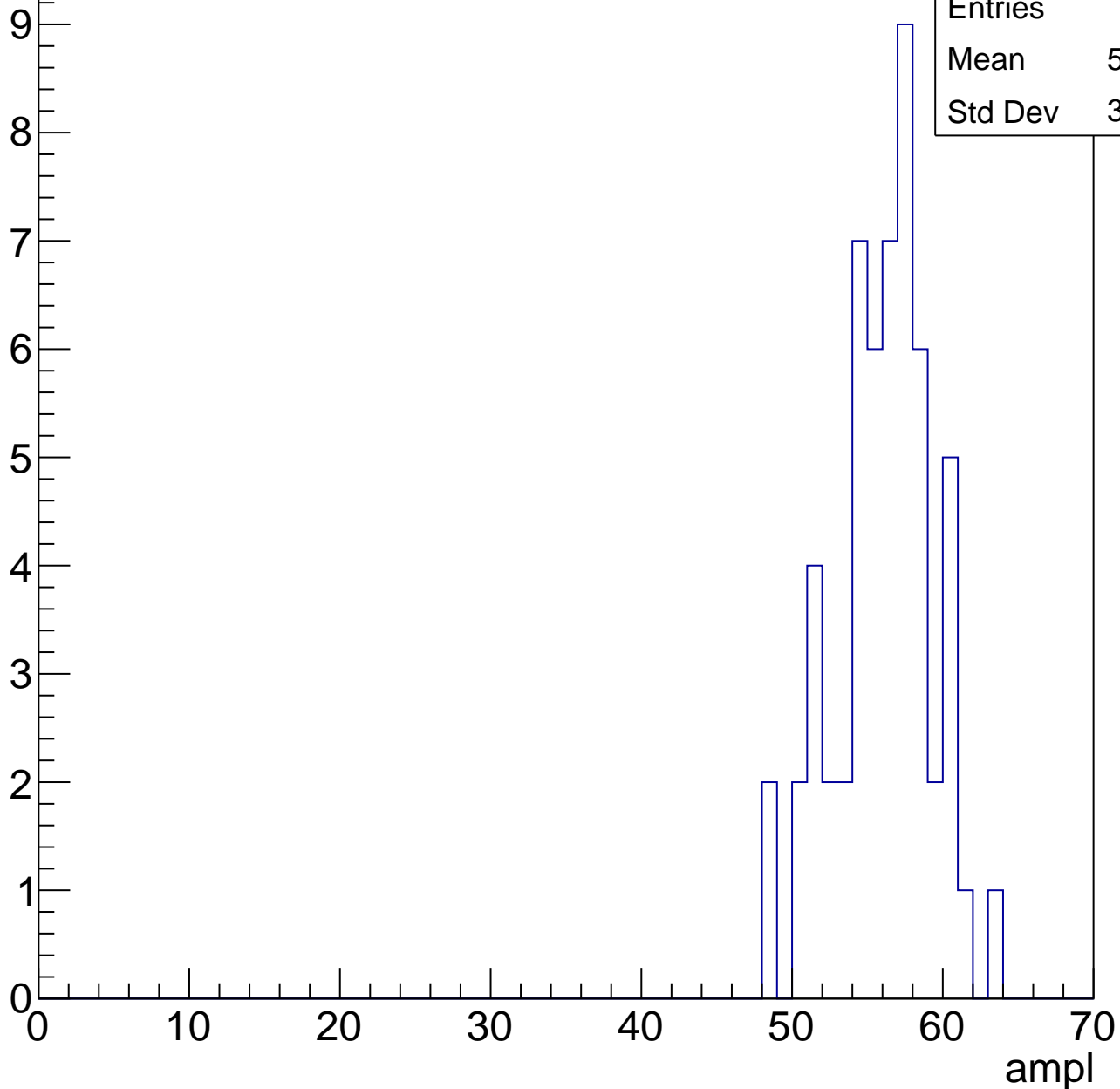
Entries	46
Mean	50.63
Std Dev	3.219



B0L002S, U4-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch6, adc5

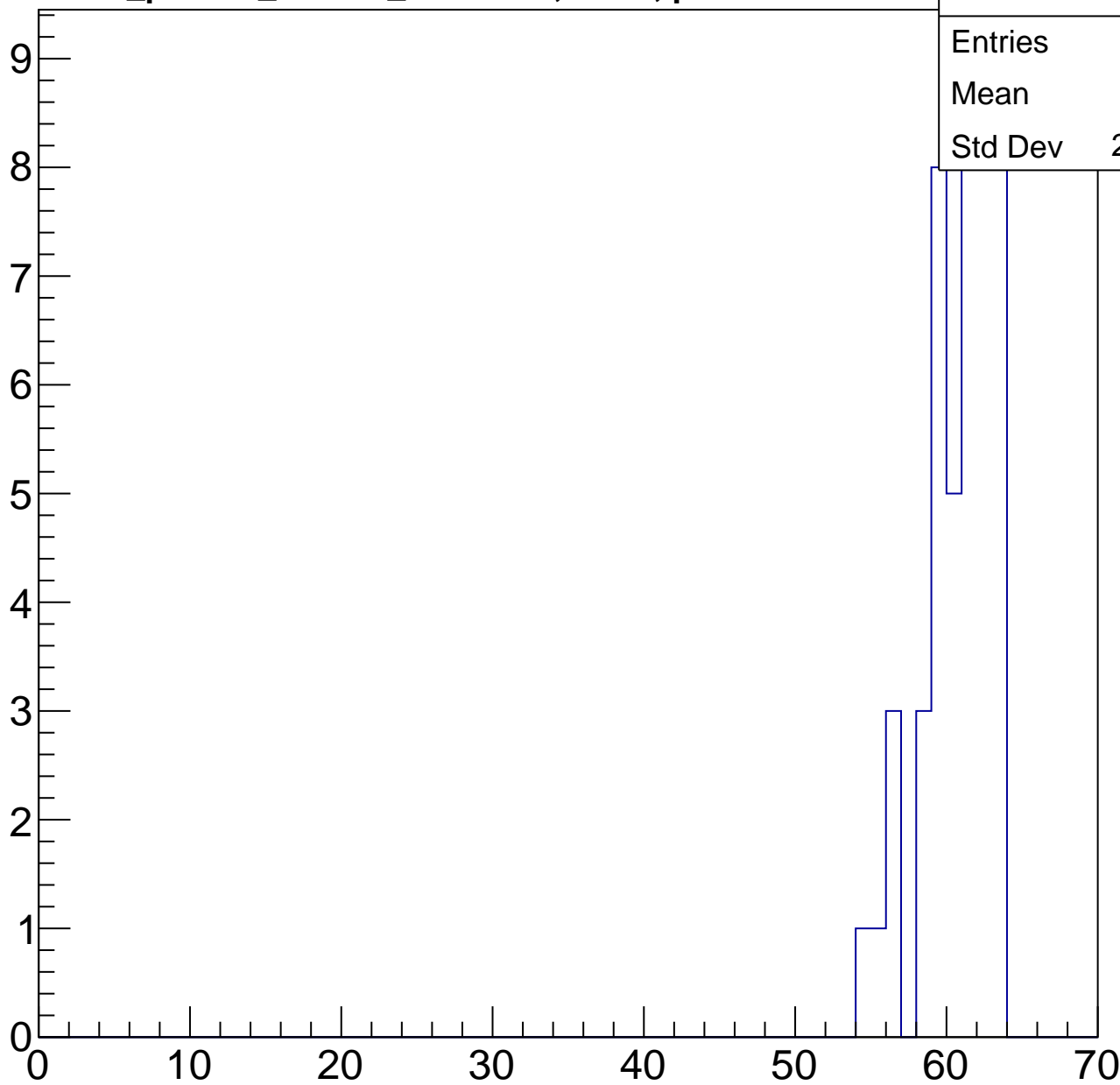
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.3
Std Dev	2.268

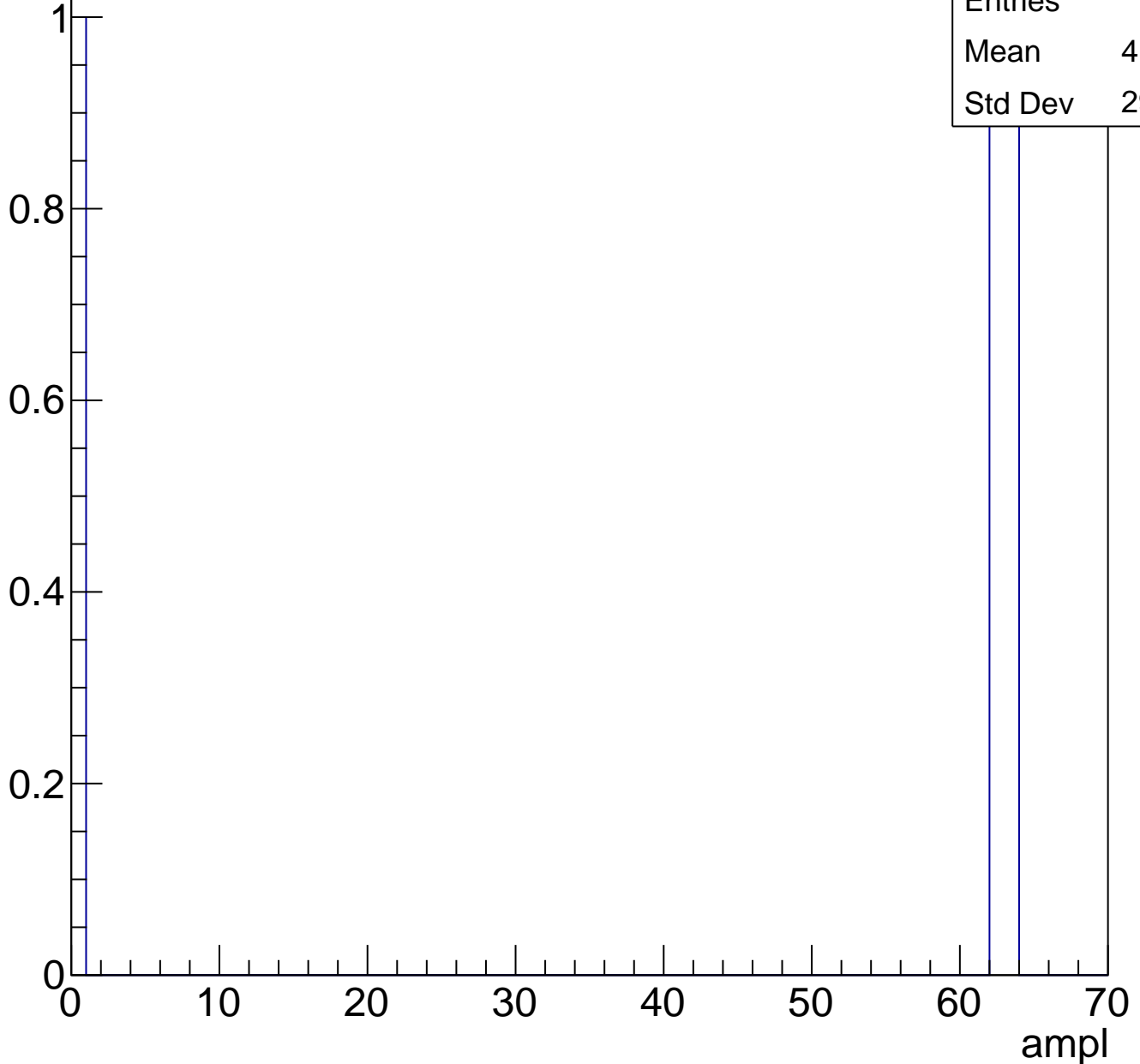
ampl



B0L002S, U4-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch7, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	29.05
Std Dev	4.593

Gaus mean : 30.0146

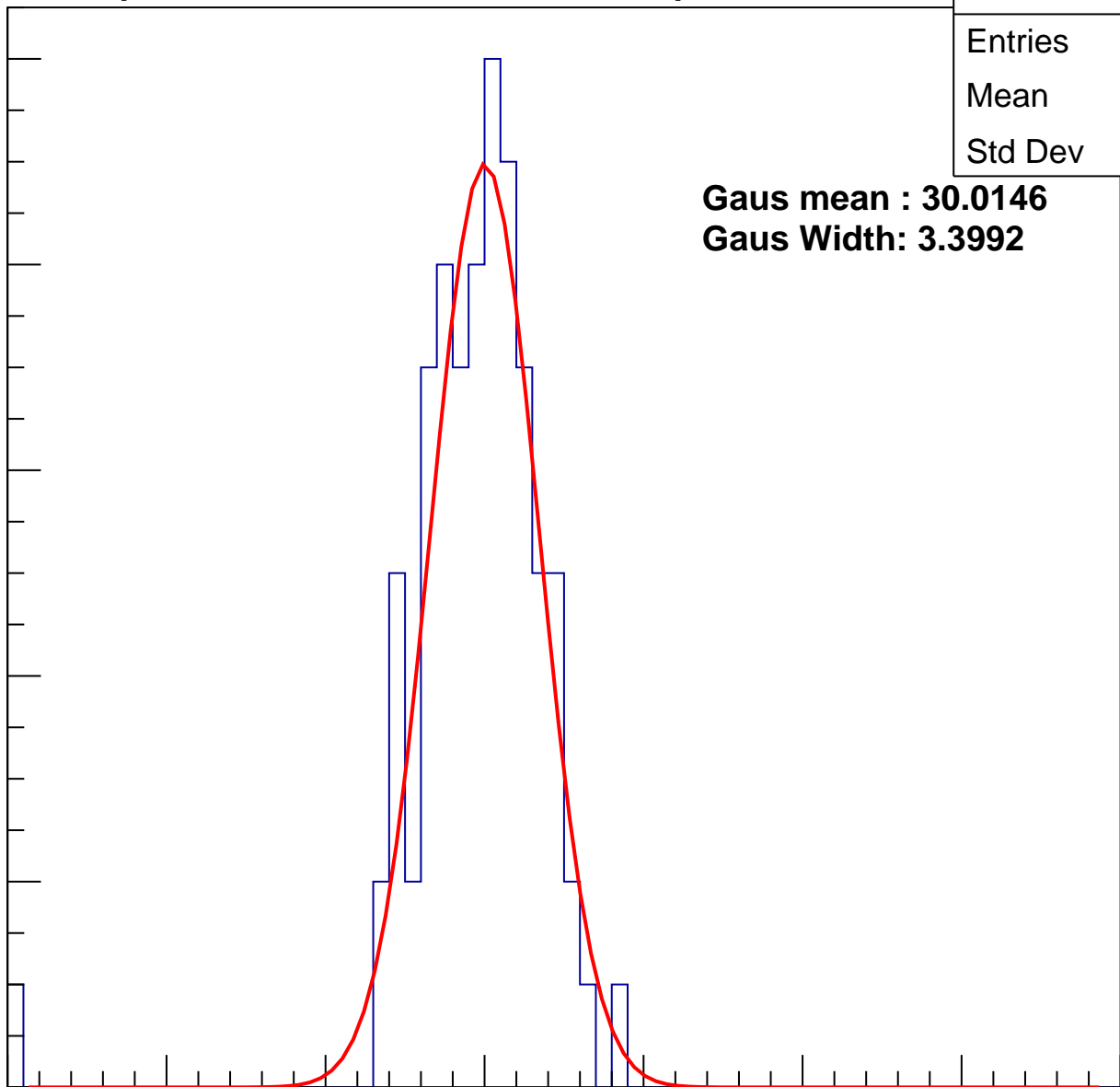
Gaus Width: 3.3992

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch7, adc1

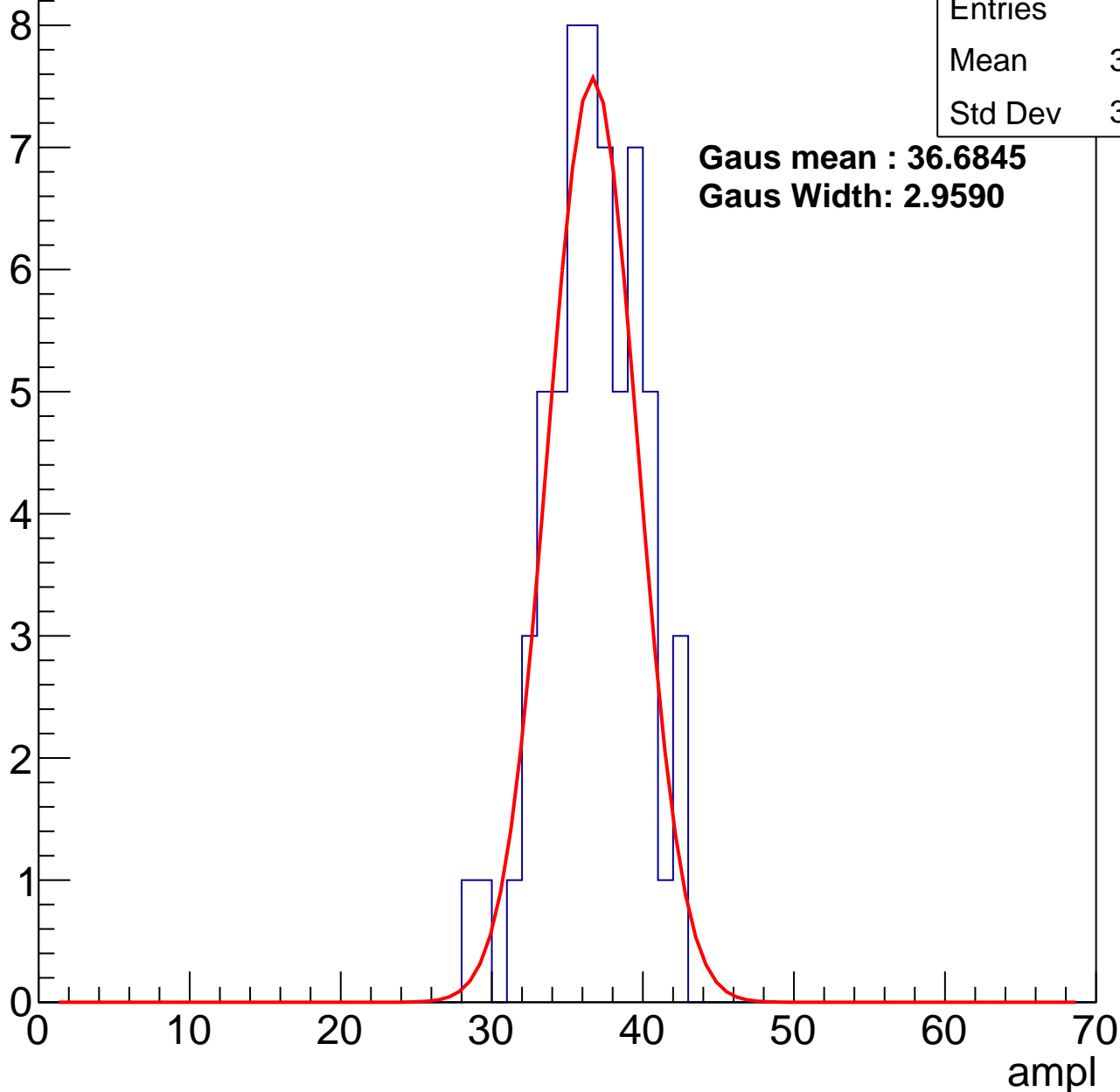
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.27
Std Dev	3.049

Gaus mean : 36.6845

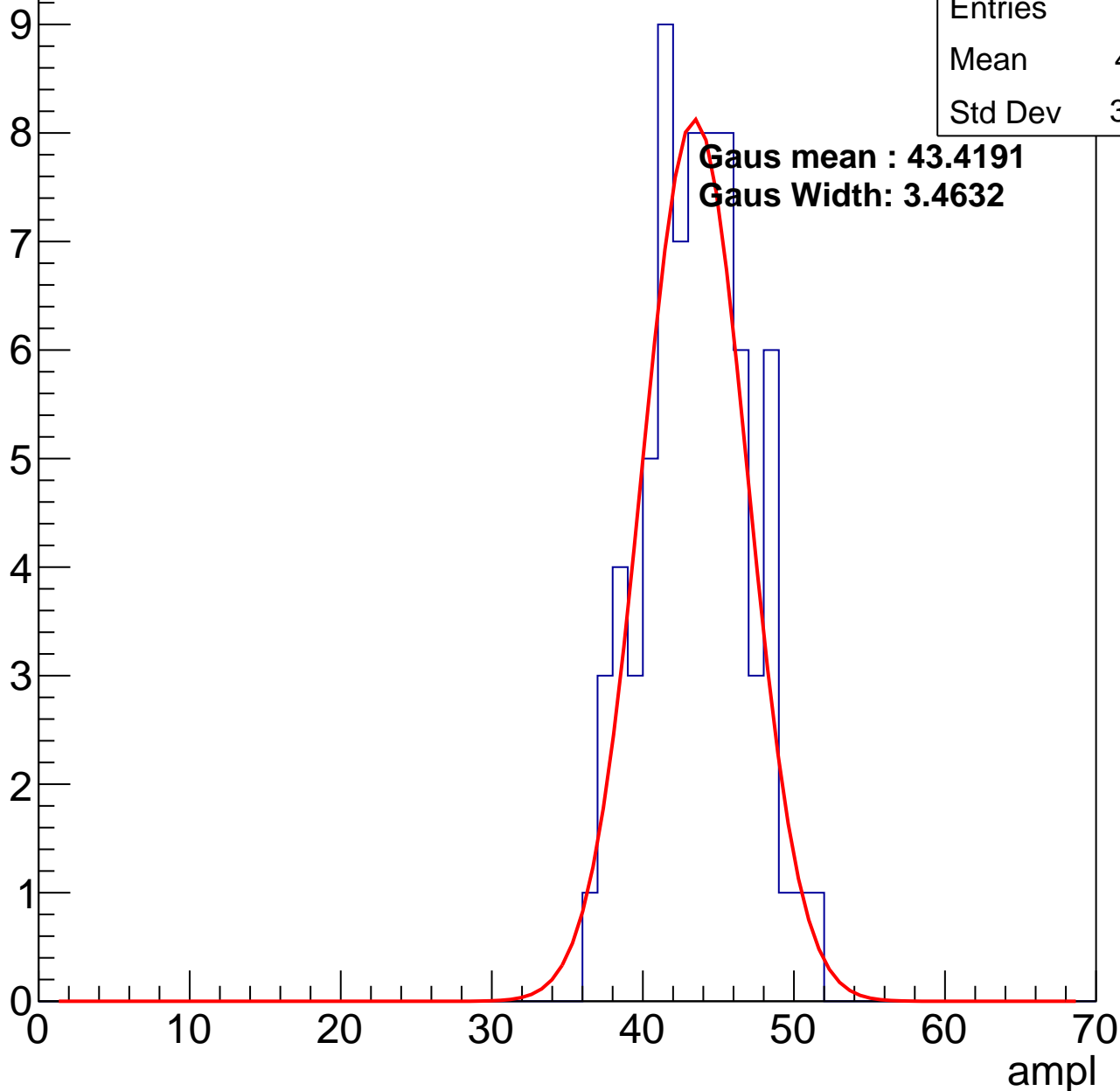
Gaus Width: 2.9590



B0L002S, U4-ch7, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

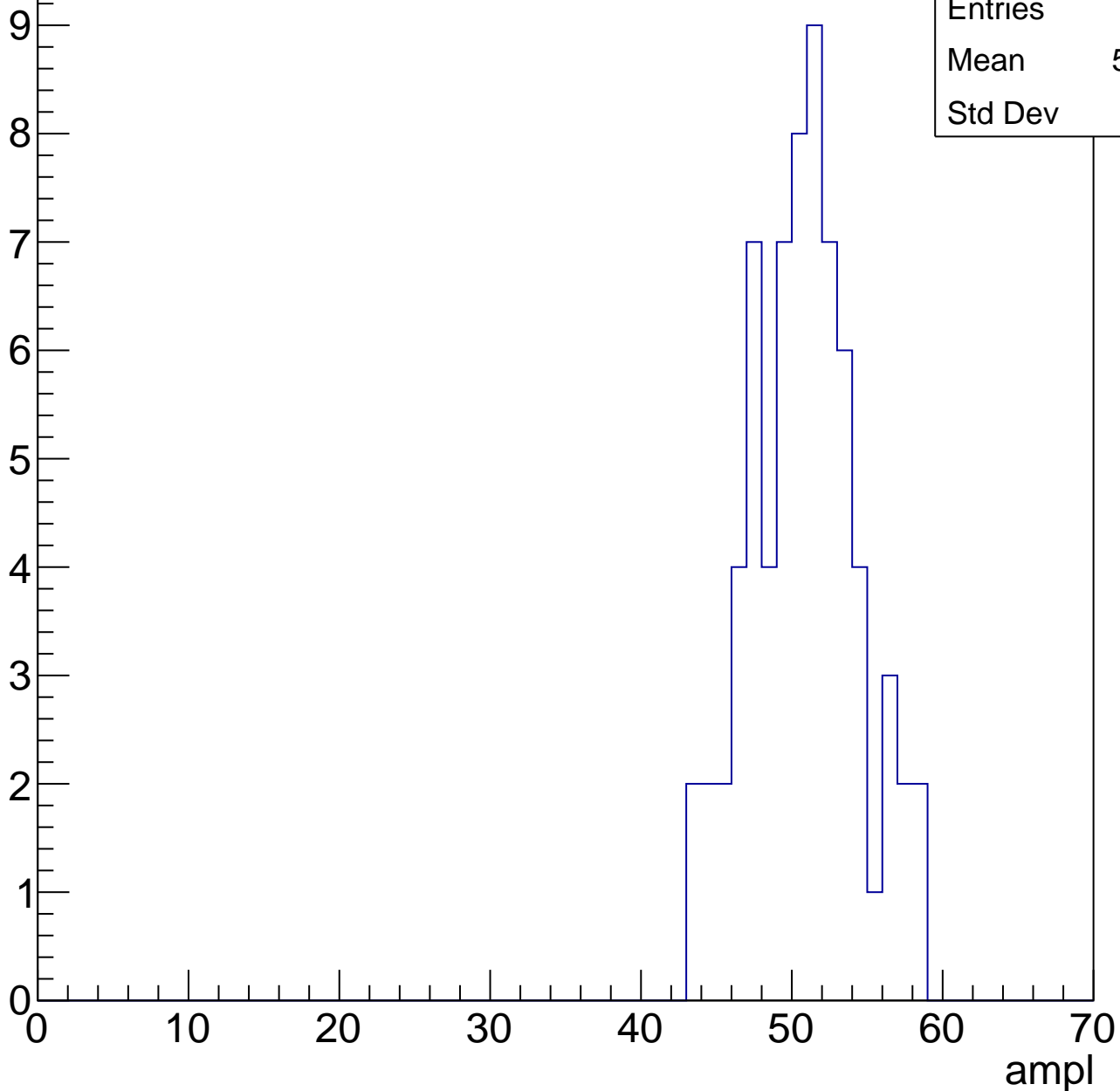


B0L002S, U4-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	50.31
Std Dev	3.54

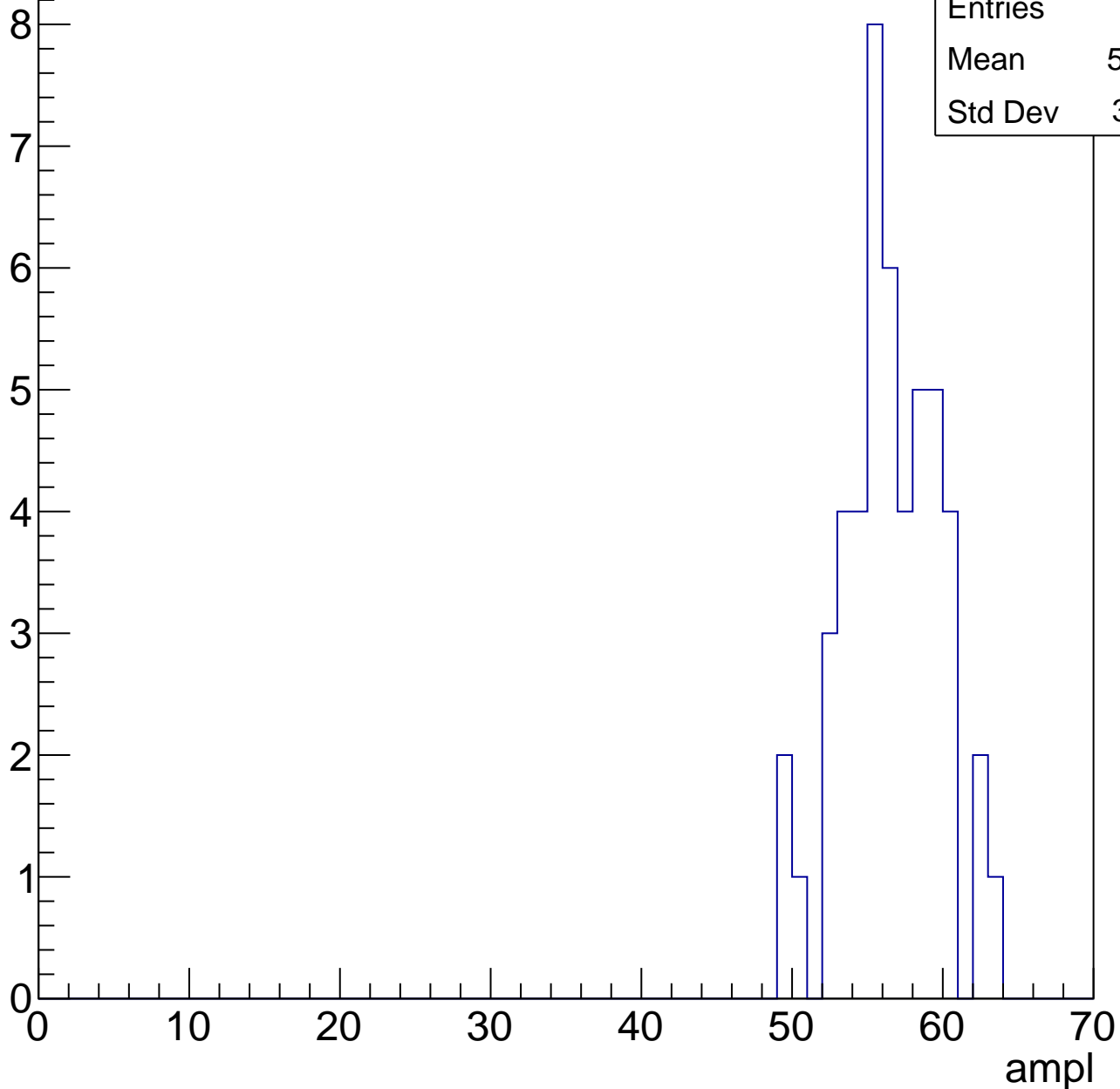


B0L002S, U4-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	56.08
Std Dev	3.181



B0L002S, U4-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

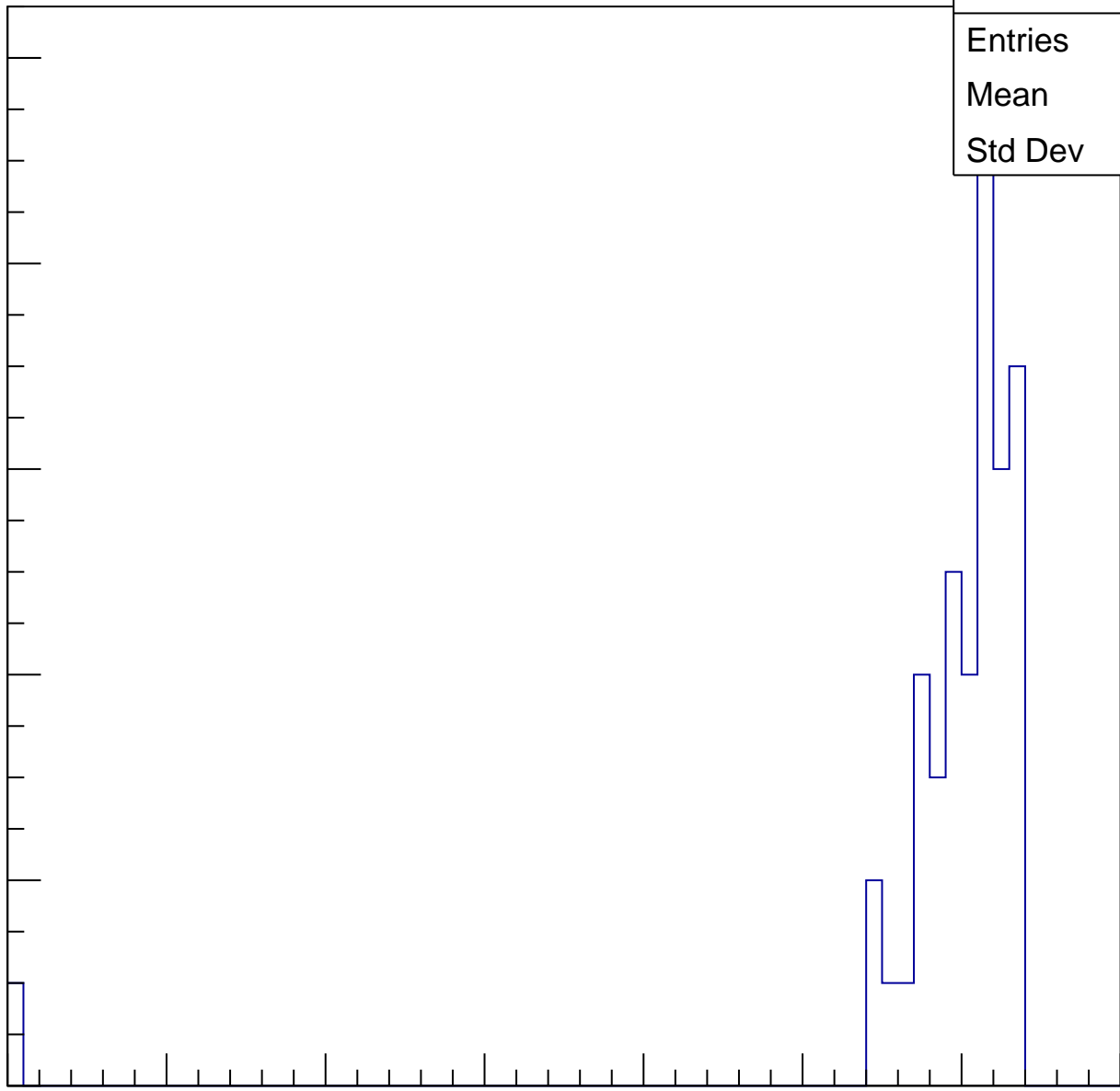
Entries	44
Mean	58.61
Std Dev	9.267

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.247

0 10 20 30 40 50 60 70

ampl

B0L002S, U4-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch8, adc0

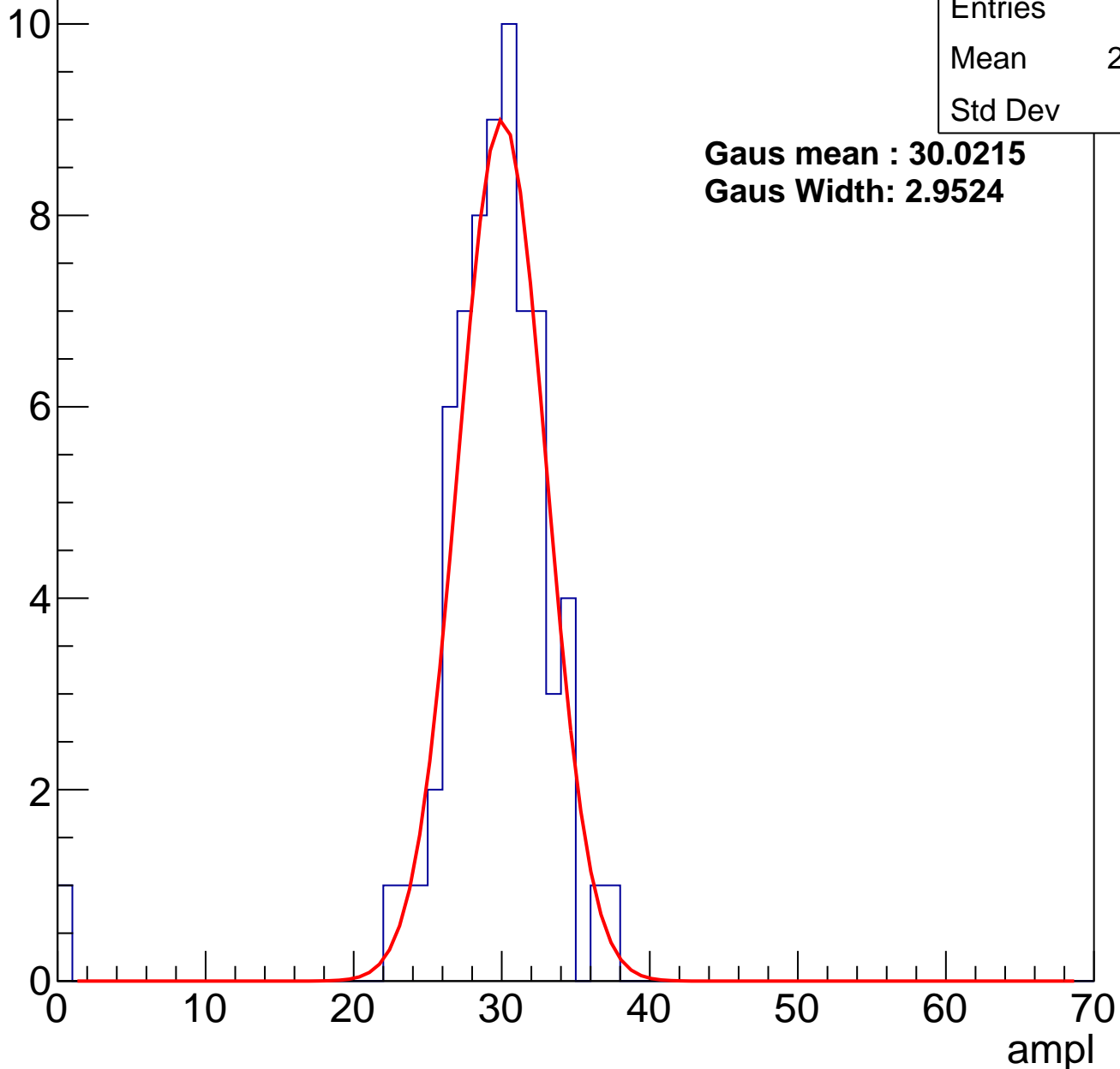
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	28.96
Std Dev	4.56

Gaus mean : 30.0215

Gaus Width: 2.9524

Entry



B0L002S, U4-ch8, adc1

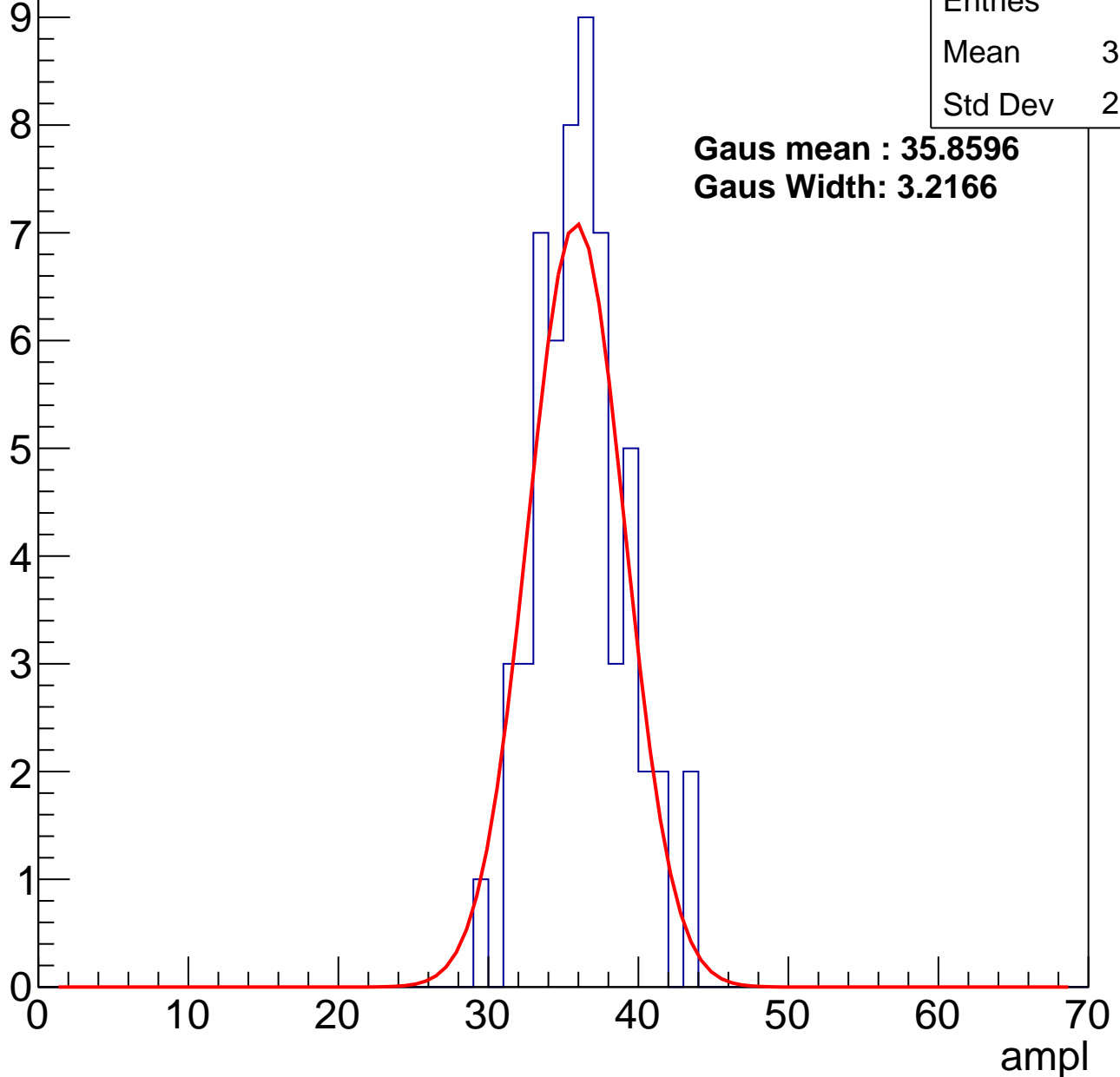
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	35.74
Std Dev	2.957

Gaus mean : 35.8596

Gaus Width: 3.2166



B0L002S, U4-ch8, adc2

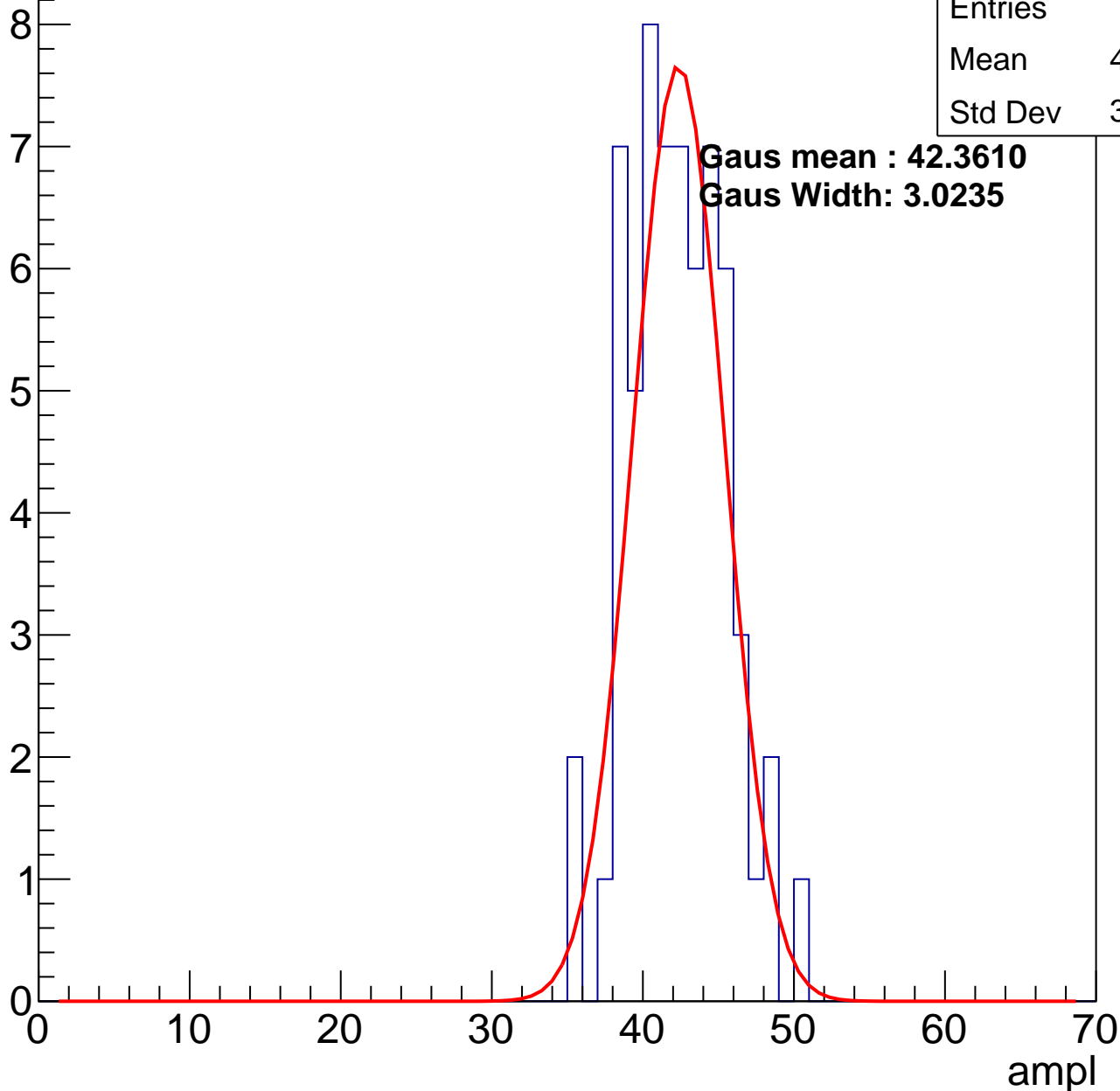
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.84
Std Dev	3.123

Gaus mean : 42.3610

Gaus Width: 3.0235



B0L002S, U4-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

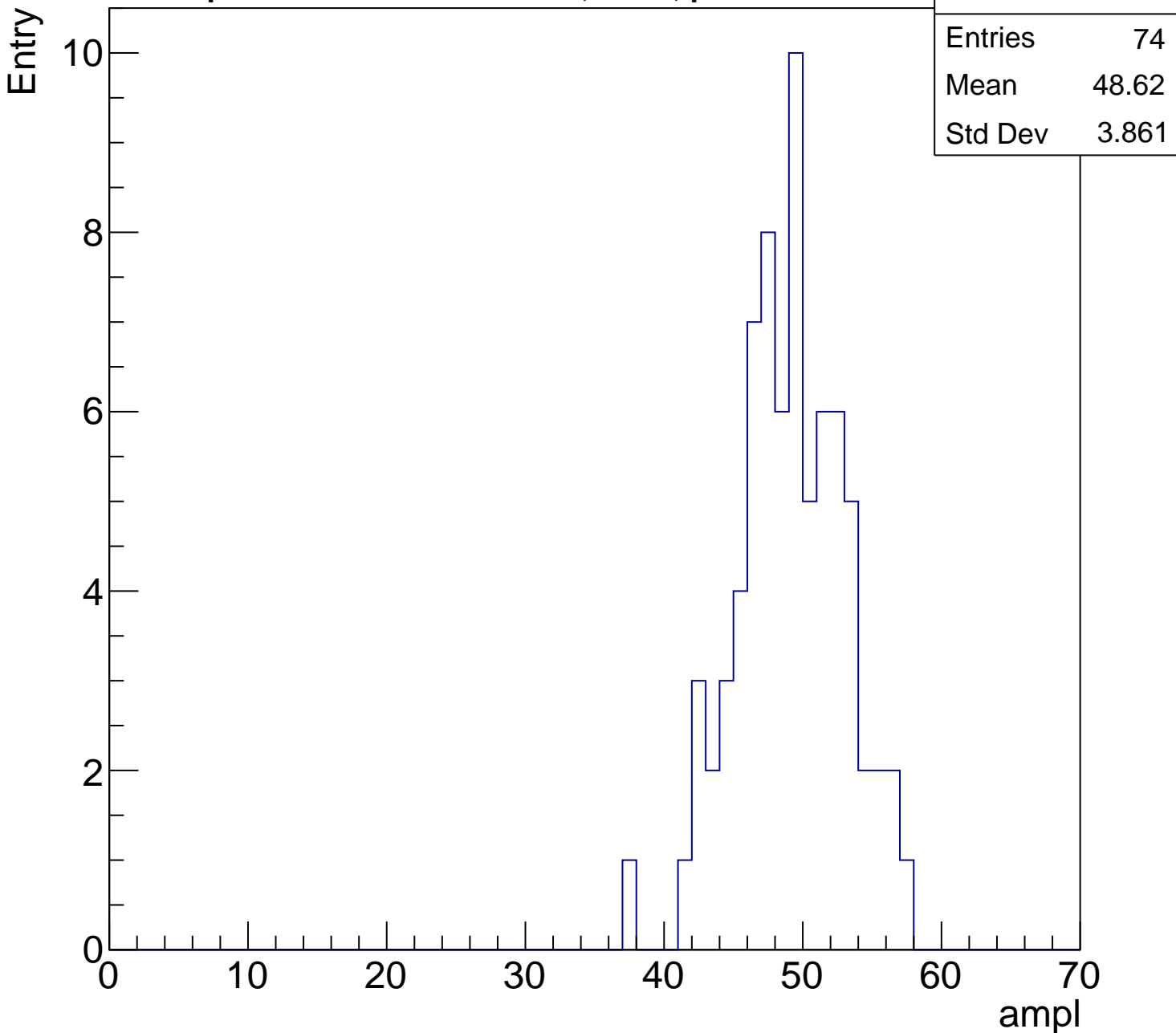
Entries	74
Mean	48.62
Std Dev	3.861

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

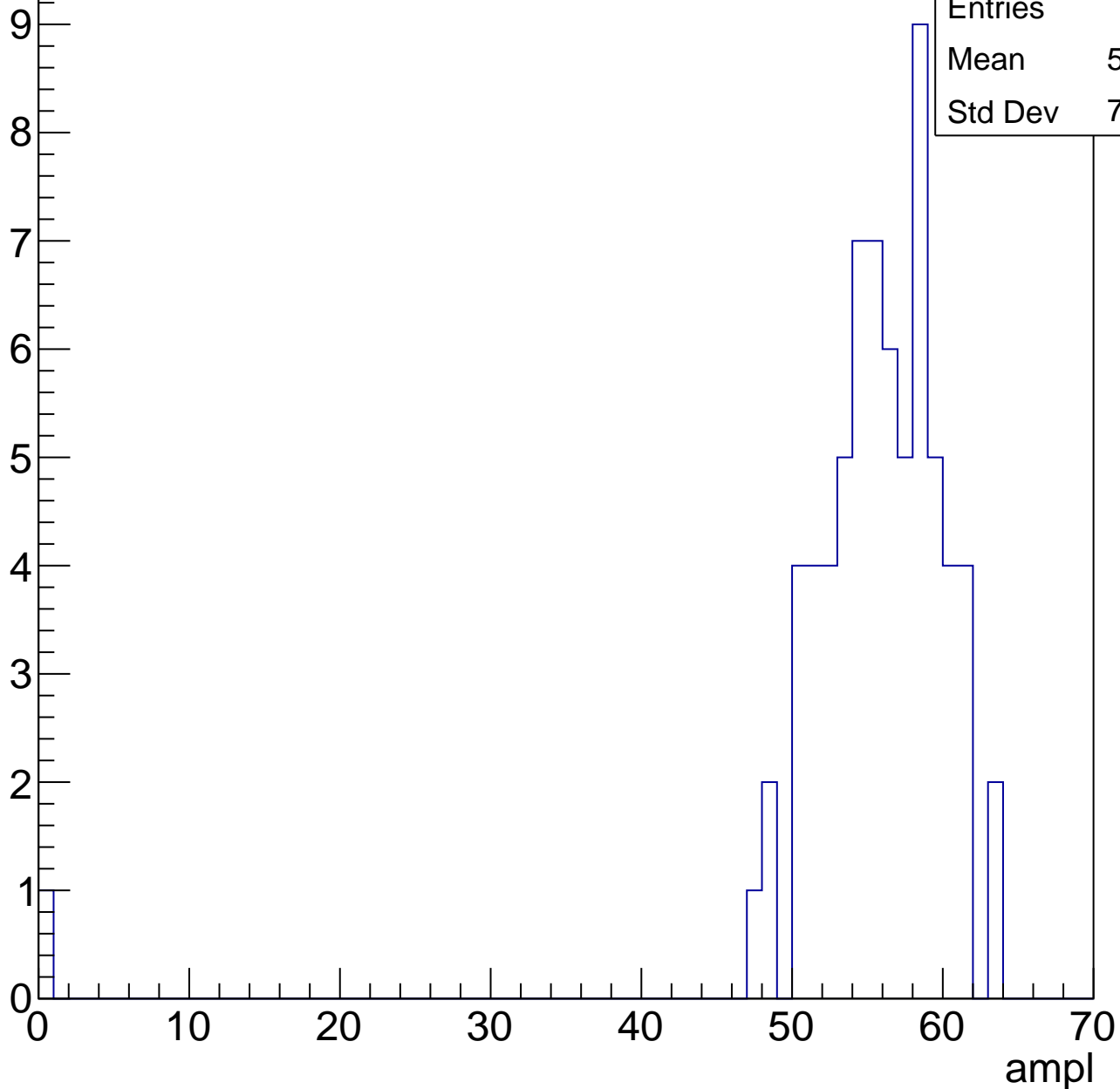


B0L002S, U4-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	54.73
Std Dev	7.529



B0L002S, U4-ch8, adc5

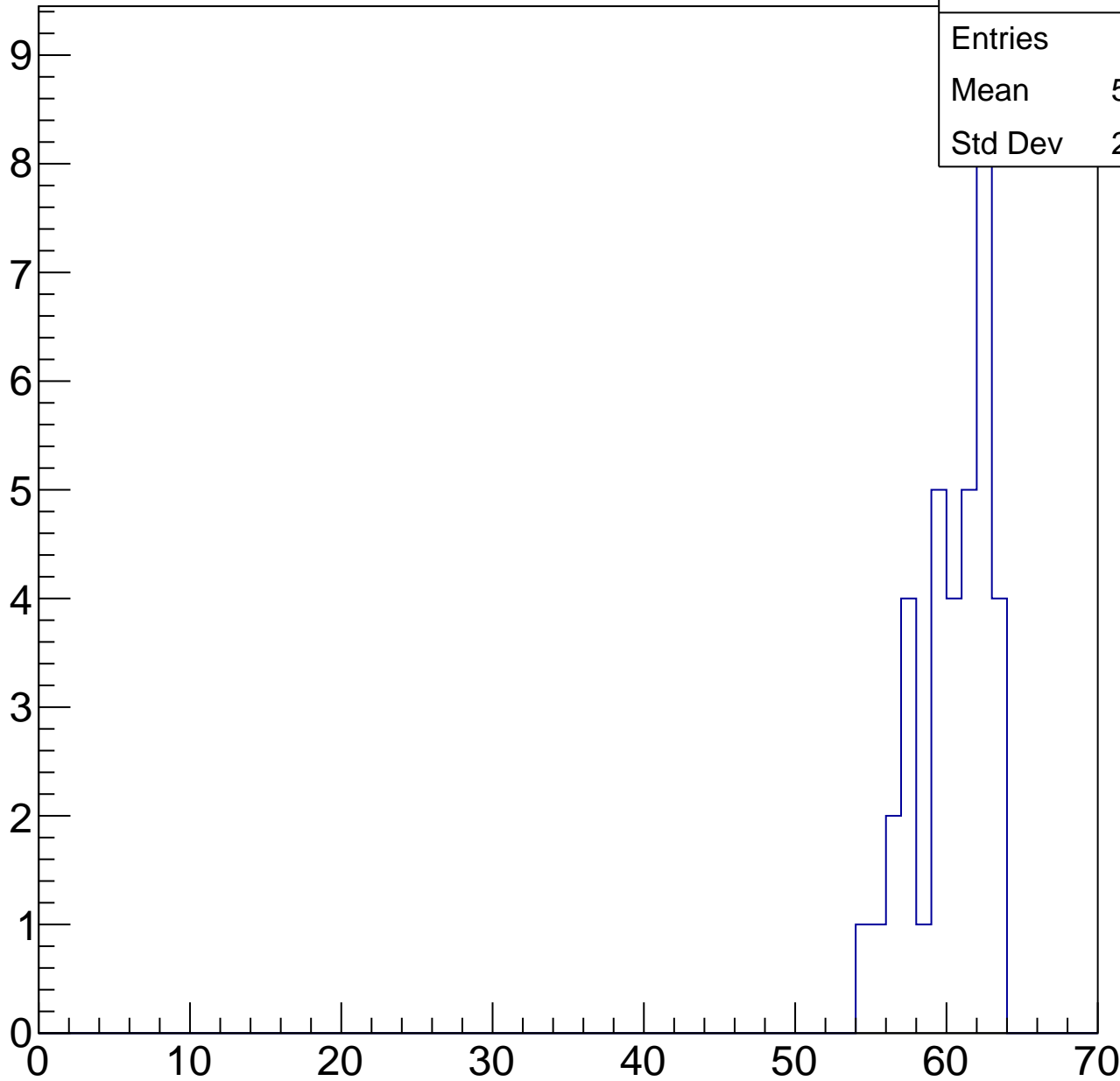
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	36
Mean	59.92
Std Dev	2.442

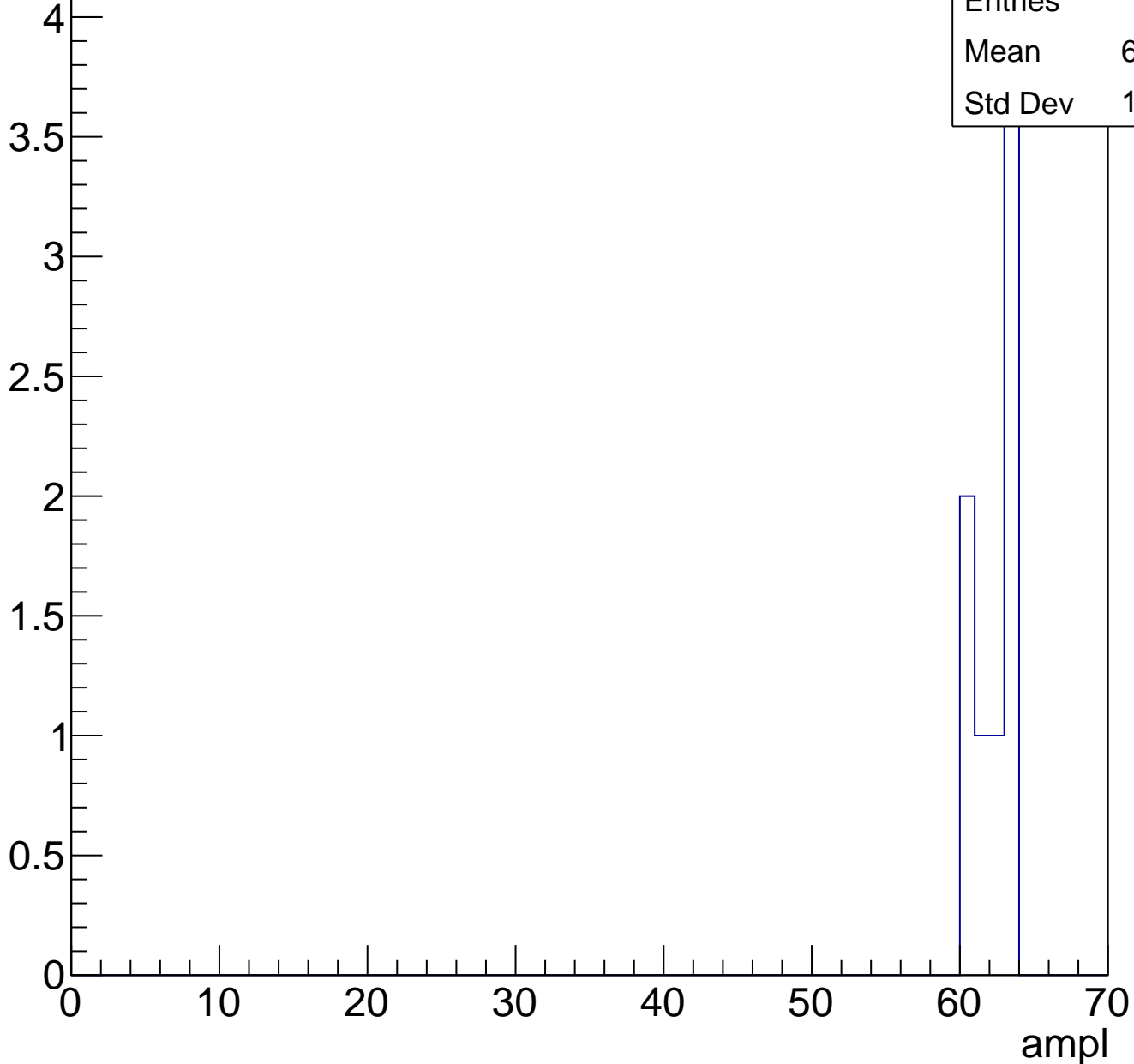
ampl



B0L002S, U4-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch9, adc0

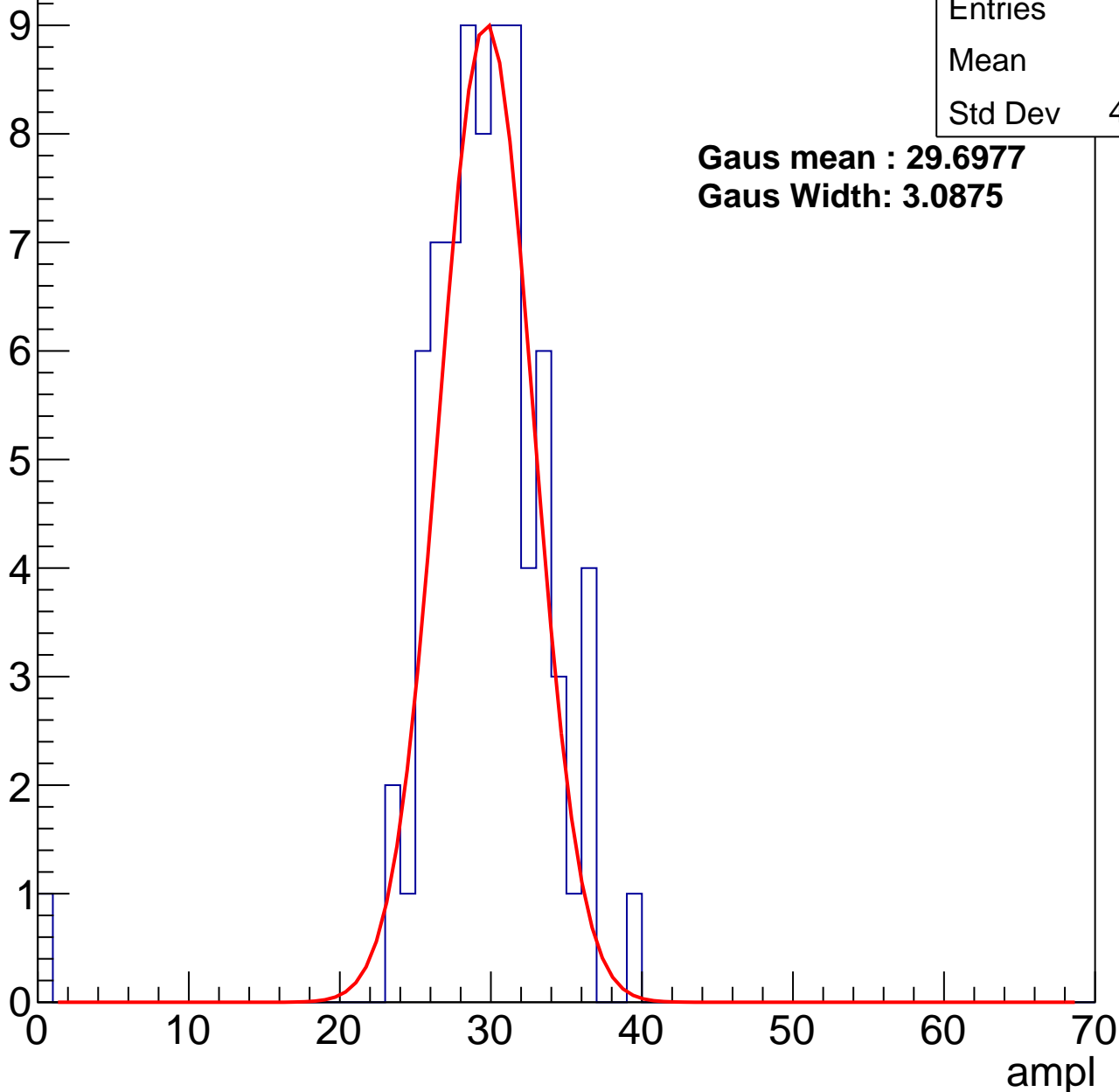
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.1
Std Dev	4.692

Gaus mean : 29.6977

Gaus Width: 3.0875



B0L002S, U4-ch9, adc1

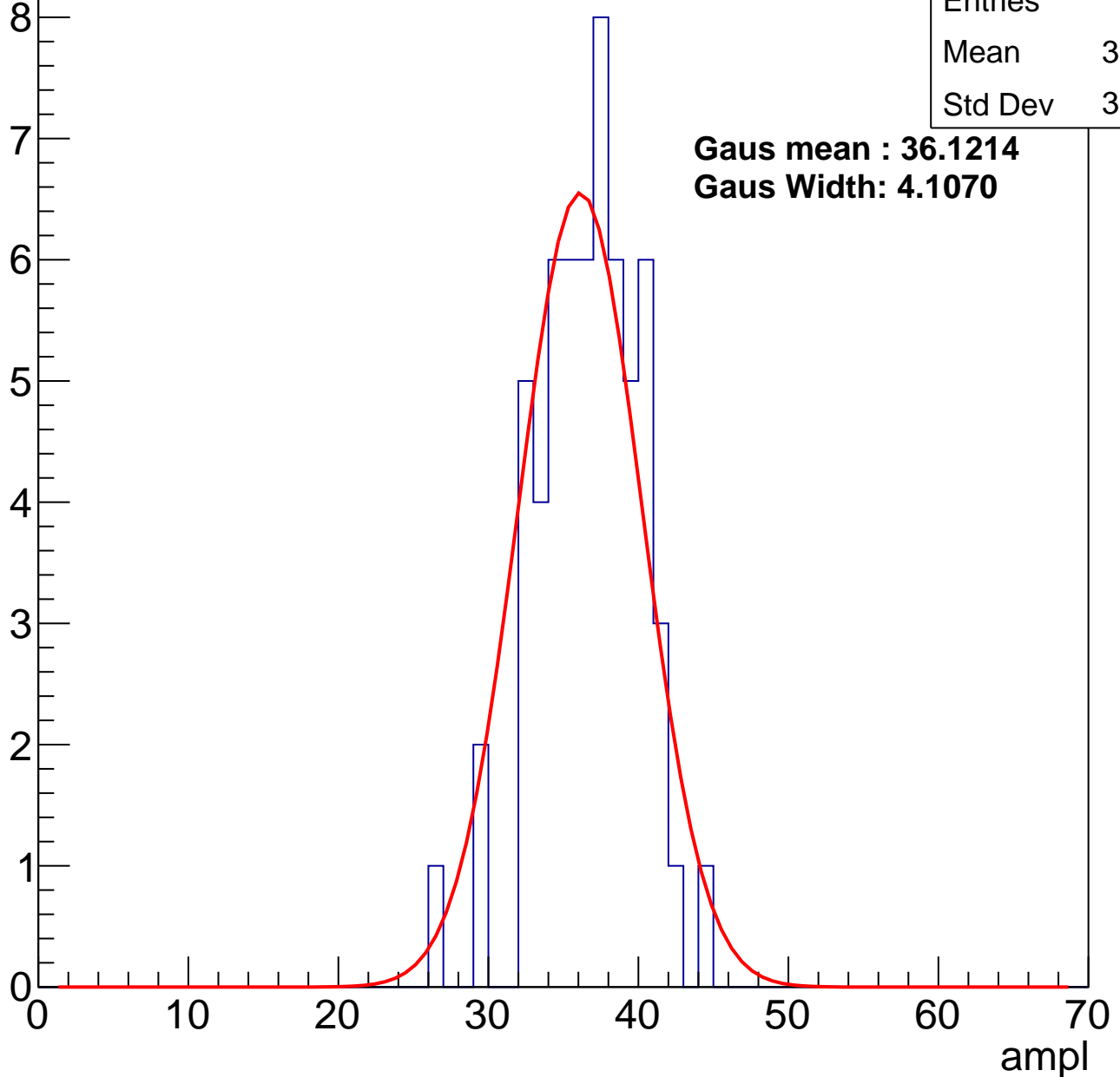
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.23
Std Dev	3.388

Gaus mean : 36.1214

Gaus Width: 4.1070



B0L002S, U4-ch9, adc2

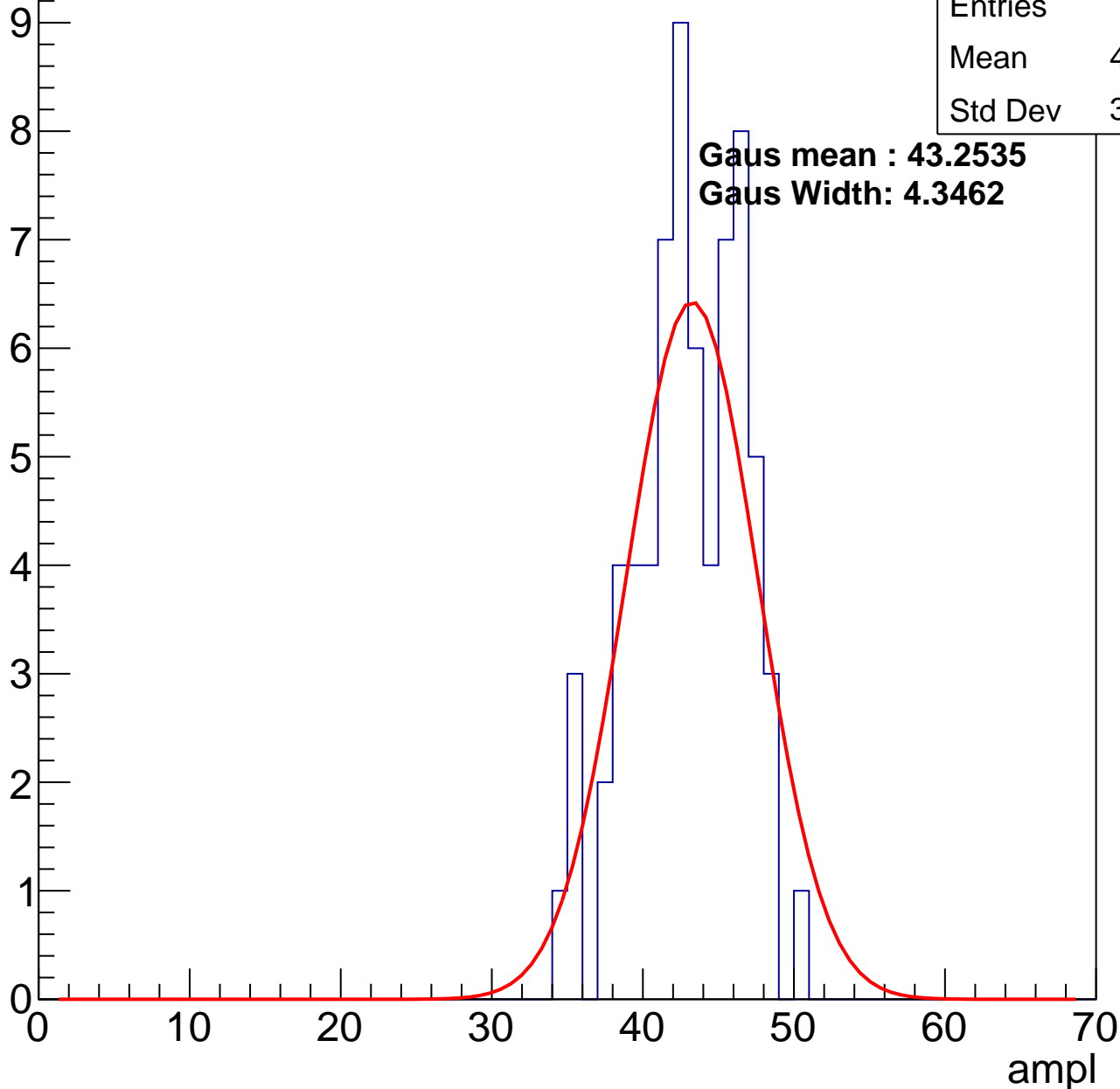
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.53
Std Dev	3.587

Gaus mean : 43.2535

Gaus Width: 4.3462



B0L002S, U4-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	49.05
Std Dev	3.504

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

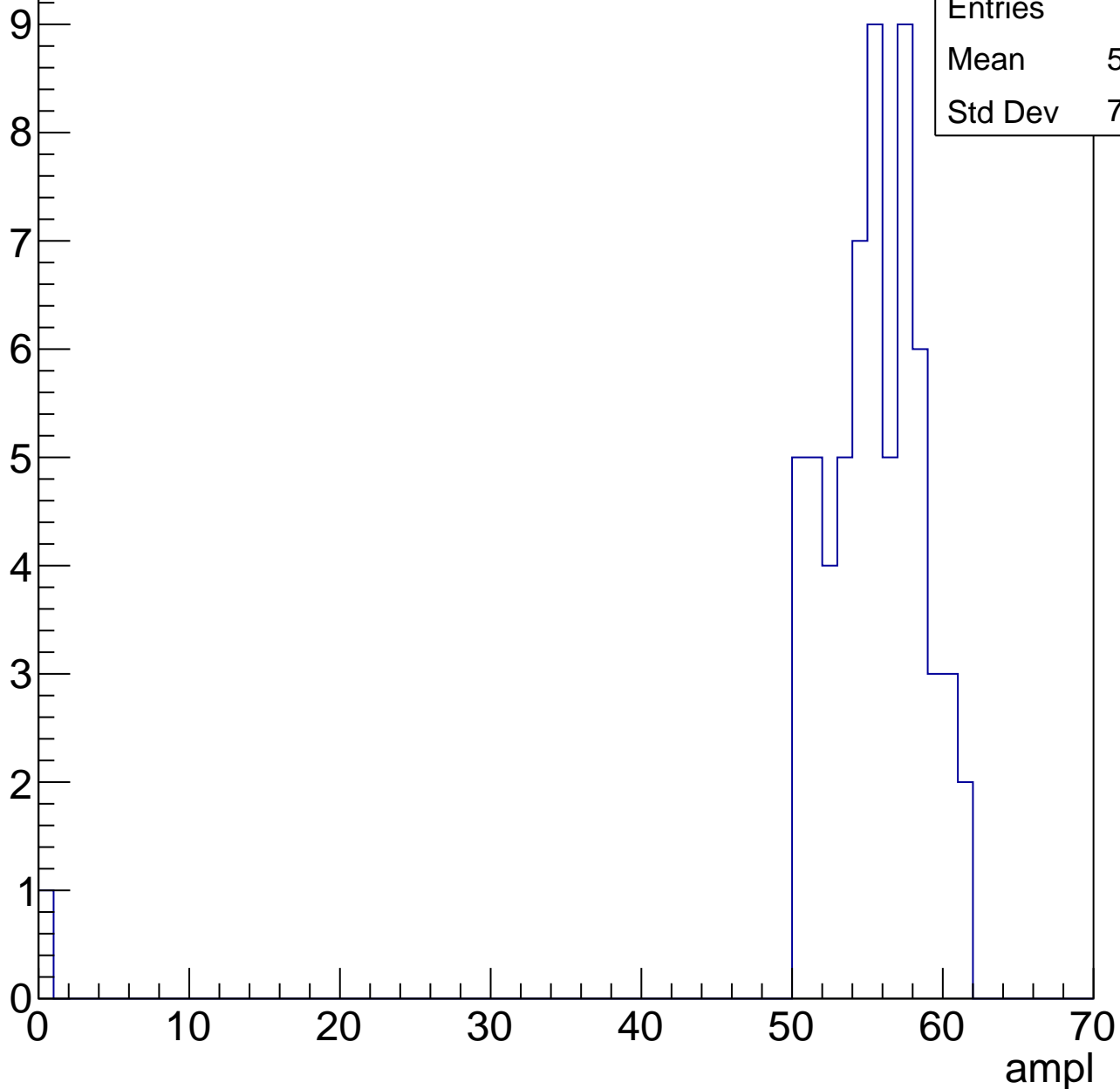


B0L002S, U4-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	54.23
Std Dev	7.443



B0L002S, U4-ch9, adc5

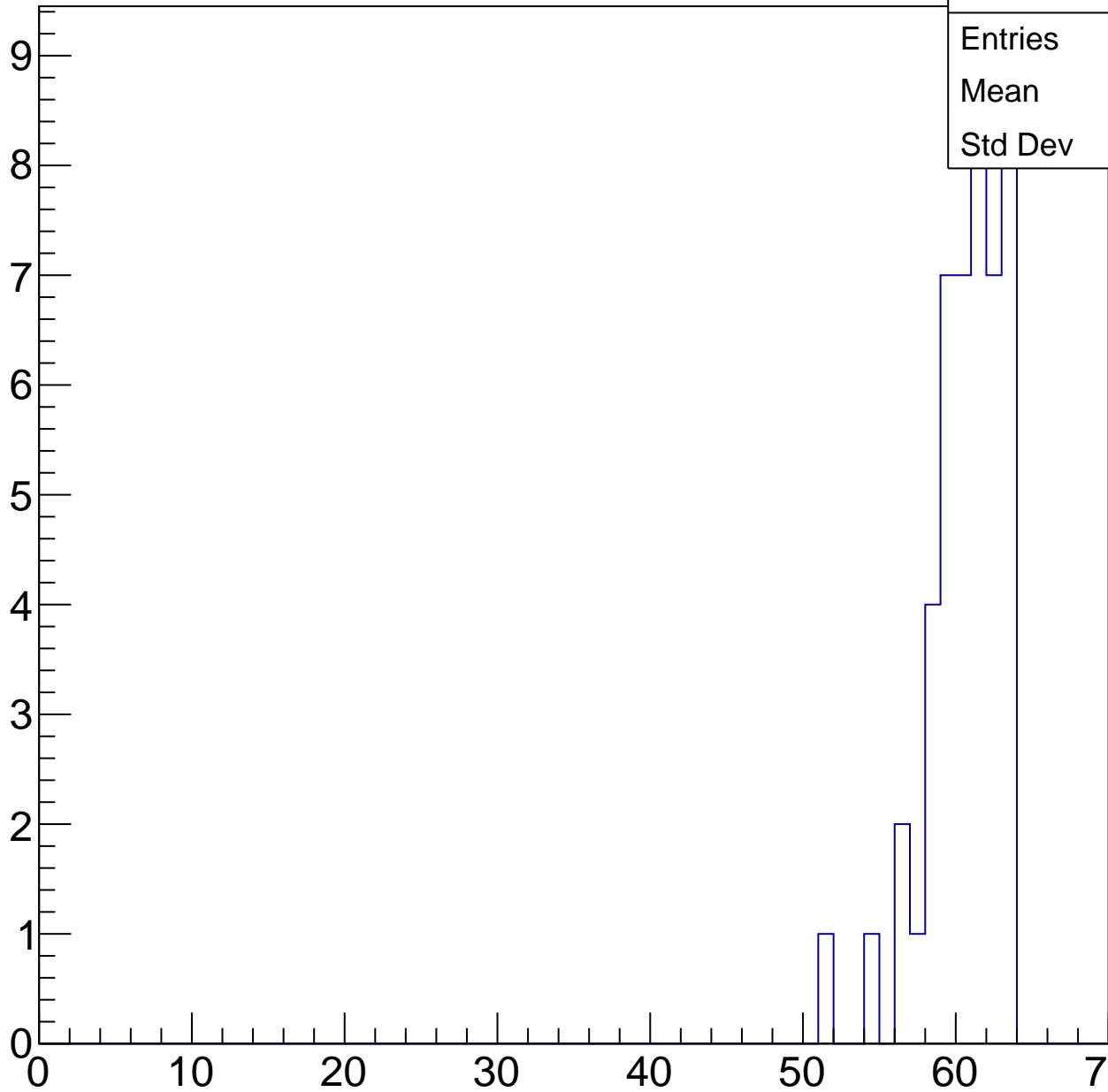
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	60.17
Std Dev	2.512

ampl



B0L002S, U4-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U4-ch10, adc0

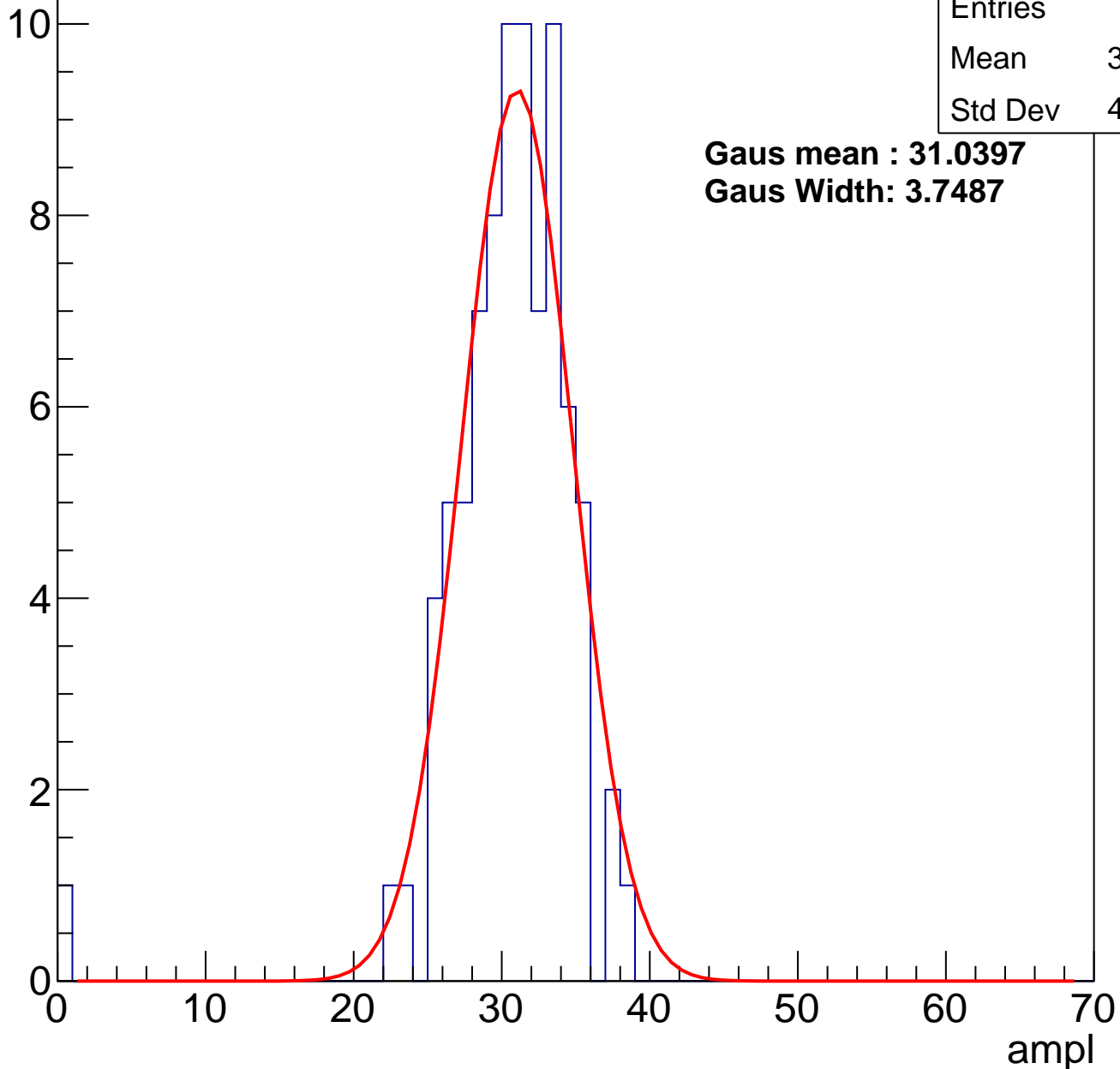
calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	30.04
Std Dev	4.637

Gaus mean : 31.0397

Gaus Width: 3.7487

Entry



B0L002S, U4-ch10, adc1

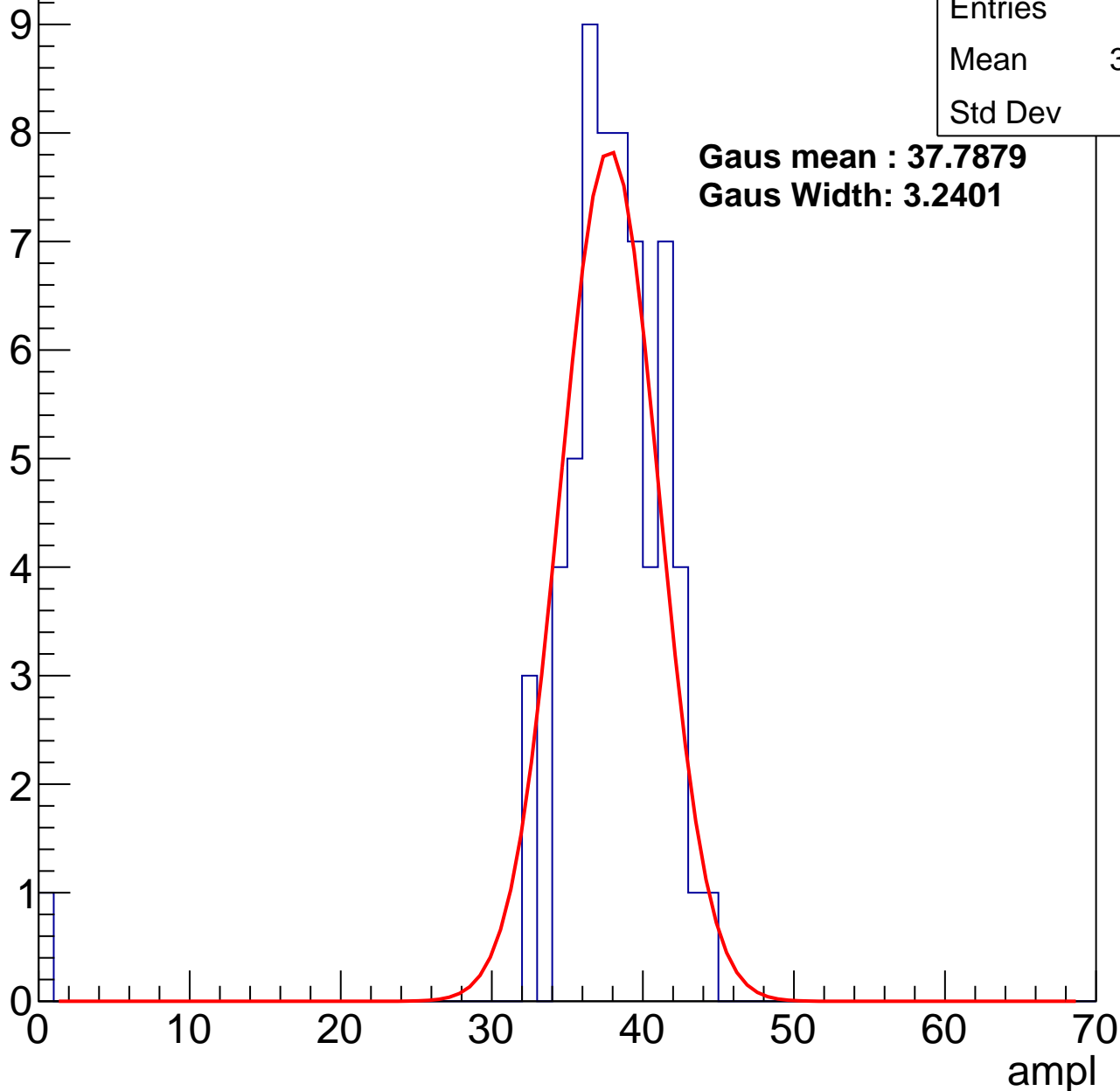
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.19
Std Dev	5.5

Gaus mean : 37.7879

Gaus Width: 3.2401



B0L002S, U4-ch10, adc2

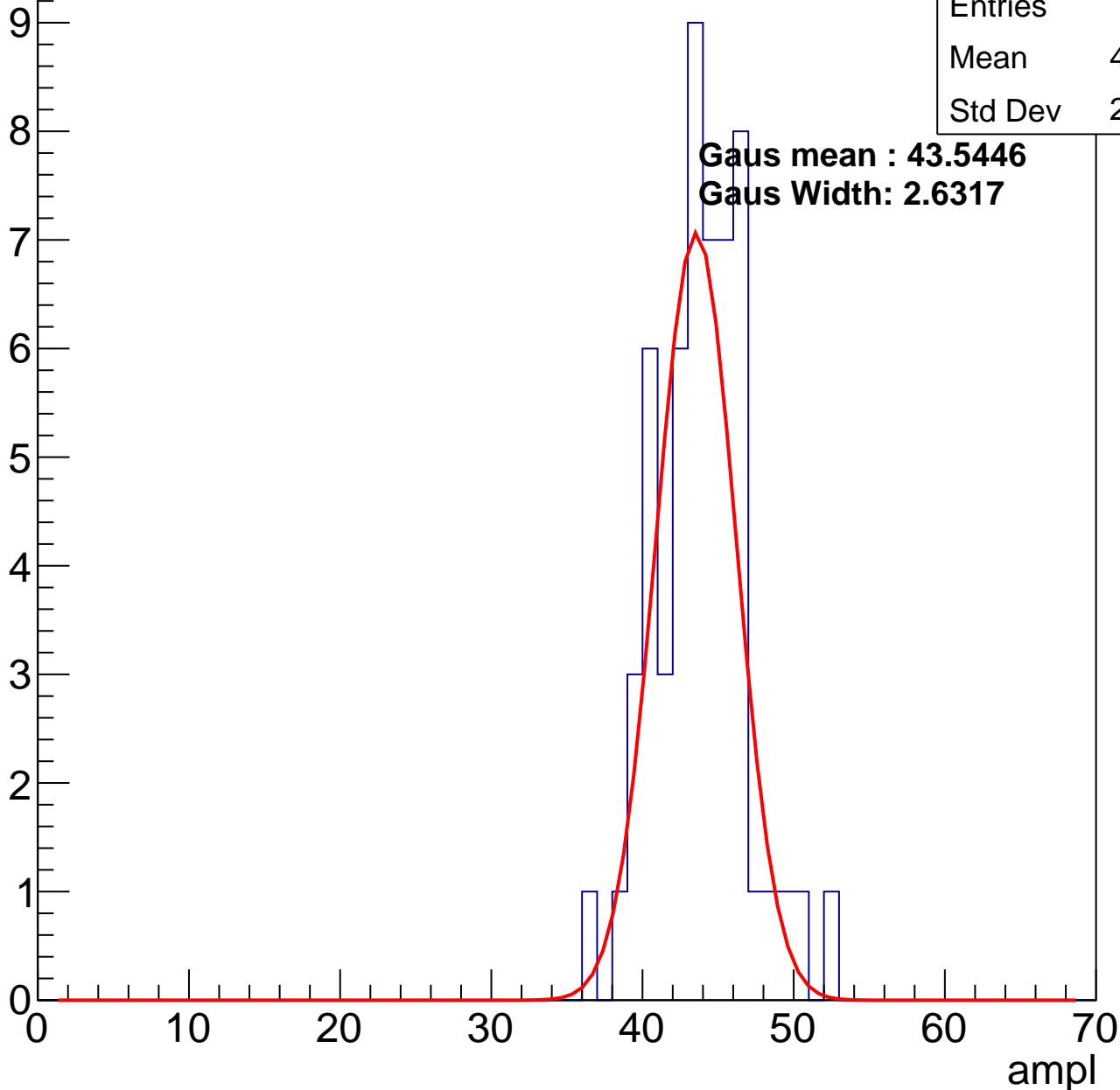
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.39
Std Dev	2.986

Gaus mean : 43.5446

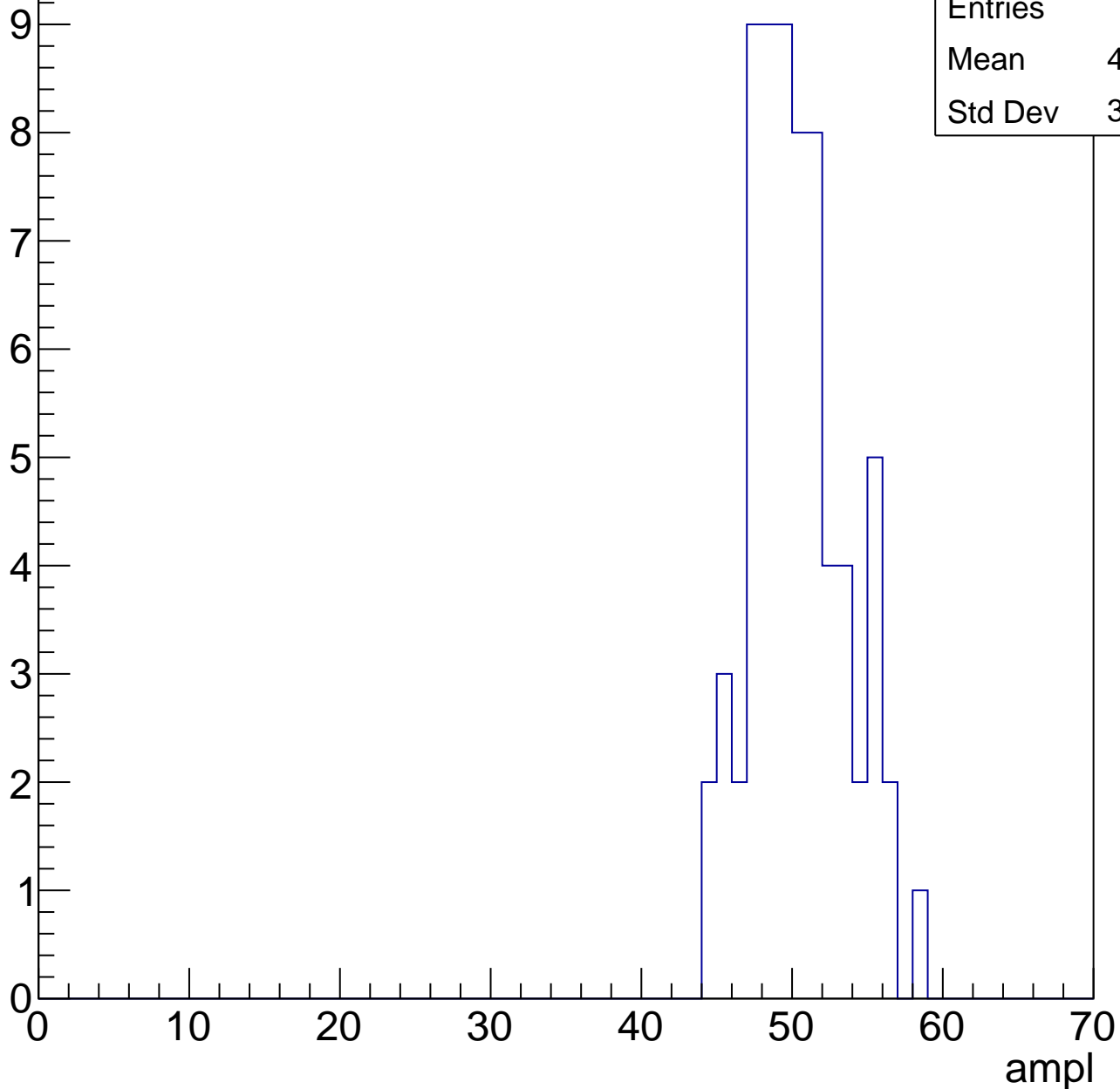
Gaus Width: 2.6317



B0L002S, U4-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

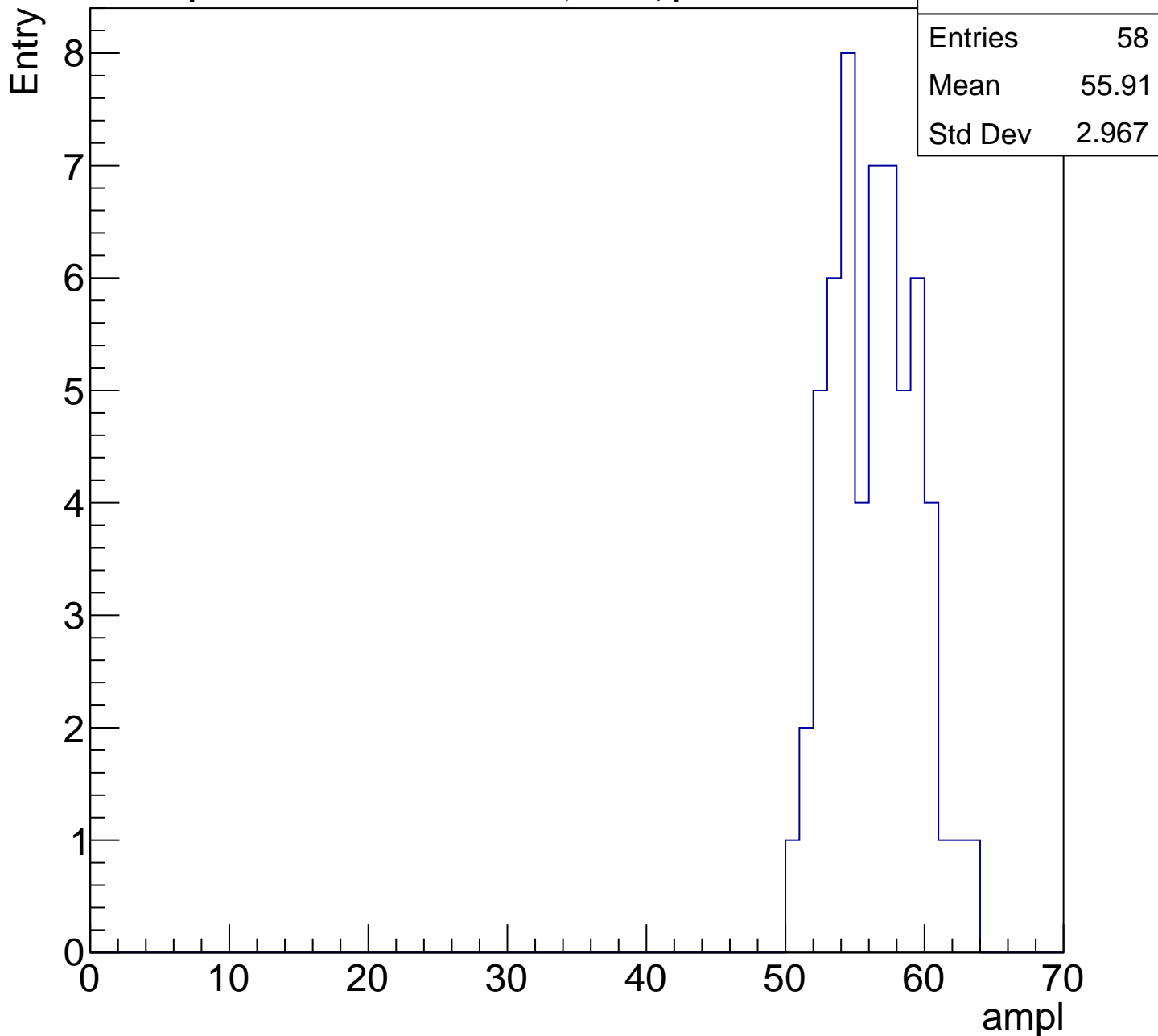
Entry



Entries	68
Mean	49.88
Std Dev	3.108

B0L002S, U4-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

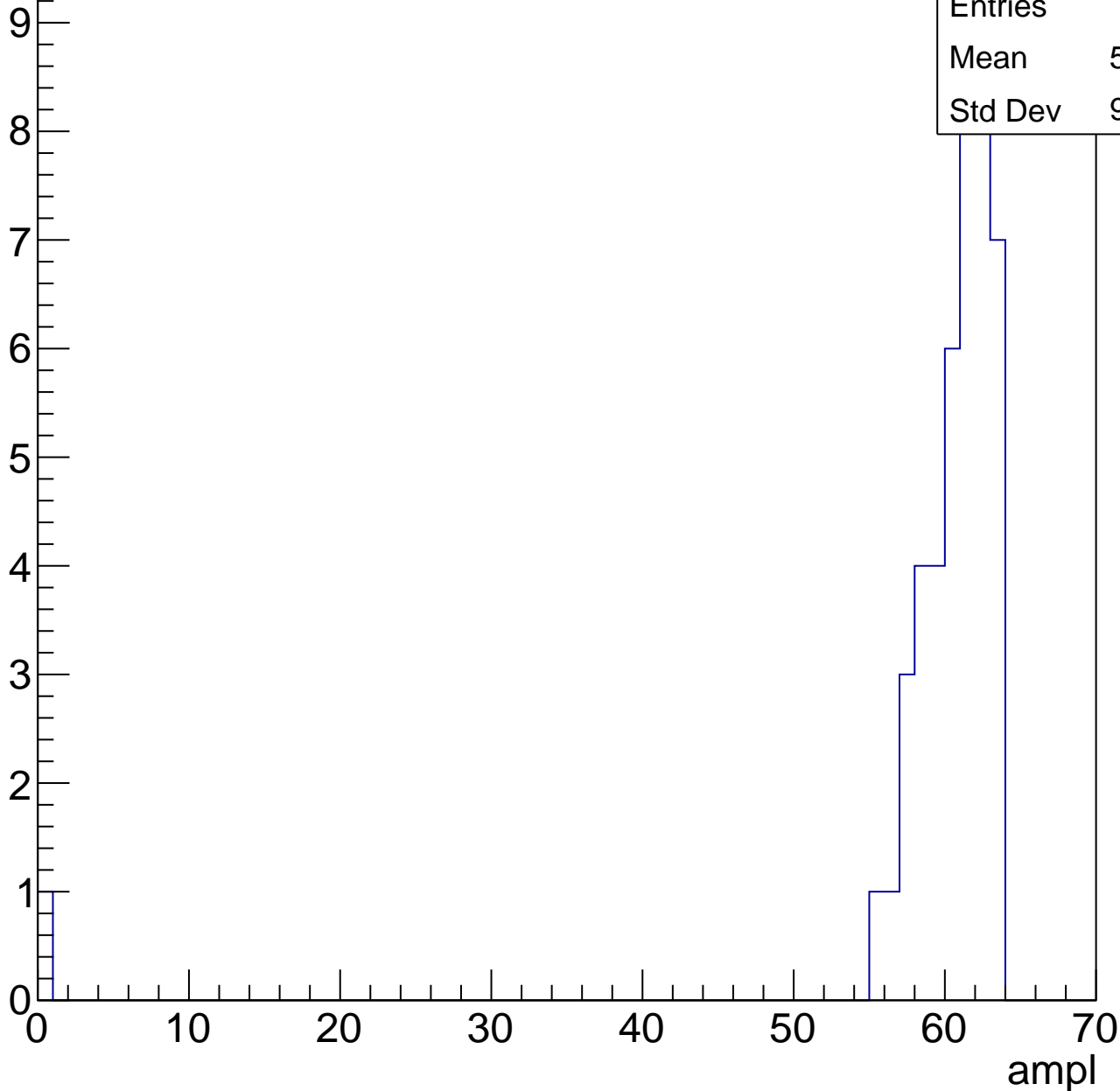


B0L002S, U4-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	59.07
Std Dev	9.137



B0L002S, U4-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch11, adc0

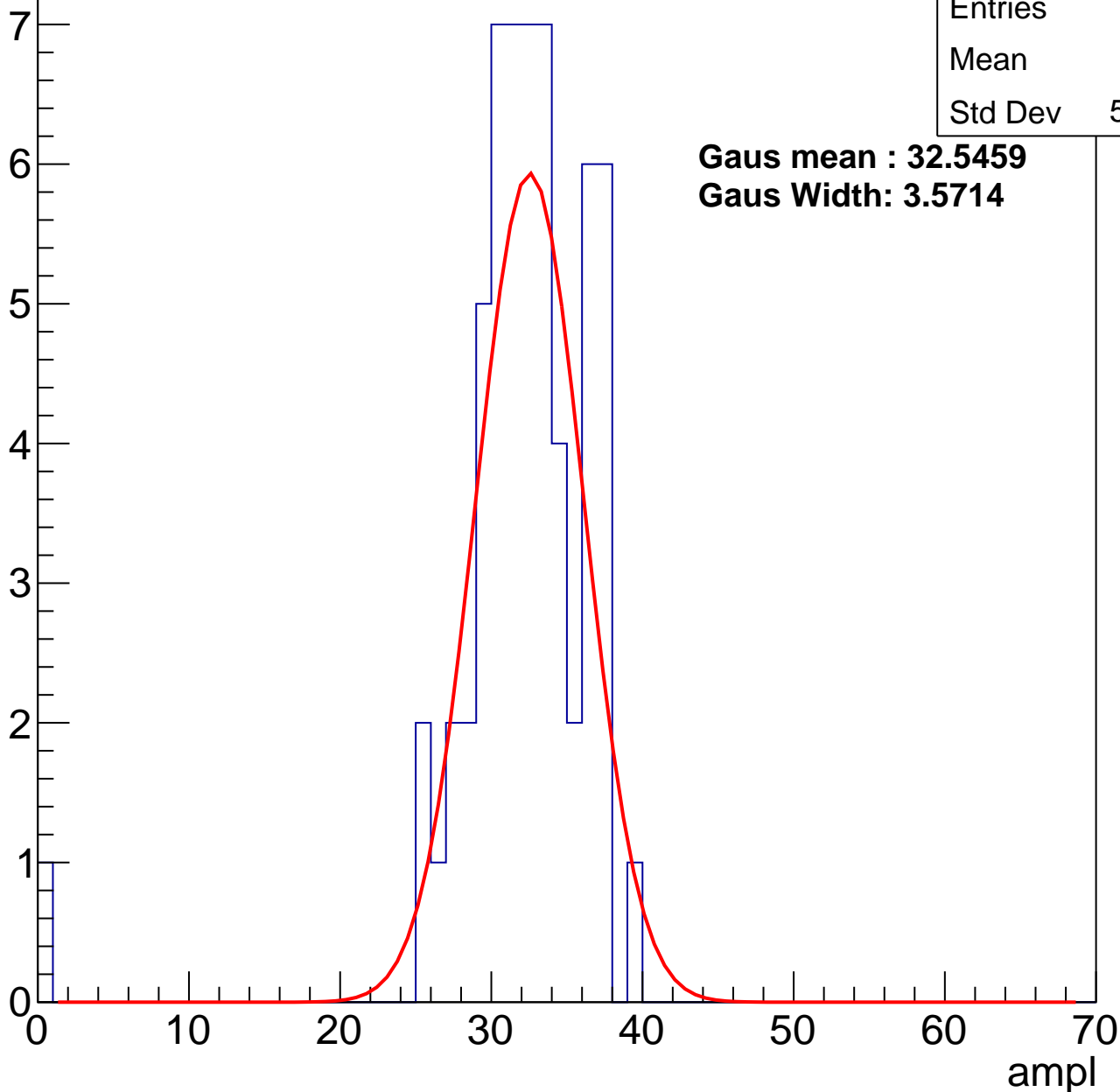
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.6
Std Dev	5.238

Gaus mean : 32.5459

Gaus Width: 3.5714



B0L002S, U4-ch11, adc1

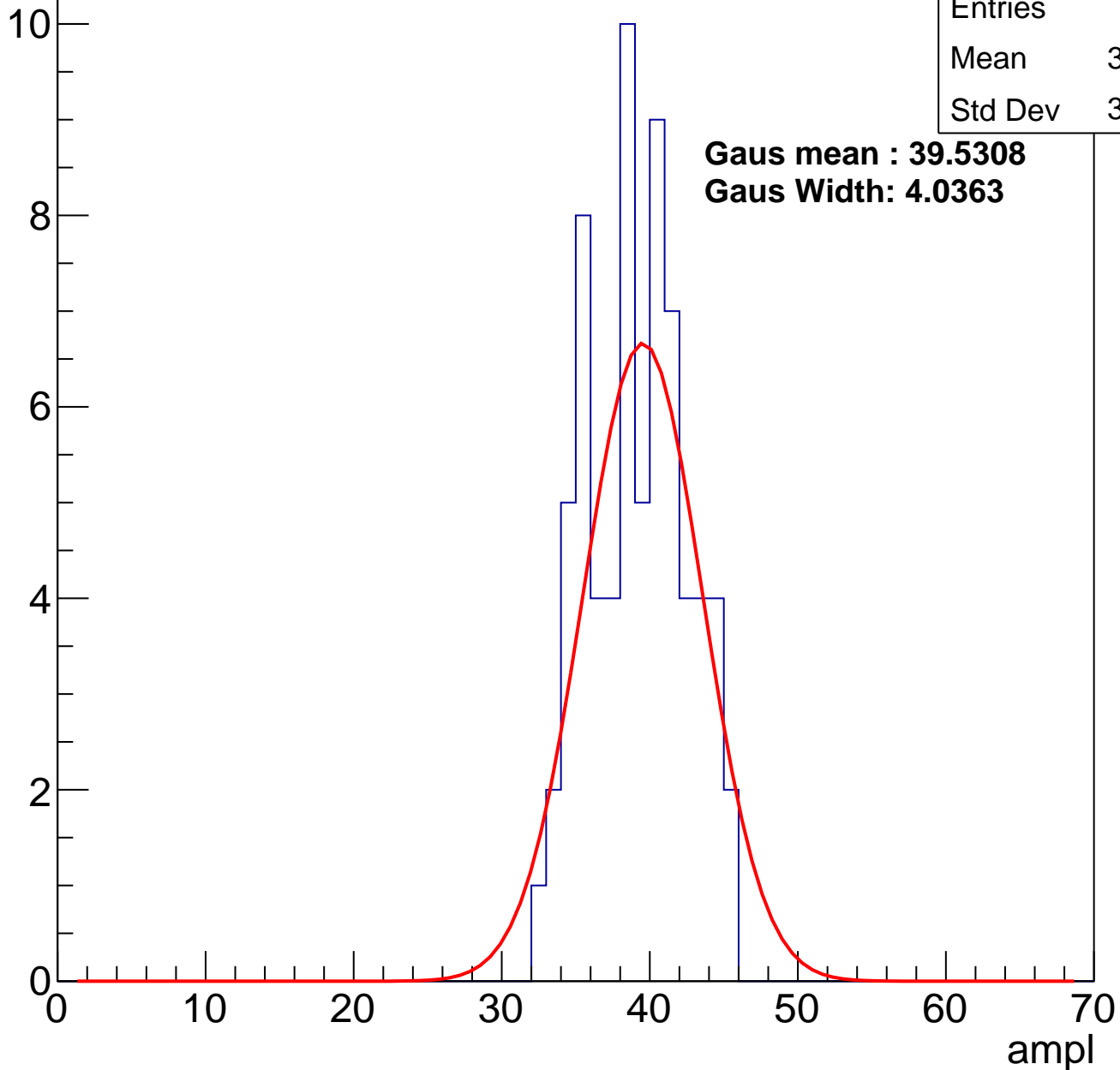
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	38.67
Std Dev	3.269

Gaus mean : 39.5308

Gaus Width: 4.0363

Entry



B0L002S, U4-ch11, adc2

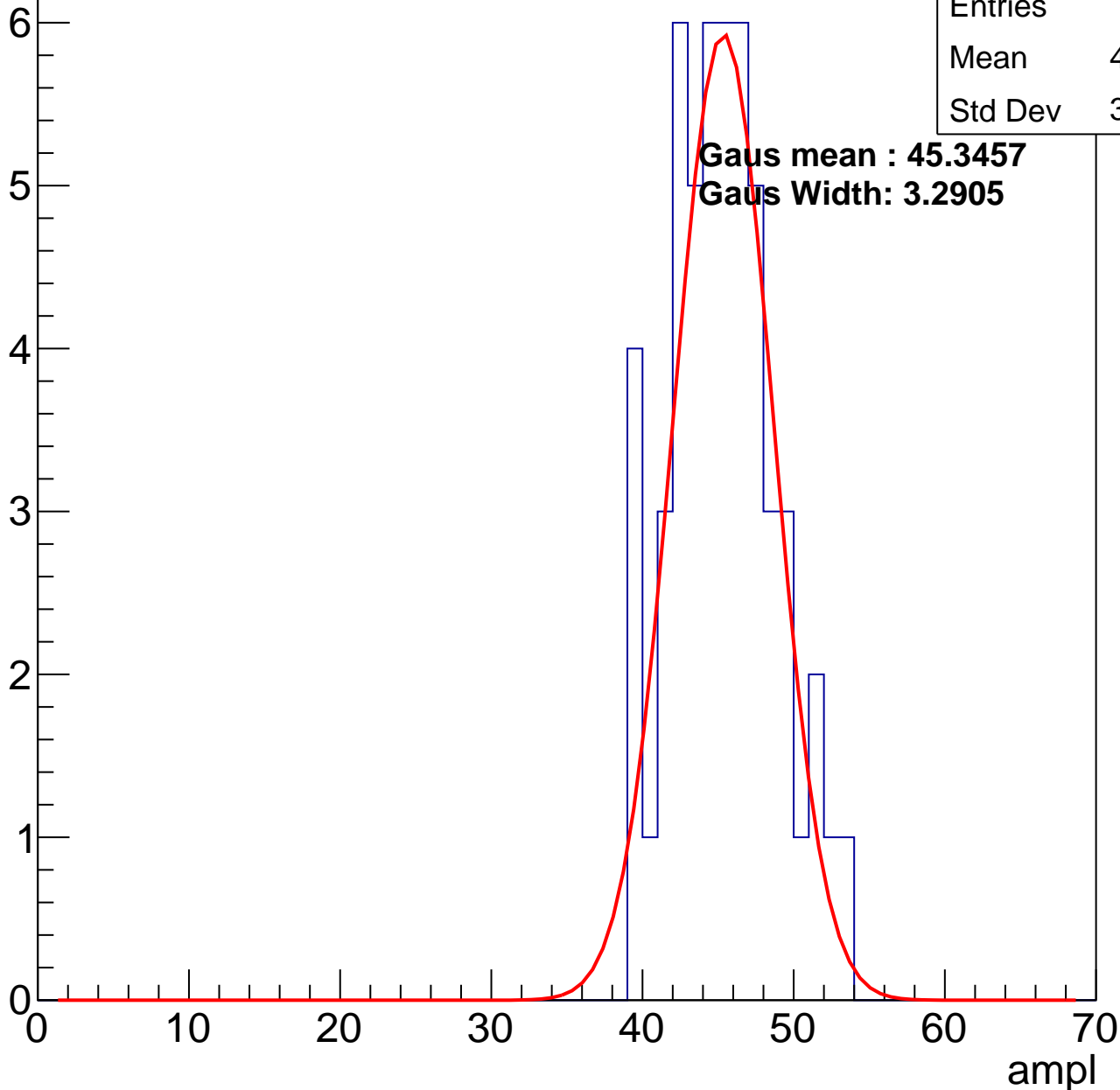
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	44.89
Std Dev	3.385

Gaus mean : 45.3457

Gaus Width: 3.2905

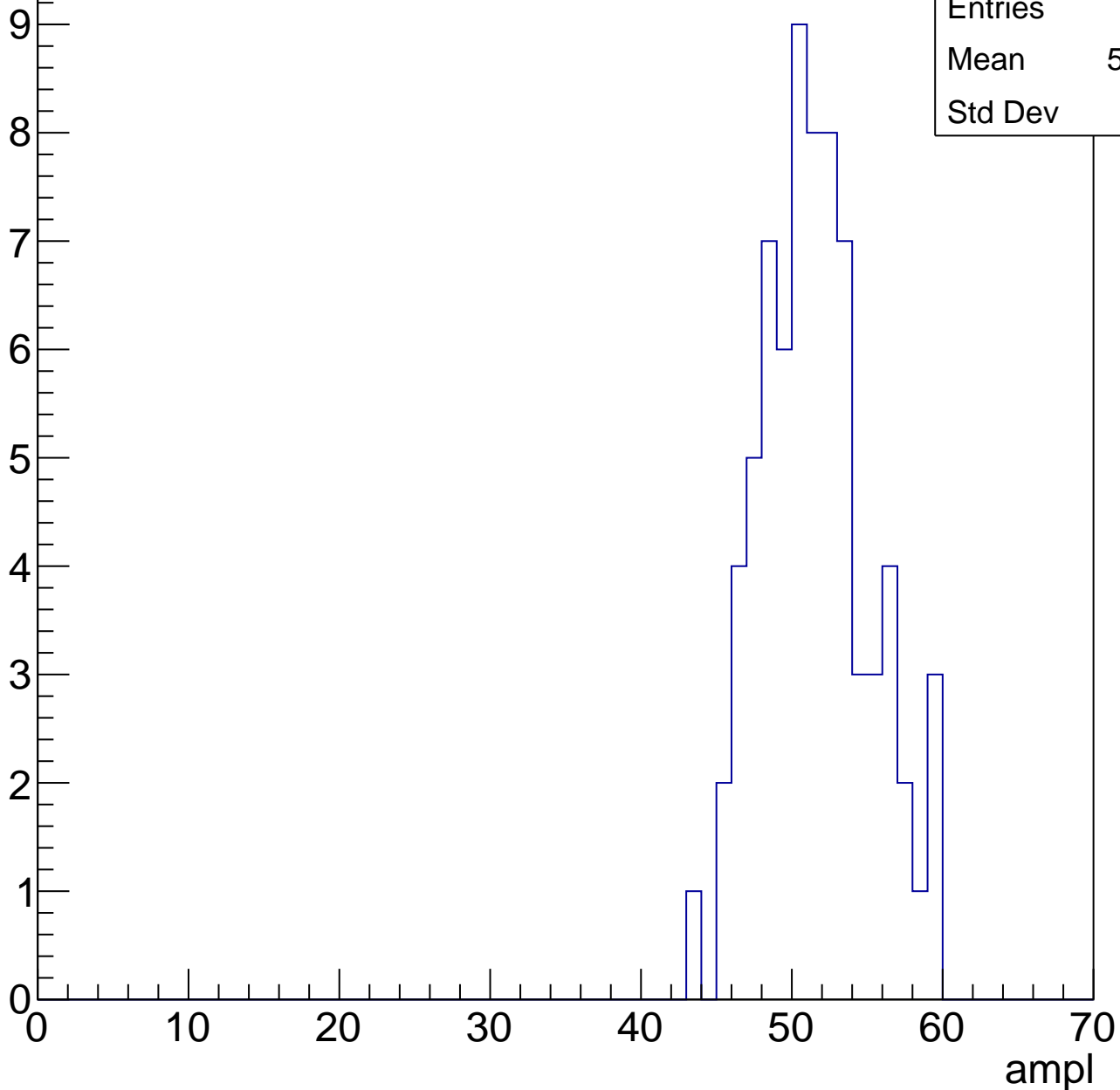


B0L002S, U4-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	51.05
Std Dev	3.58

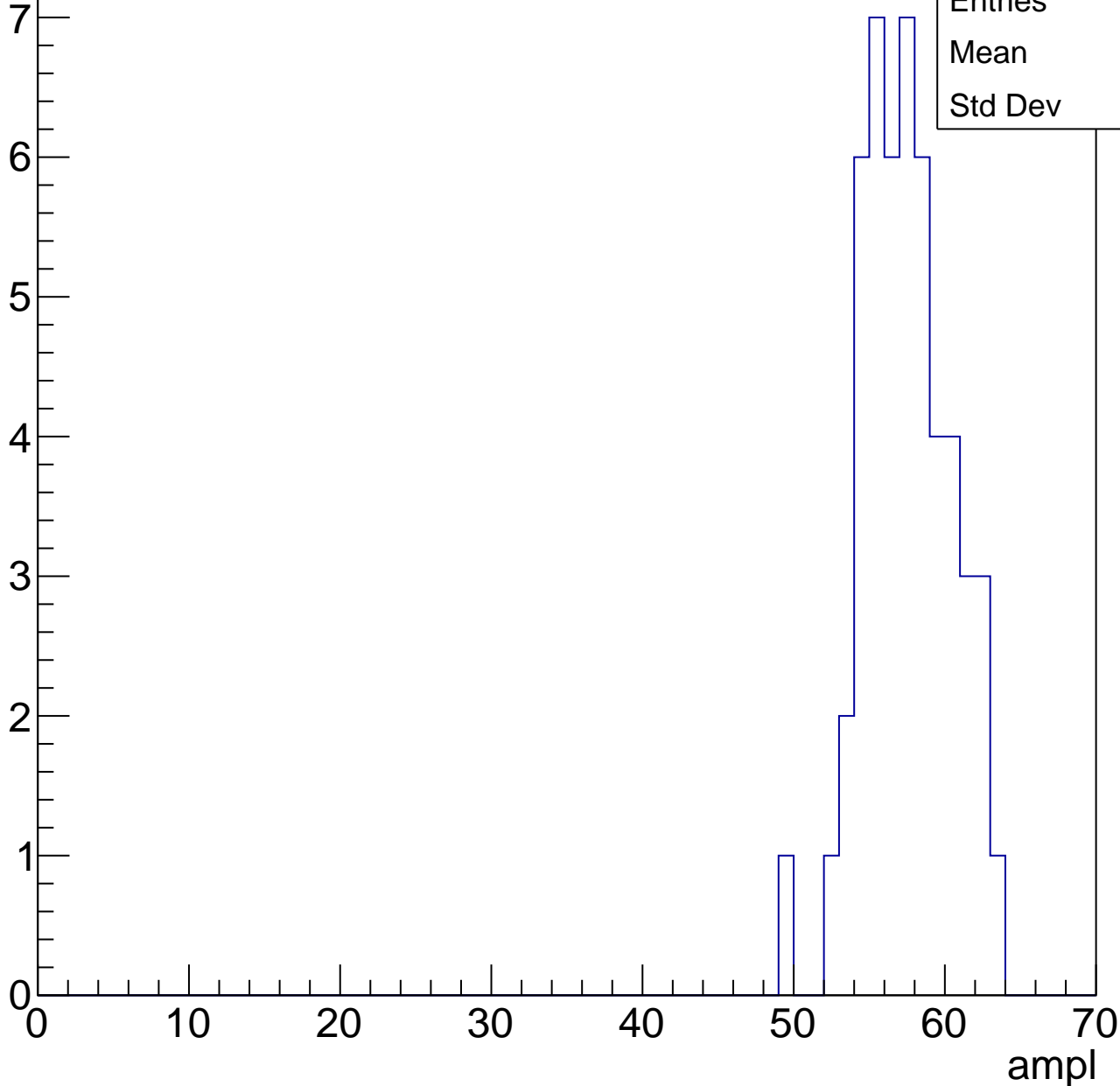


B0L002S, U4-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57
Std Dev	2.89

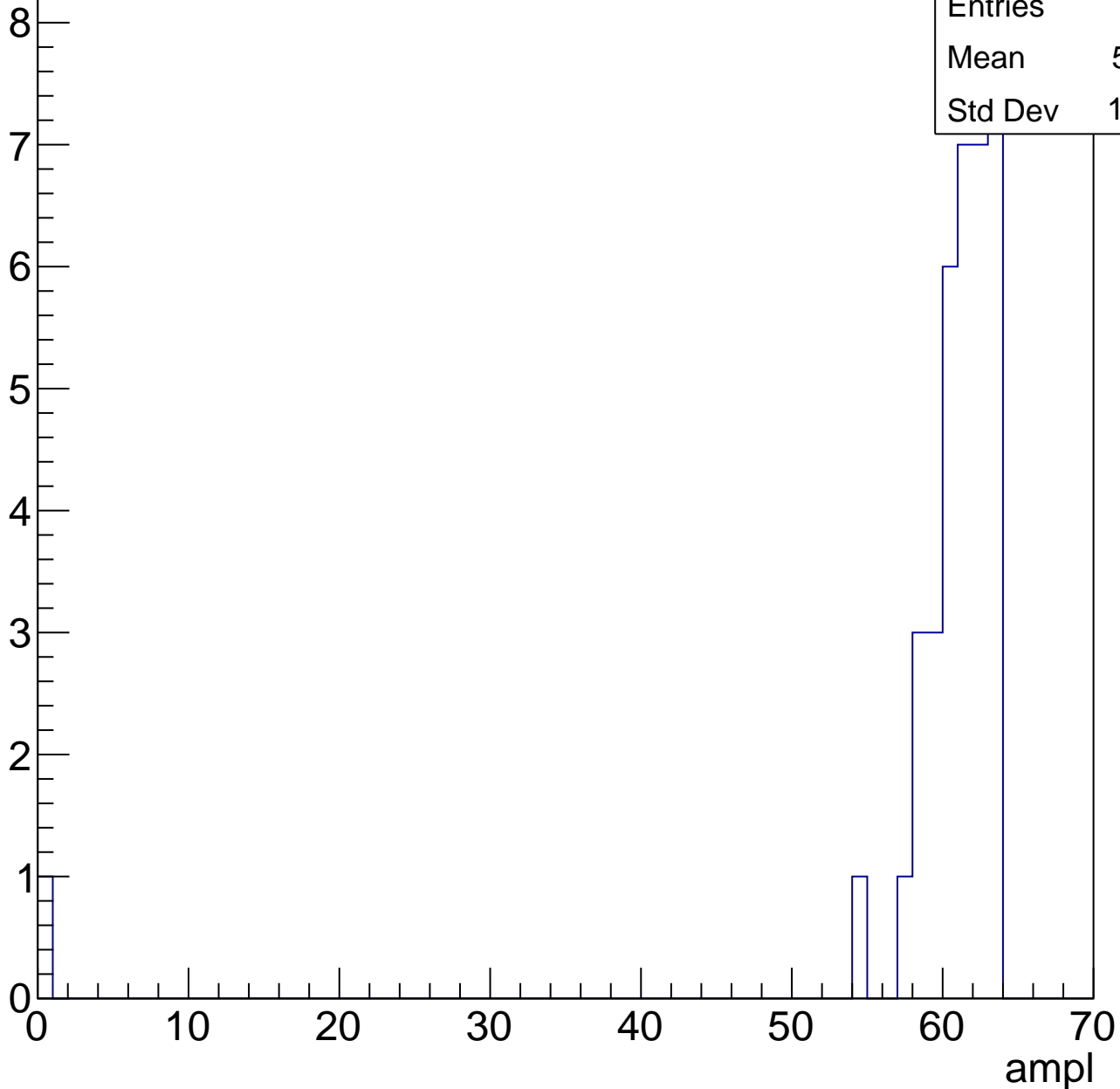


B0L002S, U4-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.11
Std Dev	10.05



B0L002S, U4-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L002S, U4-ch12, adc0

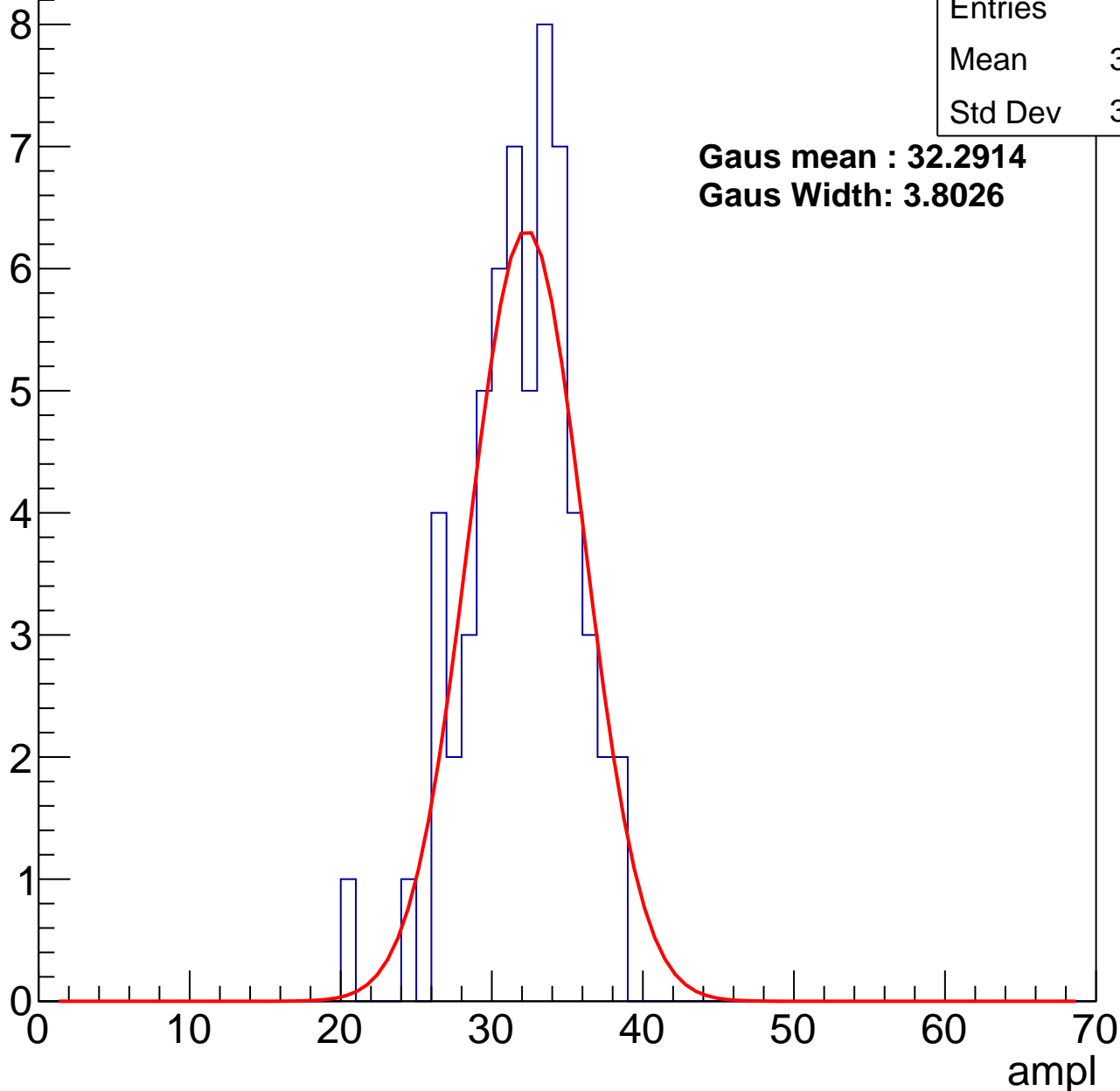
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.47
Std Dev	3.538

Gaus mean : 32.2914

Gaus Width: 3.8026



B0L002S, U4-ch12, adc1

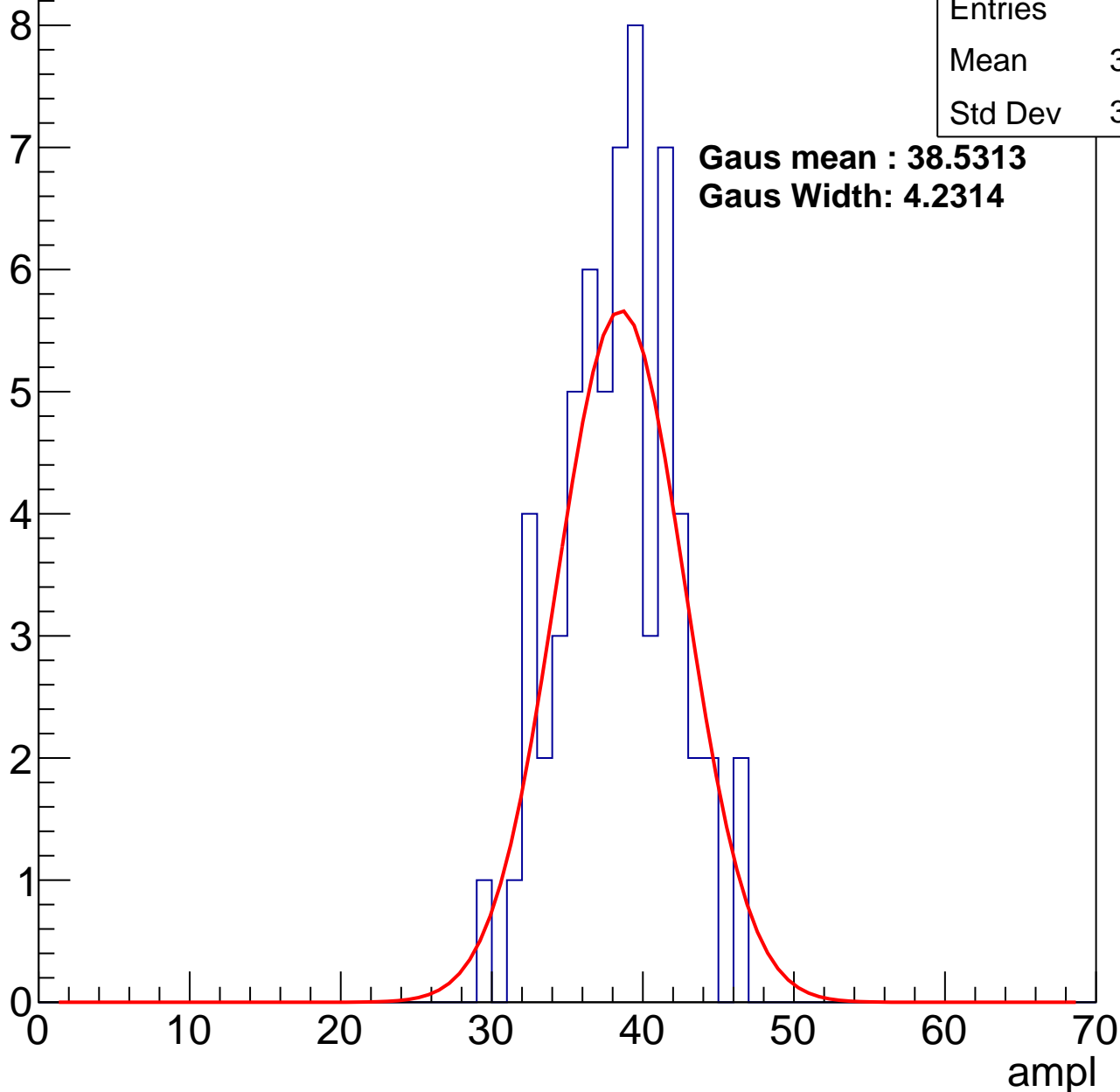
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.92
Std Dev	3.686

Gaus mean : 38.5313

Gaus Width: 4.2314



B0L002S, U4-ch12, adc2

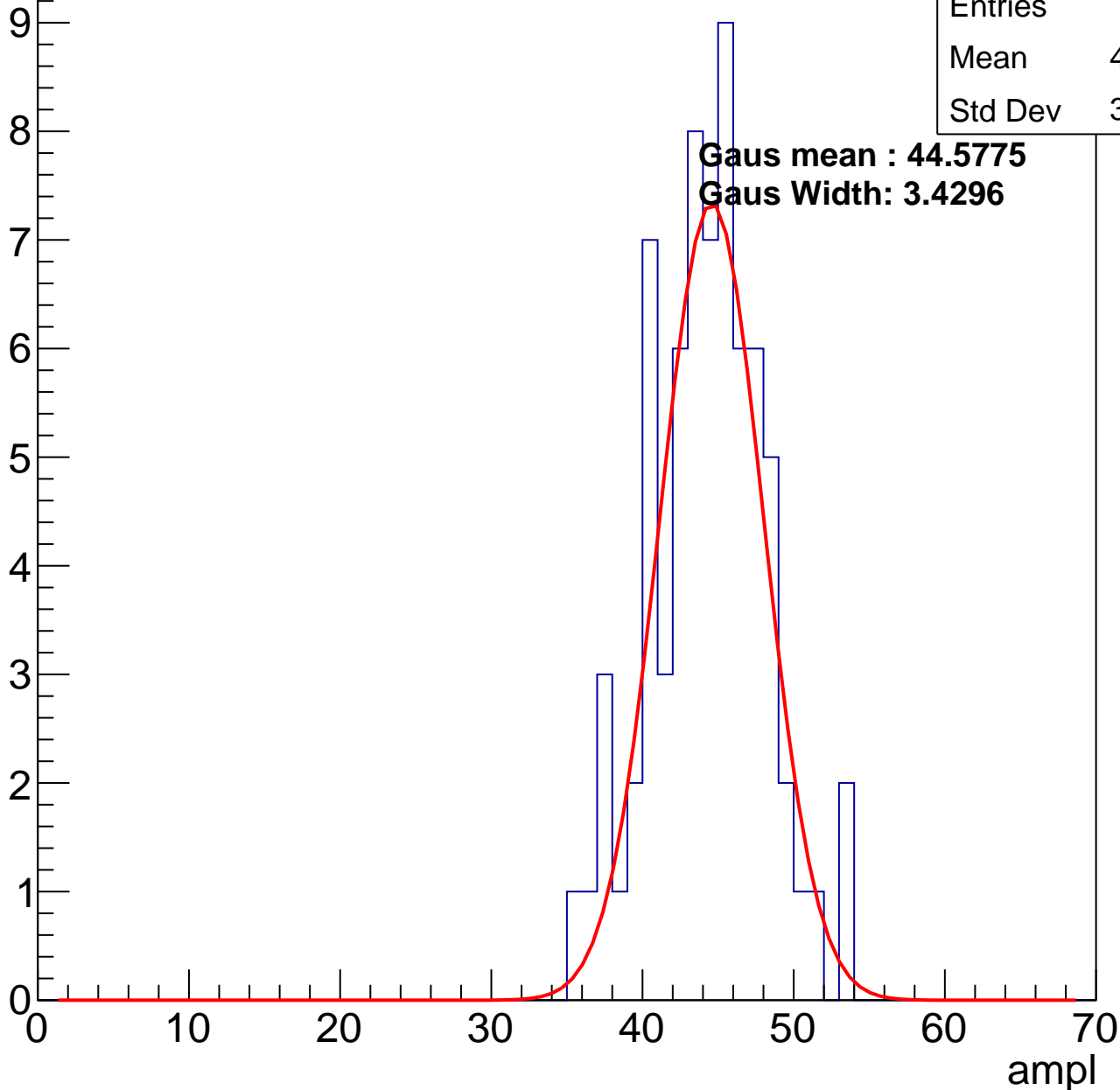
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.85
Std Dev	3.774

Gaus mean : 44.5775

Gaus Width: 3.4296

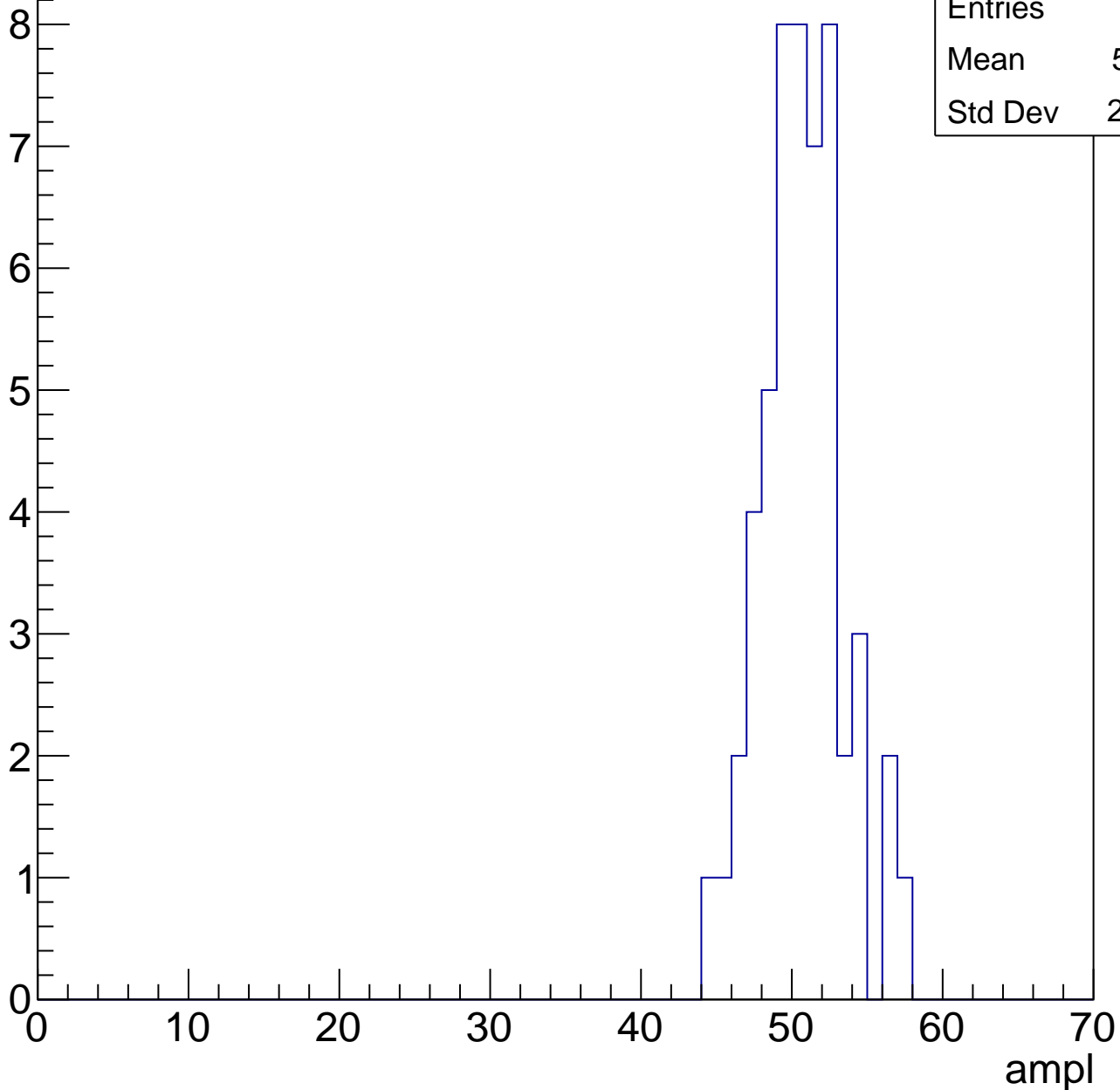


B0L002S, U4-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	50.21
Std Dev	2.706

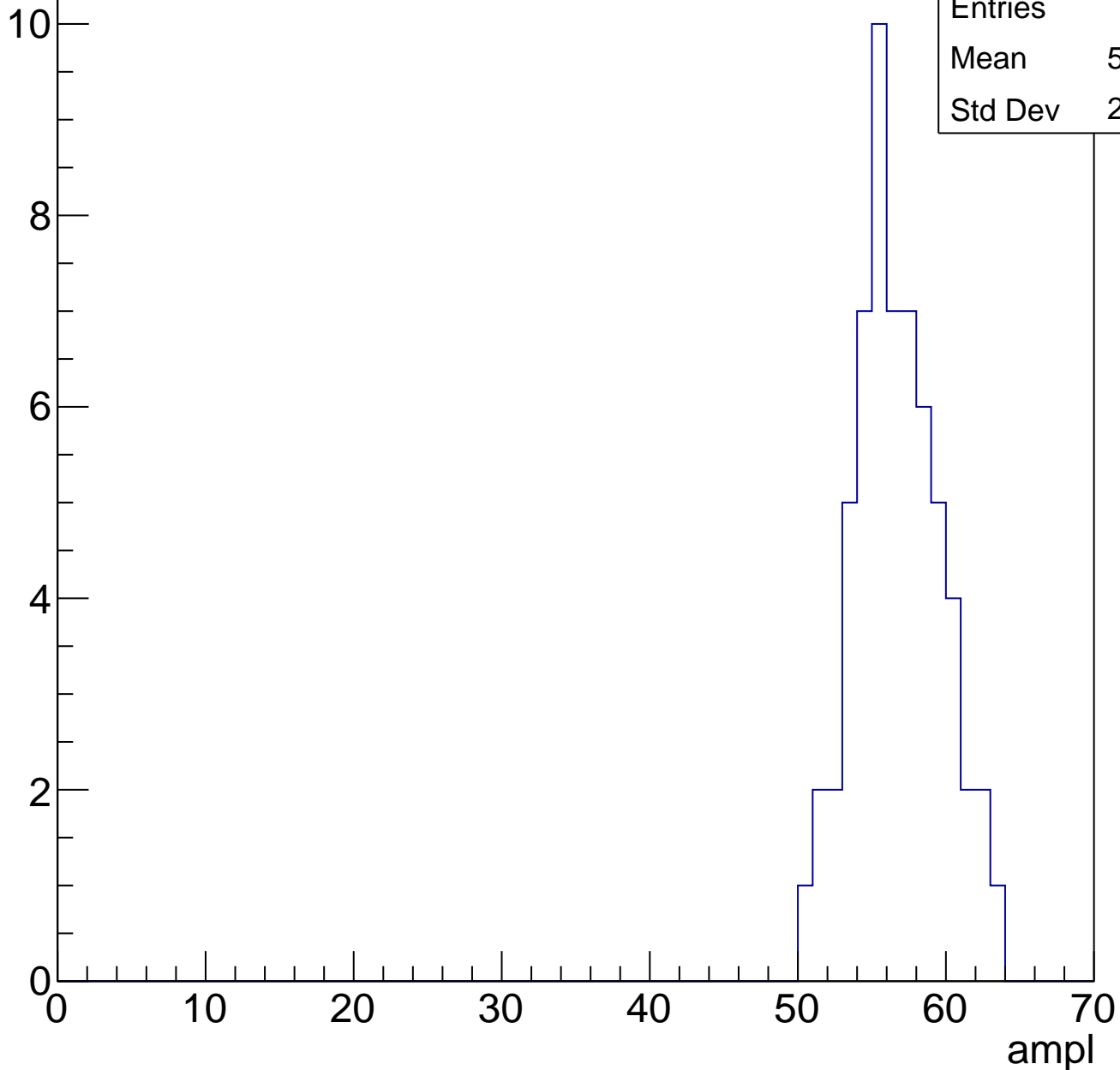


B0L002S, U4-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	56.26
Std Dev	2.885

Entry

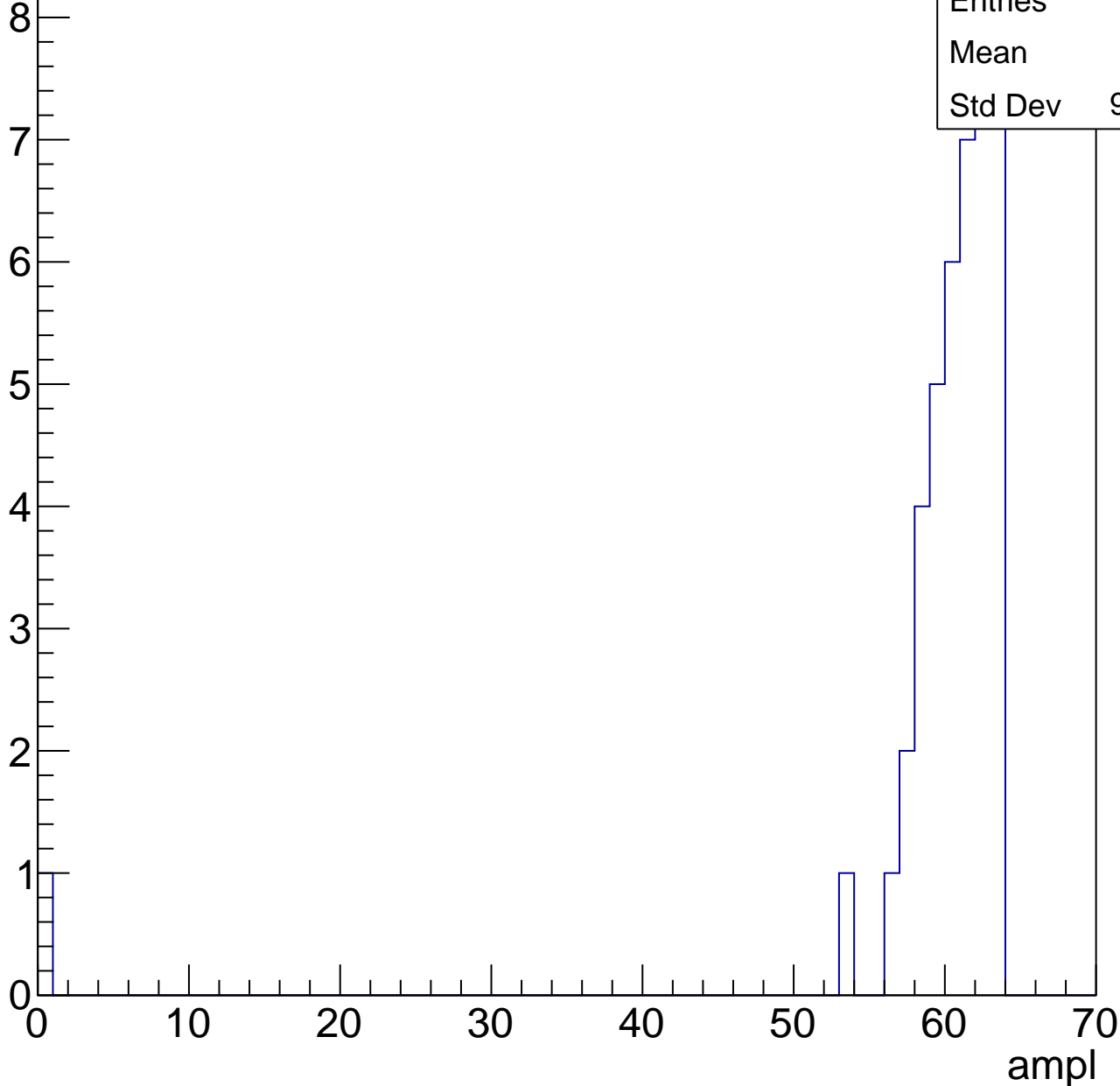


B0L002S, U4-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59
Std Dev	9.366



B0L002S, U4-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch13, adc0

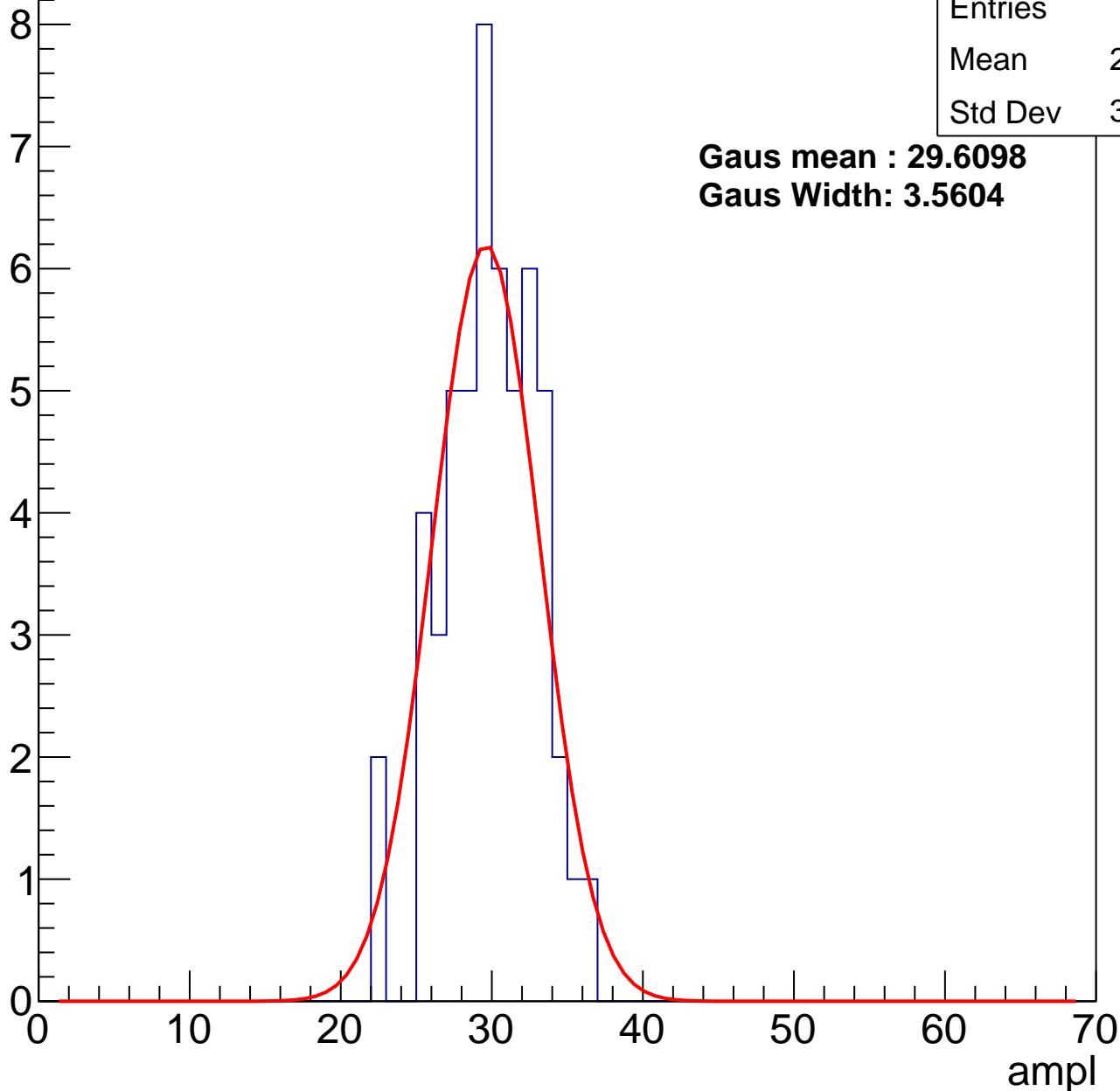
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	29.43
Std Dev	3.068

Gaus mean : 29.6098

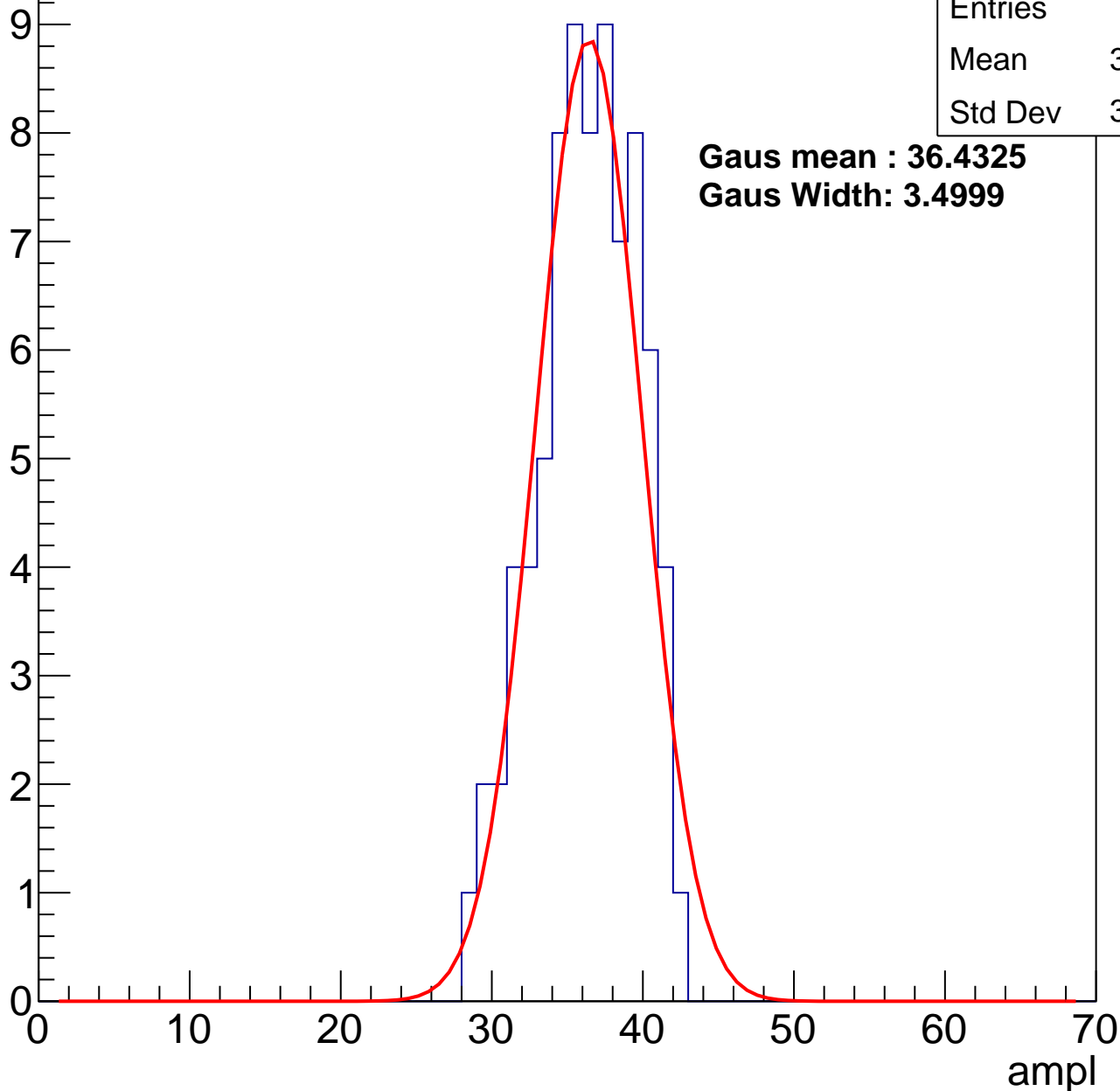
Gaus Width: 3.5604



B0L002S, U4-ch13, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch13, adc2

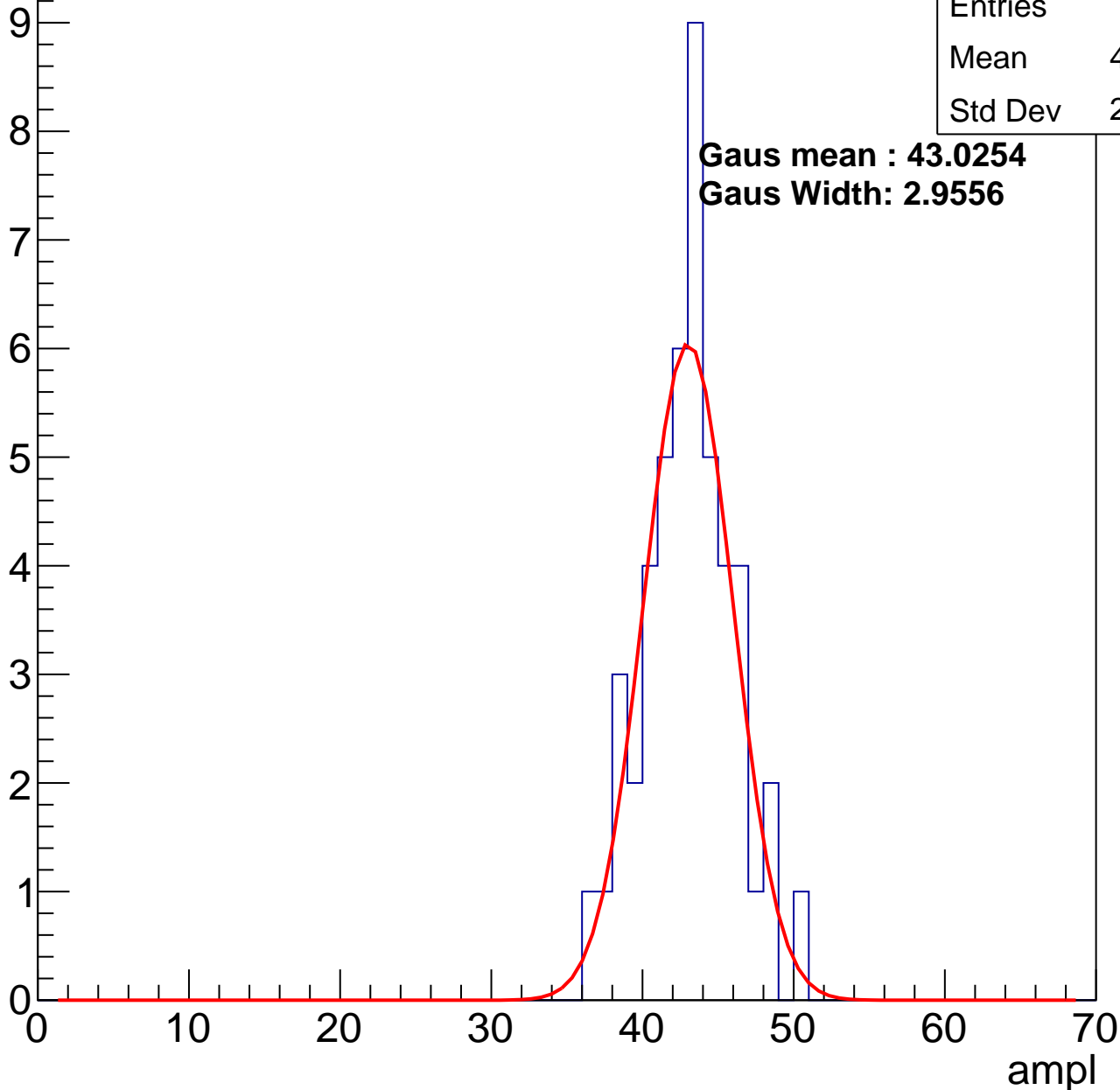
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	42.62
Std Dev	2.955

Gaus mean : 43.0254

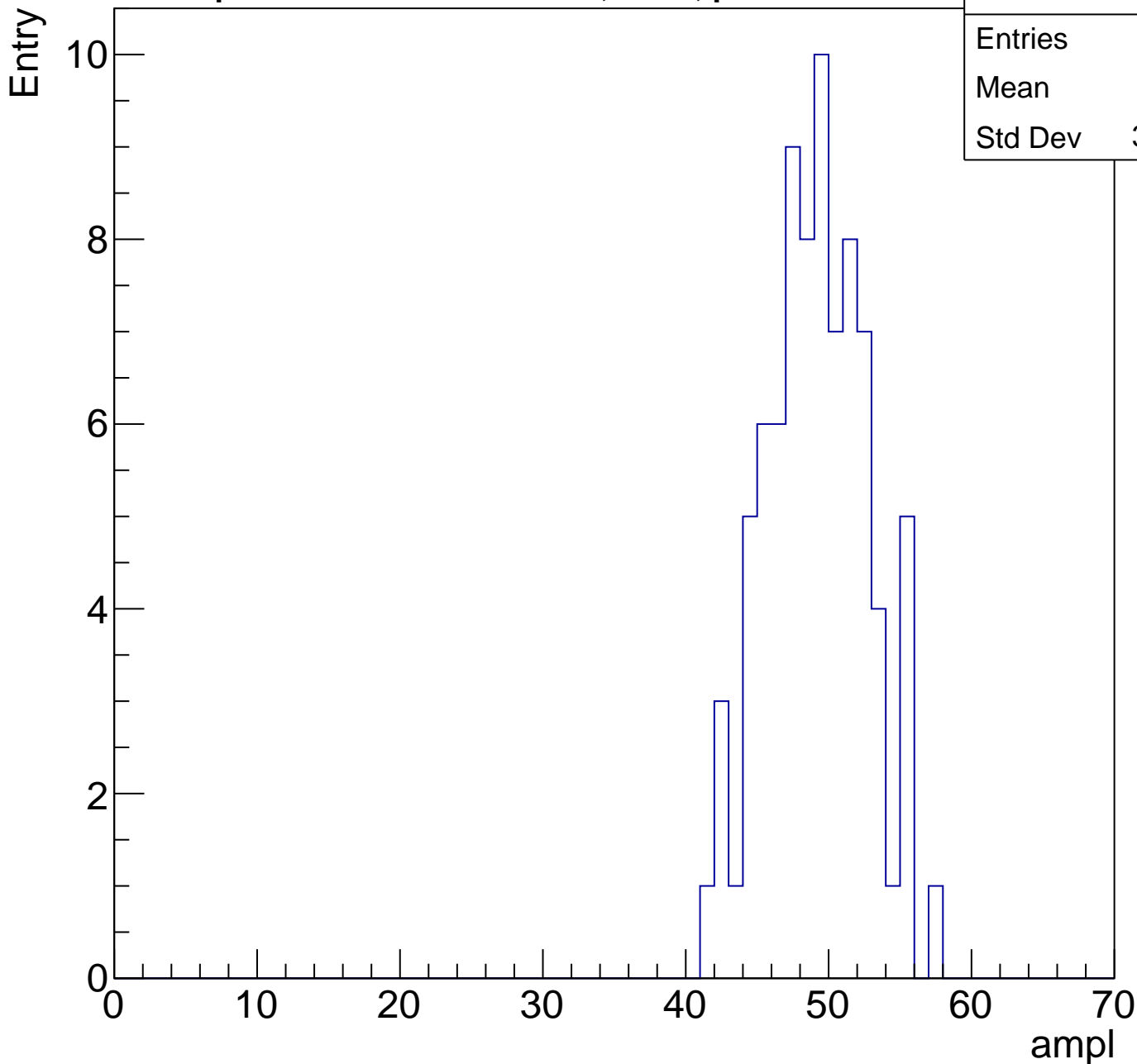
Gaus Width: 2.9556



B0L002S, U4-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

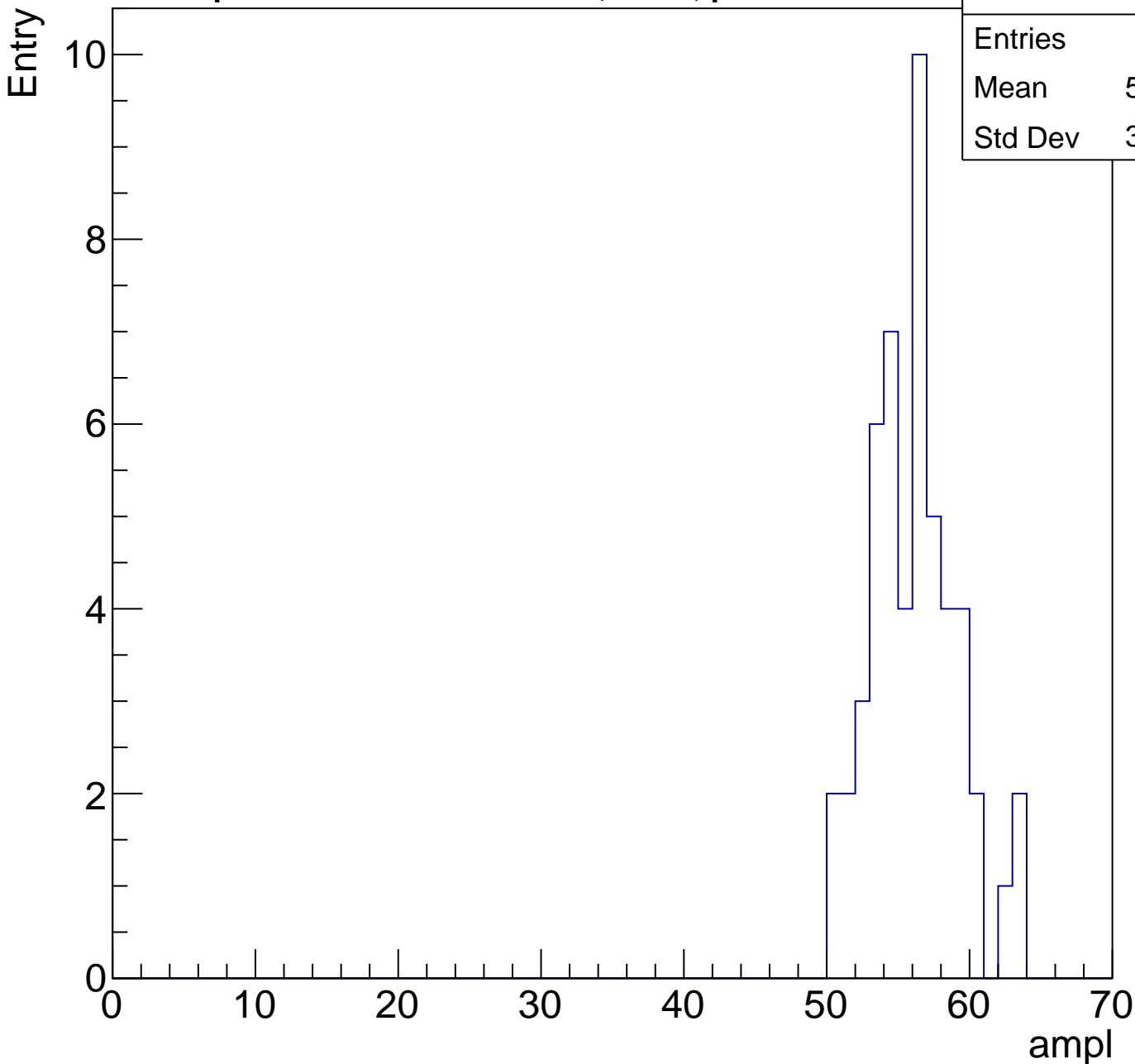
Entries	82
Mean	48.7
Std Dev	3.481



B0L002S, U4-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	52
Mean	55.67
Std Dev	3.017

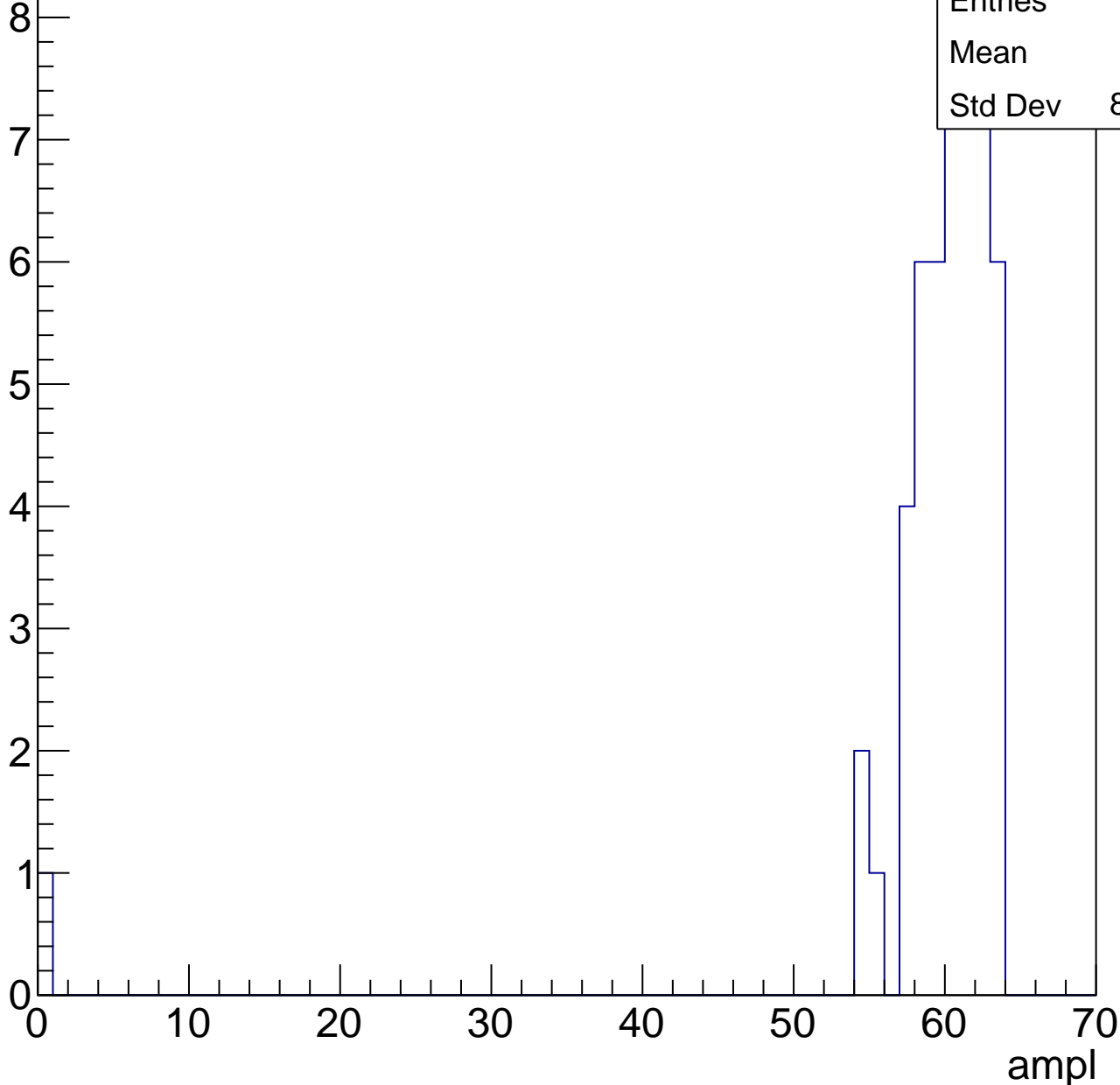


B0L002S, U4-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

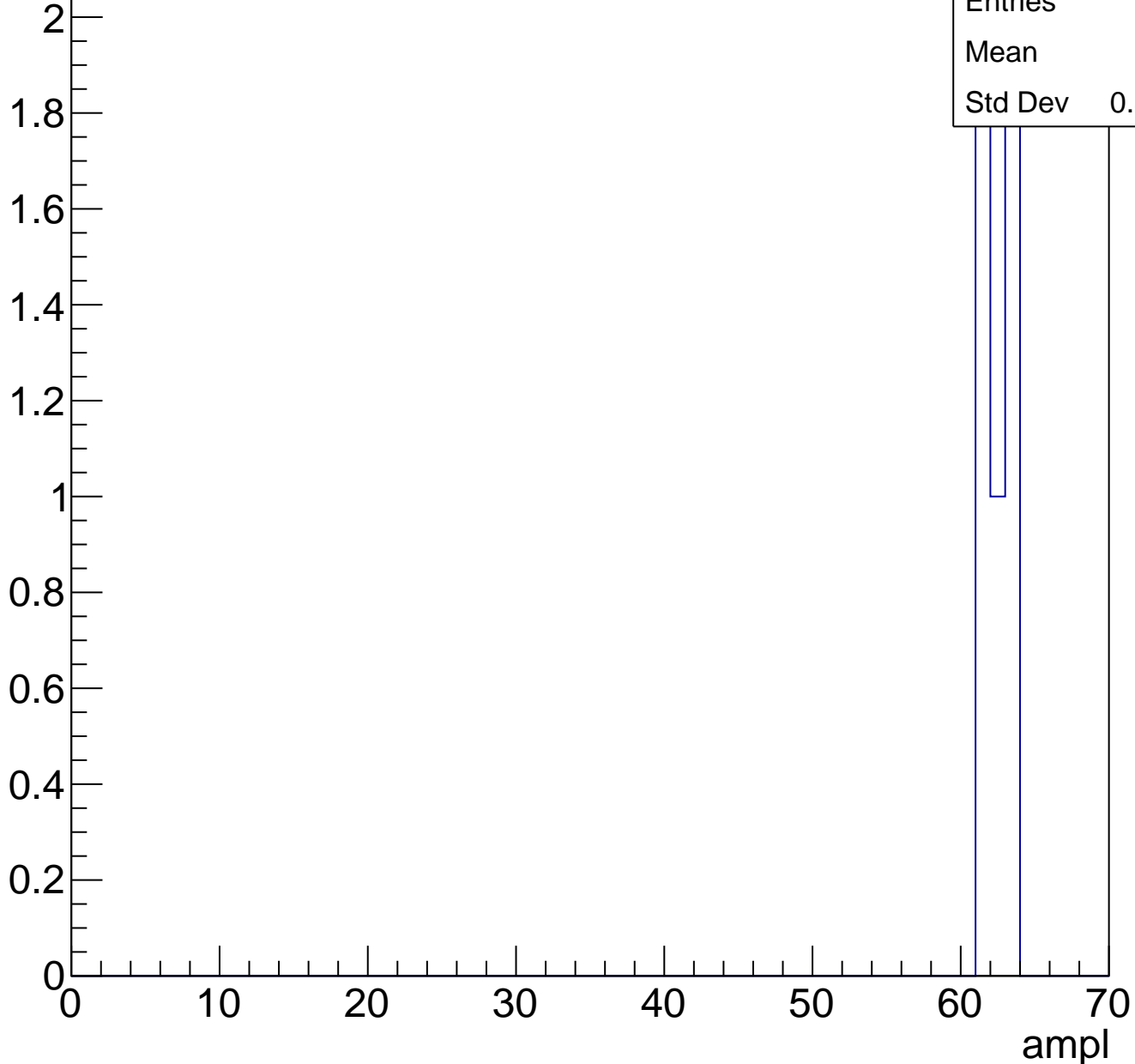
Entries	50
Mean	58.7
Std Dev	8.686



B0L002S, U4-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch14, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	29.43
Std Dev	4.804

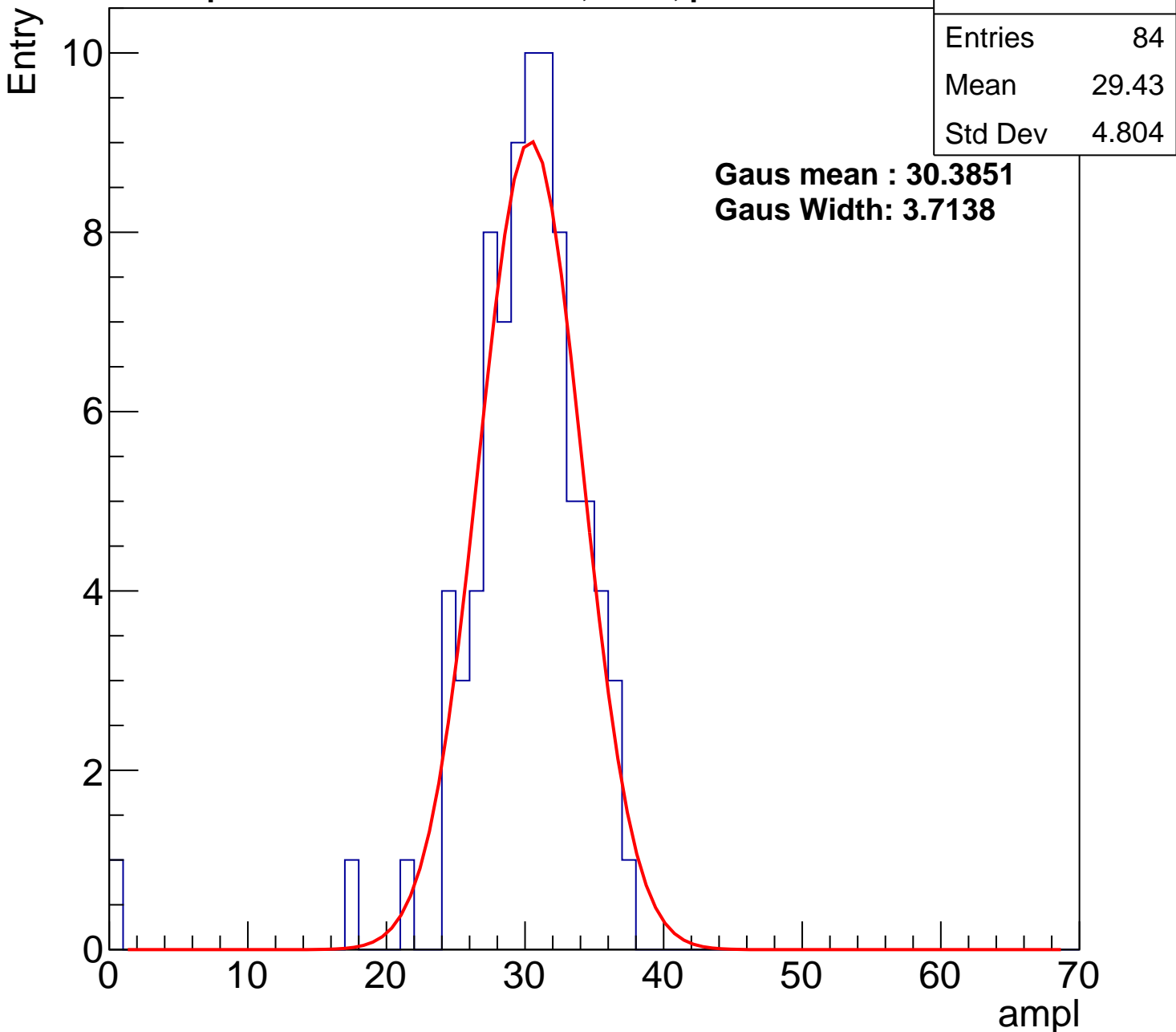
Gaus mean : 30.3851
Gaus Width: 3.7138

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



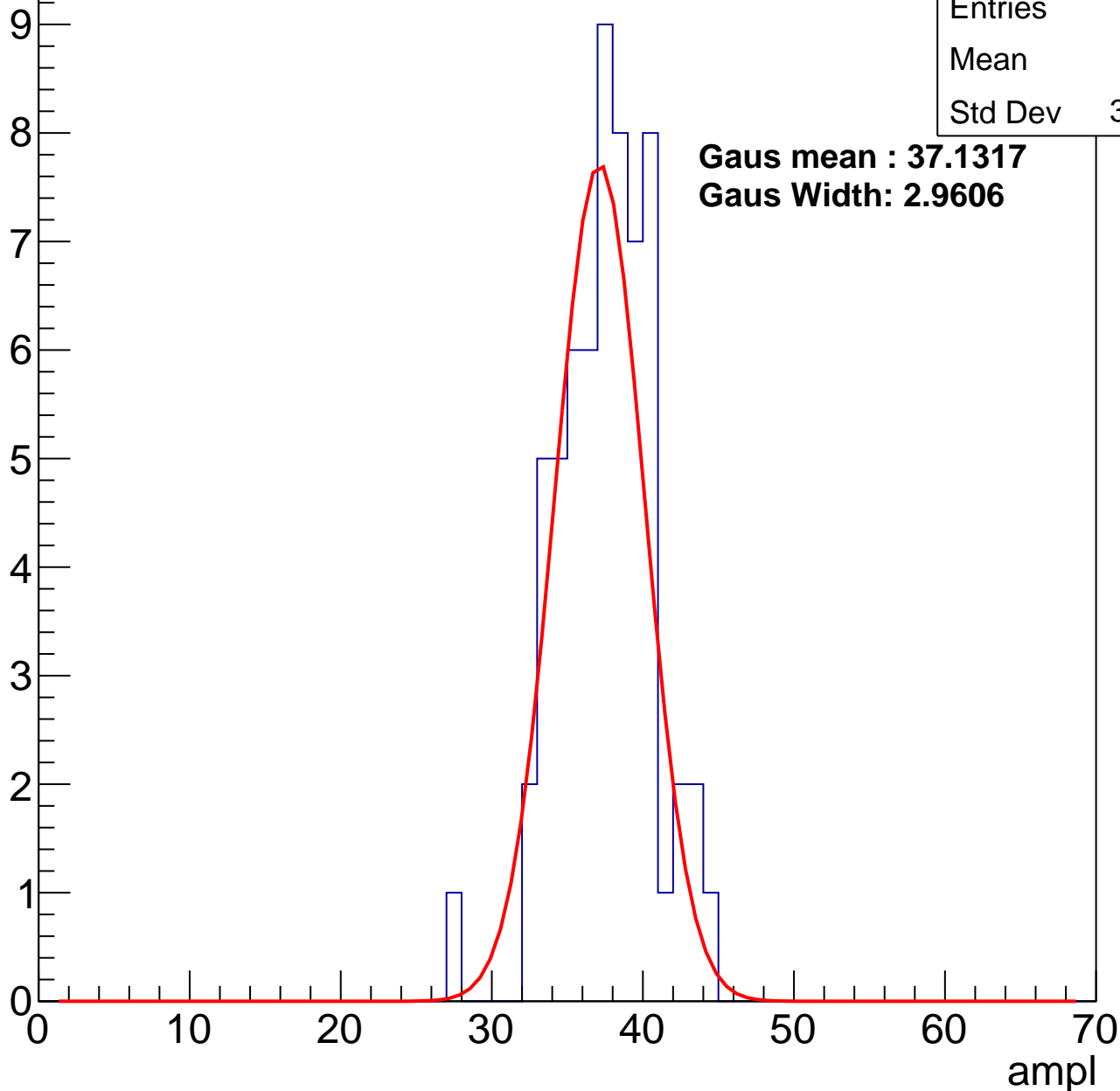
B0L002S, U4-ch14, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.1
Std Dev	3.085

Gaus mean : 37.1317
Gaus Width: 2.9606



B0L002S, U4-ch14, adc2

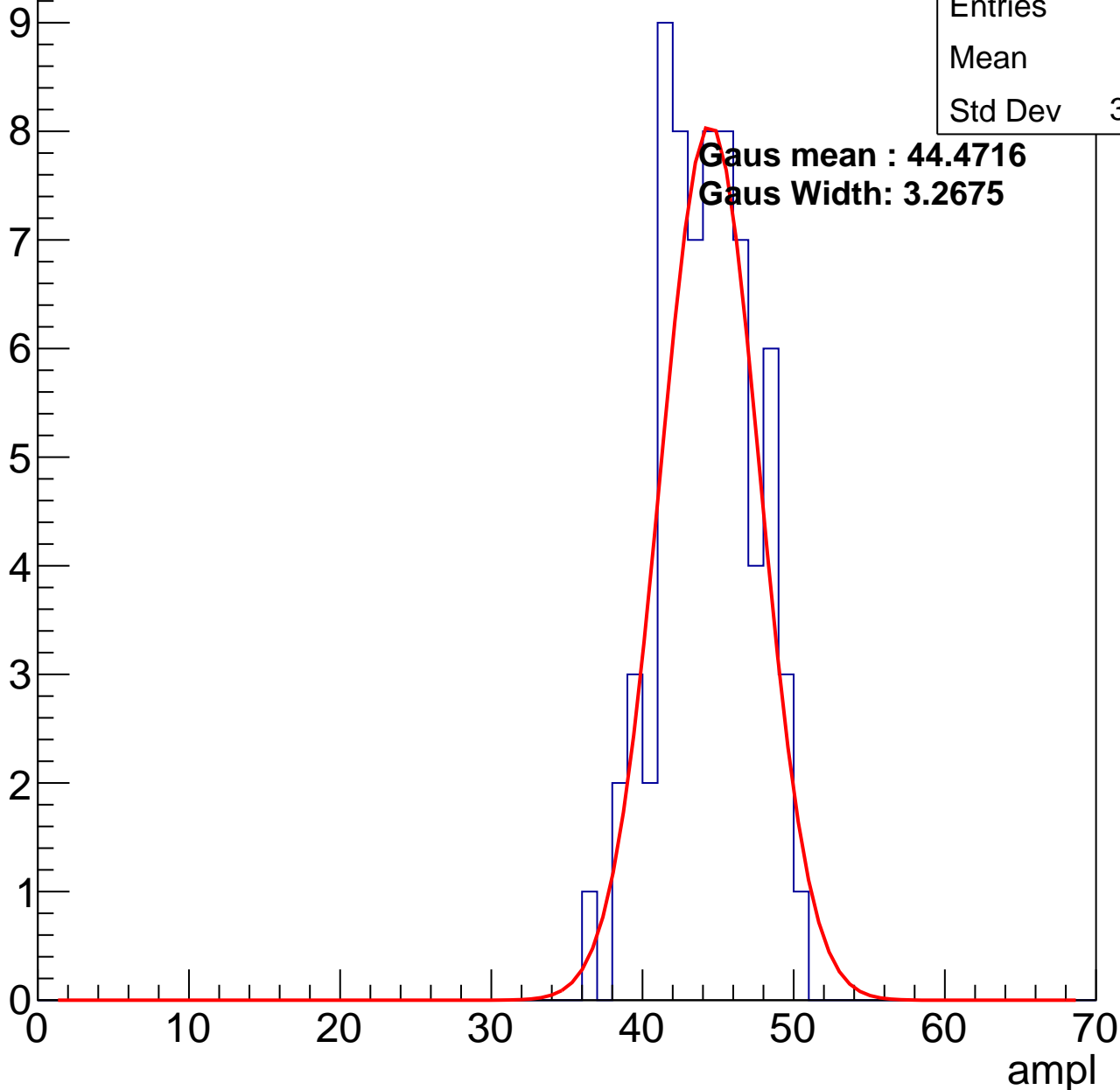
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.8
Std Dev	3.044

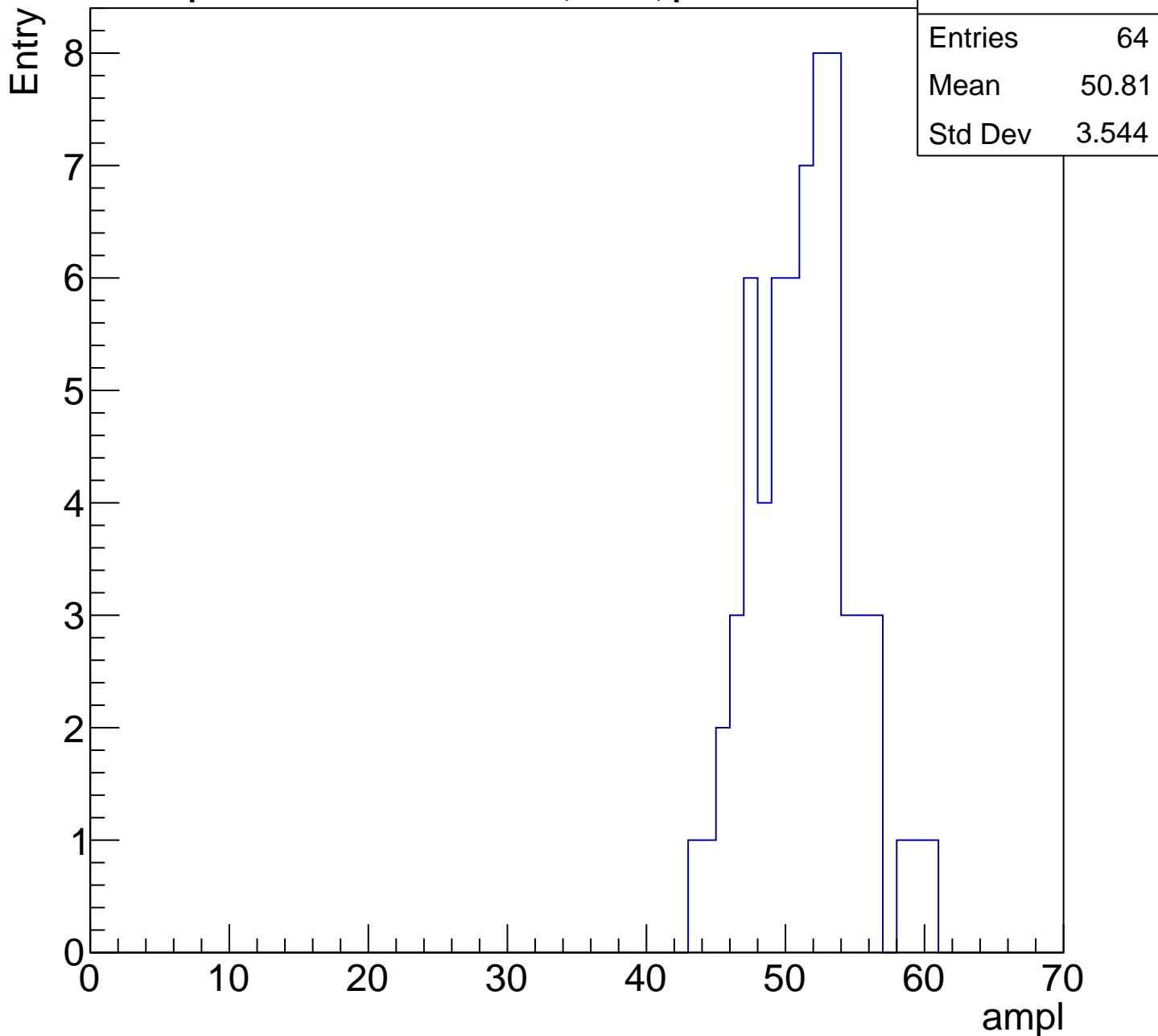
Gaus mean : 44.4716

Gaus Width: 3.2675



B0L002S, U4-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

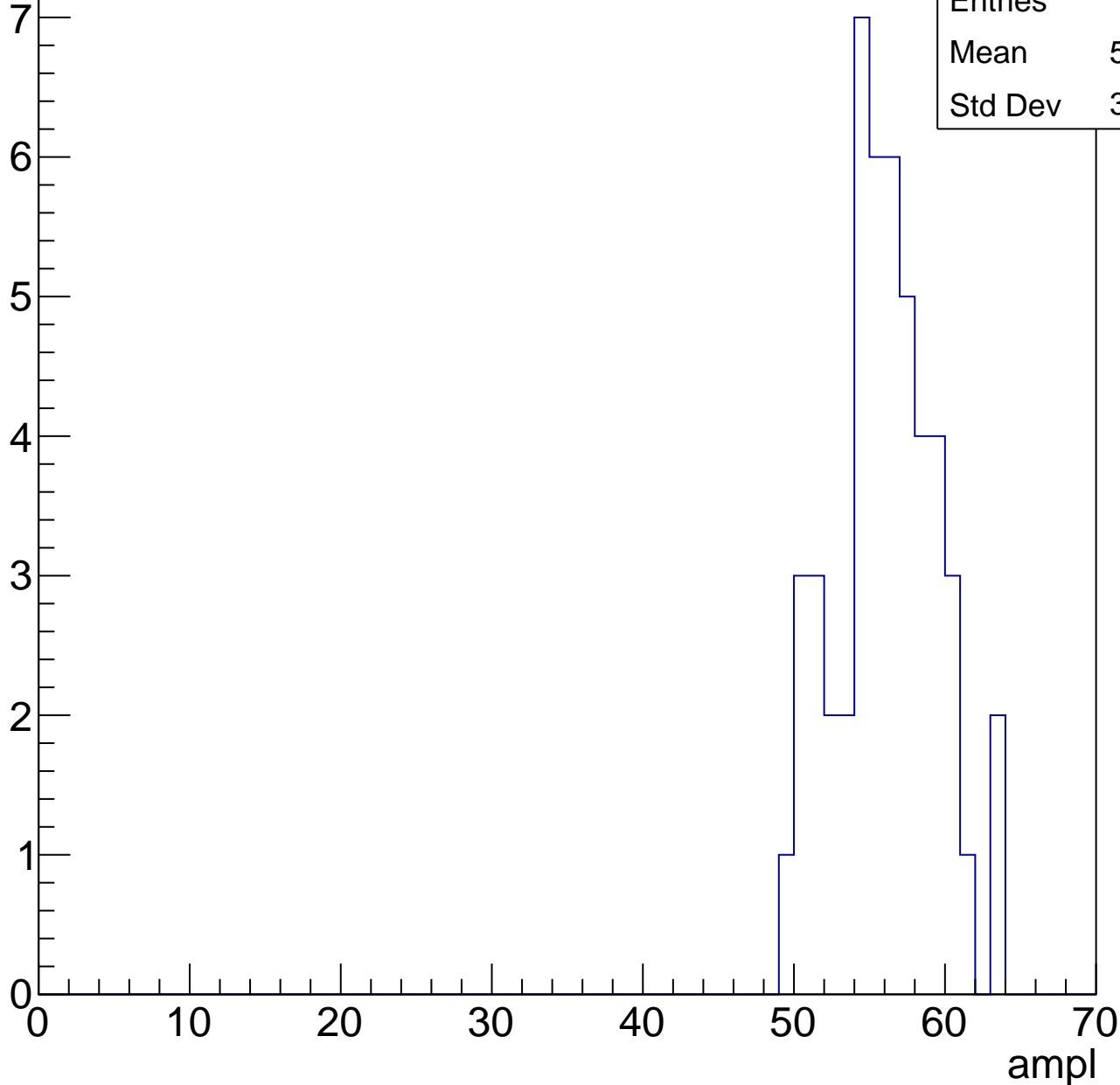


B0L002S, U4-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	55.63
Std Dev	3.312



B0L002S, U4-ch14, adc5

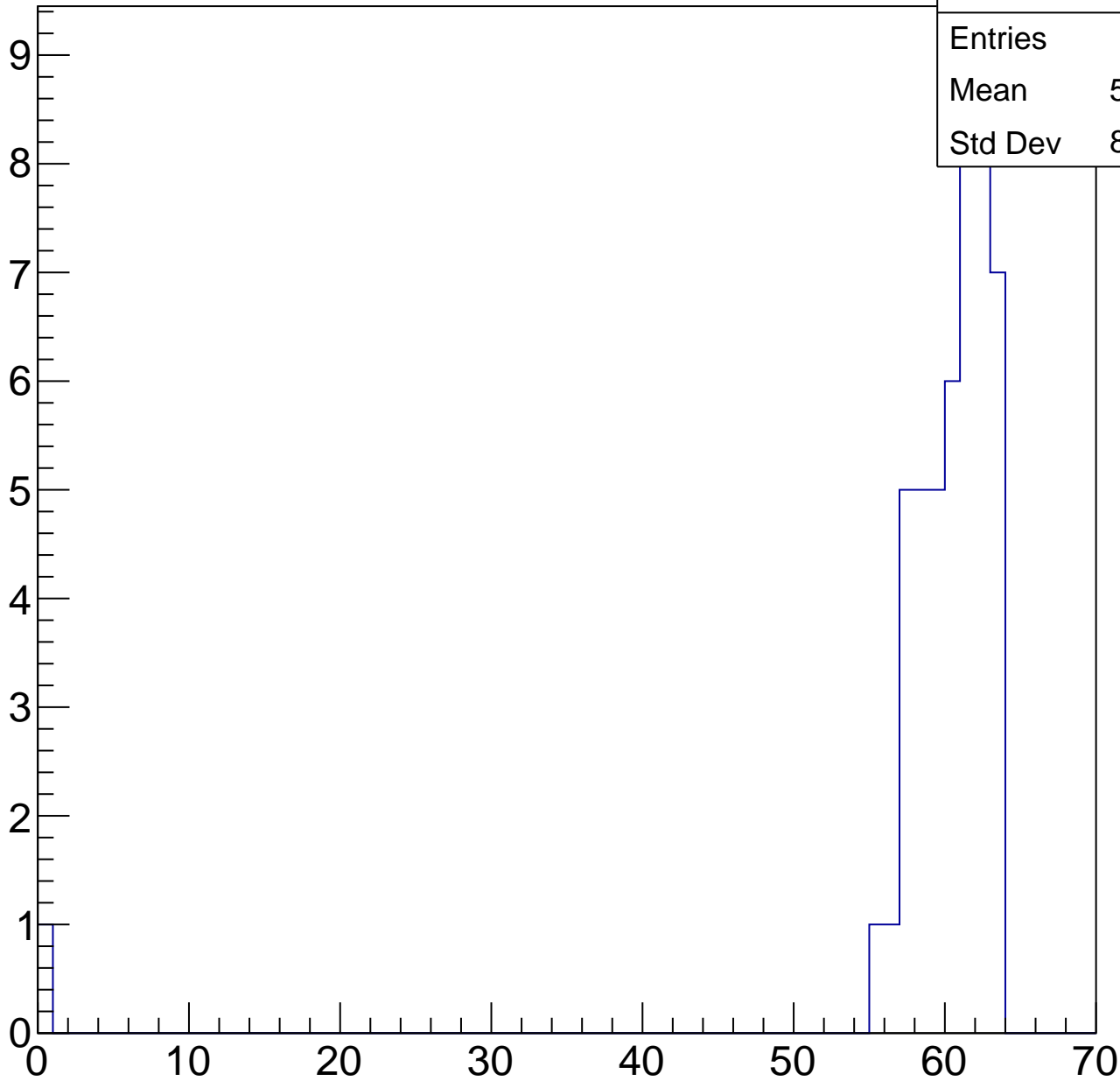
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.96
Std Dev	8.767

ampl



B0L002S, U4-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch15, adc0

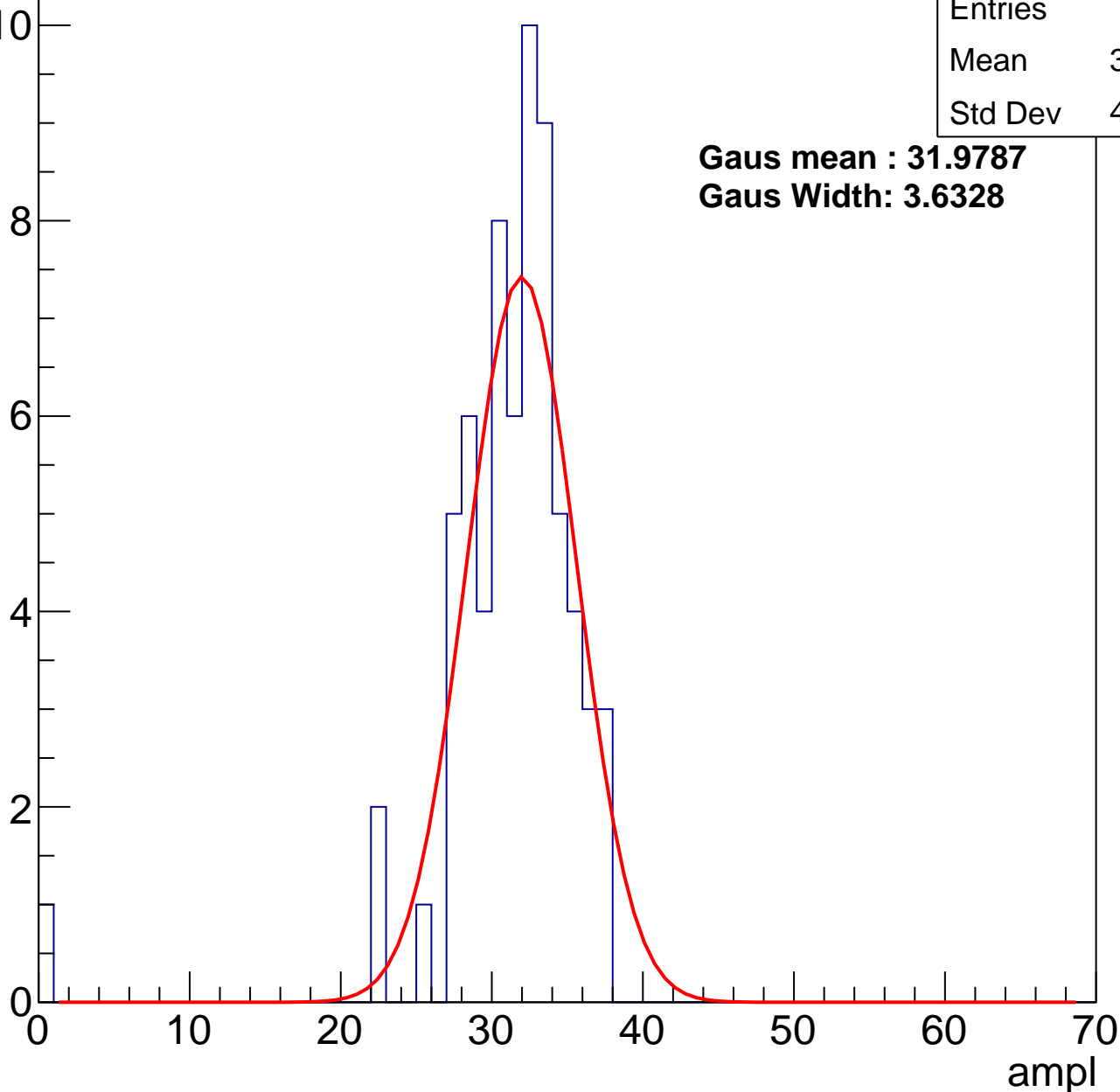
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.75
Std Dev	4.964

Gaus mean : 31.9787

Gaus Width: 3.6328



B0L002S, U4-ch15, adc1

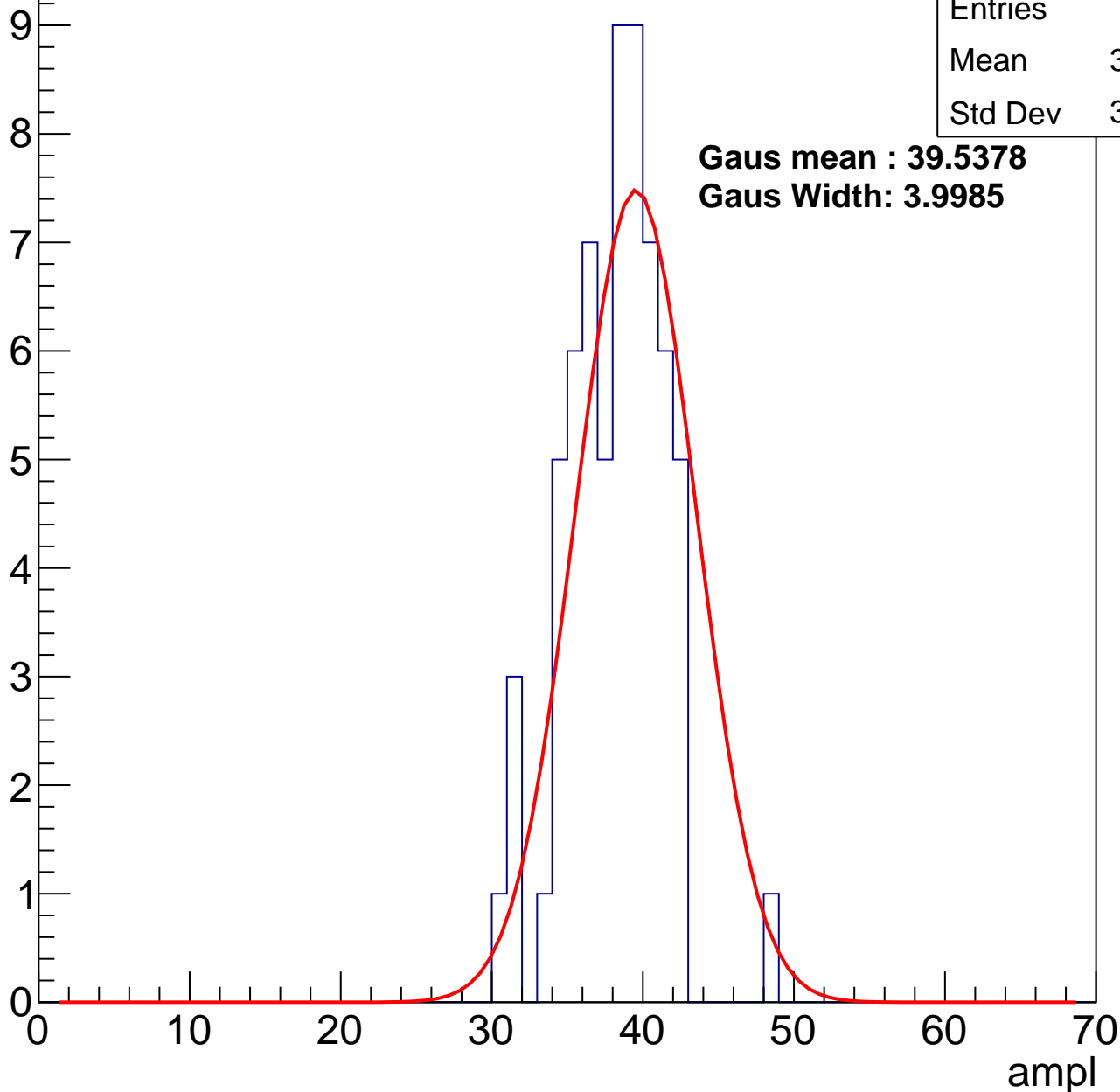
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.69
Std Dev	3.205

Gaus mean : 39.5378

Gaus Width: 3.9985



B0L002S, U4-ch15, adc2

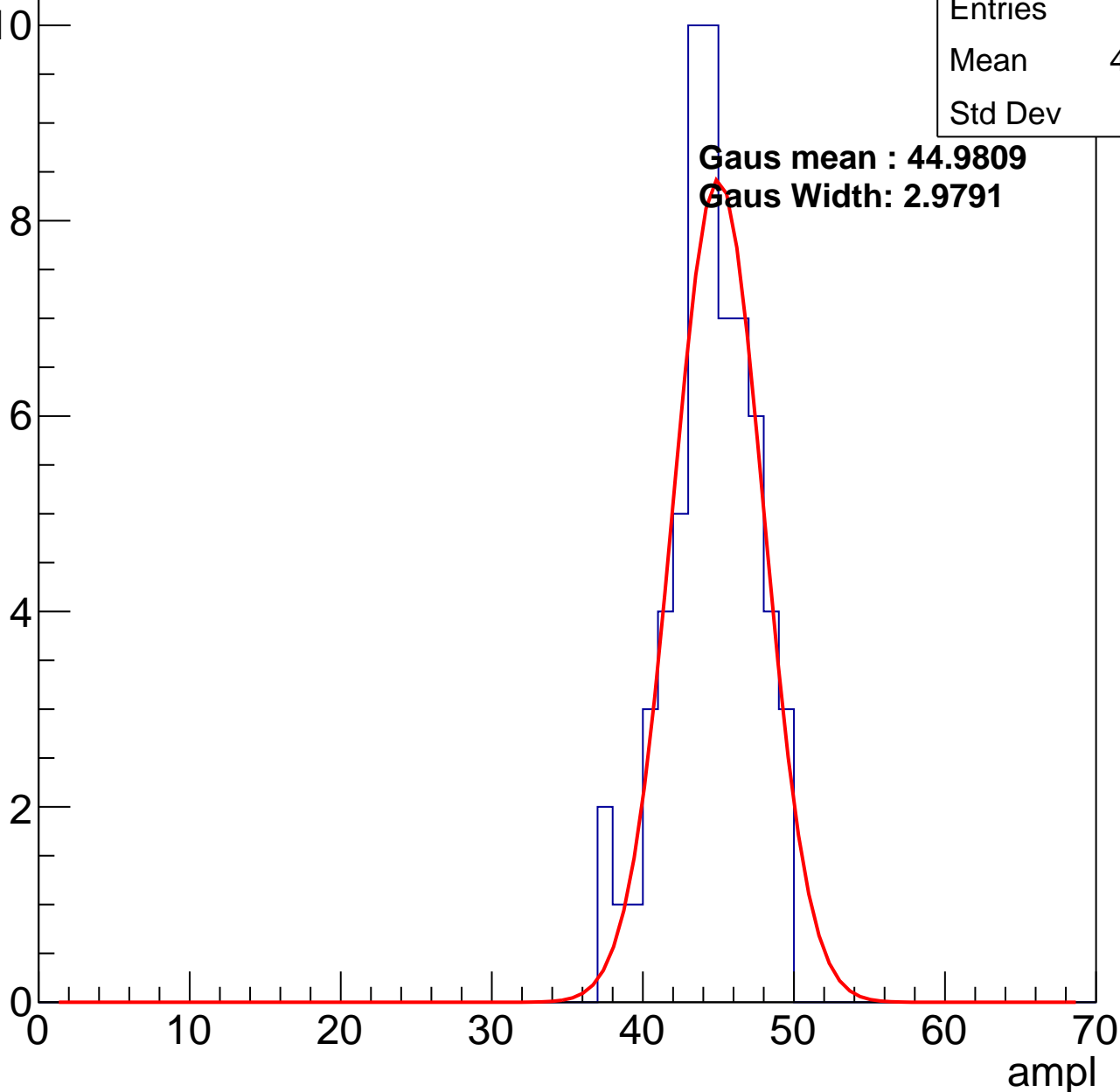
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44.02
Std Dev	2.82

Gaus mean : 44.9809

Gaus Width: 2.9791

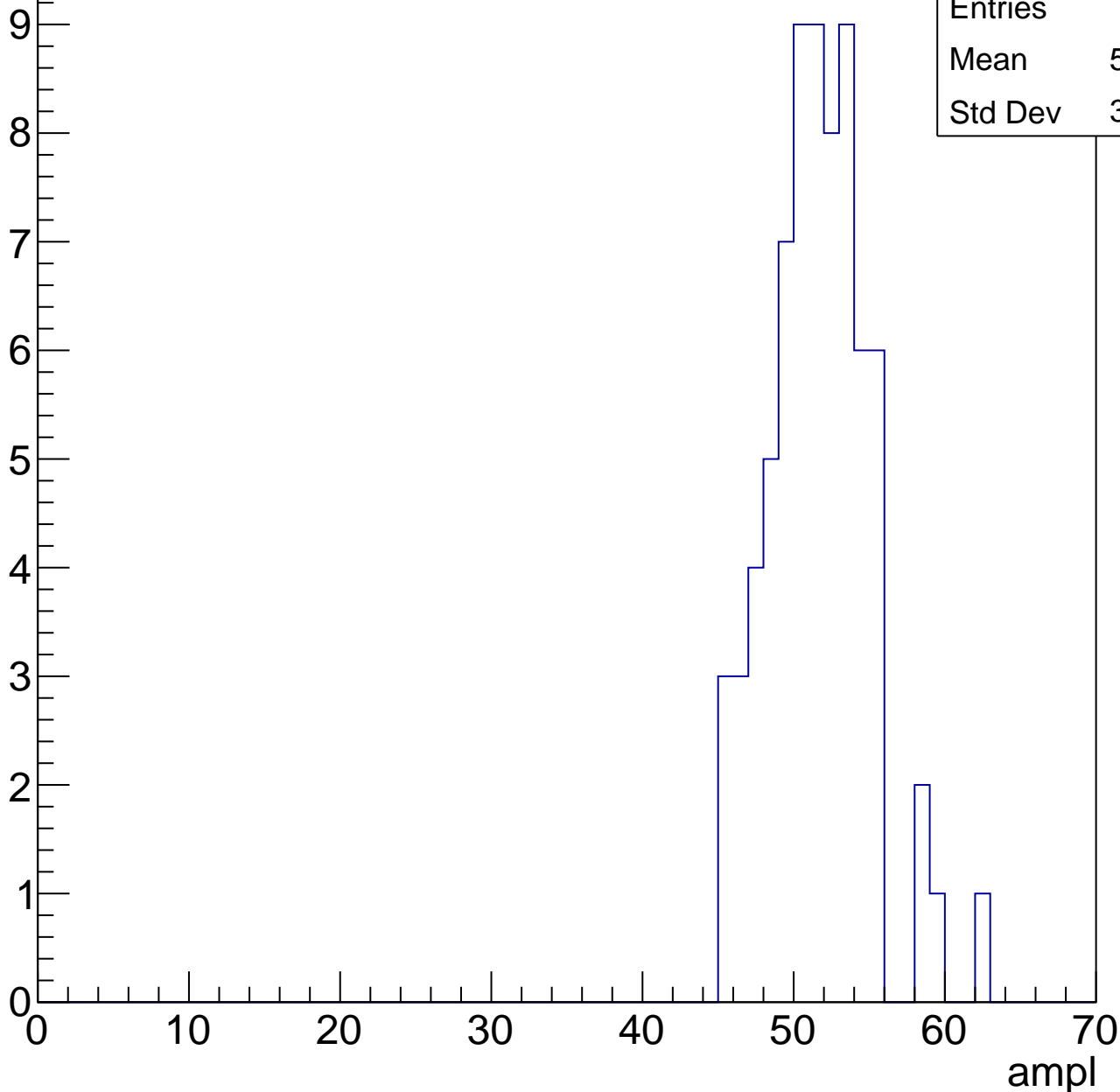


B0L002S, U4-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

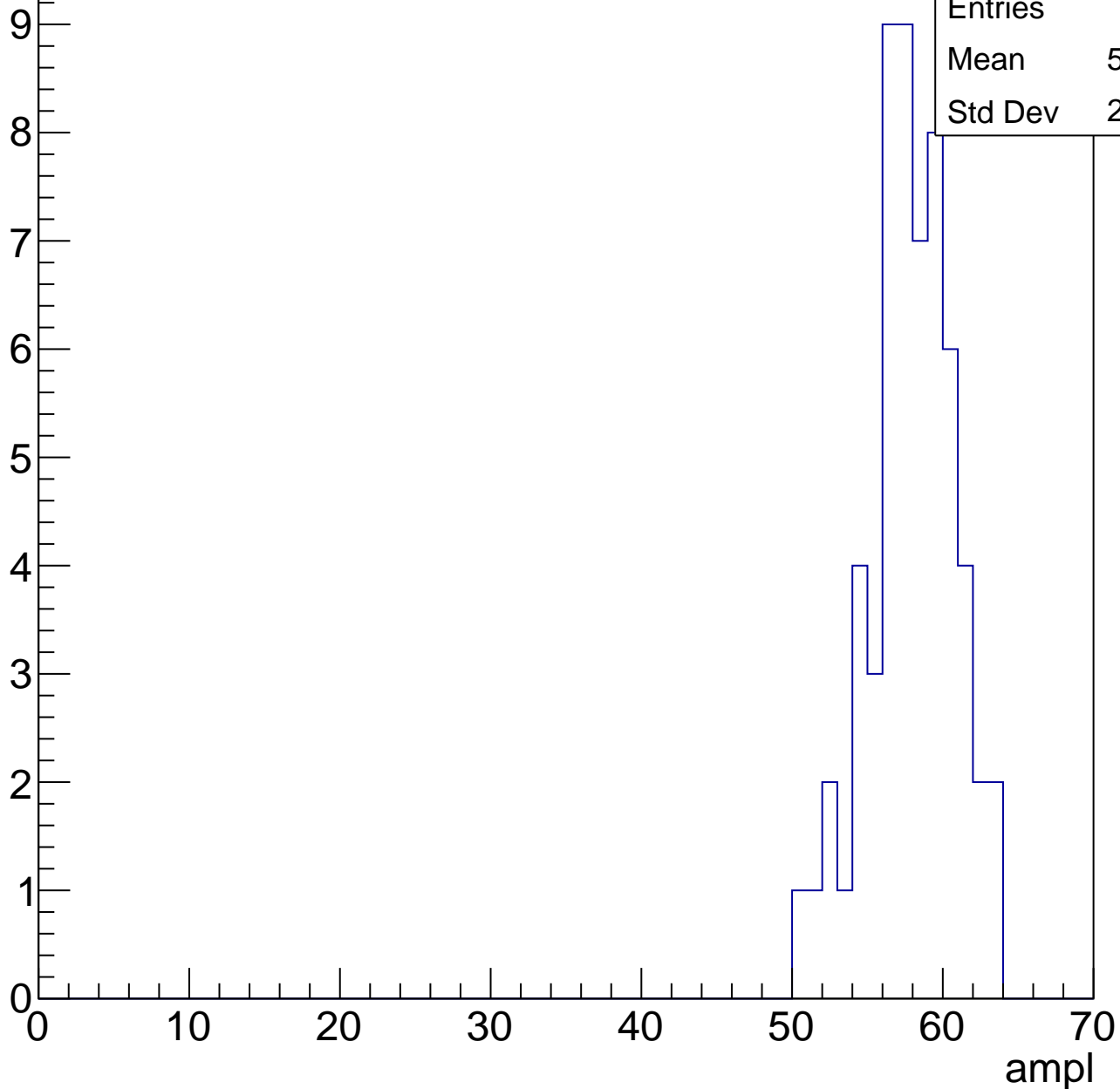
Entries	73
Mean	51.19
Std Dev	3.317



B0L002S, U4-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



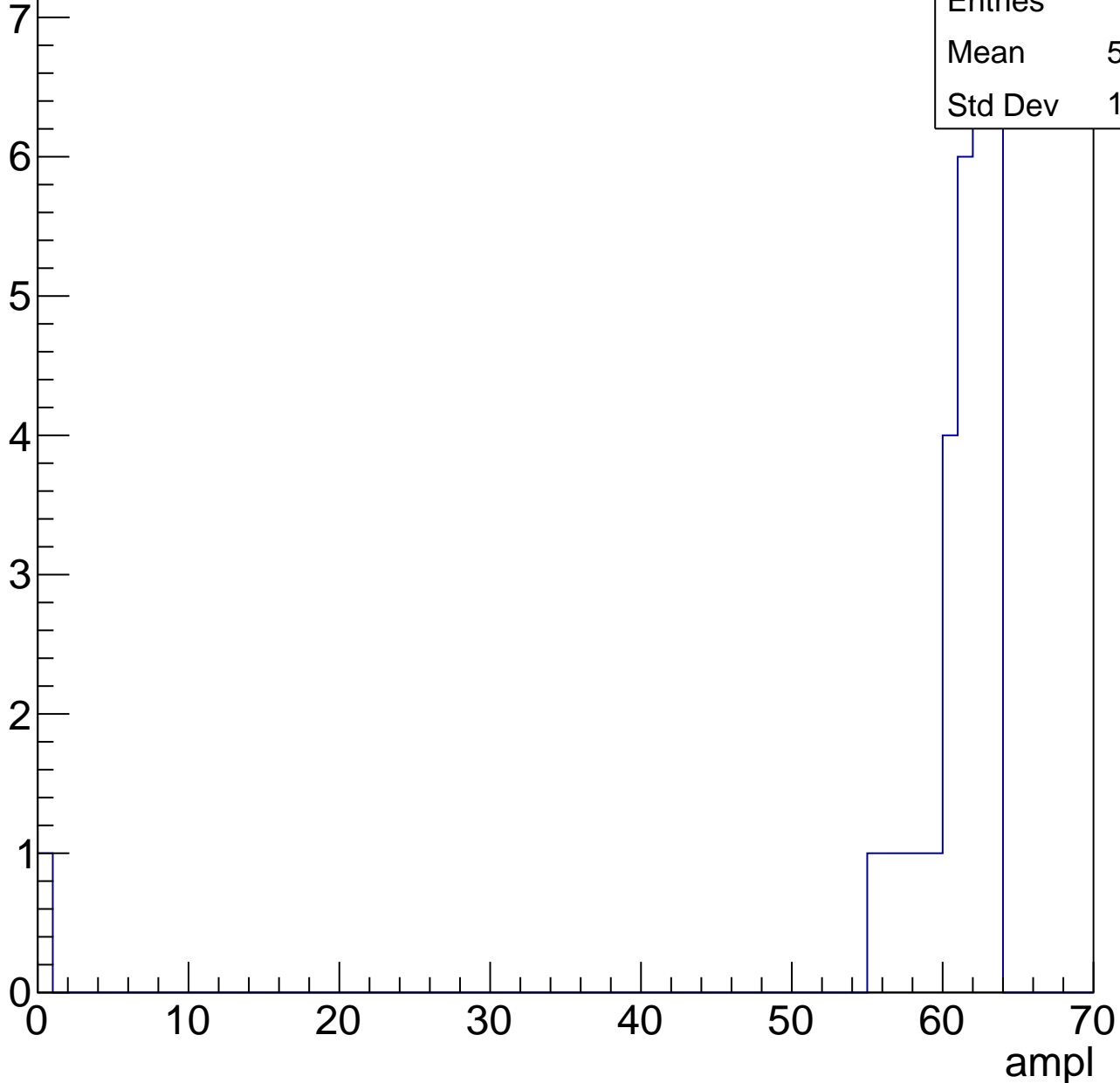
Entries	59
Mean	57.42
Std Dev	2.842

B0L002S, U4-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.87
Std Dev	11.13



B0L002S, U4-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch16, adc0

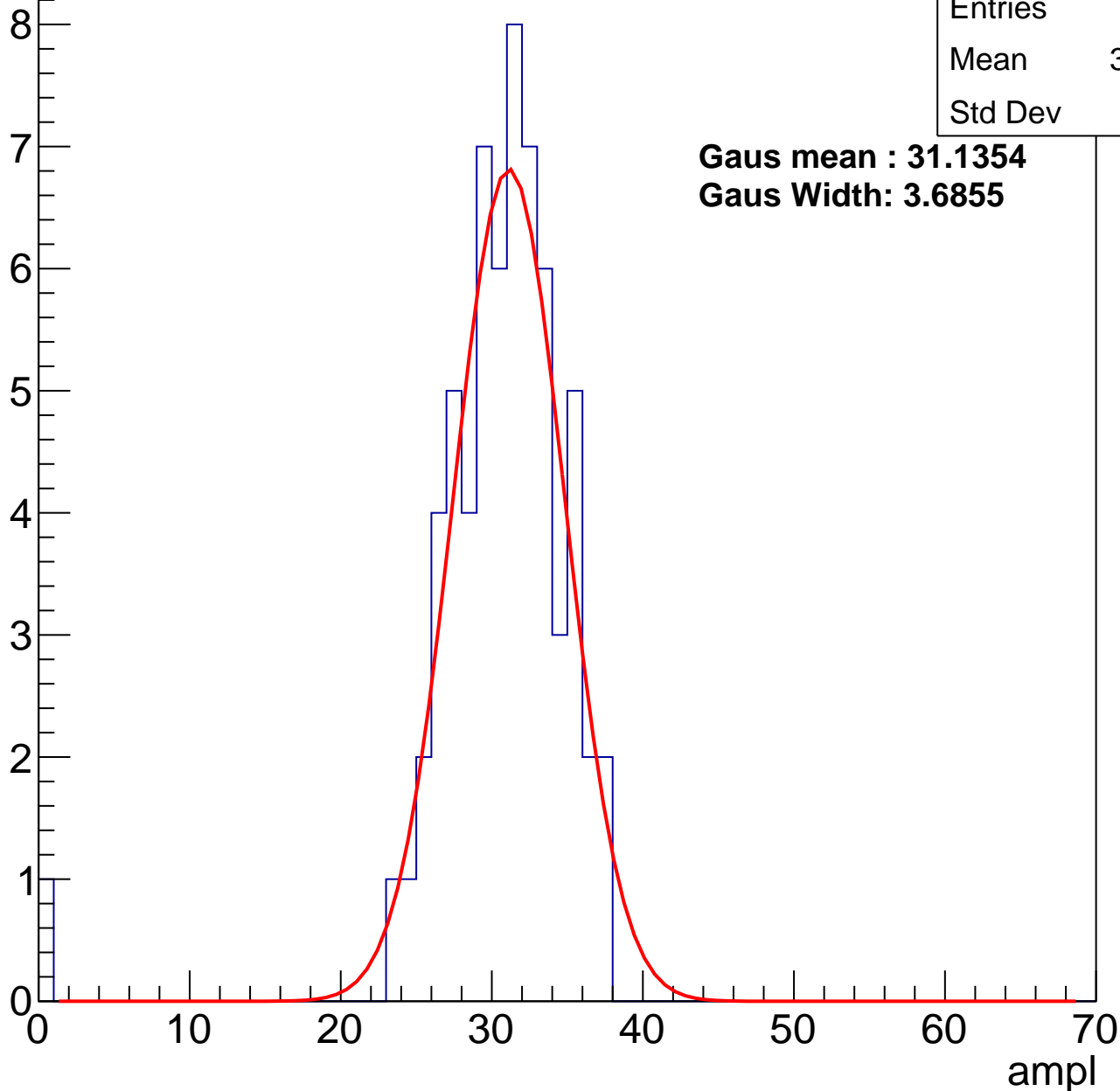
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.06
Std Dev	5

Gaus mean : 31.1354

Gaus Width: 3.6855



B0L002S, U4-ch16, adc1

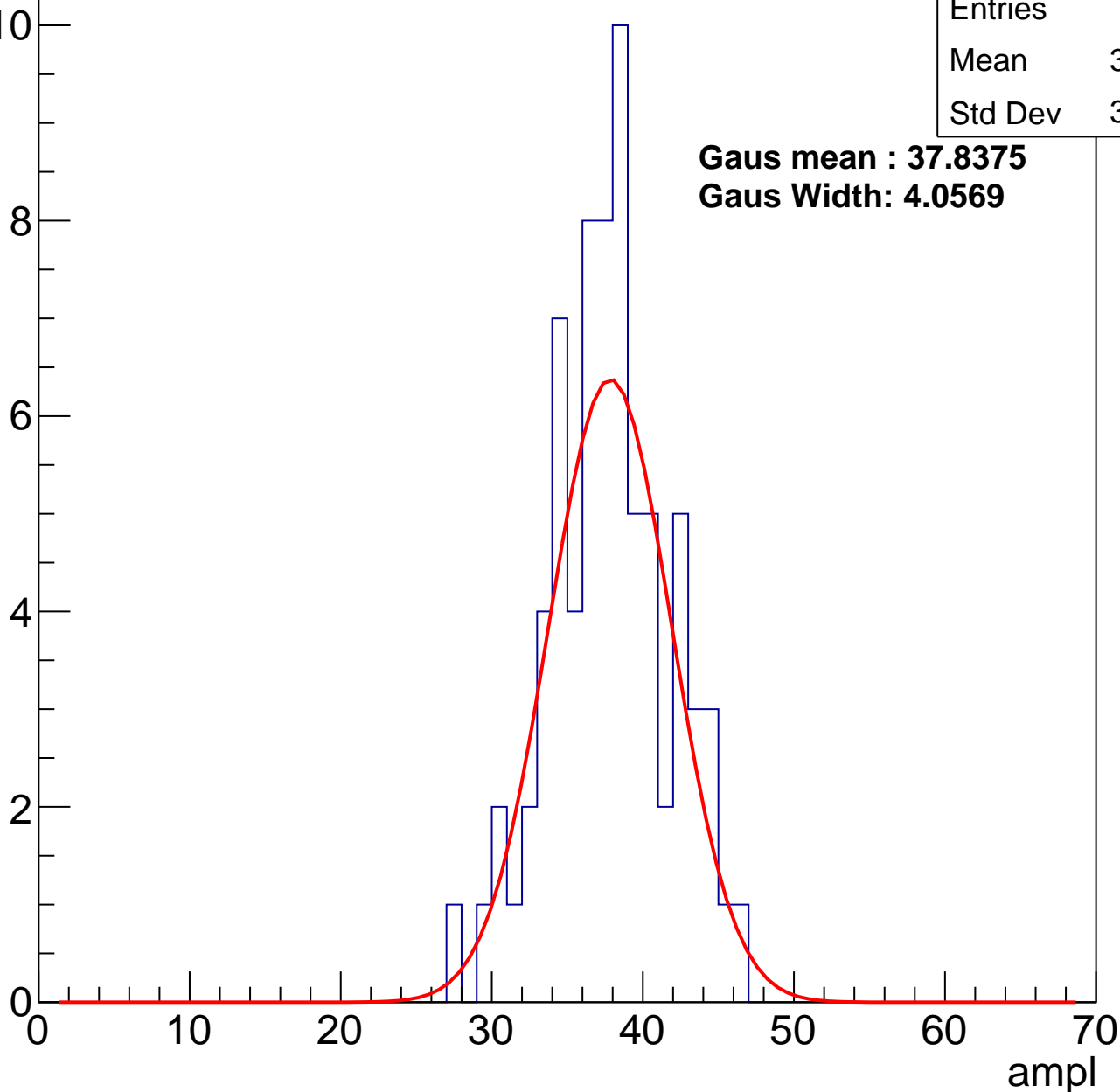
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.32
Std Dev	3.924

Gaus mean : 37.8375

Gaus Width: 4.0569



B0L002S, U4-ch16, adc2

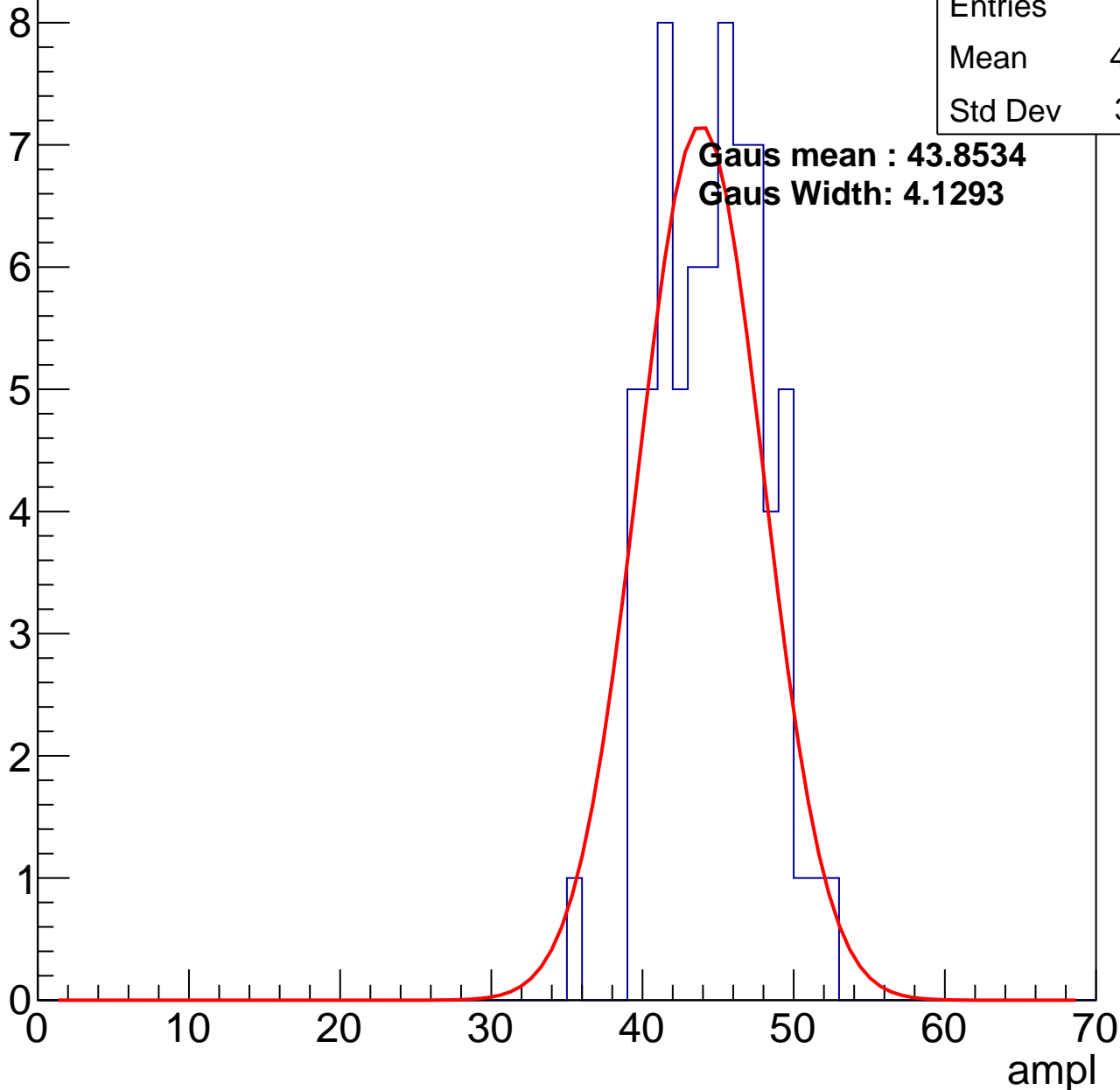
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.16
Std Dev	3.421

Gaus mean : 43.8534

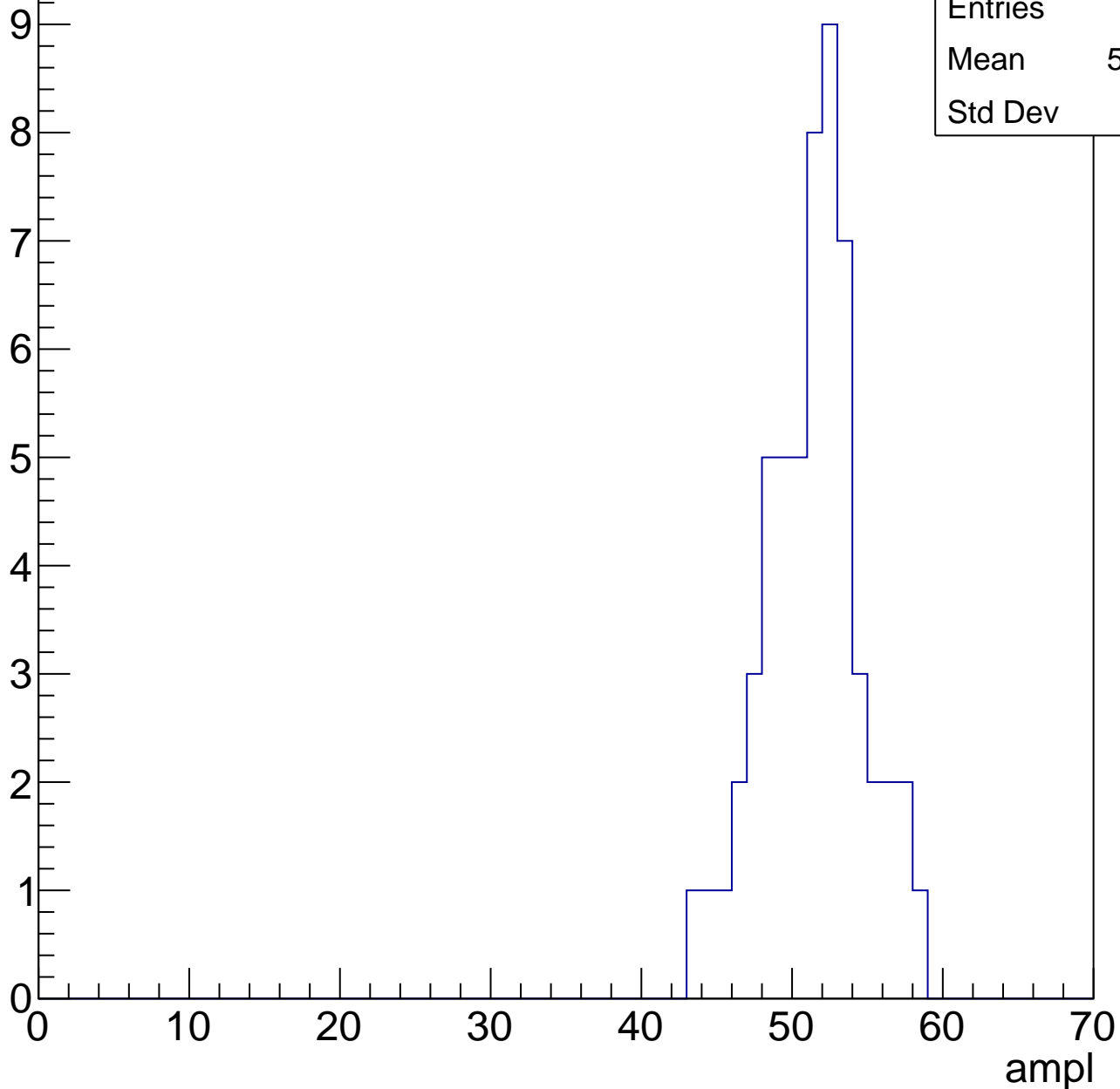
Gaus Width: 4.1293



B0L002S, U4-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

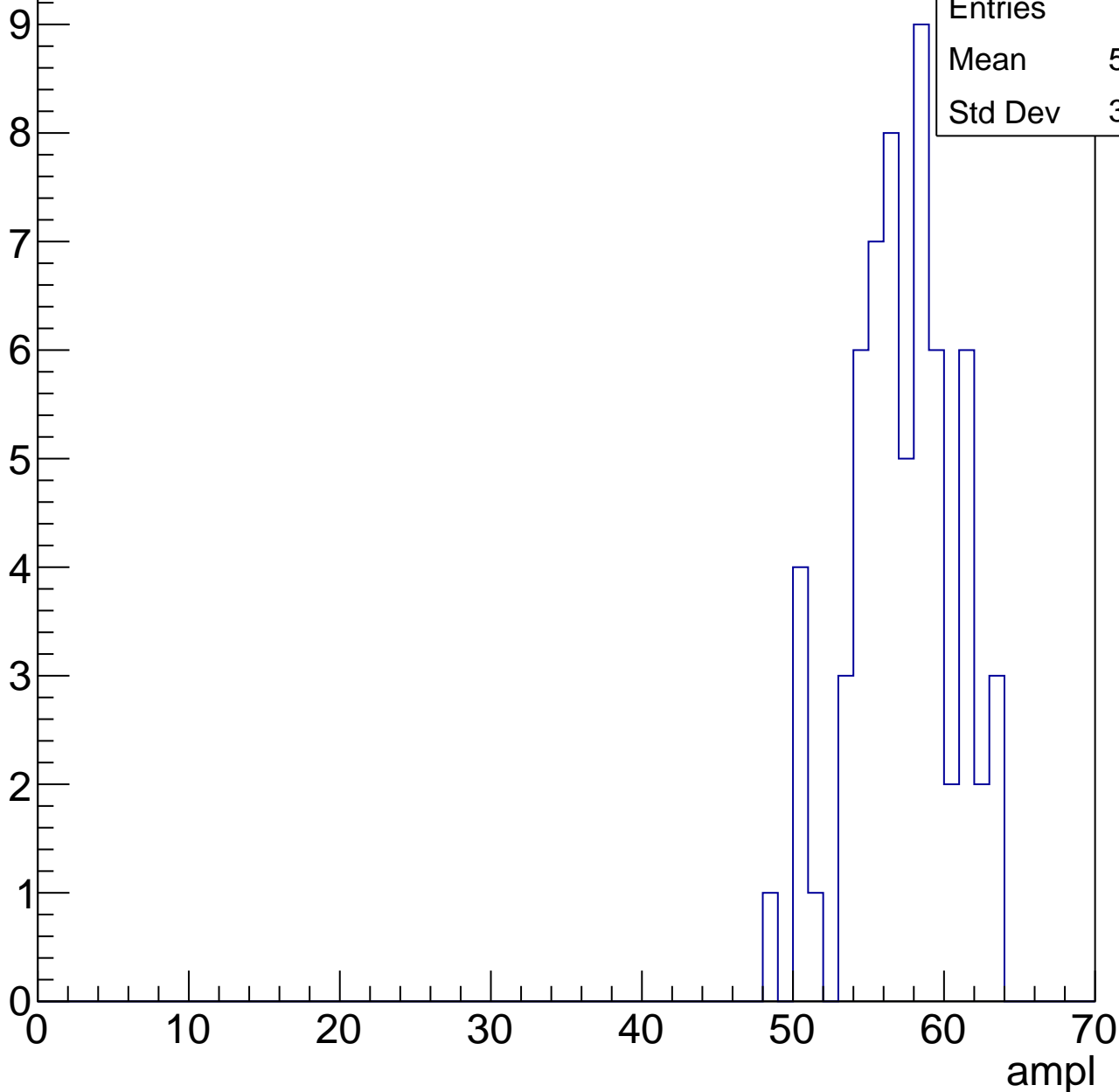


B0L002S, U4-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.75
Std Dev	3.455

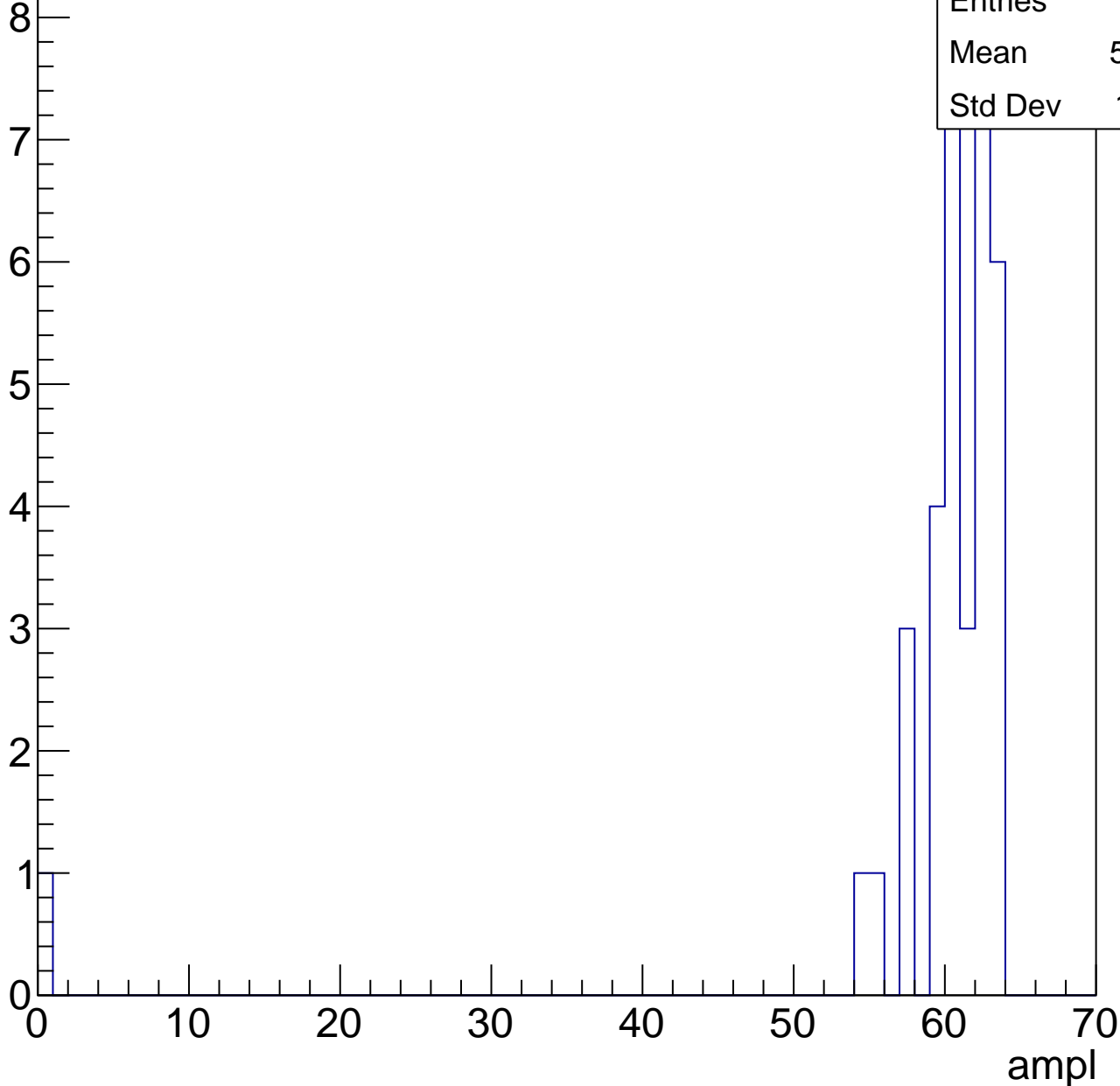


B0L002S, U4-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.66
Std Dev	10.31



B0L002S, U4-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch17, adc0

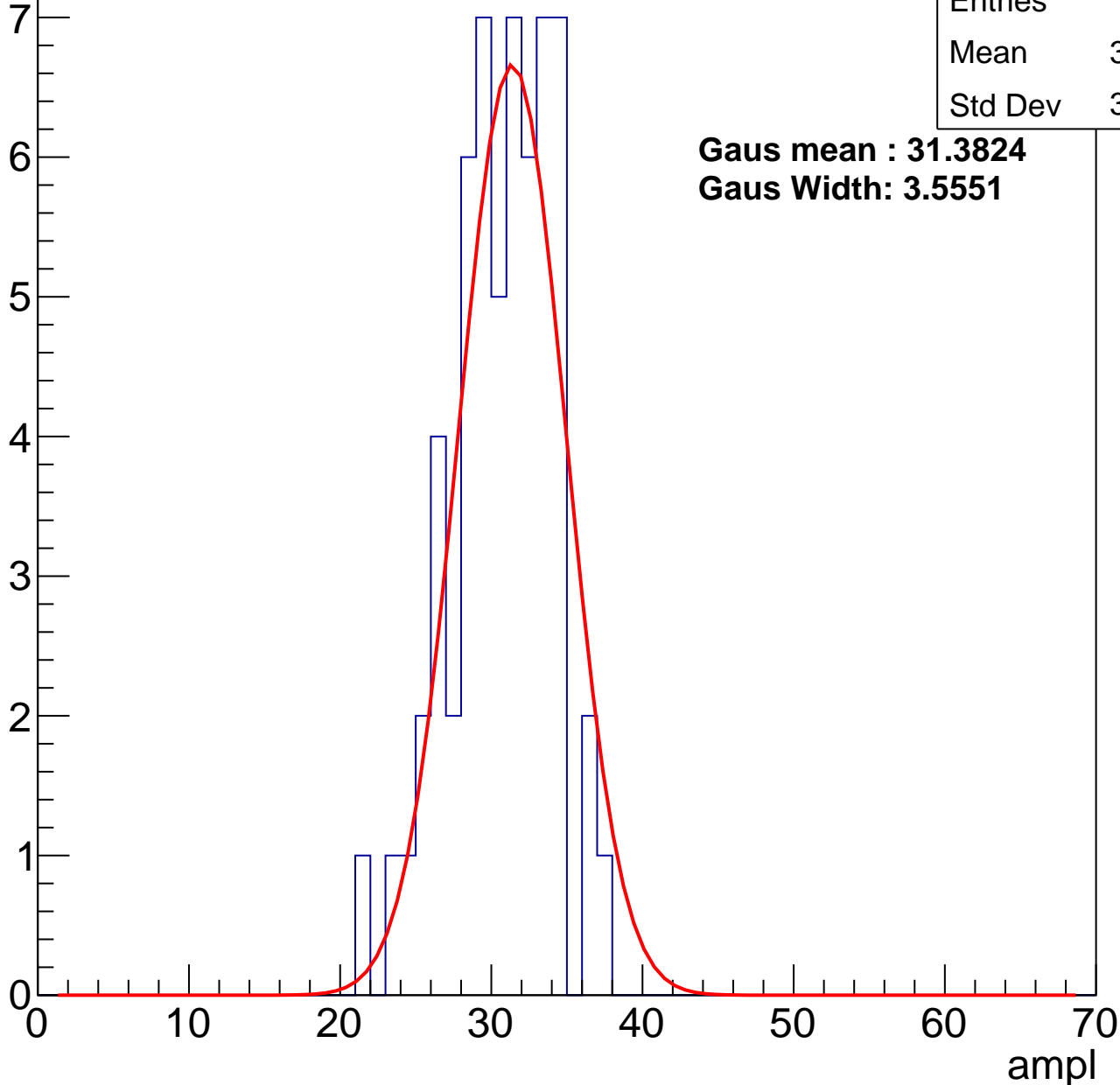
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.24
Std Dev	3.326

Gaus mean : 31.3824

Gaus Width: 3.5551



B0L002S, U4-ch17, adc1

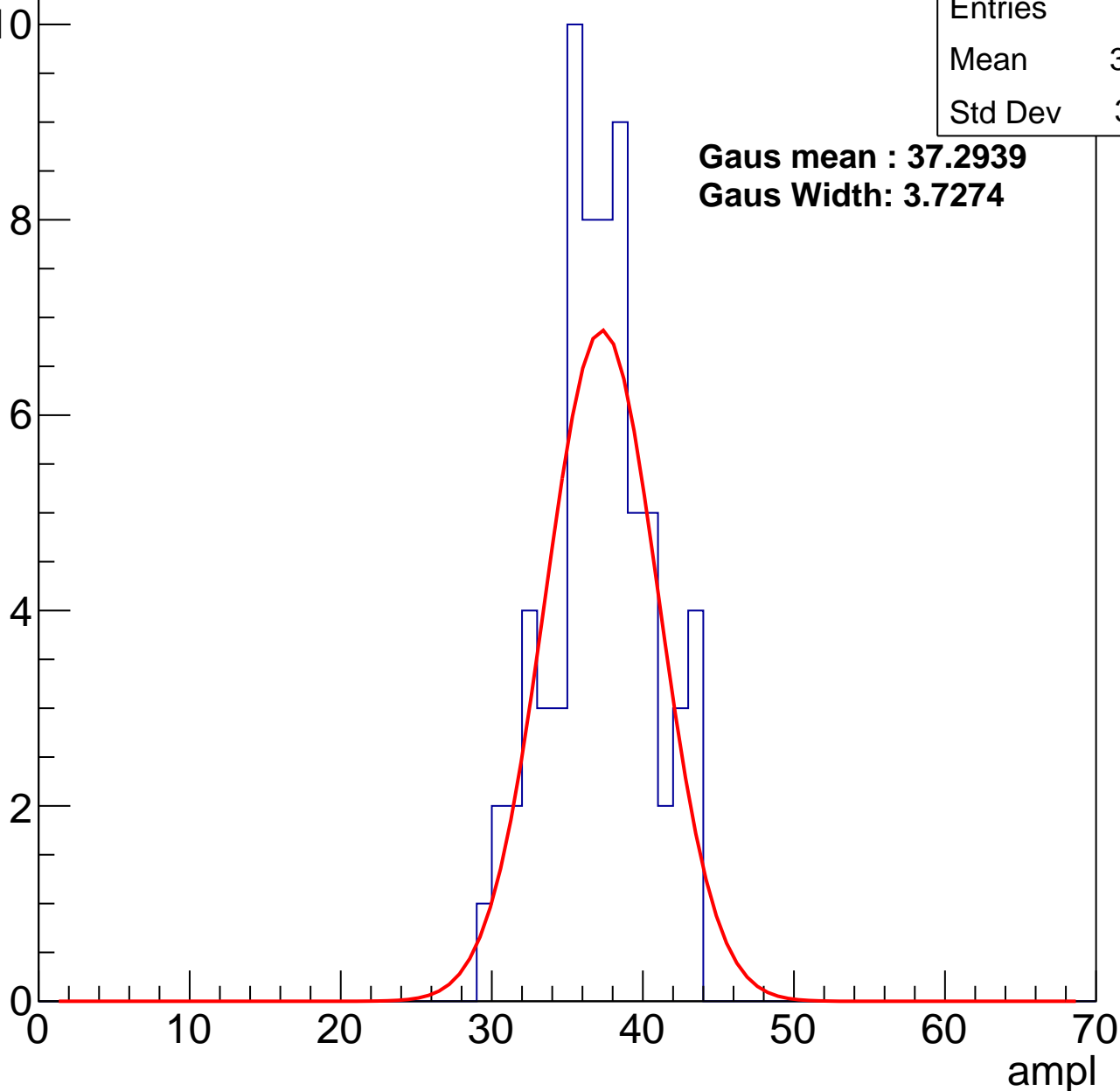
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.68
Std Dev	3.351

Gaus mean : 37.2939

Gaus Width: 3.7274



B0L002S, U4-ch17, adc2

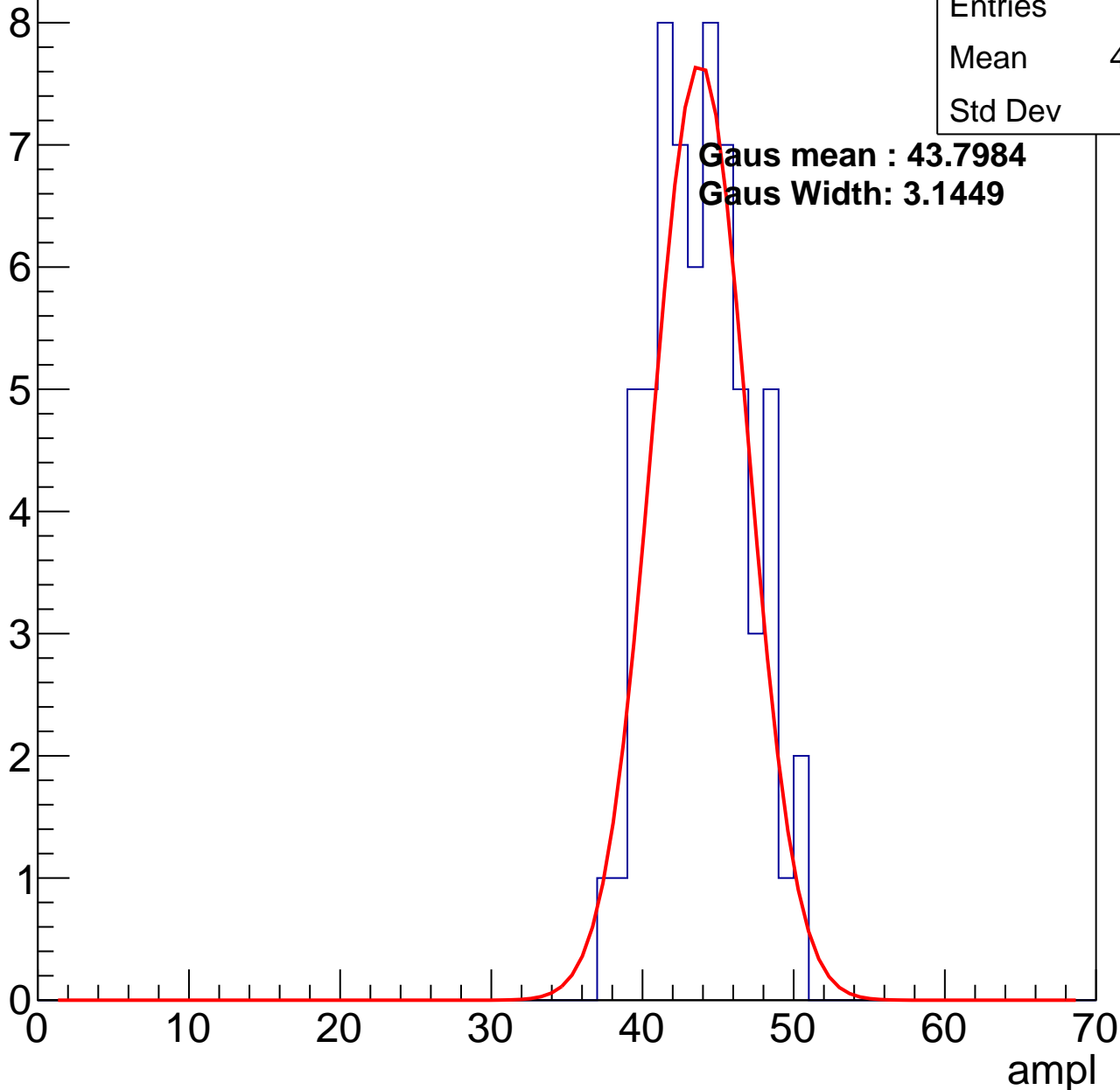
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.39
Std Dev	3.07

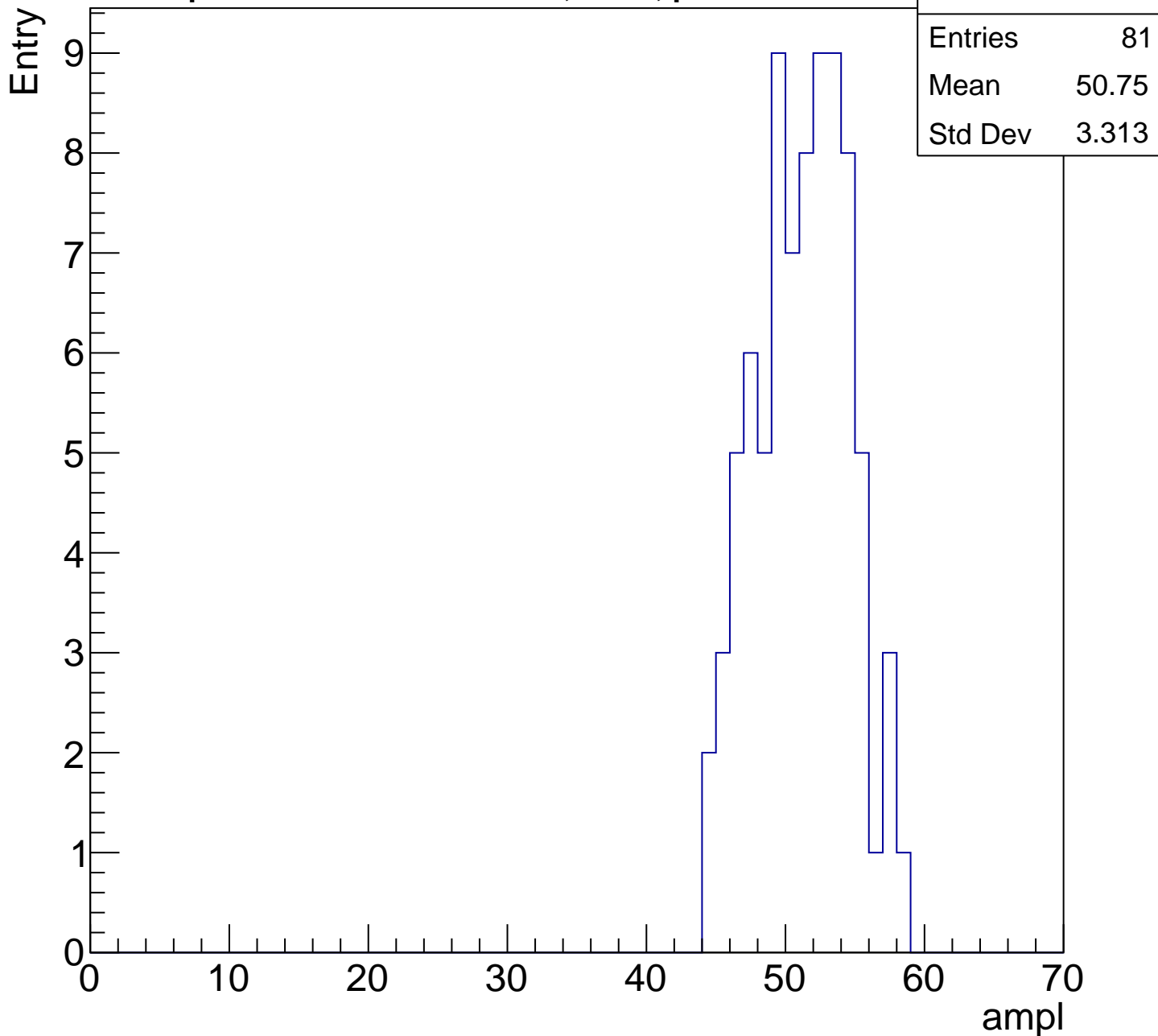
Gaus mean : 43.7984

Gaus Width: 3.1449



B0L002S, U4-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

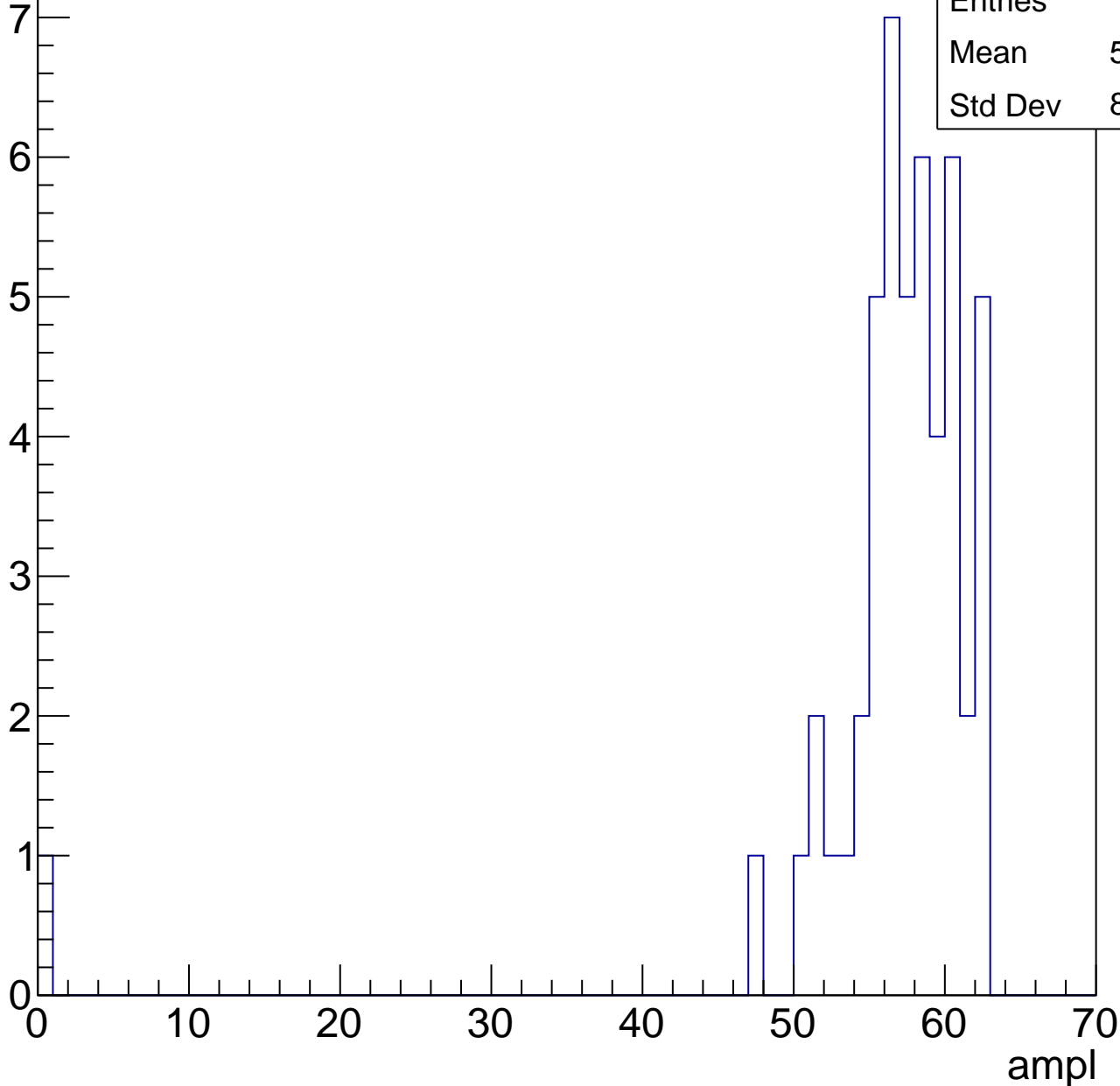


B0L002S, U4-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	55.92
Std Dev	8.727

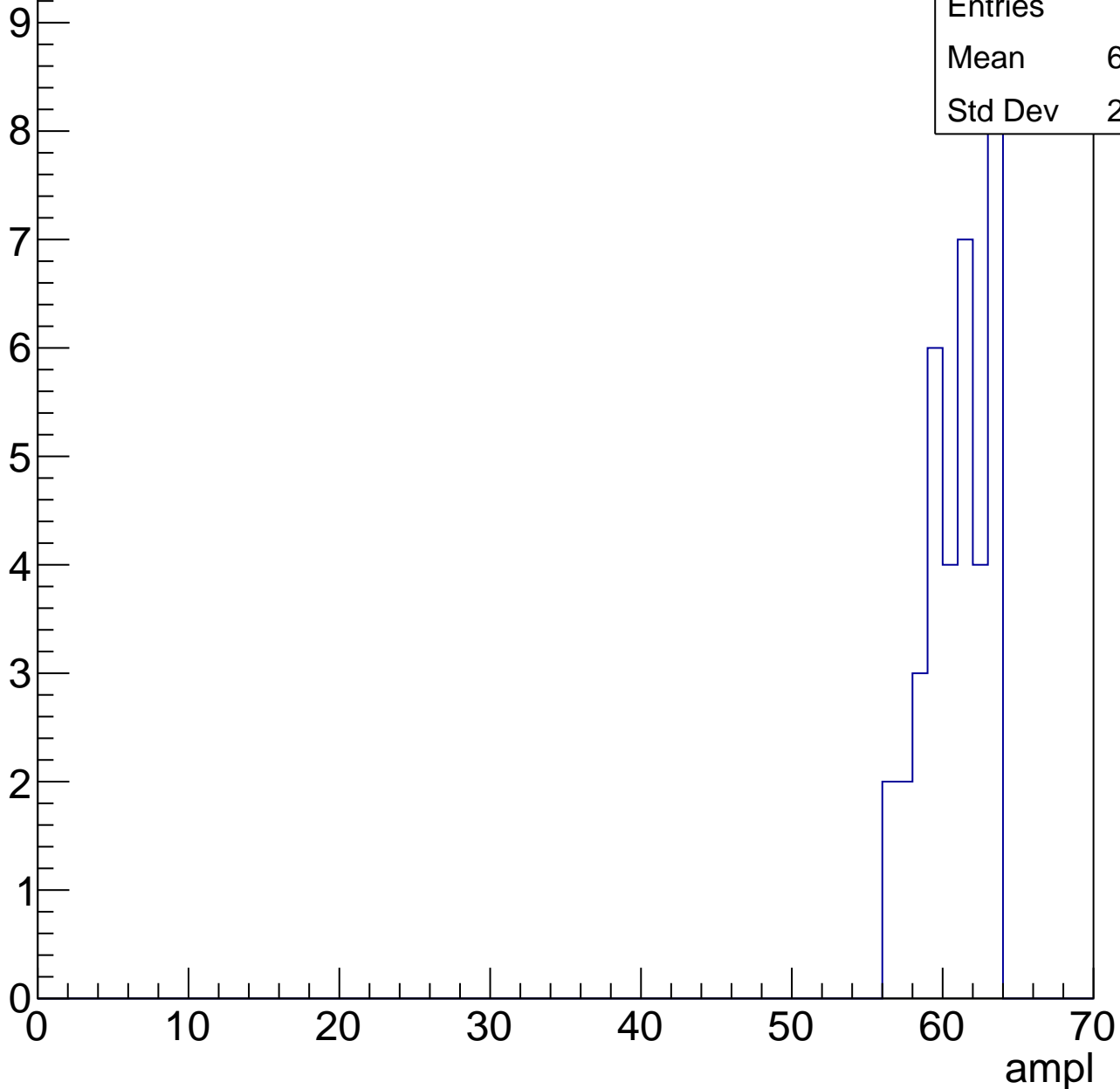


B0L002S, U4-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.43
Std Dev	2.112



B0L002S, U4-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch18, adc0

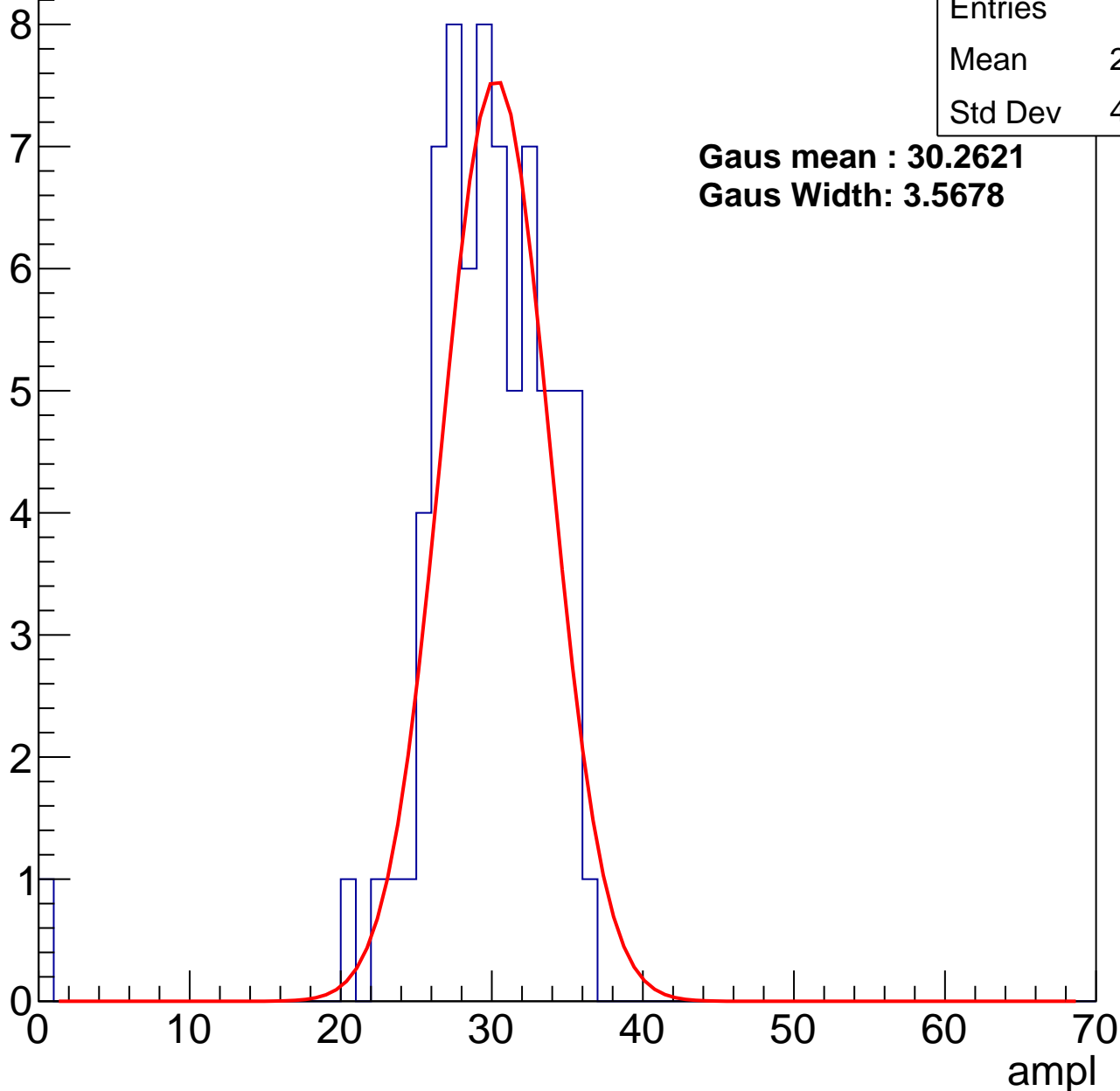
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.07
Std Dev	4.855

Gaus mean : 30.2621

Gaus Width: 3.5678



B0L002S, U4-ch18, adc1

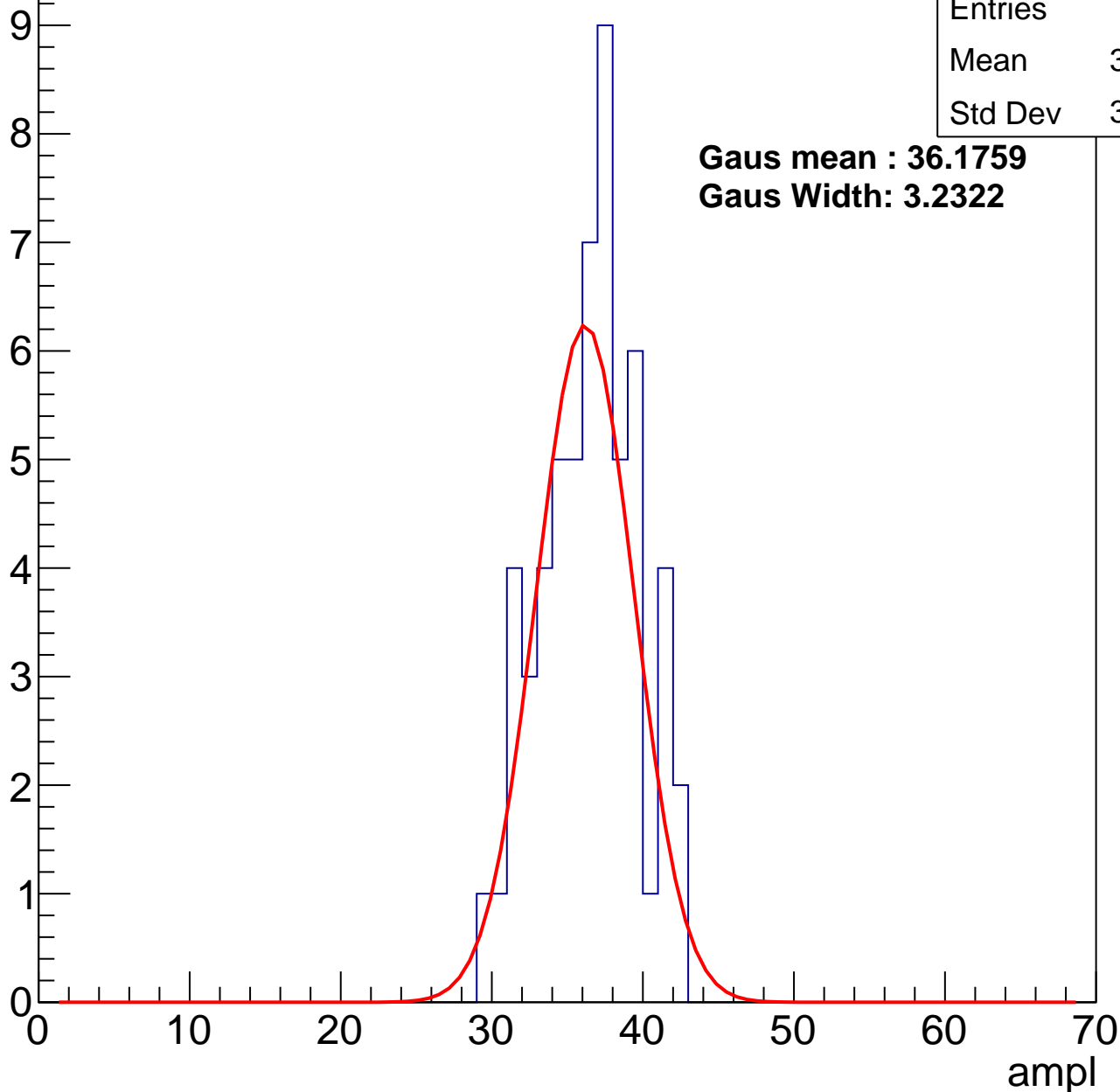
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.02
Std Dev	3.148

Gaus mean : 36.1759

Gaus Width: 3.2322



B0L002S, U4-ch18, adc2

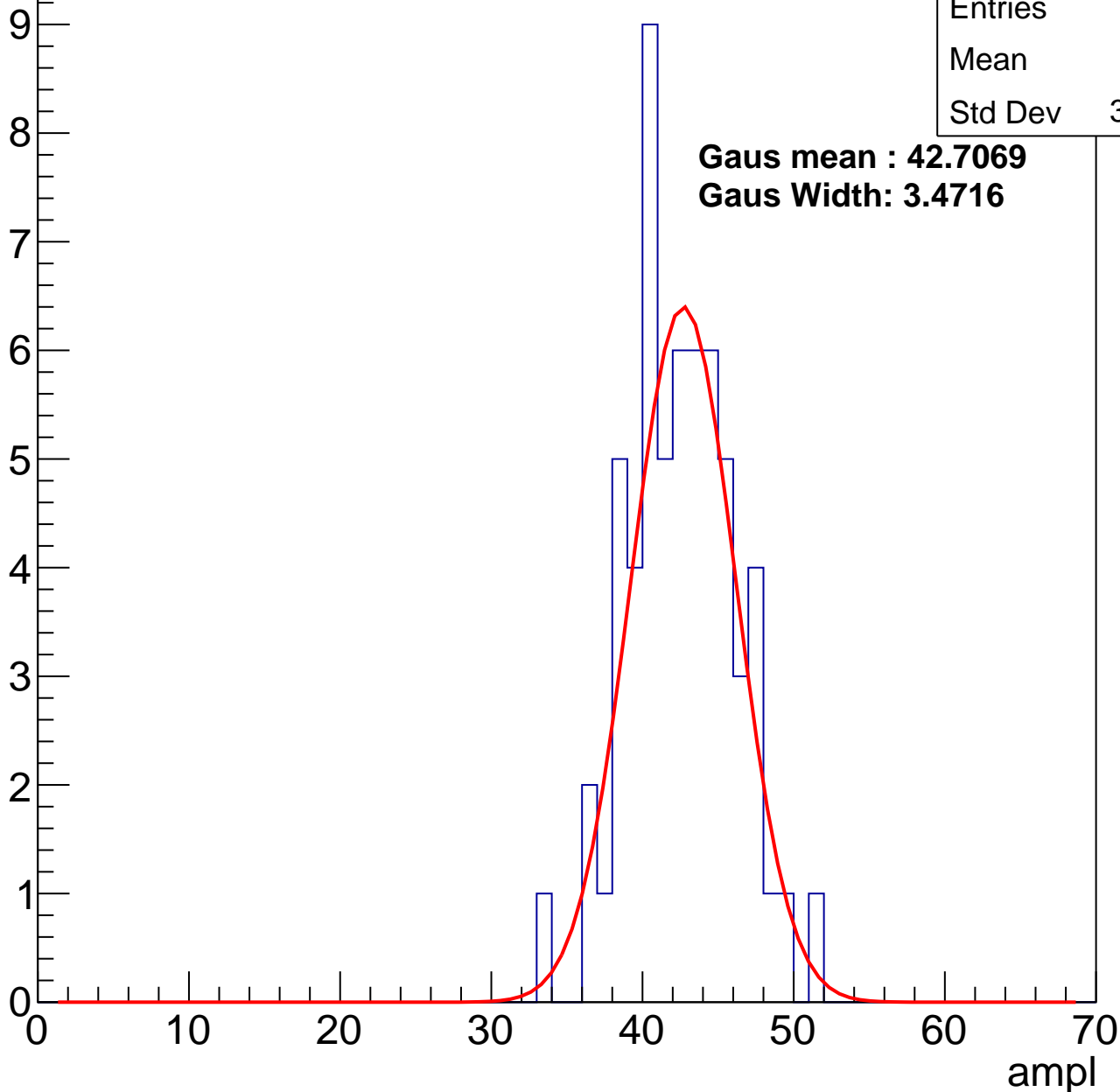
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.1
Std Dev	3.472

Gaus mean : 42.7069

Gaus Width: 3.4716

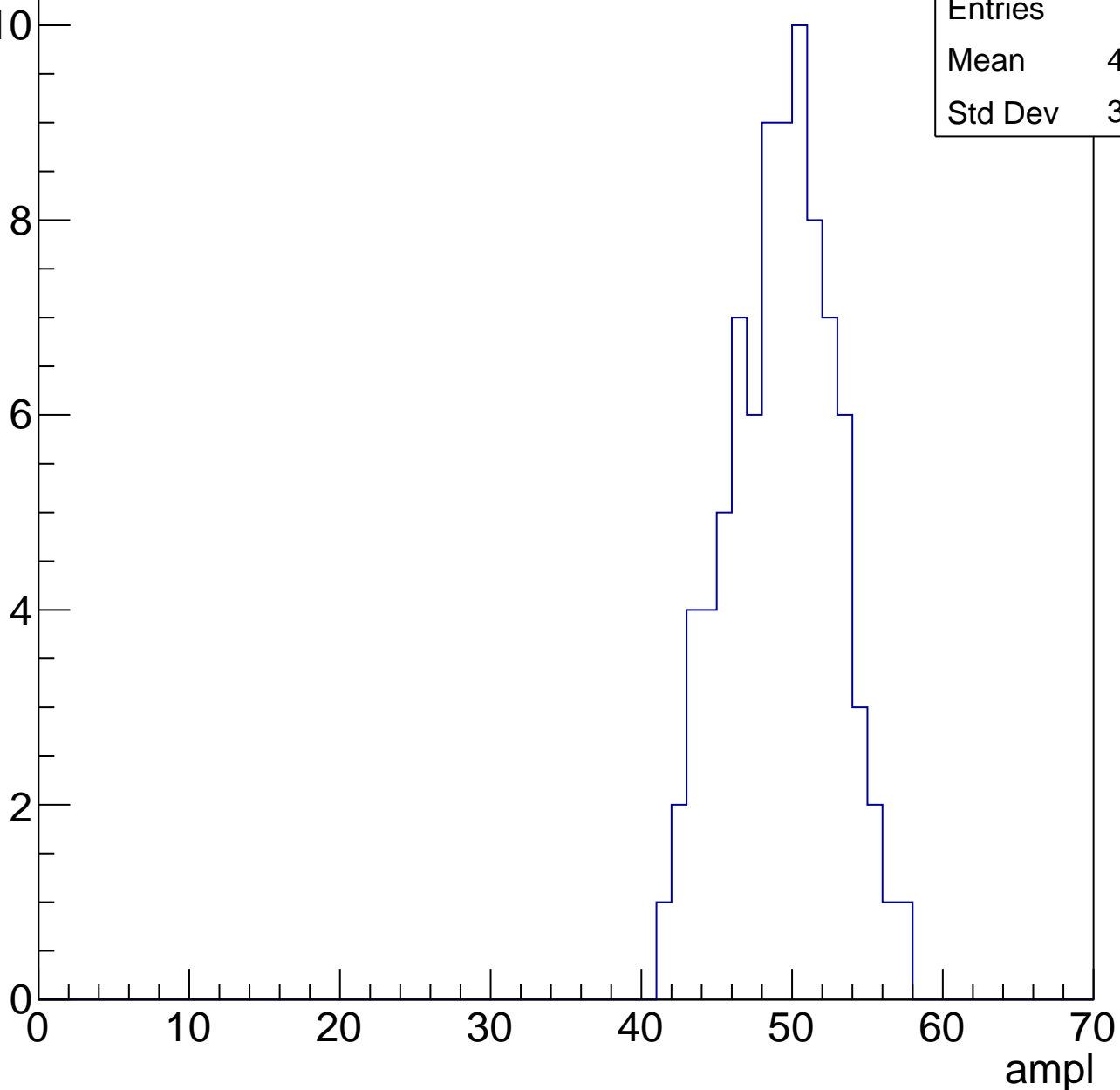


B0L002S, U4-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	48.82
Std Dev	3.498



B0L002S, U4-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

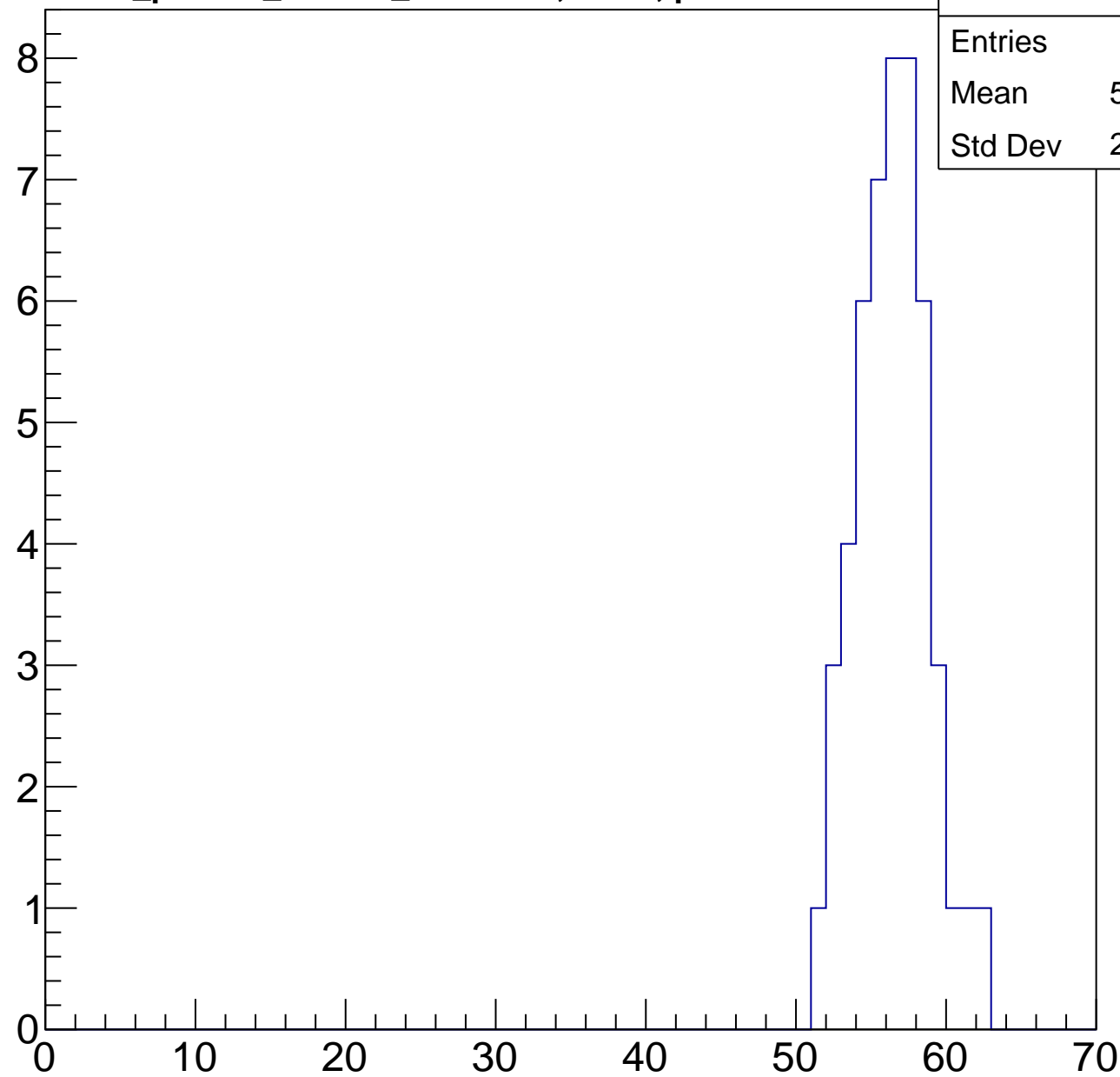
Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	55.92
Std Dev	2.372

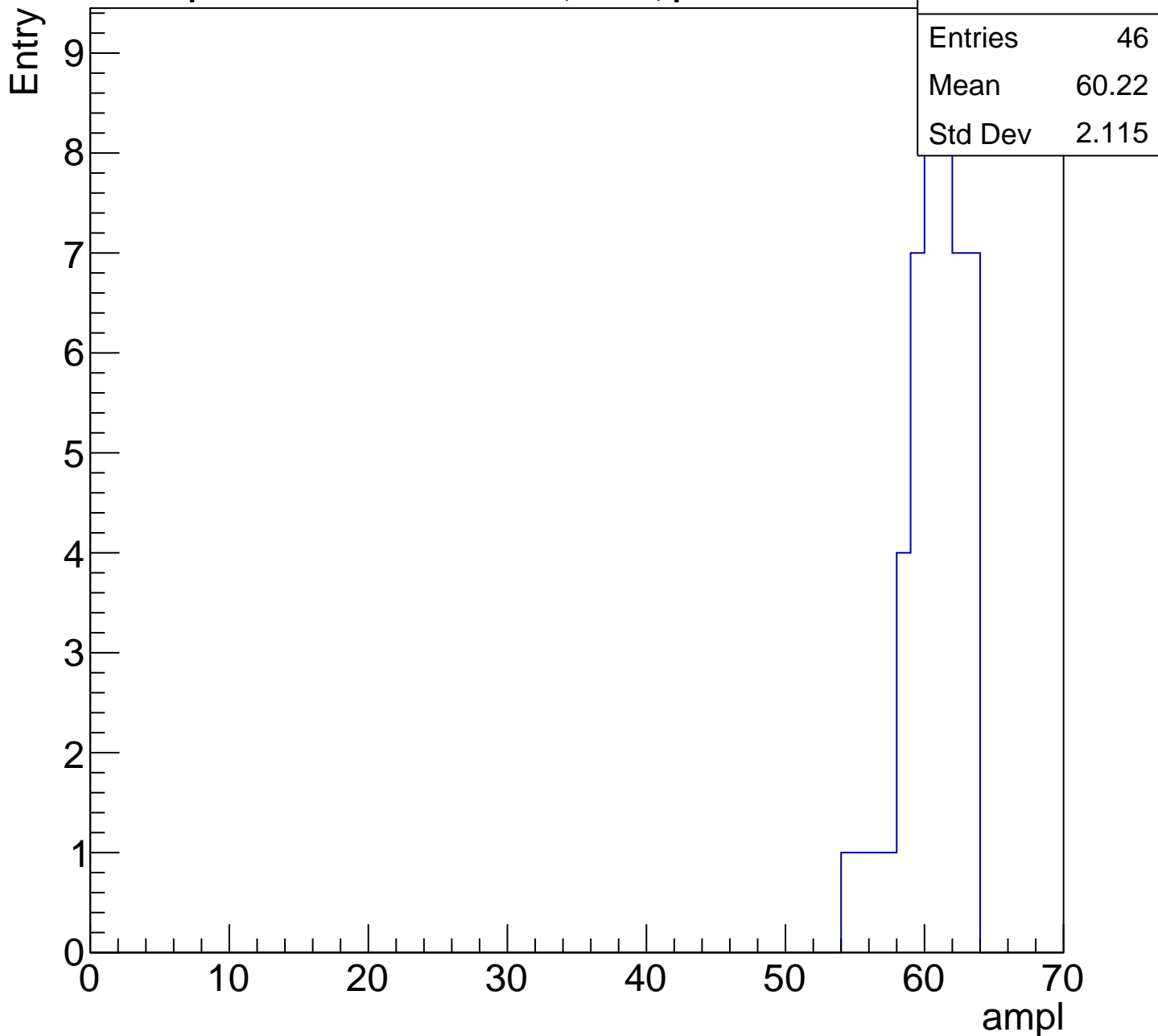
ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	7
Mean	53.43
Std Dev	21.82

B0L002S, U4-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch19, adc0

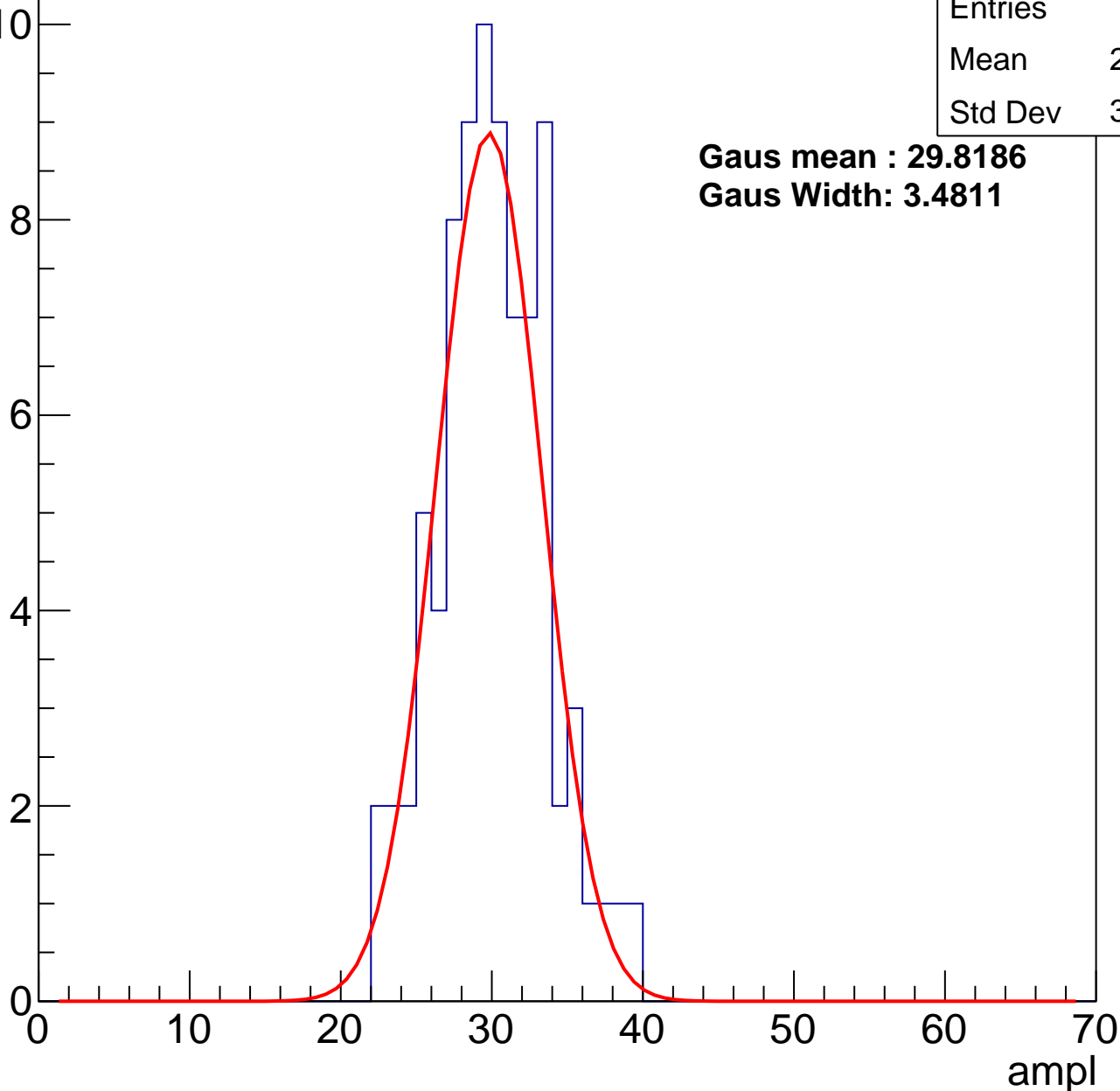
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.59
Std Dev	3.547

Gaus mean : 29.8186

Gaus Width: 3.4811



B0L002S, U4-ch19, adc1

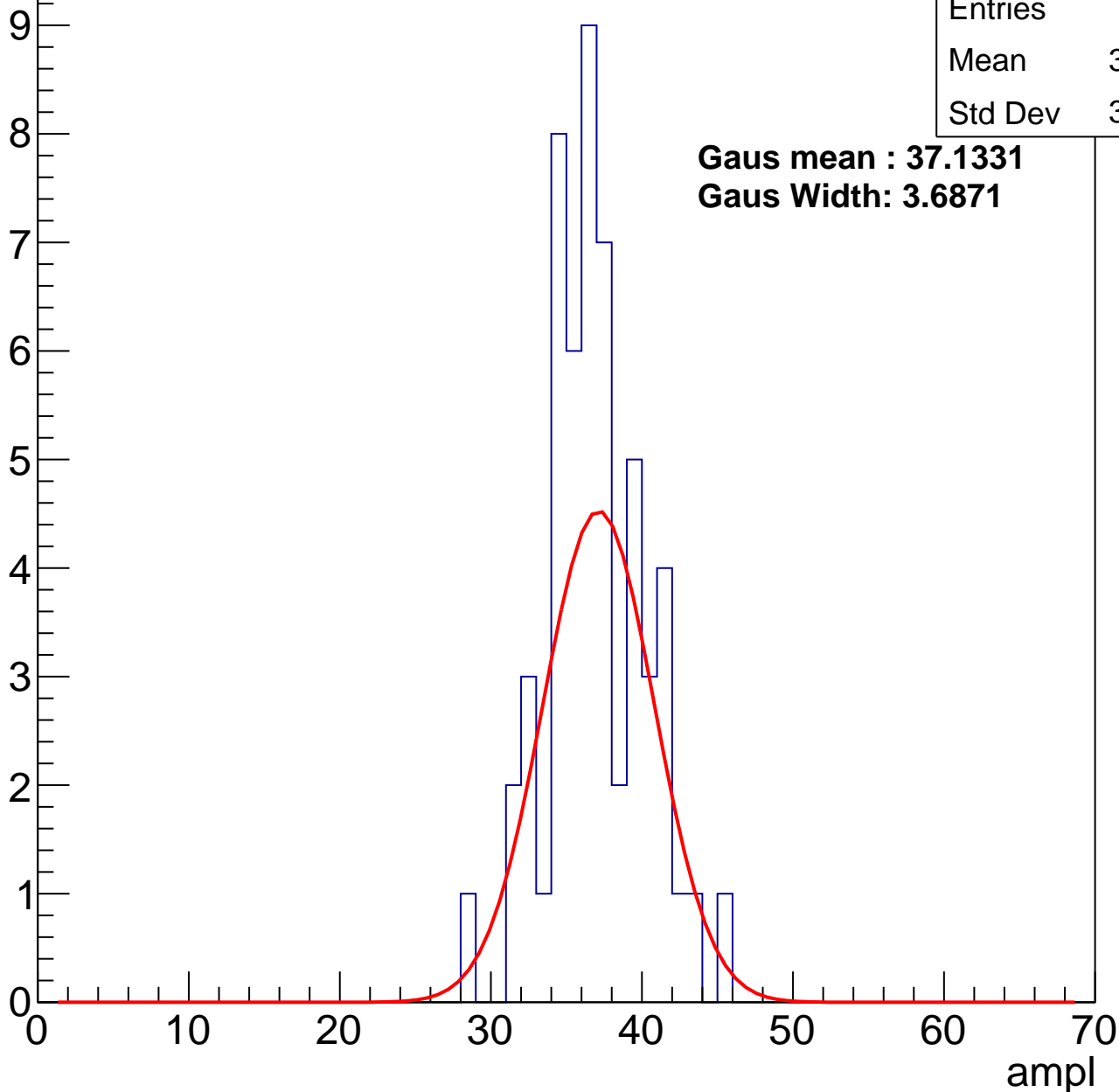
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	36.46
Std Dev	3.253

Gaus mean : 37.1331

Gaus Width: 3.6871



B0L002S, U4-ch19, adc2

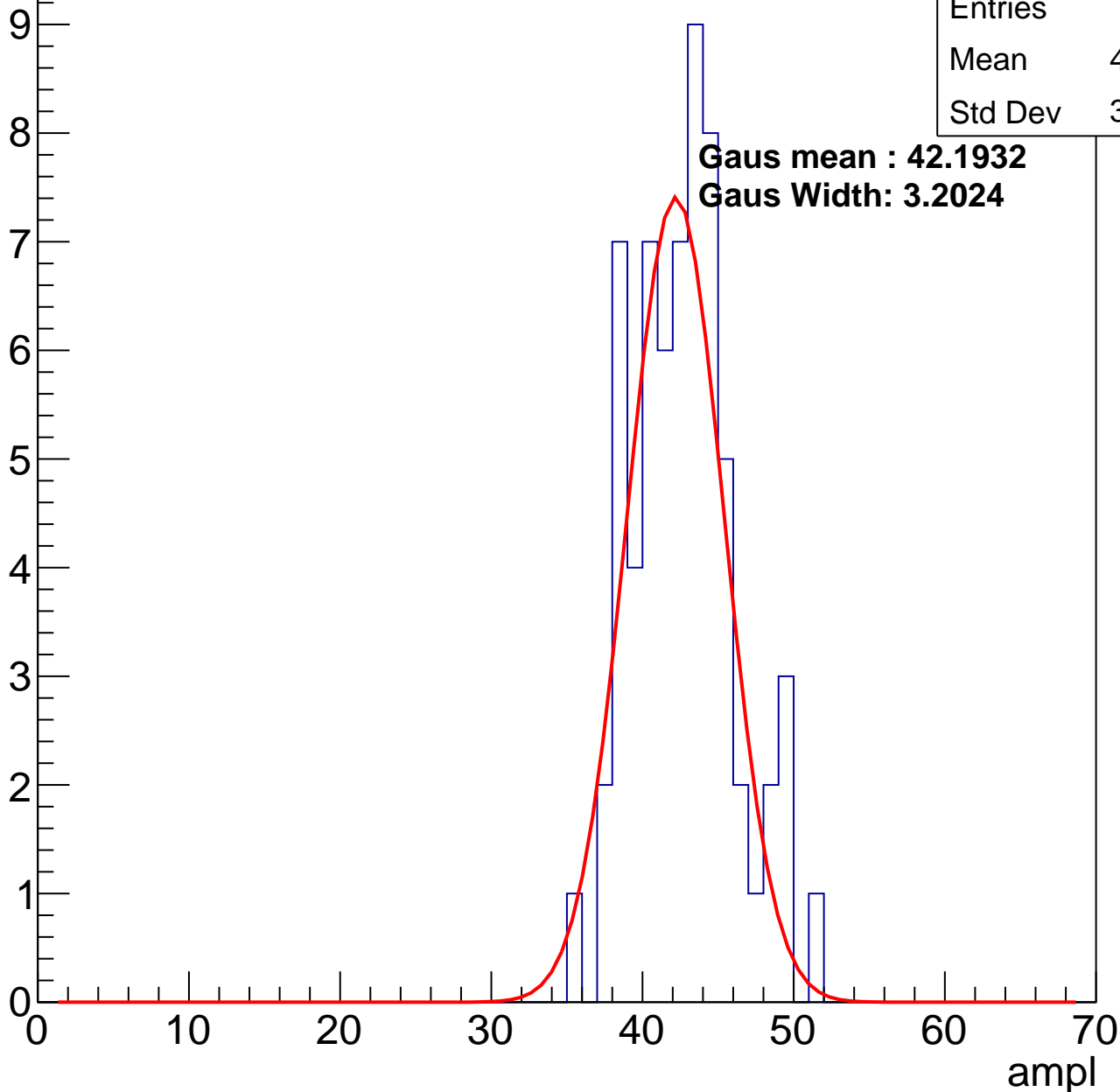
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42.28
Std Dev	3.326

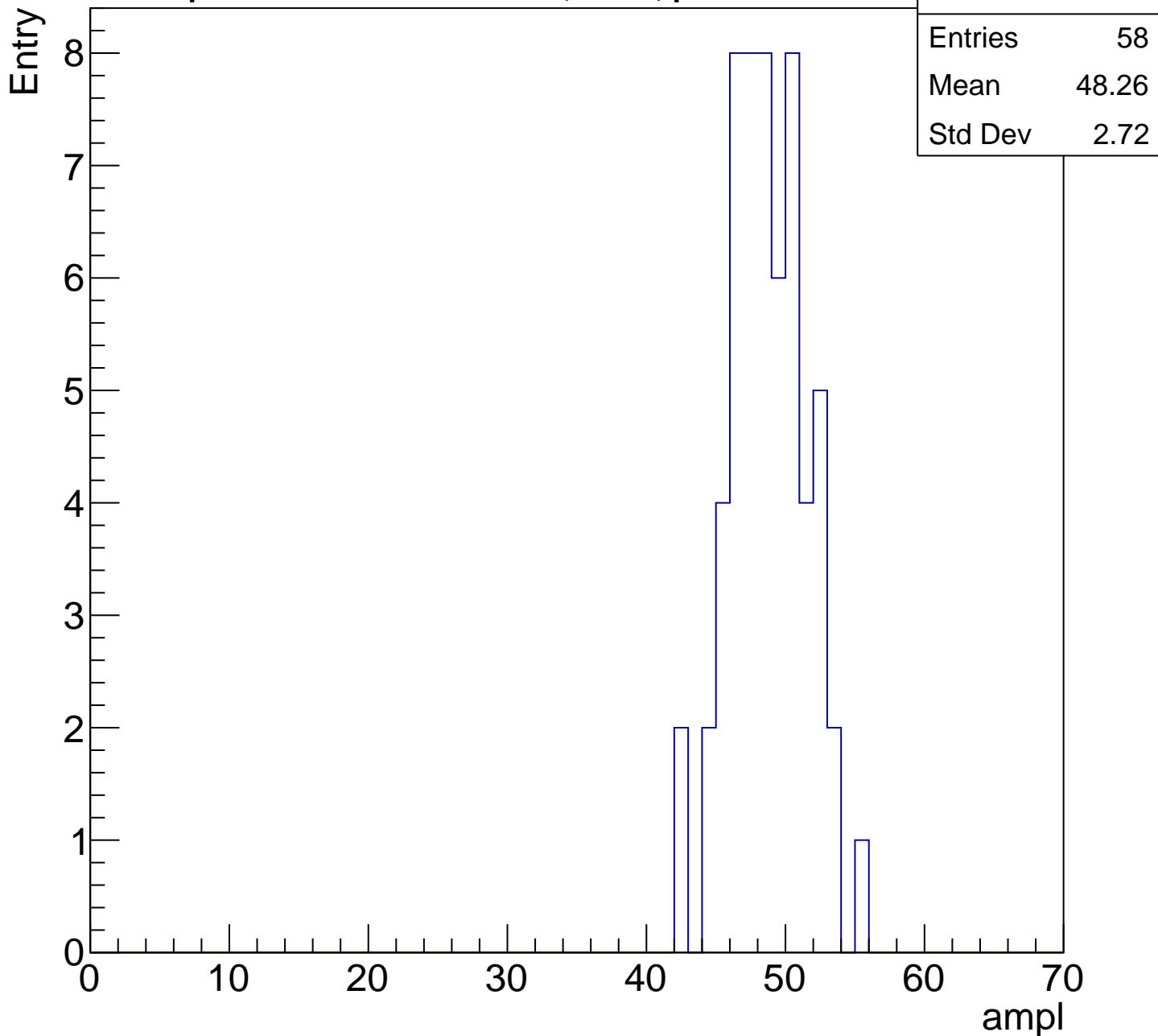
Gaus mean : 42.1932

Gaus Width: 3.2024



B0L002S, U4-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

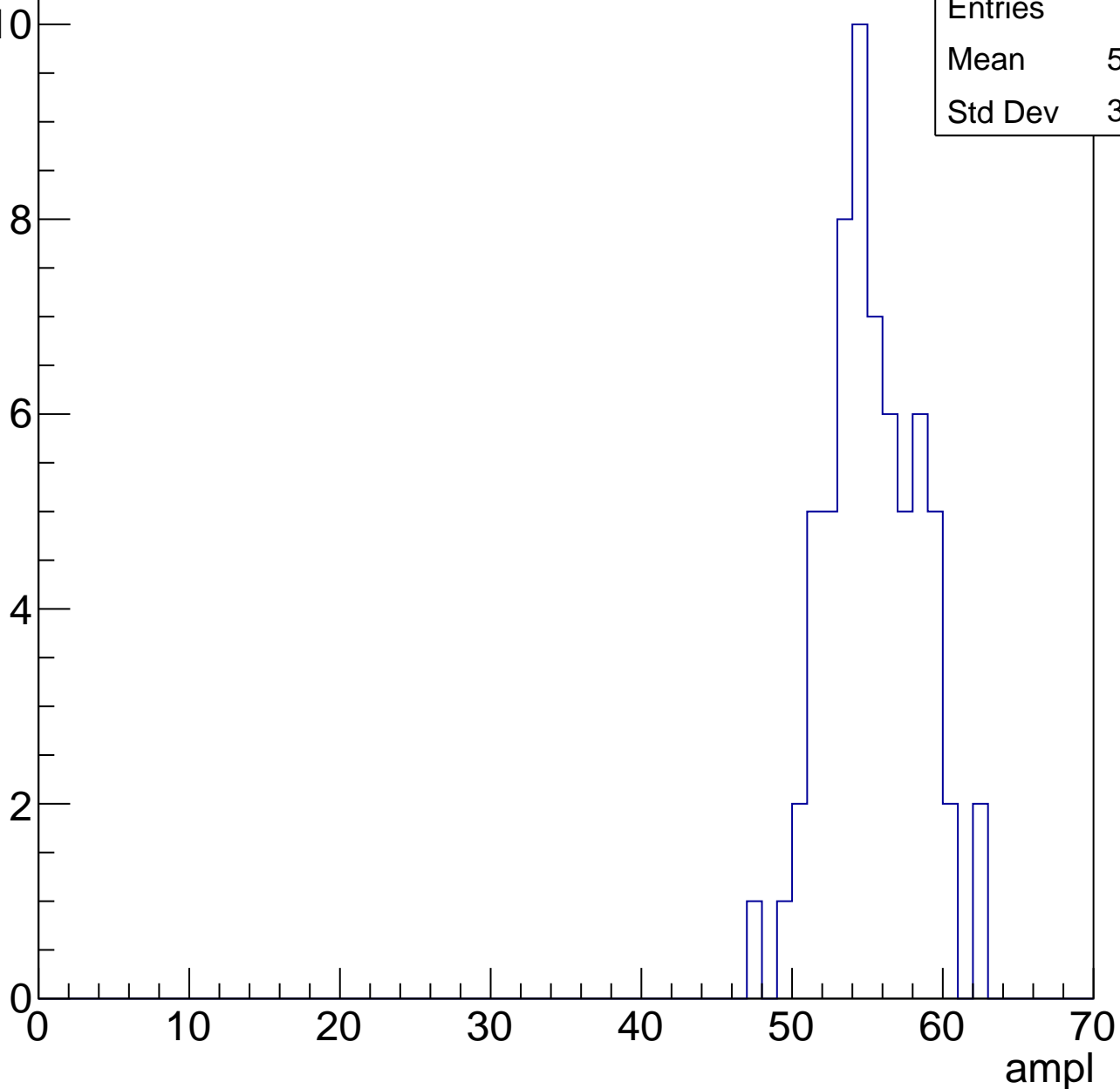


B0L002S, U4-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	54.89
Std Dev	3.099

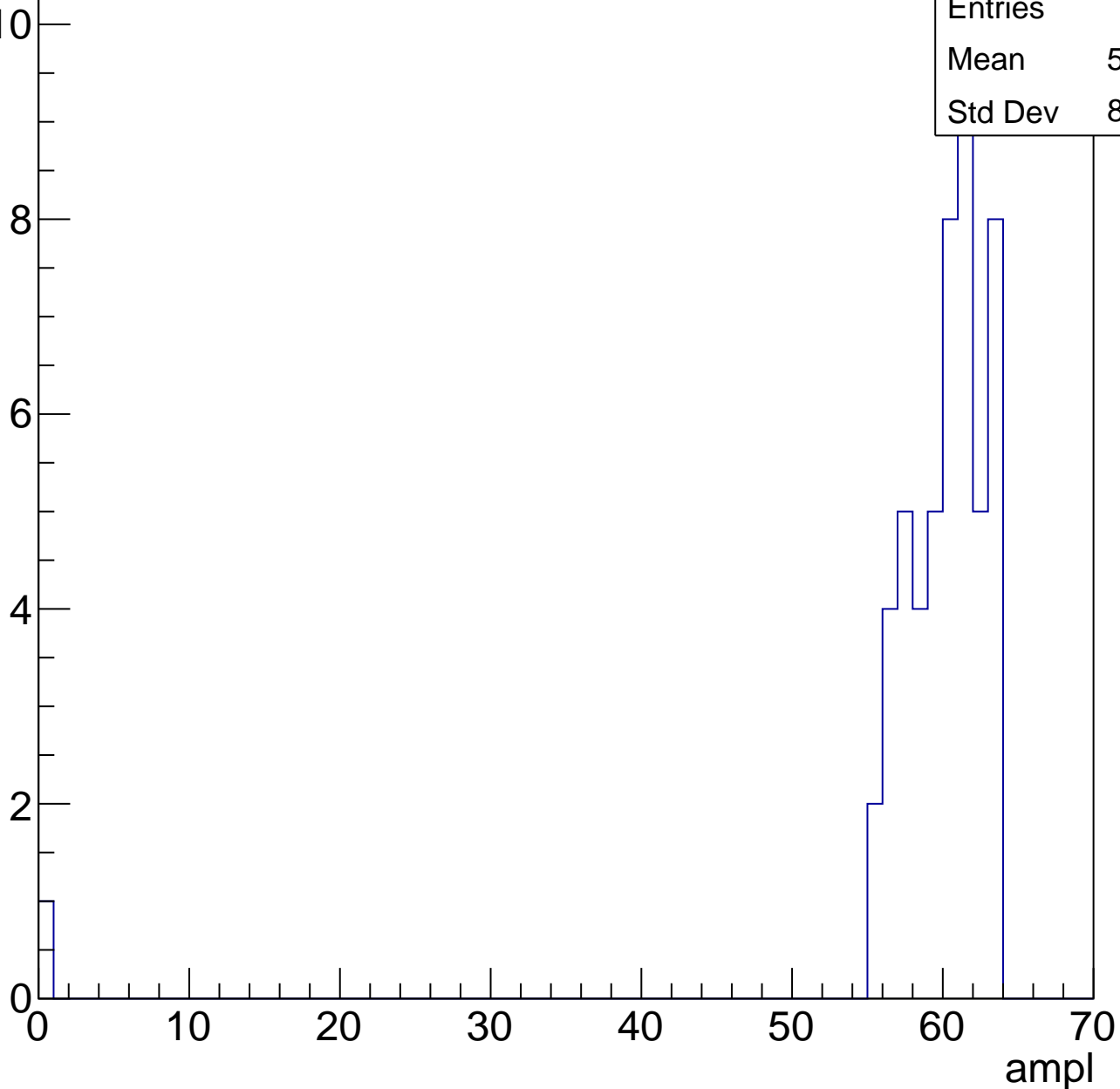


B0L002S, U4-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	58.65
Std Dev	8.535



B0L002S, U4-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch20, adc0

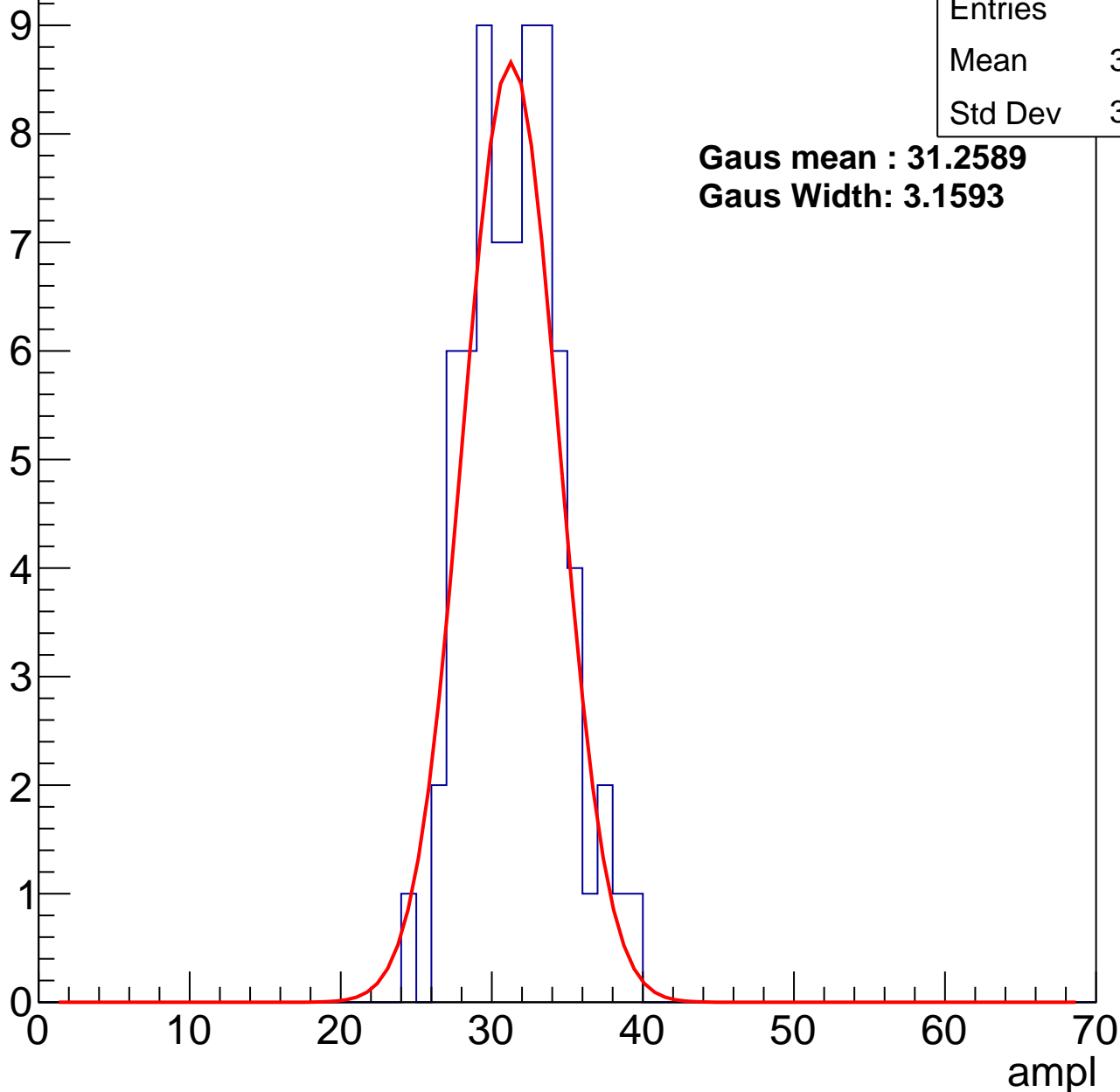
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.13
Std Dev	3.058

Gaus mean : 31.2589

Gaus Width: 3.1593



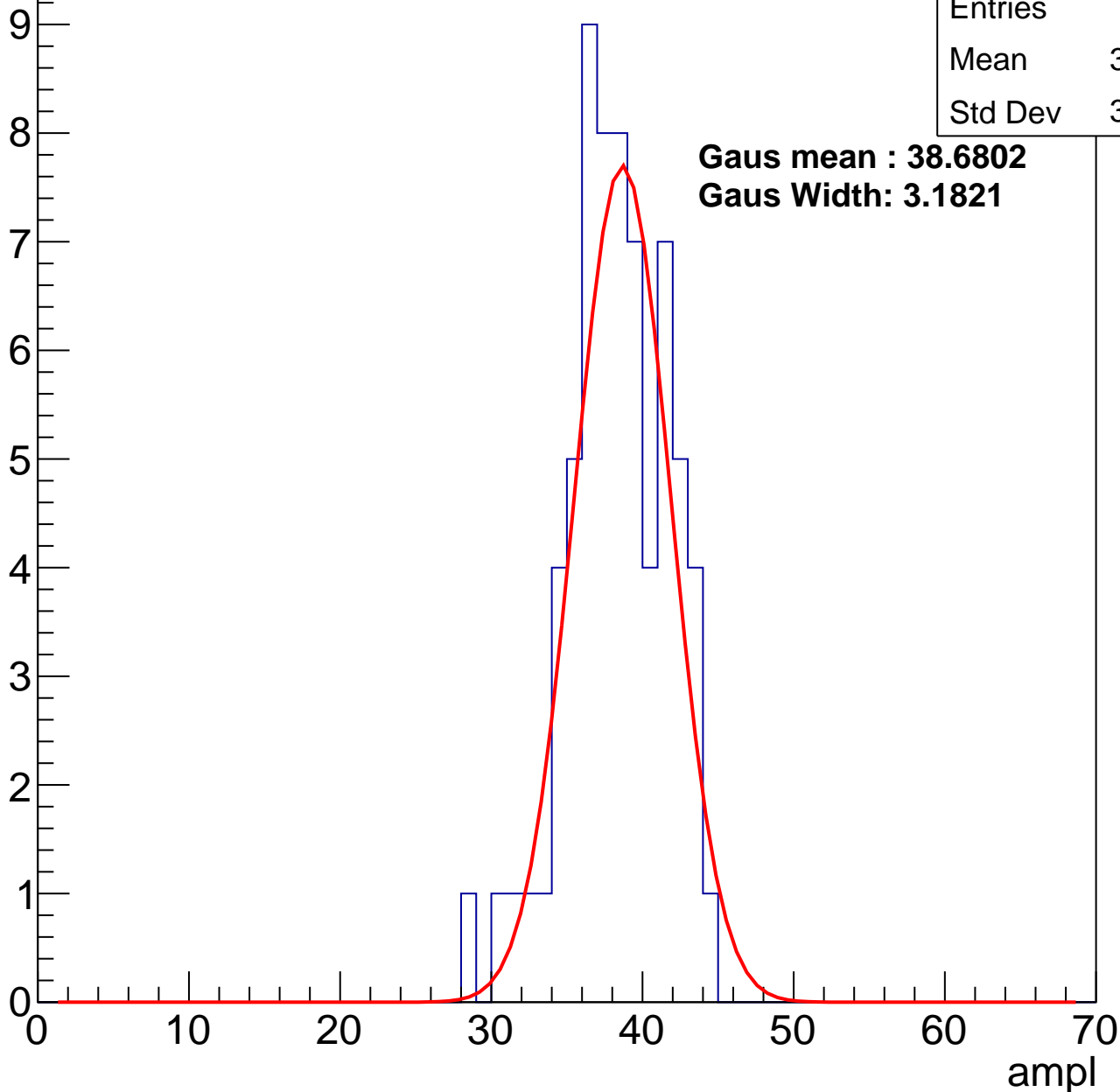
B0L002S, U4-ch20, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.84
Std Dev	3.294

Gaus mean : 38.6802
Gaus Width: 3.1821



B0L002S, U4-ch20, adc2

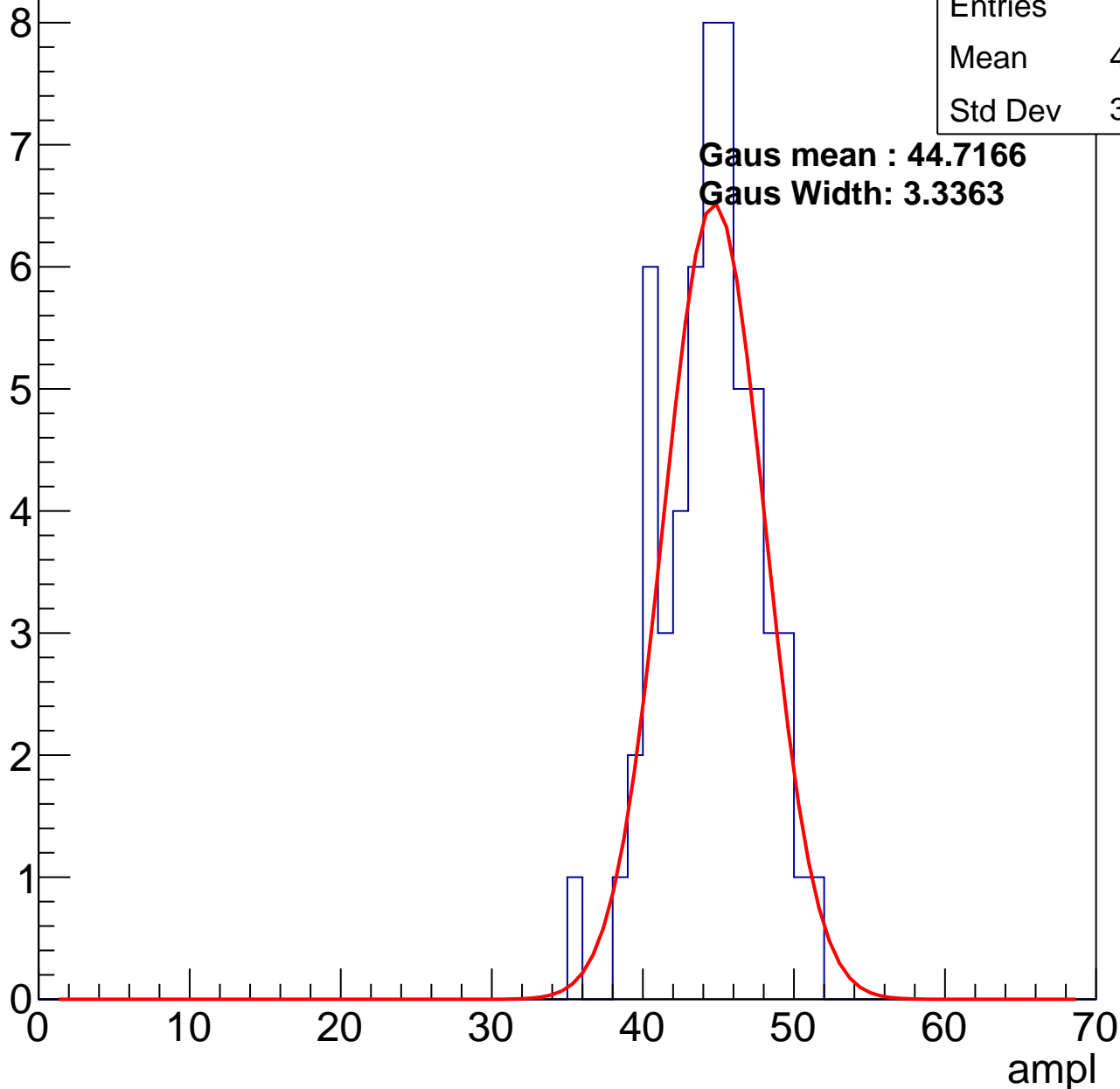
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.02
Std Dev	3.225

Gaus mean : 44.7166

Gaus Width: 3.3363

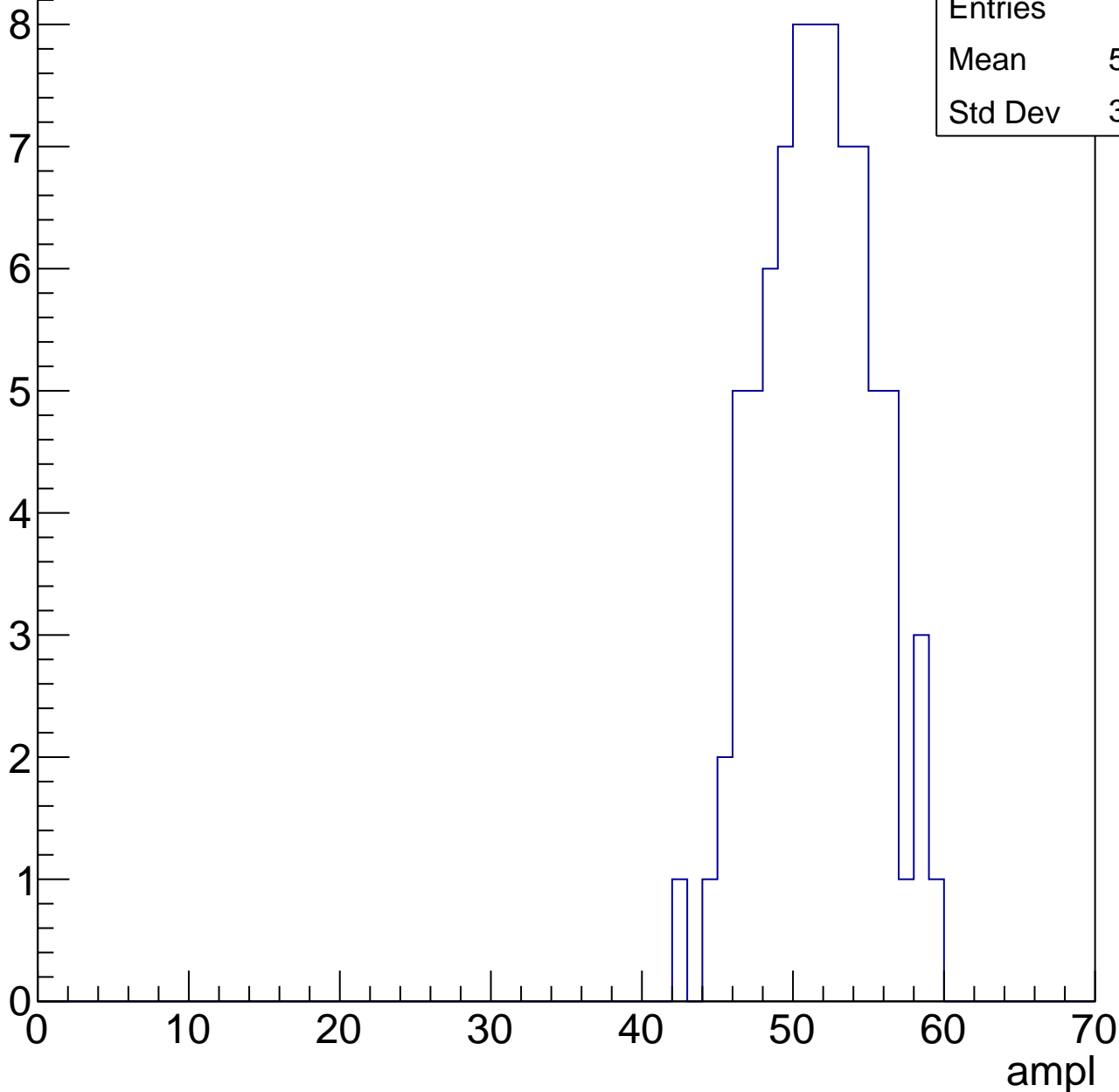


B0L002S, U4-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

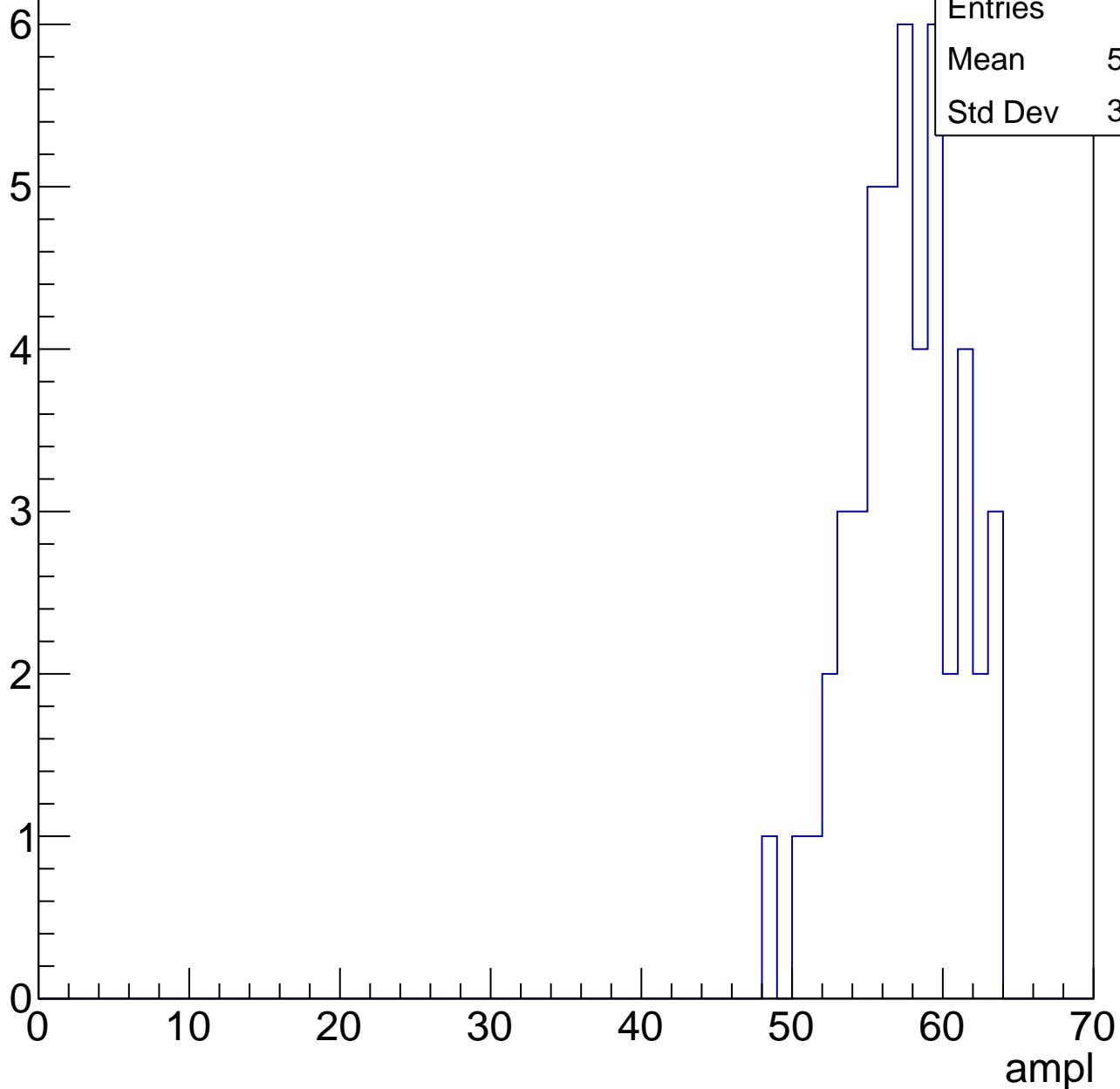
Entries	80
Mean	51.12
Std Dev	3.617



B0L002S, U4-ch20, adc4

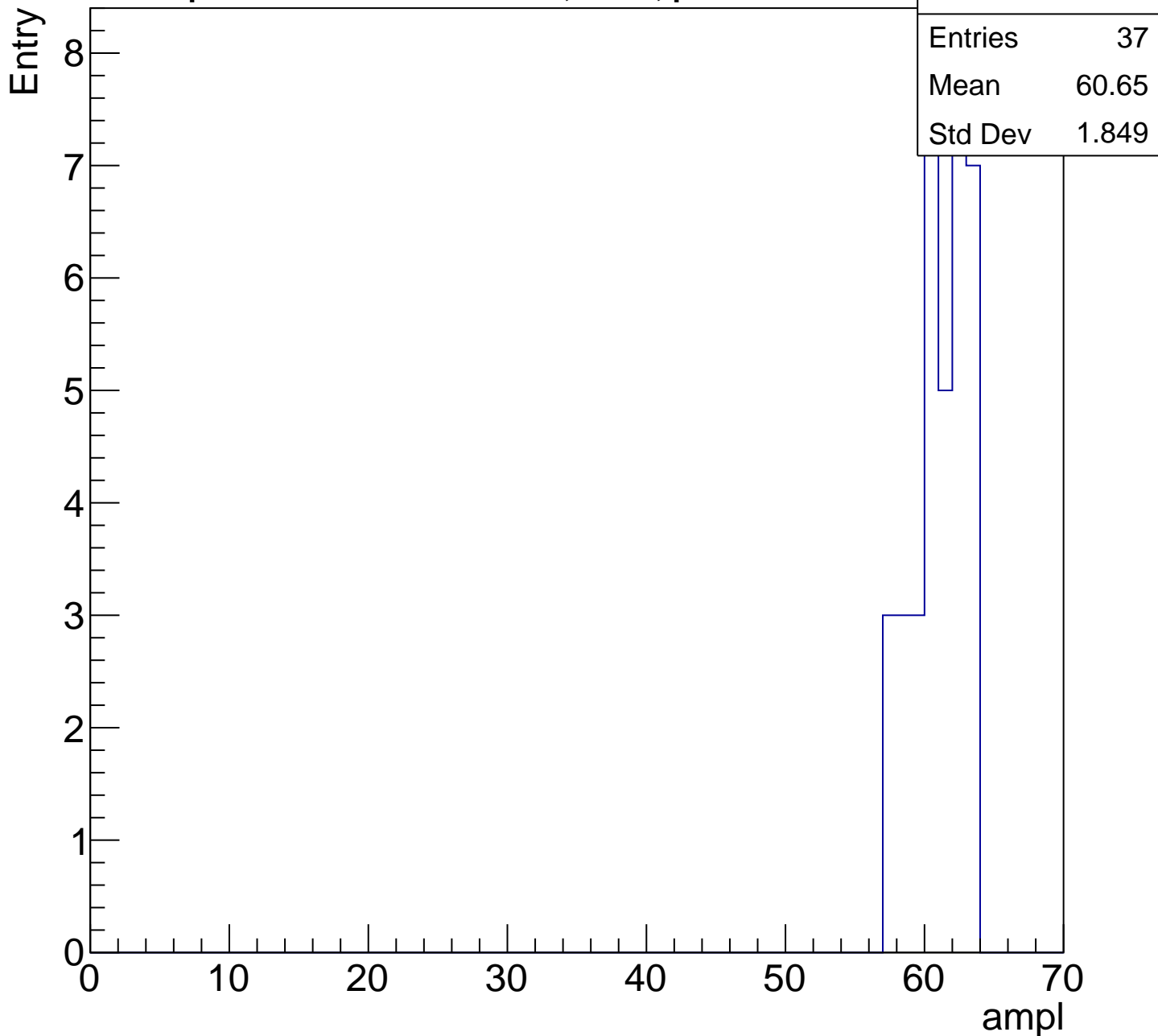
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch20, adc5

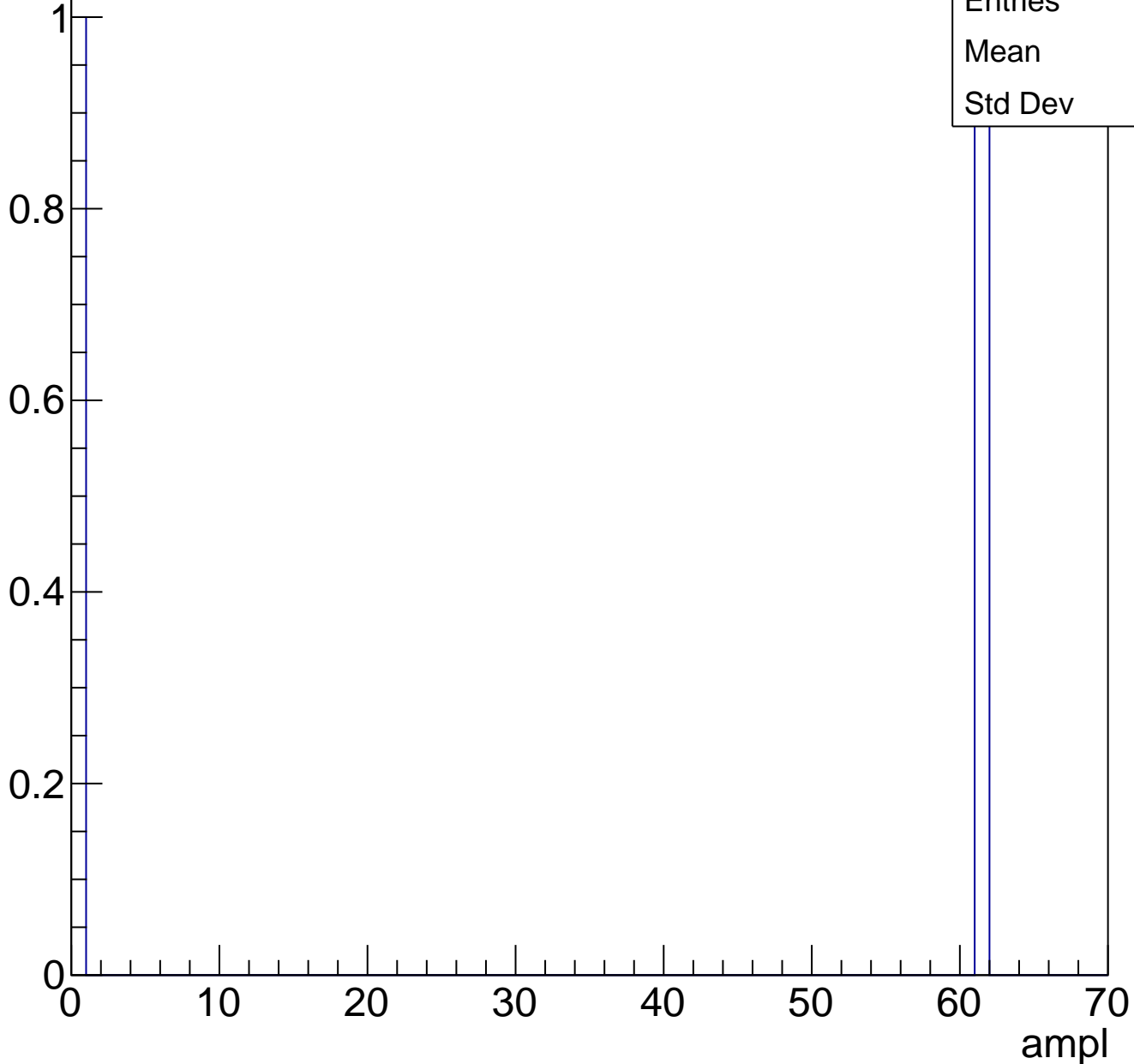
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch21, adc0

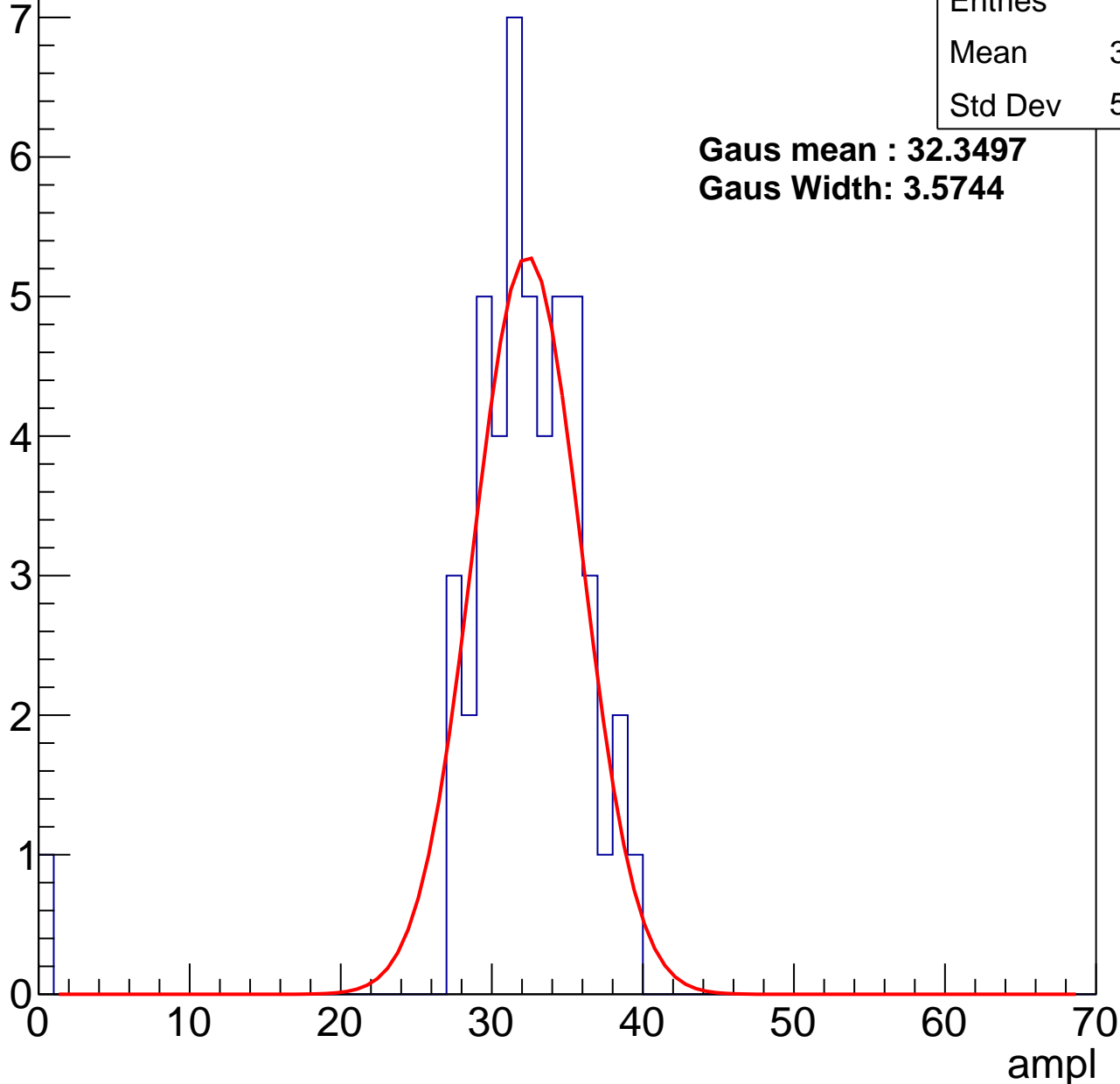
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	31.58
Std Dev	5.503

Gaus mean : 32.3497

Gaus Width: 3.5744



B0L002S, U4-ch21, adc1

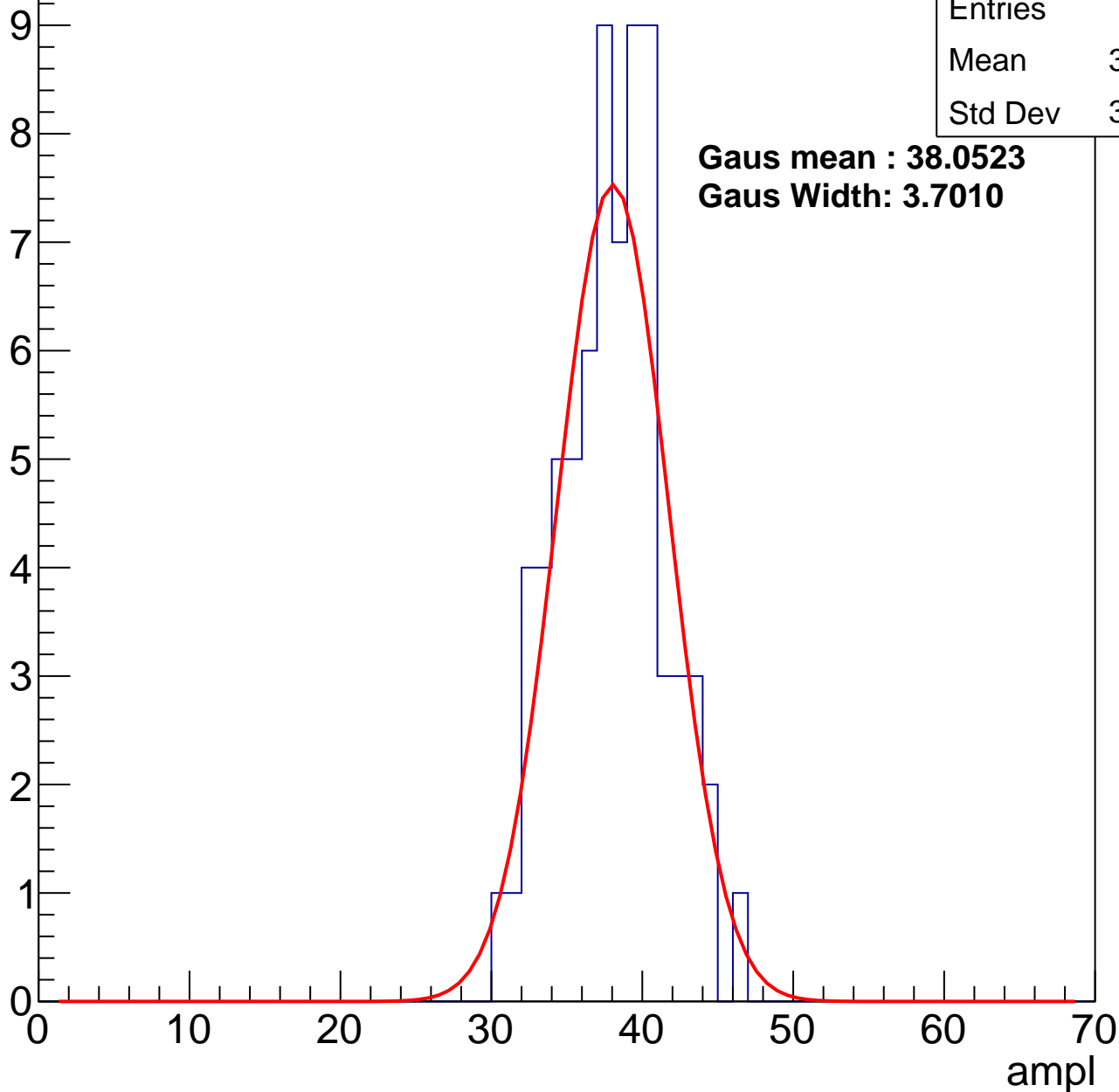
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	37.56
Std Dev	3.399

Gaus mean : 38.0523

Gaus Width: 3.7010



B0L002S, U4-ch21, adc2

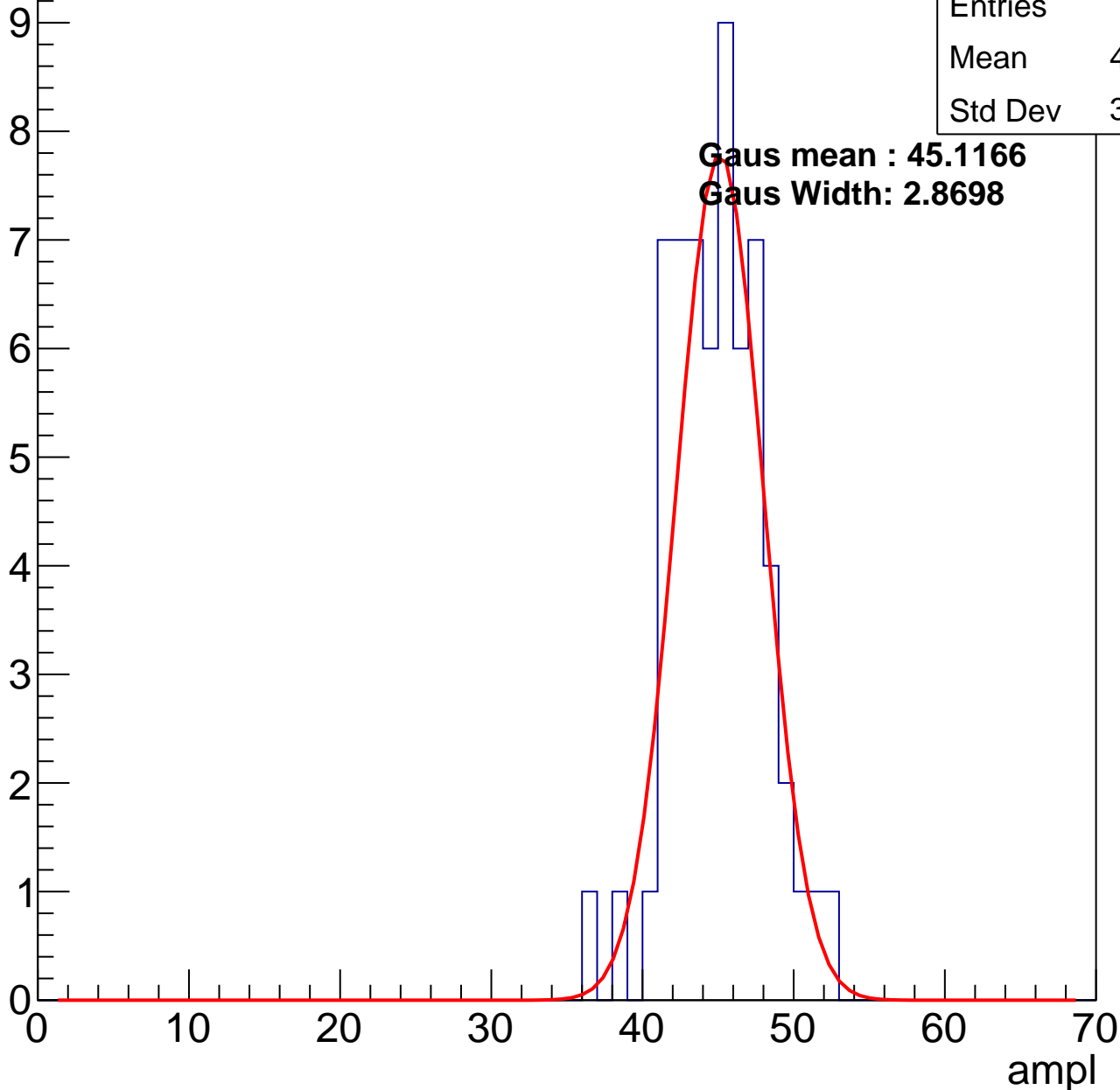
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.48
Std Dev	3.028

Gaus mean : 45.1166

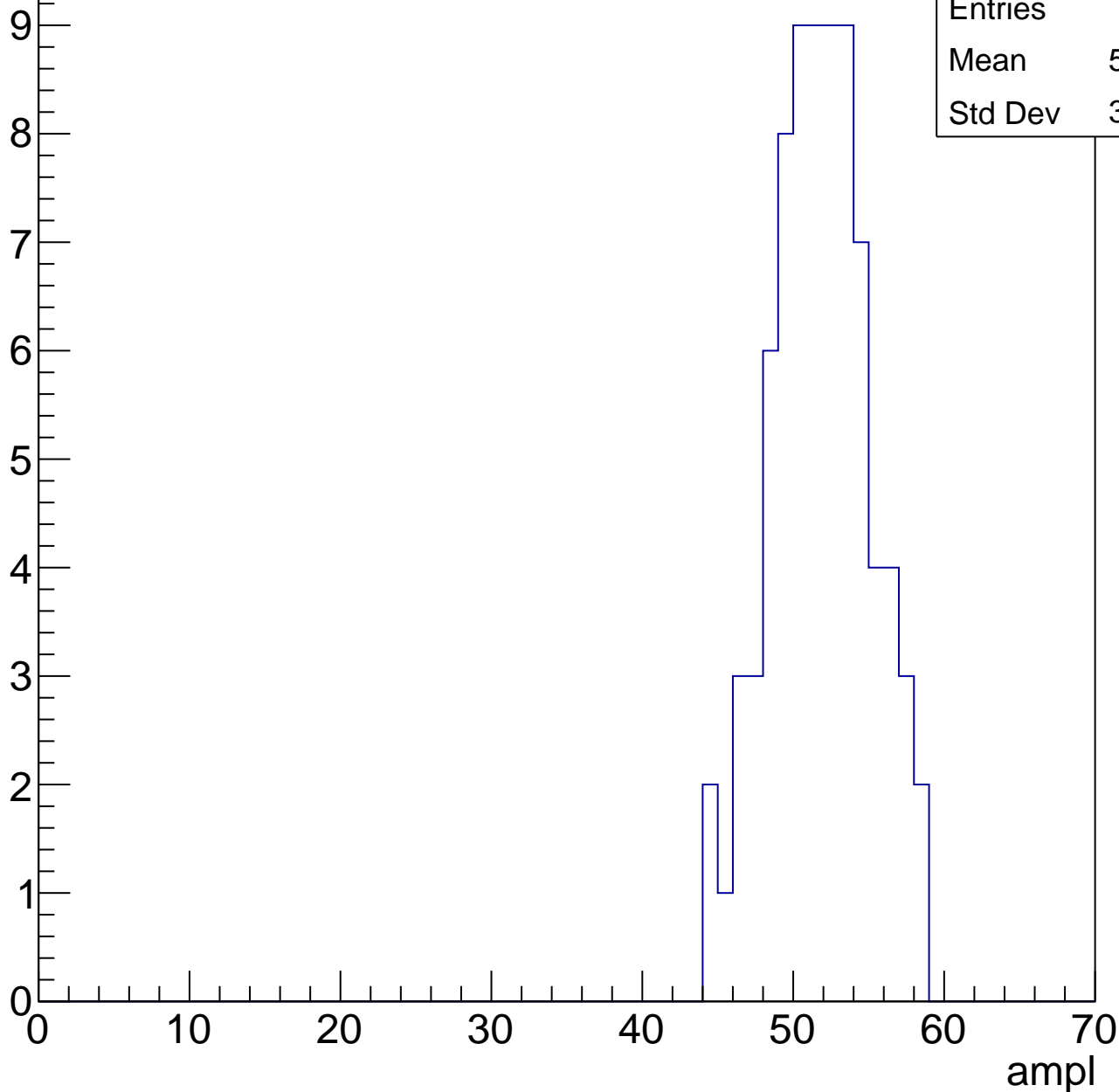
Gaus Width: 2.8698



B0L002S, U4-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

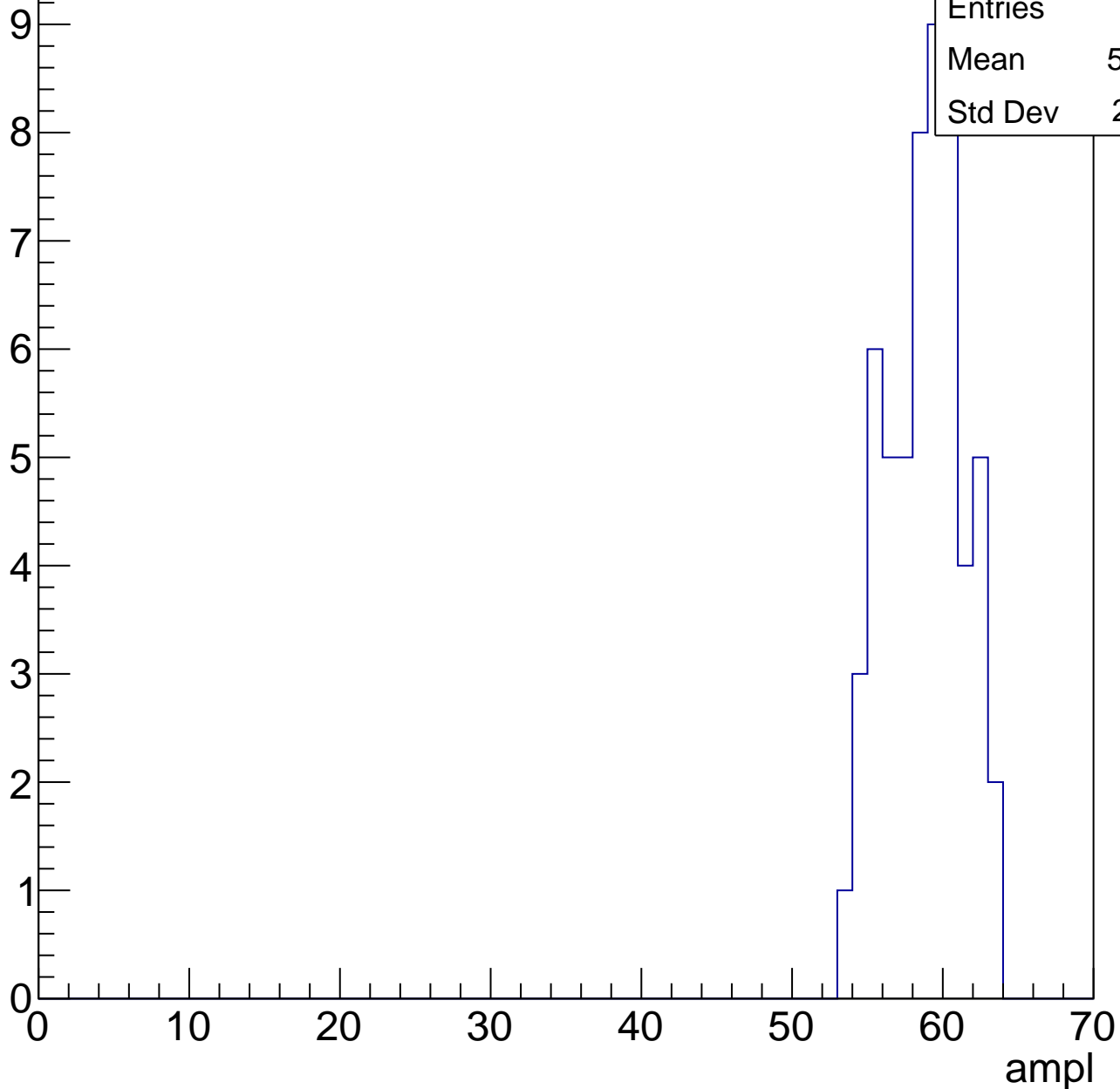


Entries	79
Mean	51.33
Std Dev	3.225

B0L002S, U4-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

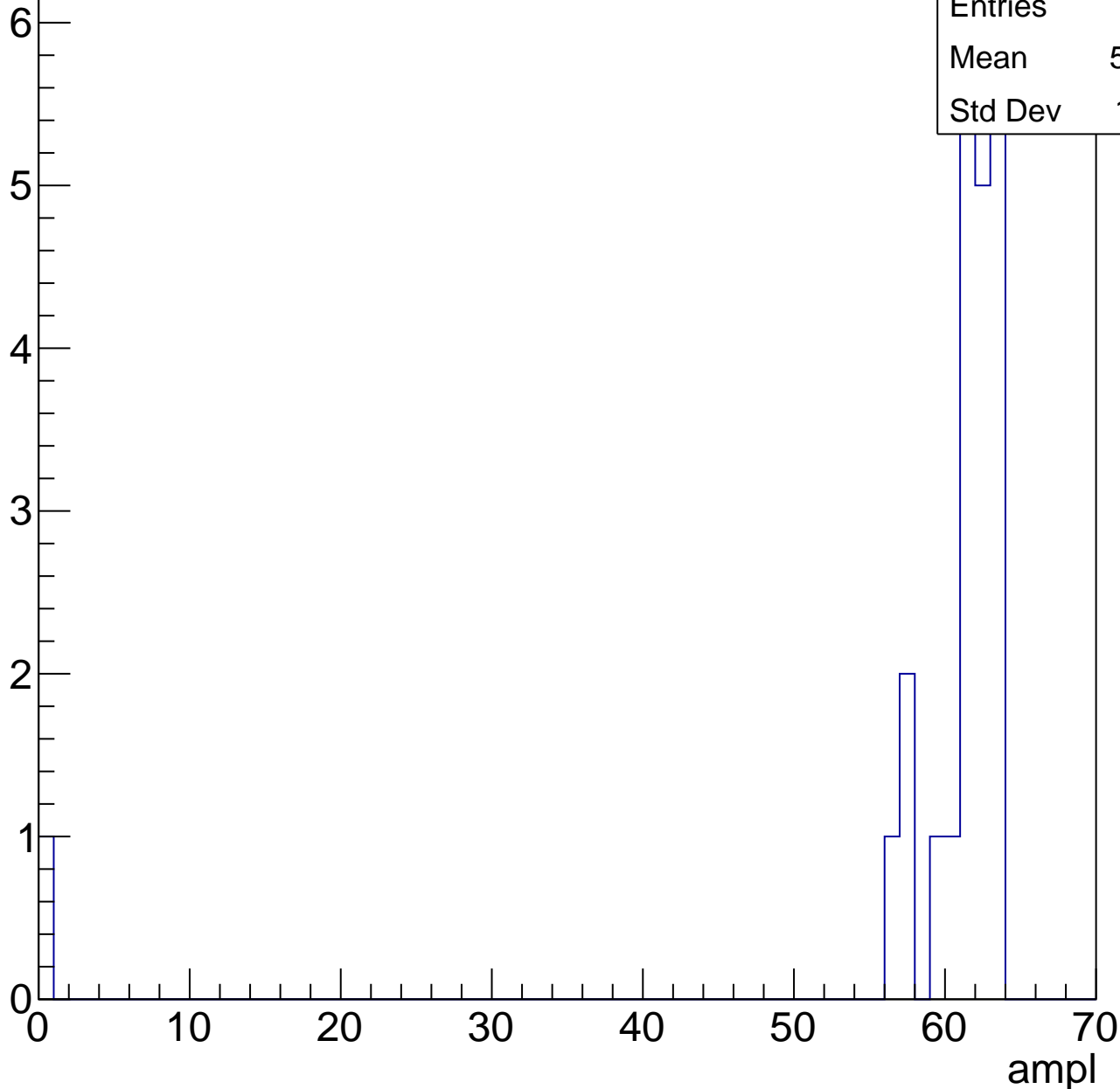


B0L002S, U4-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.39
Std Dev	12.61



B0L002S, U4-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	63
Std Dev	0

B0L002S, U4-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch22, adc0

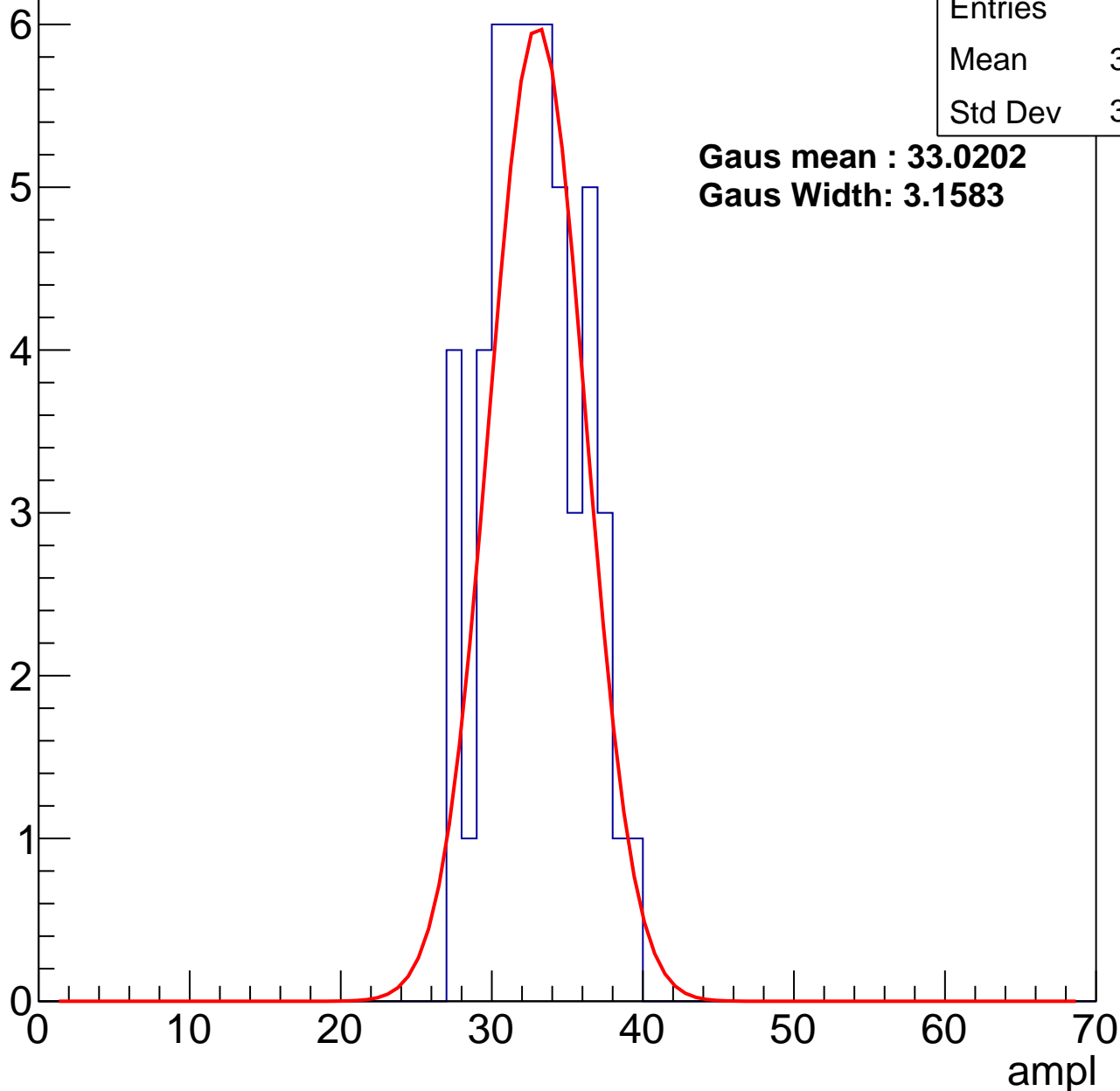
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	32.37
Std Dev	3.029

Gaus mean : 33.0202

Gaus Width: 3.1583



B0L002S, U4-ch22, adc1

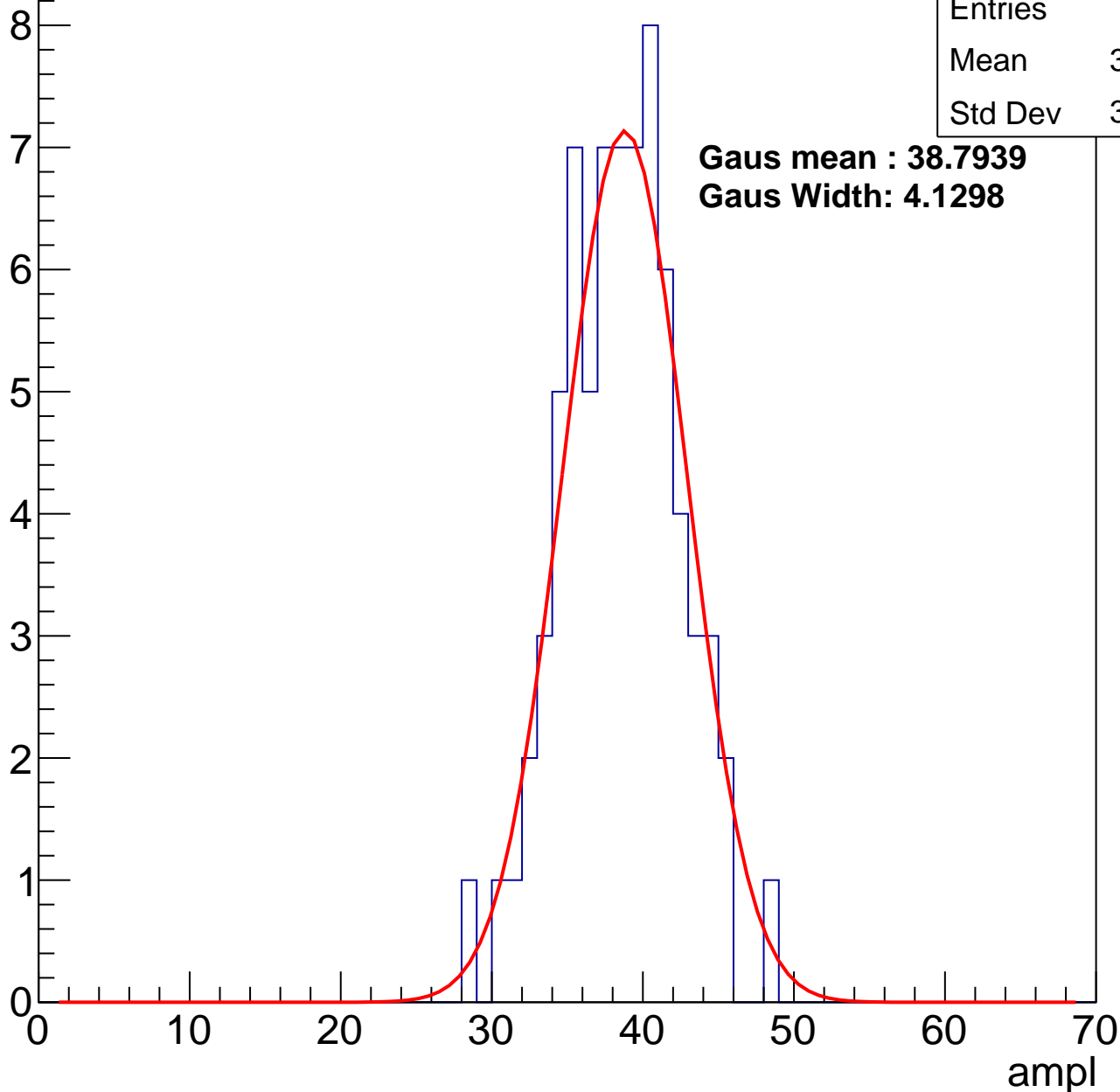
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	38.05
Std Dev	3.825

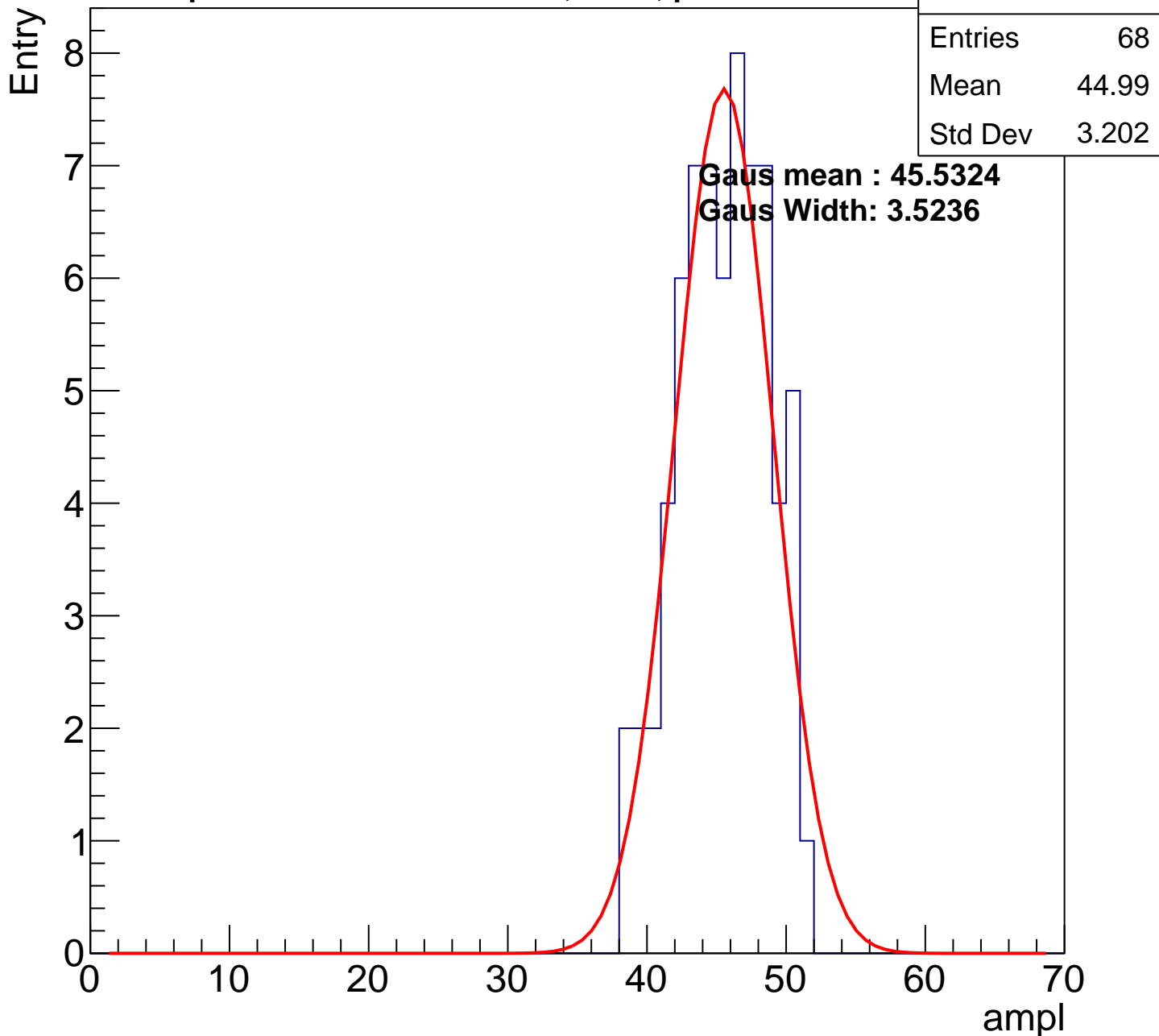
Gaus mean : 38.7939

Gaus Width: 4.1298



B0L002S, U4-ch22, adc2

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

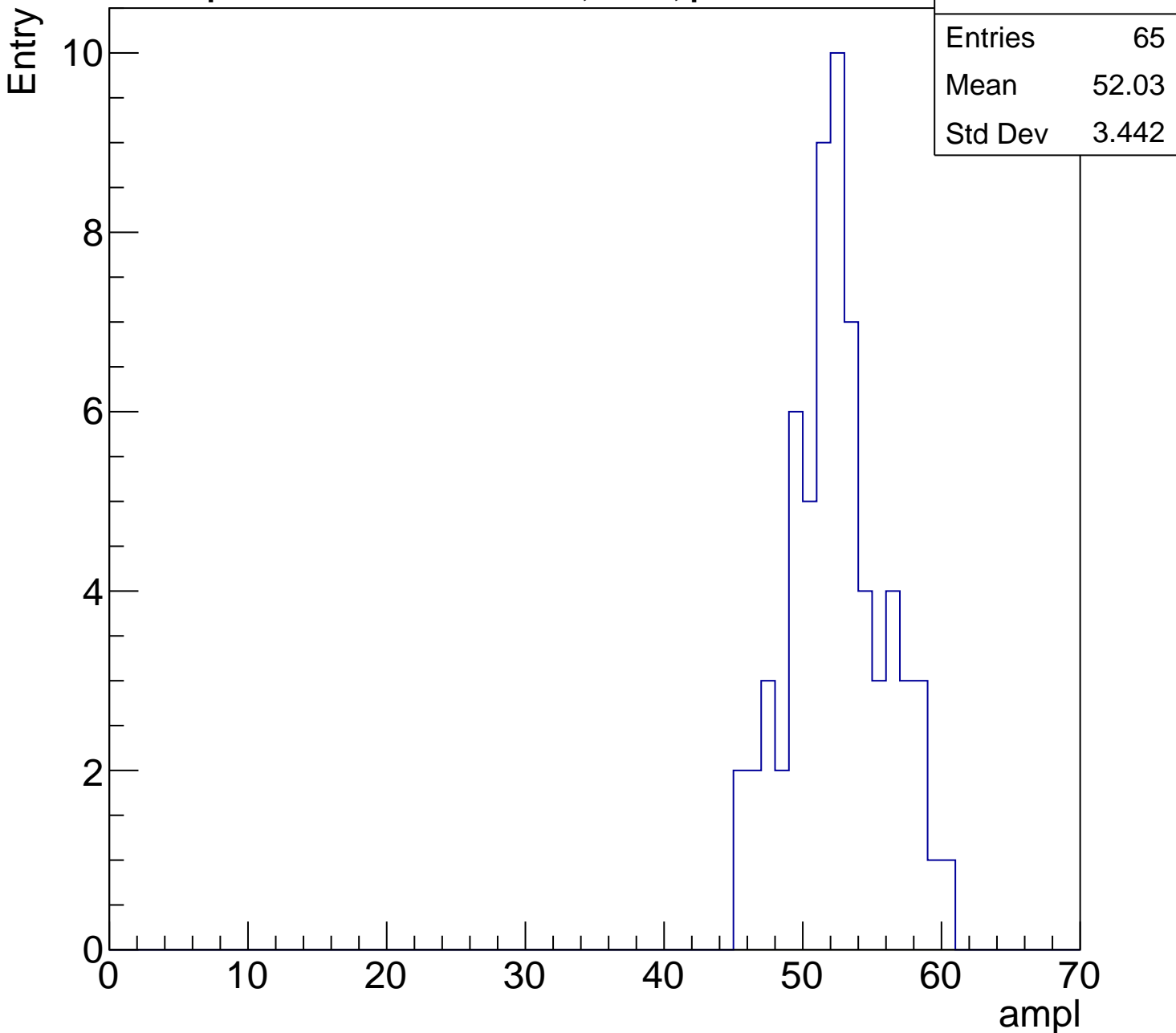
Entries	65
Mean	52.03
Std Dev	3.442

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

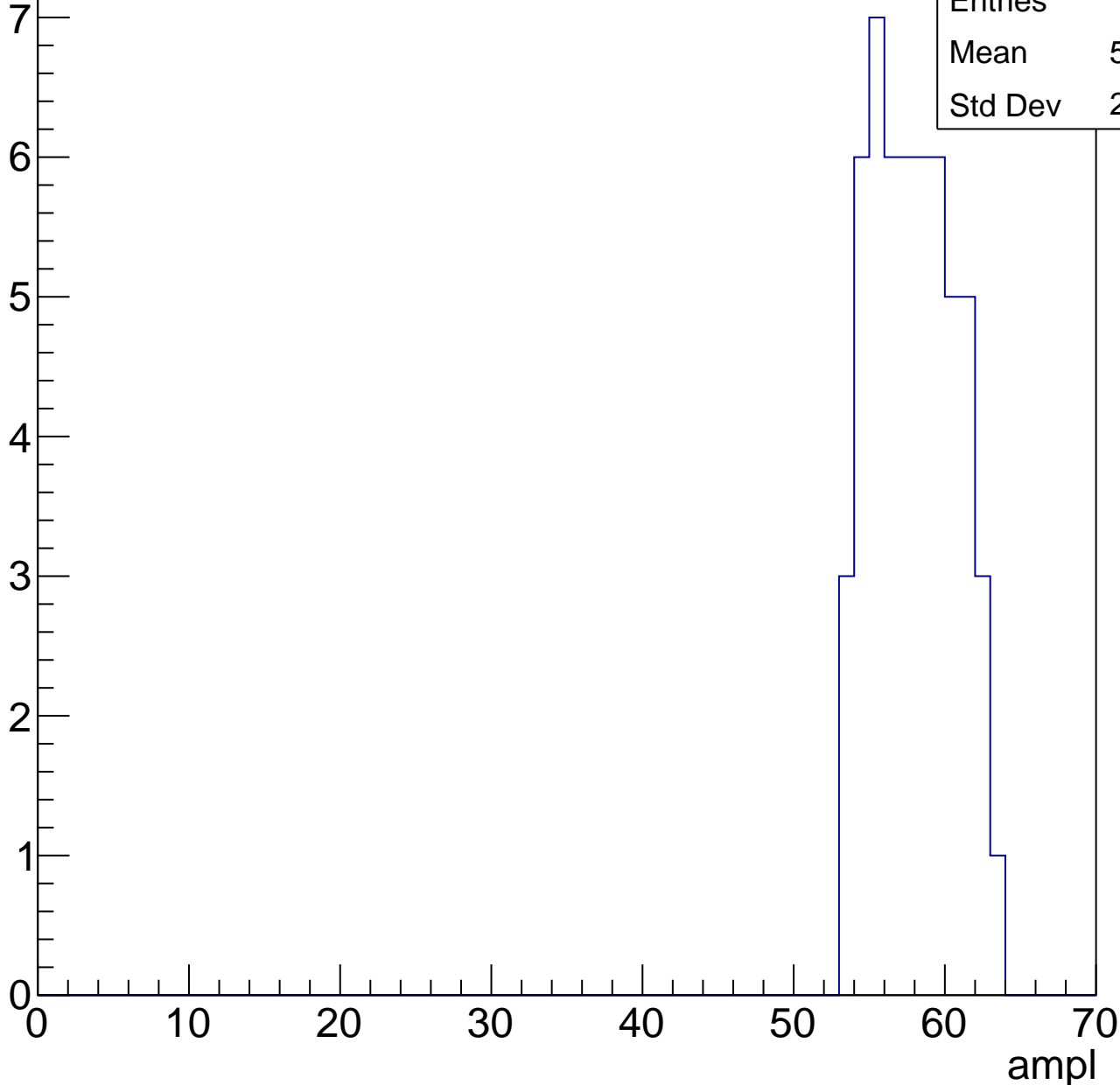


B0L002S, U4-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57.44
Std Dev	2.692

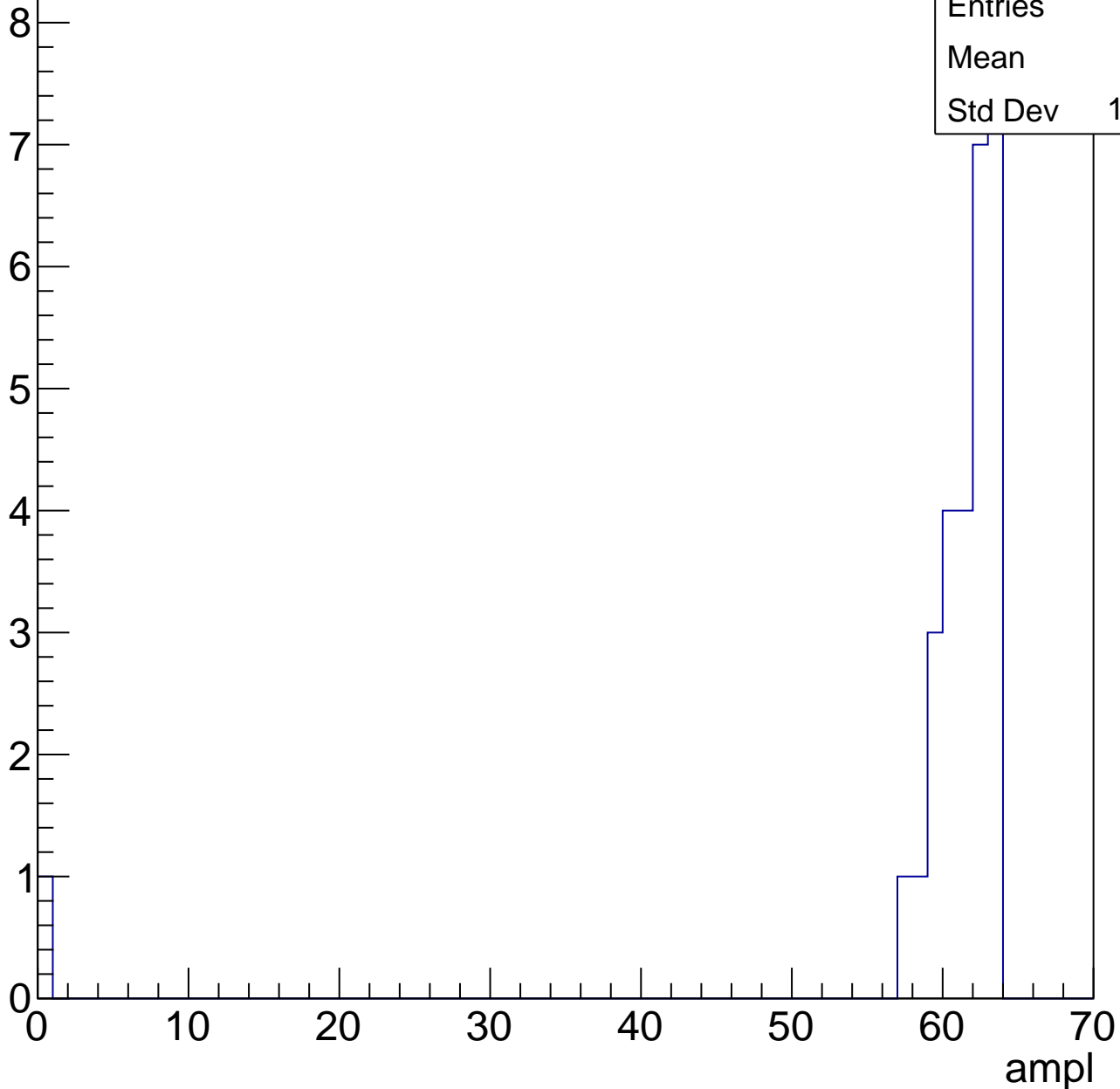


B0L002S, U4-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	59.1
Std Dev	11.29



B0L002S, U4-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch23, adc0

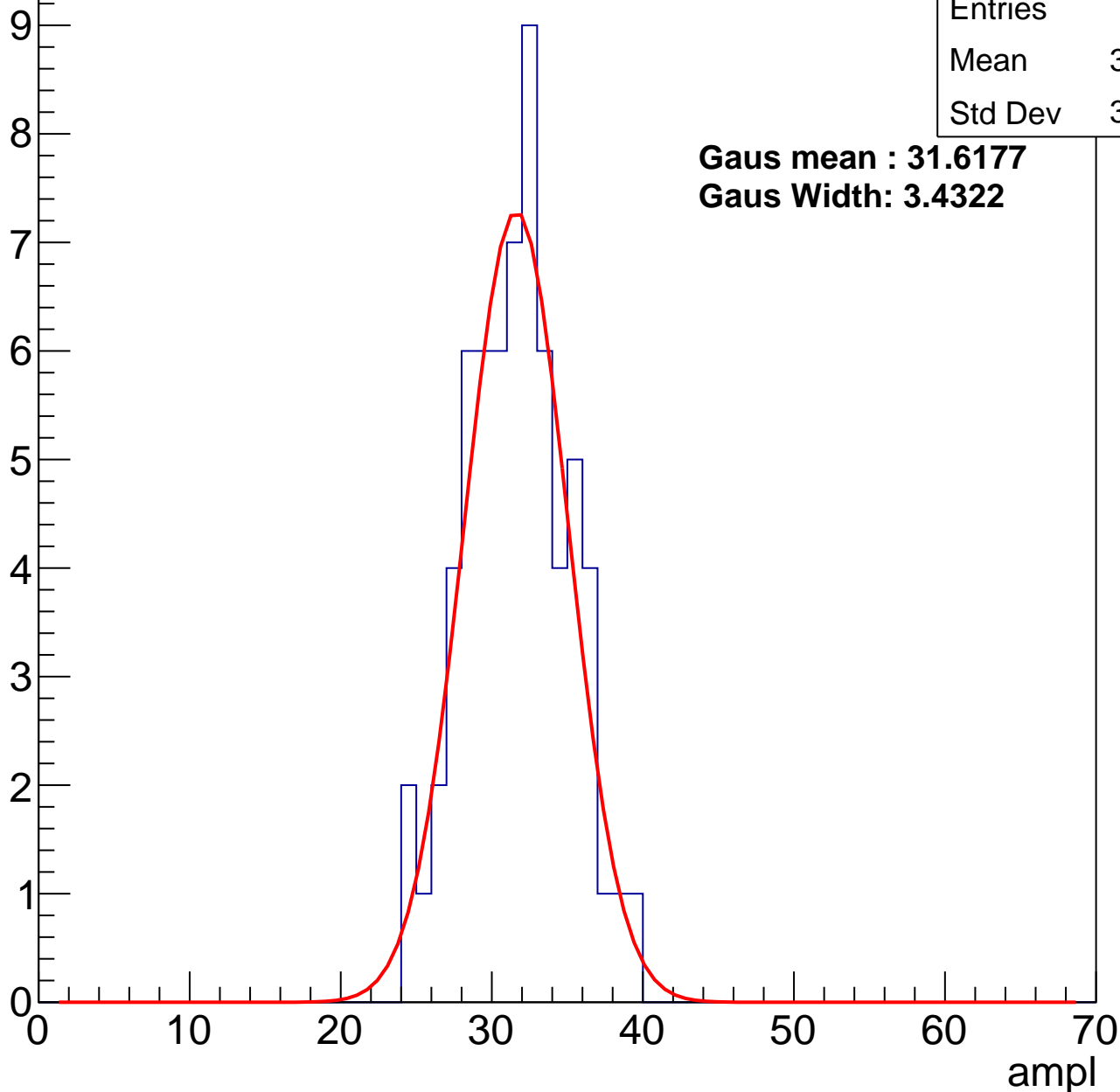
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	31.18
Std Dev	3.346

Gaus mean : 31.6177

Gaus Width: 3.4322



B0L002S, U4-ch23, adc1

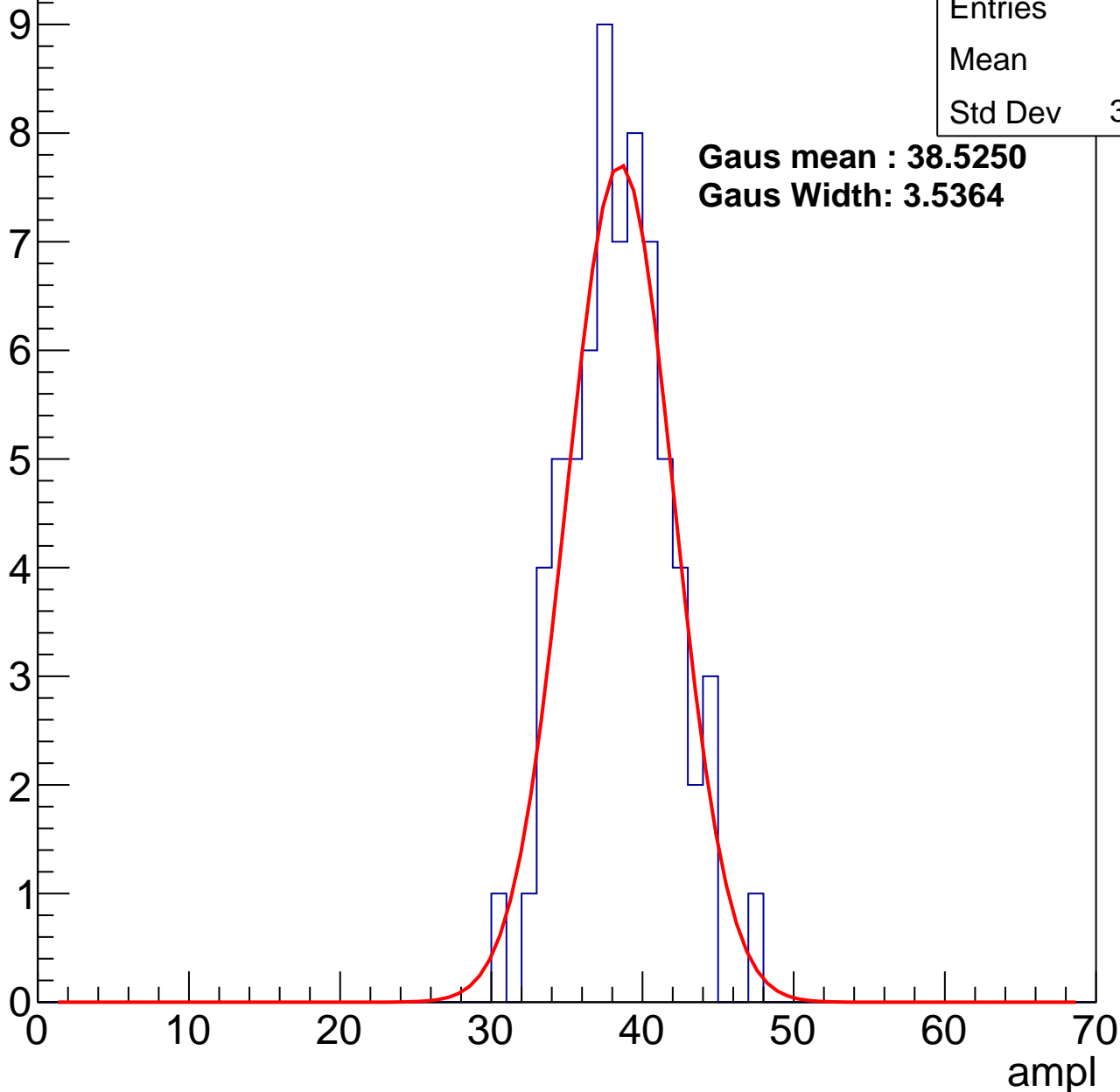
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	38
Std Dev	3.303

Gaus mean : 38.5250

Gaus Width: 3.5364

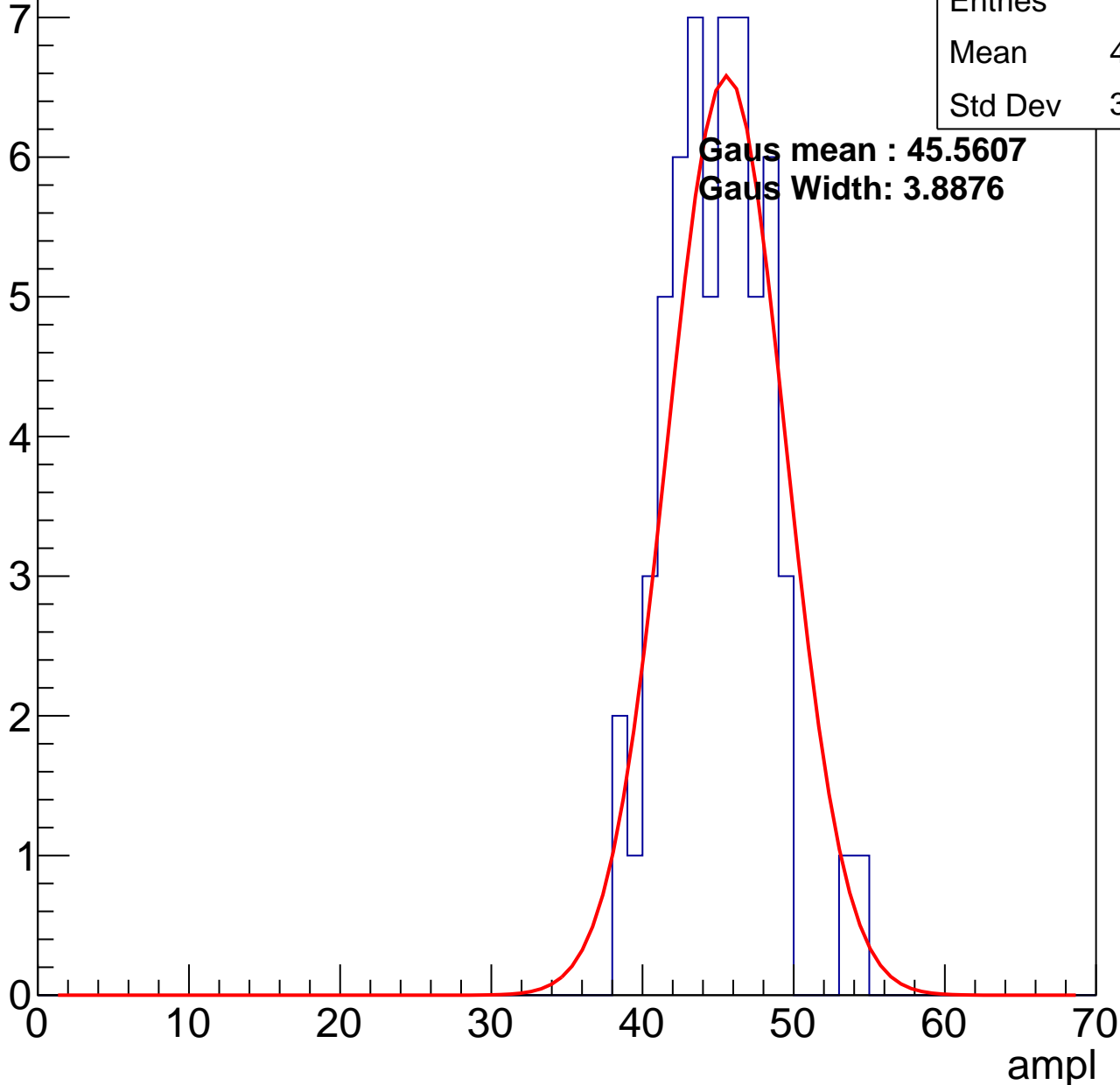


B0L002S, U4-ch23, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

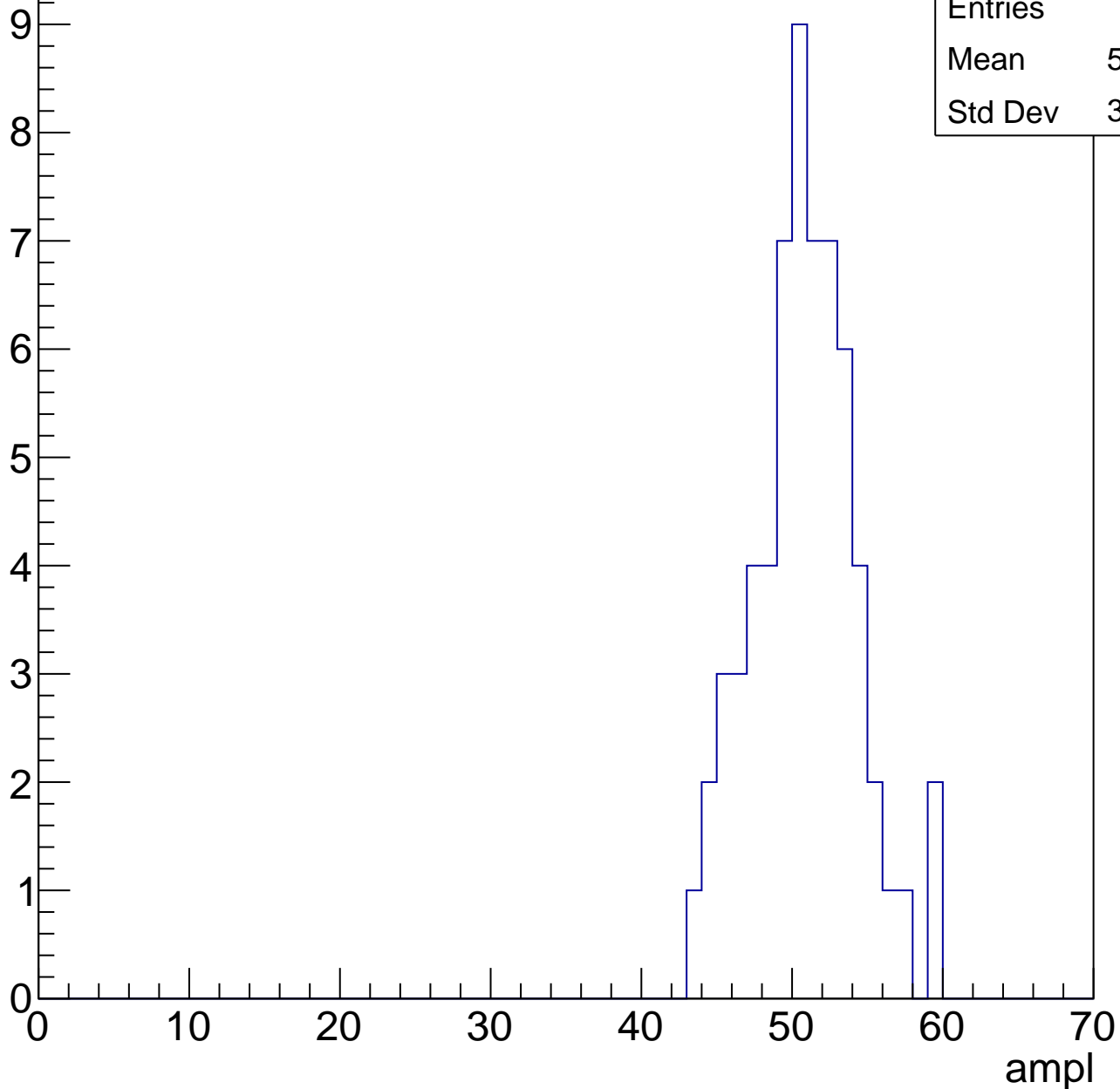
Entries	59
Mean	44.53
Std Dev	3.285



B0L002S, U4-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

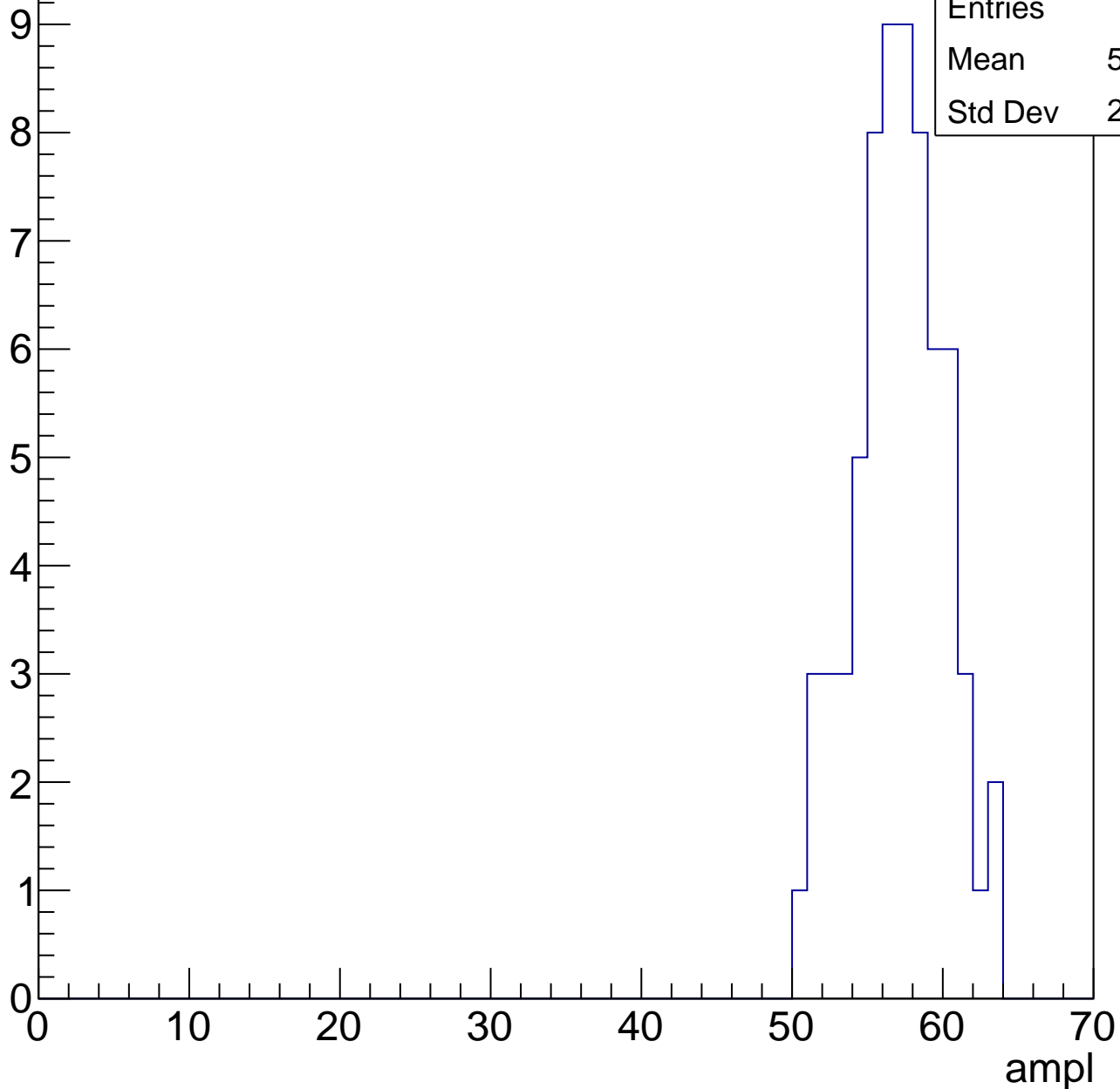


Entries	63
Mean	50.37
Std Dev	3.438

B0L002S, U4-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

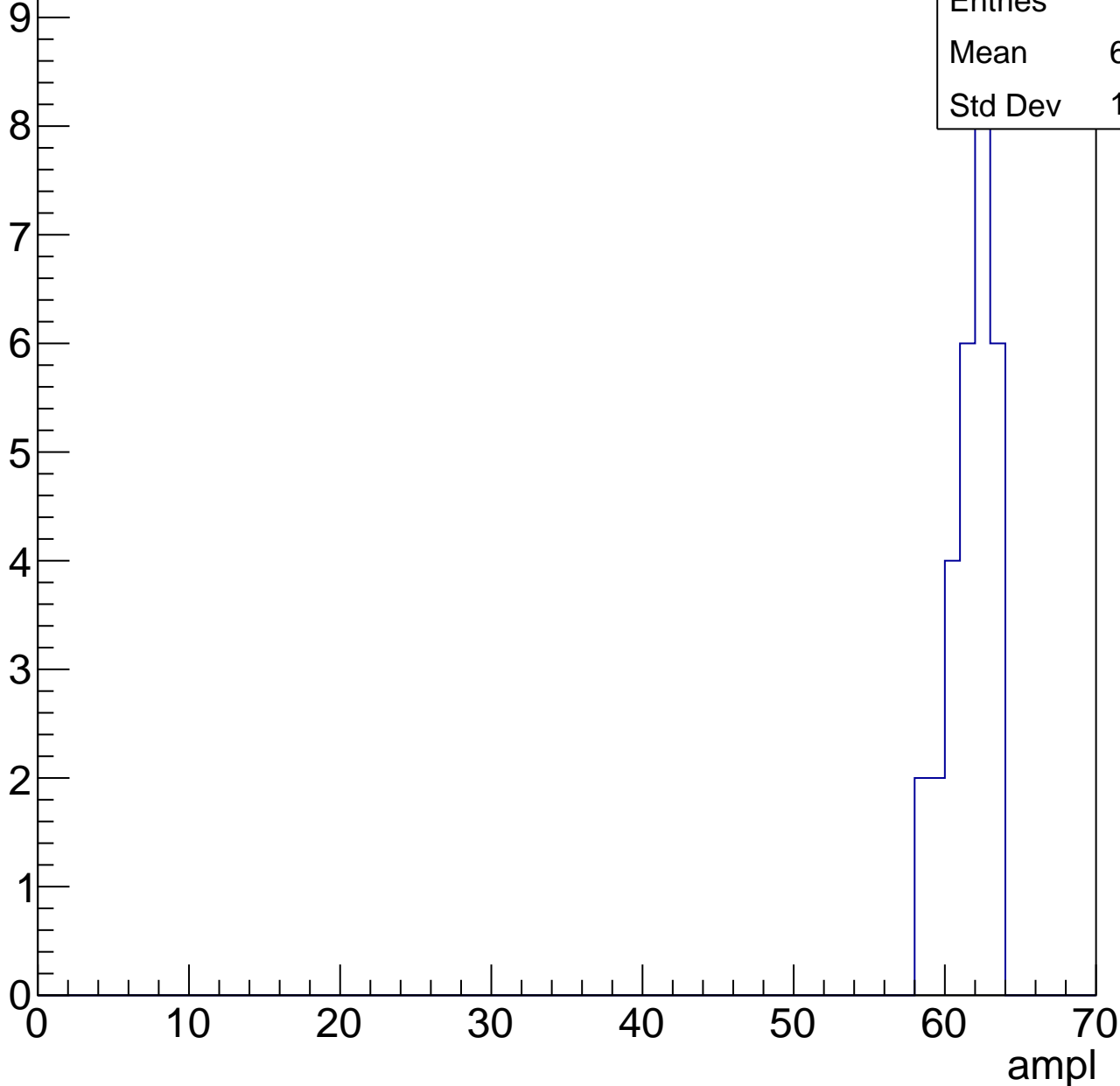


B0L002S, U4-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

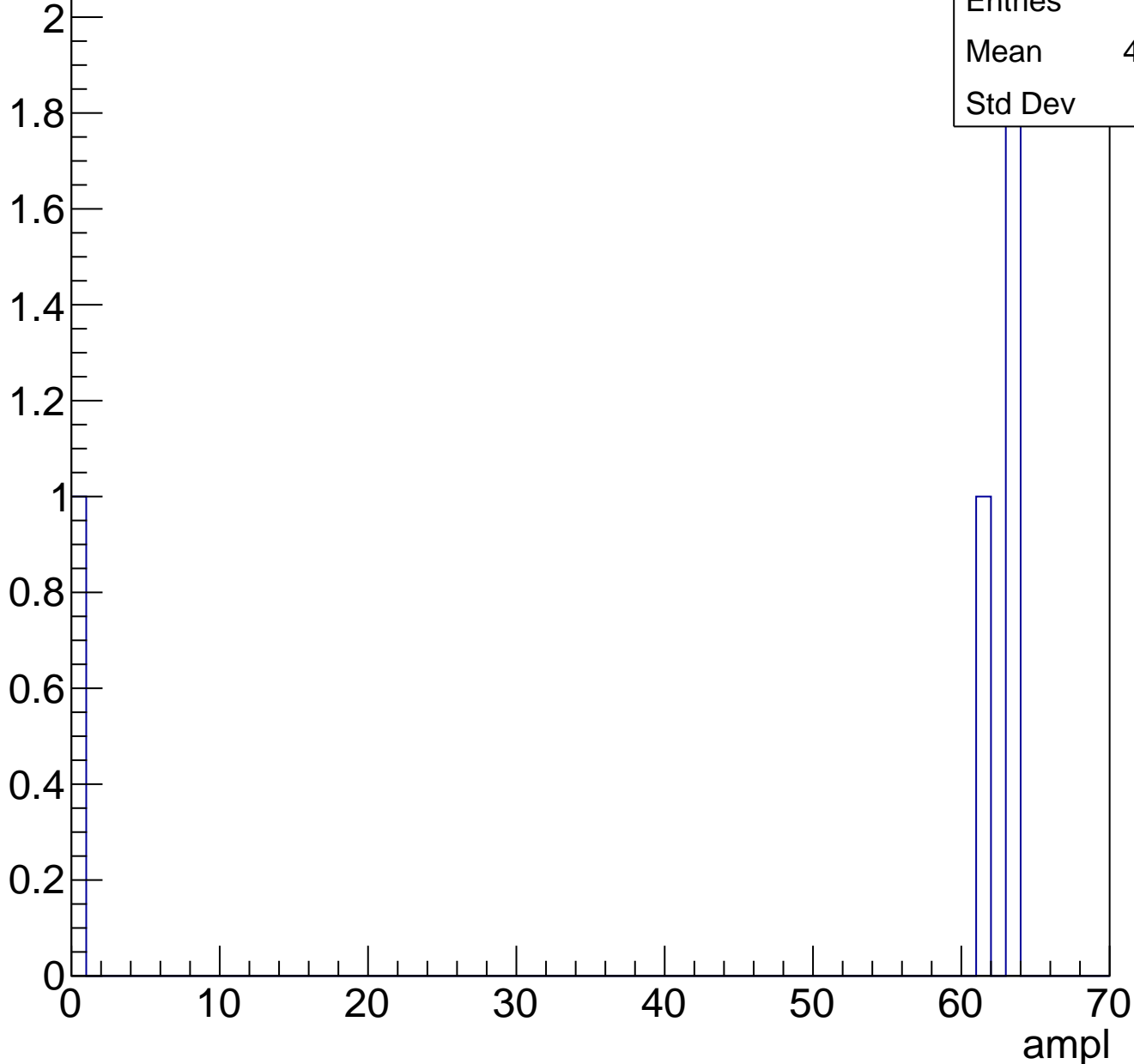
Entries	29
Mean	61.24
Std Dev	1.454



B0L002S, U4-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch24, adc0

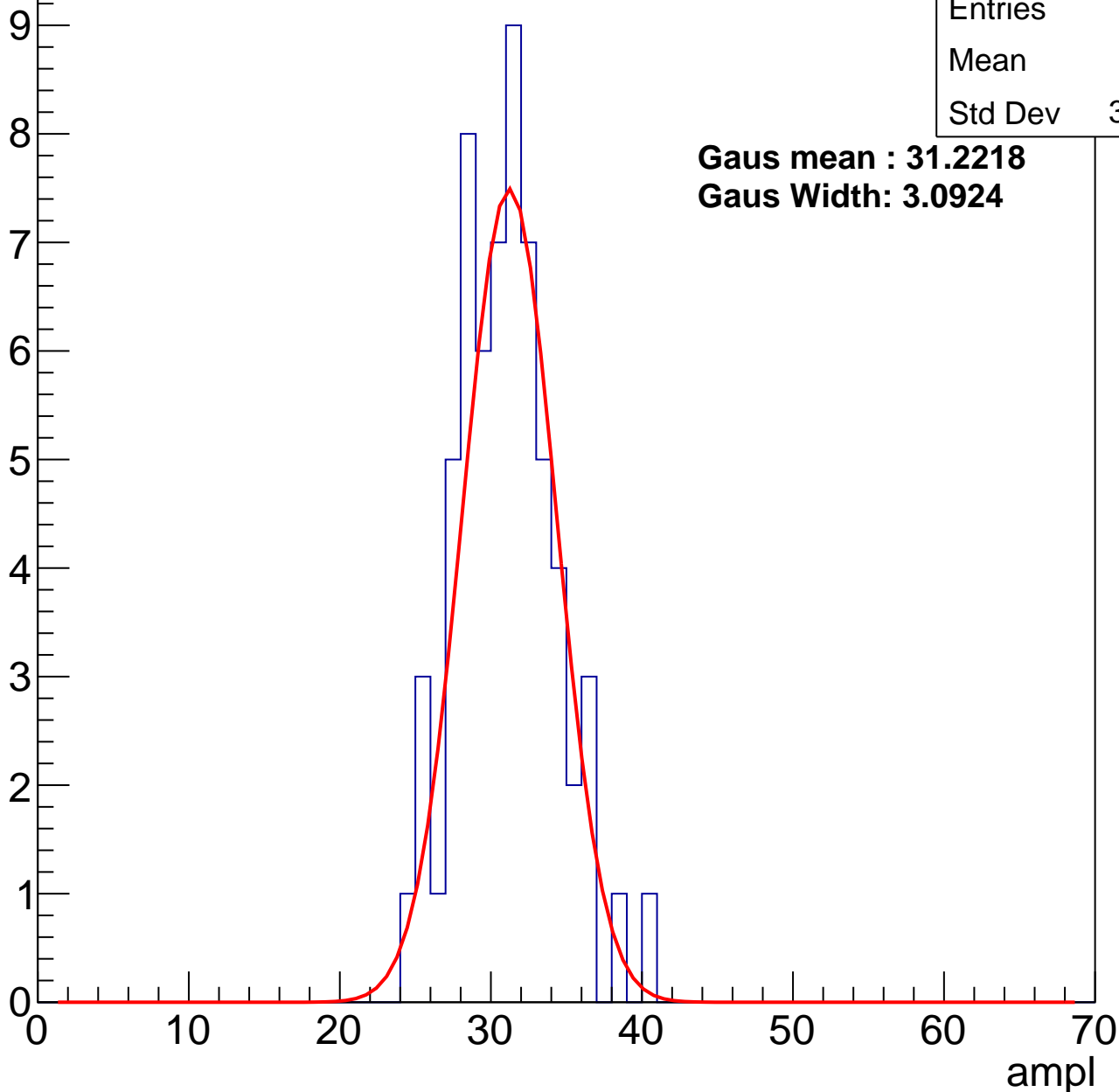
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.6
Std Dev	3.225

Gaus mean : 31.2218

Gaus Width: 3.0924



B0L002S, U4-ch24, adc1

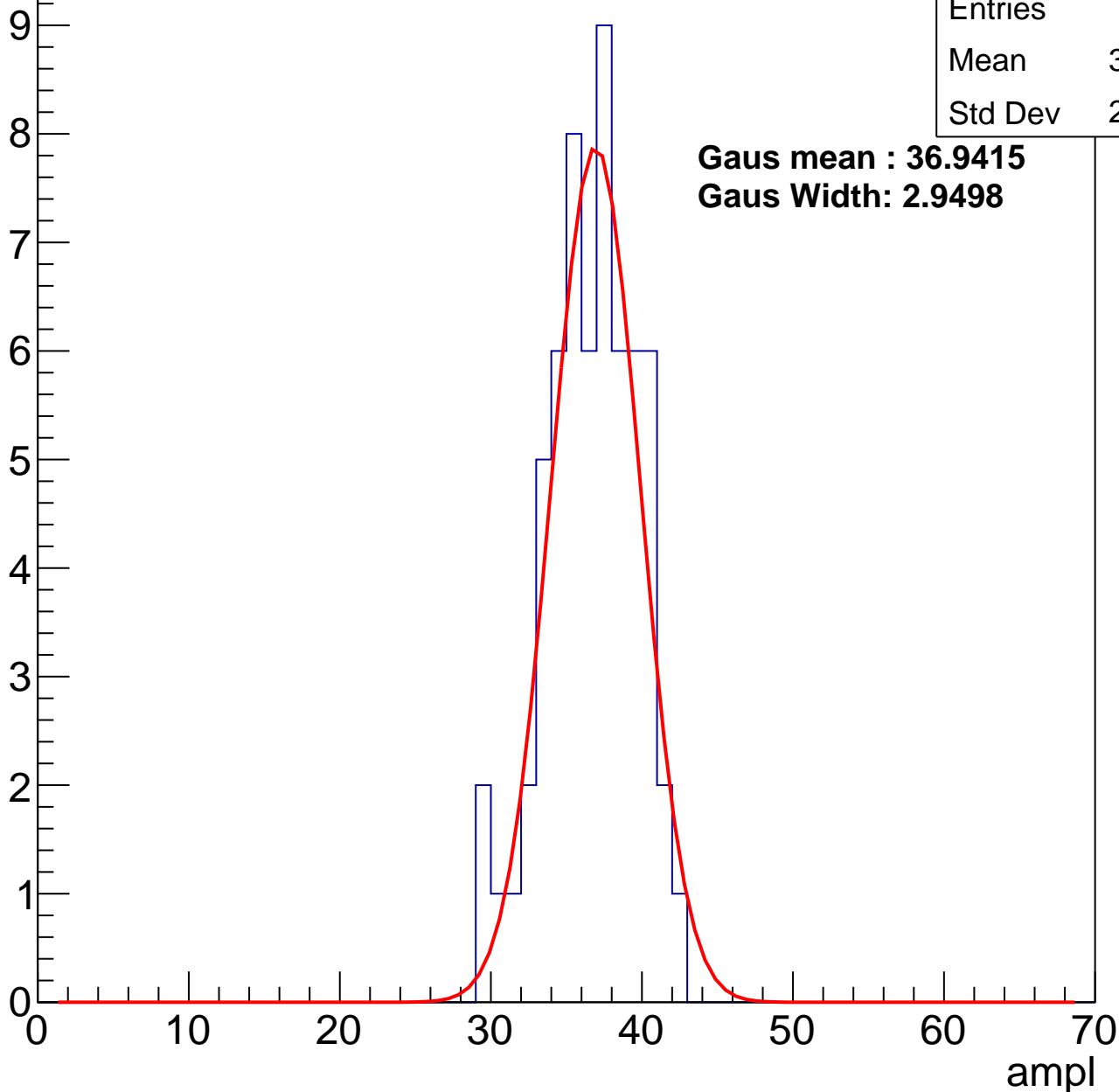
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.18
Std Dev	2.962

Gaus mean : 36.9415

Gaus Width: 2.9498



B0L002S, U4-ch24, adc2

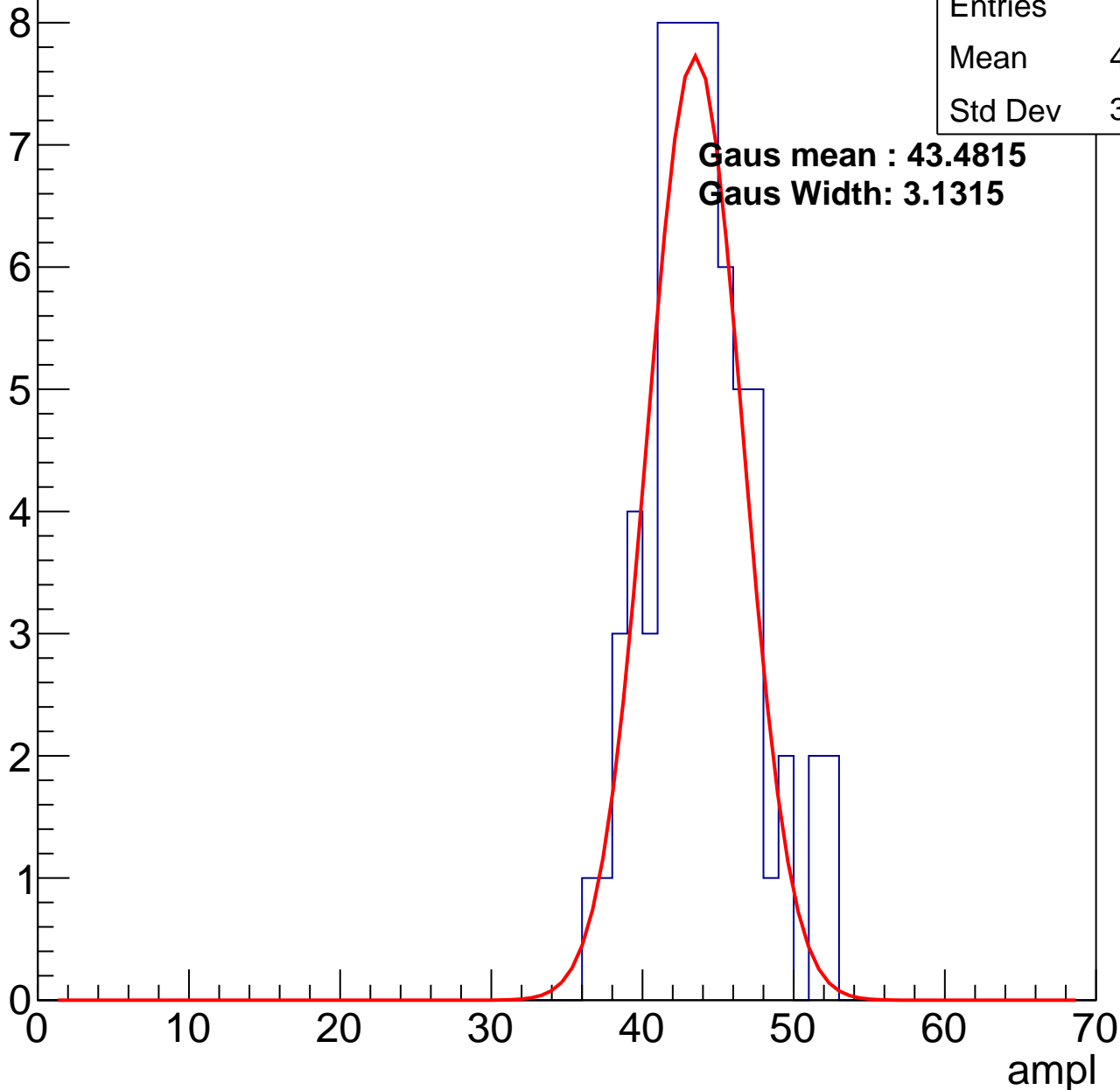
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.43
Std Dev	3.504

Gaus mean : 43.4815

Gaus Width: 3.1315

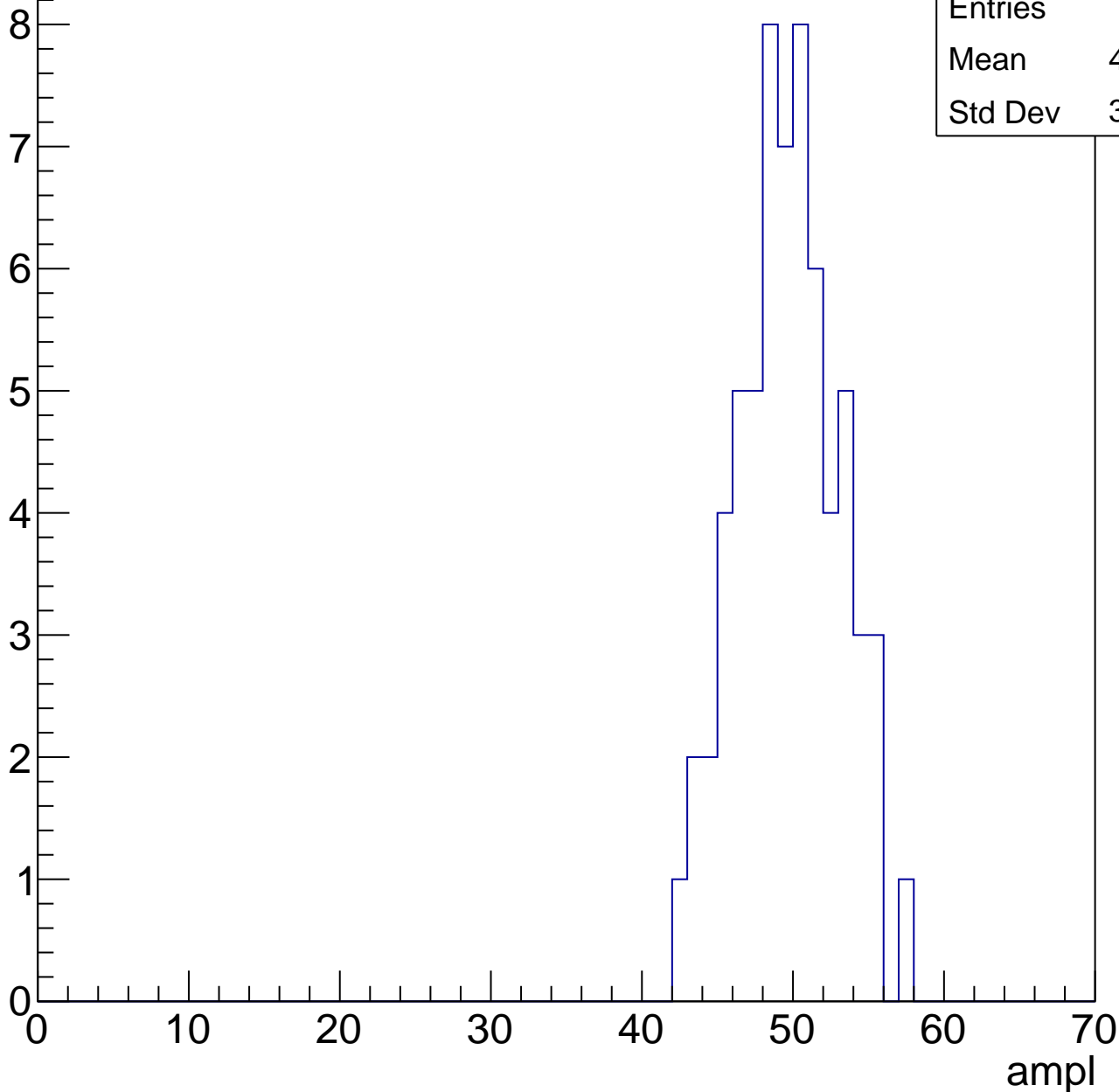


B0L002S, U4-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

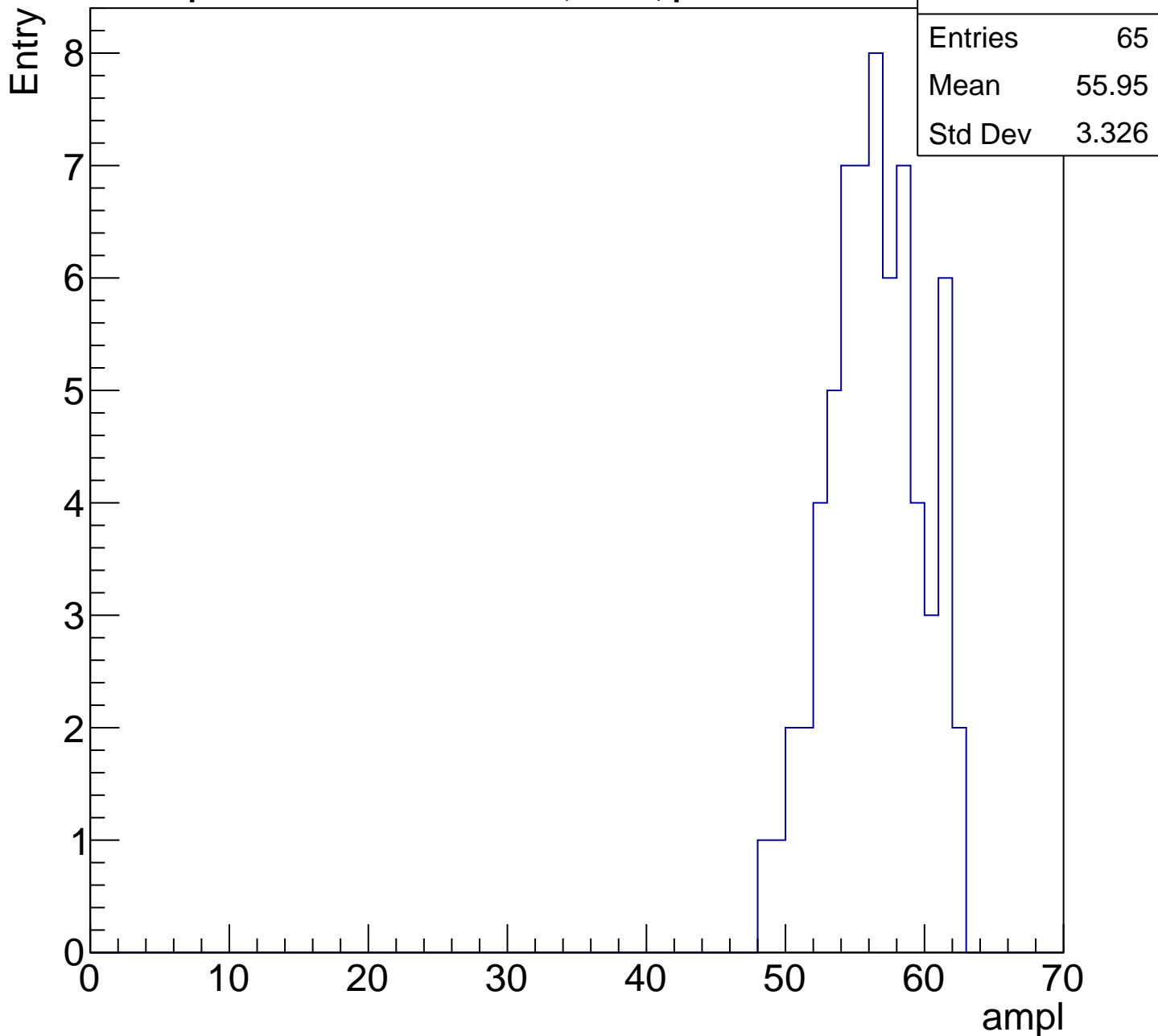
Entry

Entries	64
Mean	49.23
Std Dev	3.306



B0L002S, U4-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

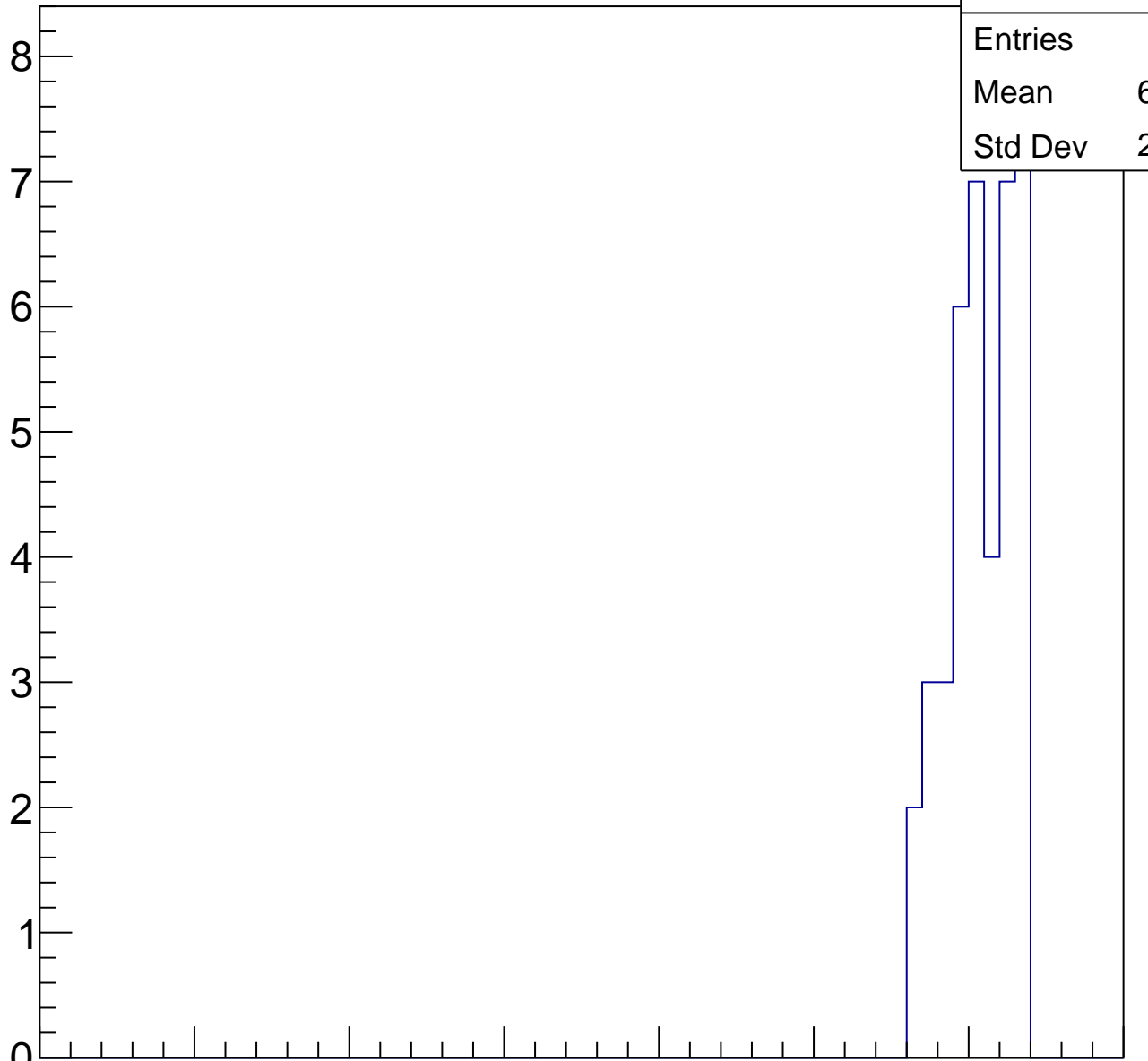
Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.33
Std Dev	2.102

ampl

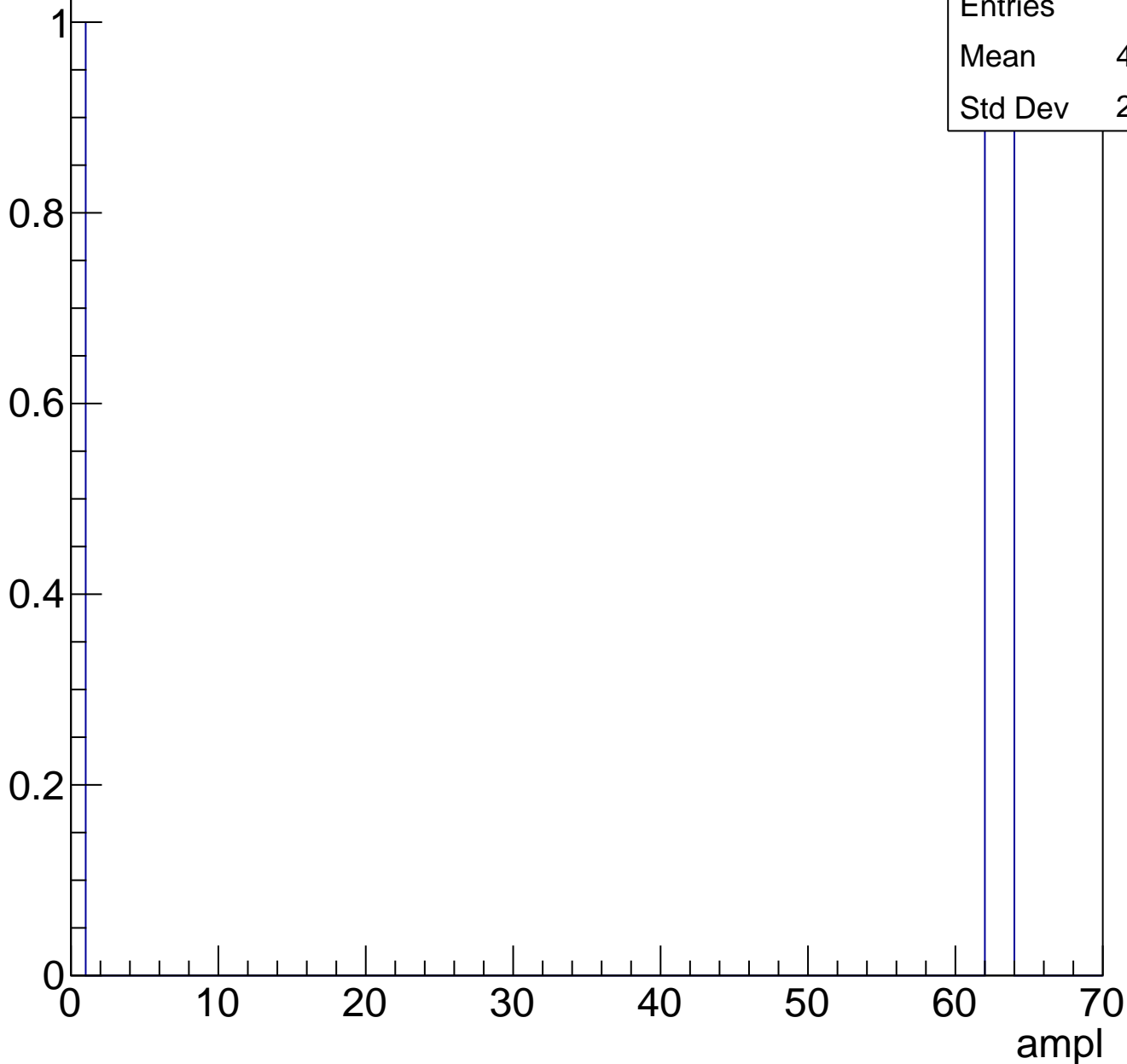
0 10 20 30 40 50 60 70



B0L002S, U4-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch25, adc0

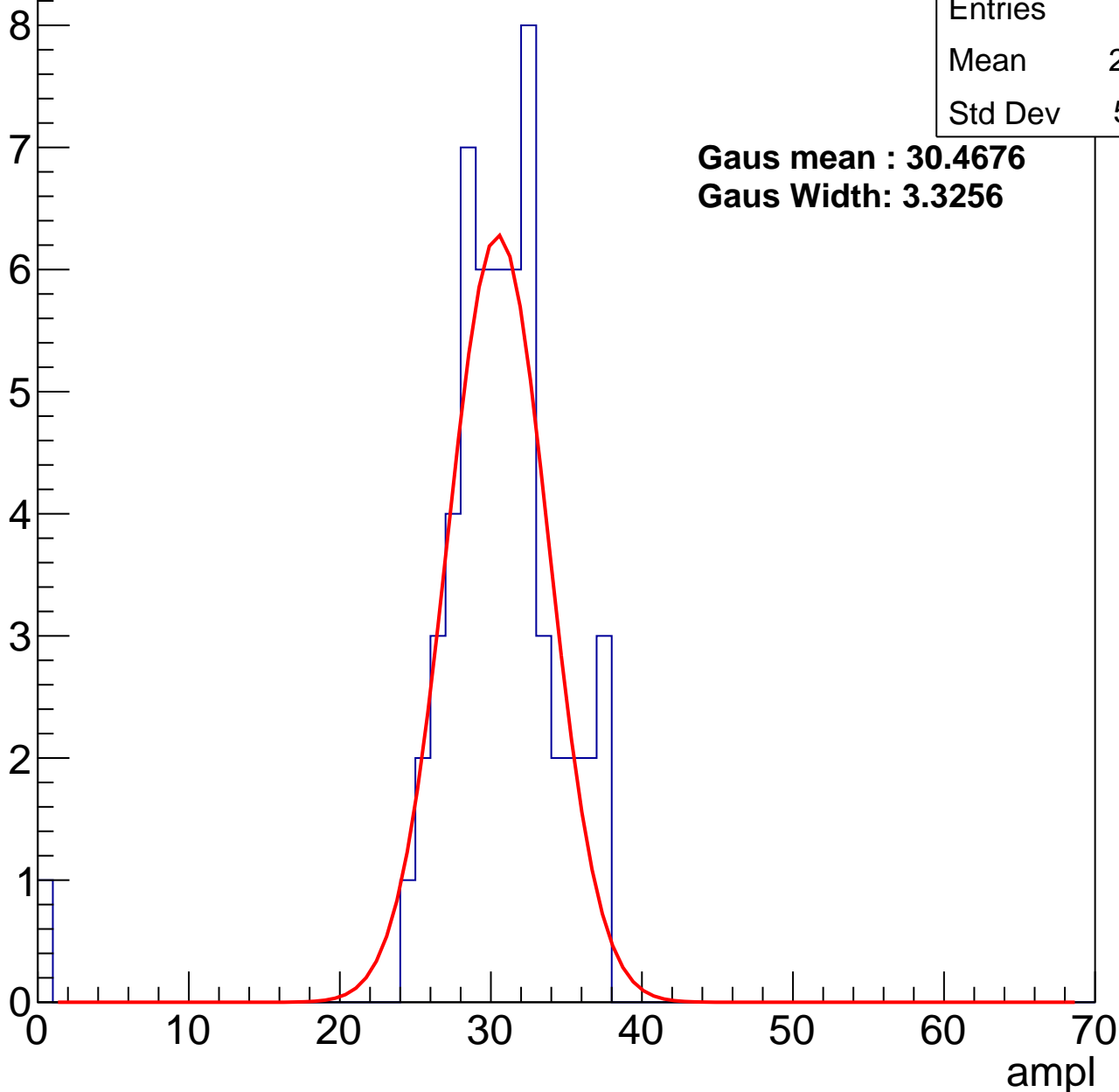
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	29.86
Std Dev	5.121

Gaus mean : 30.4676

Gaus Width: 3.3256



B0L002S, U4-ch25, adc1

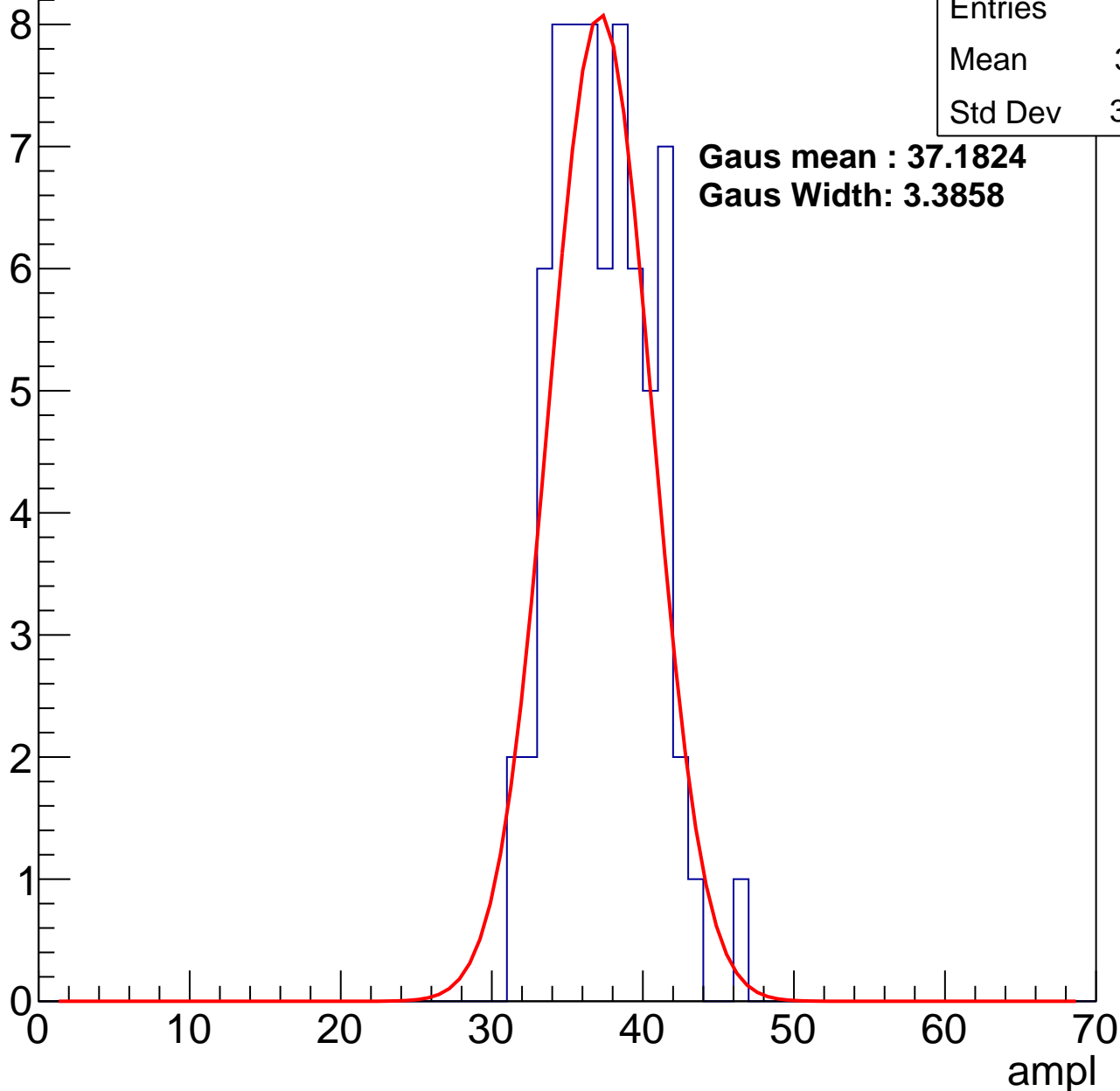
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36.91
Std Dev	3.129

Gaus mean : 37.1824

Gaus Width: 3.3858



B0L002S, U4-ch25, adc2

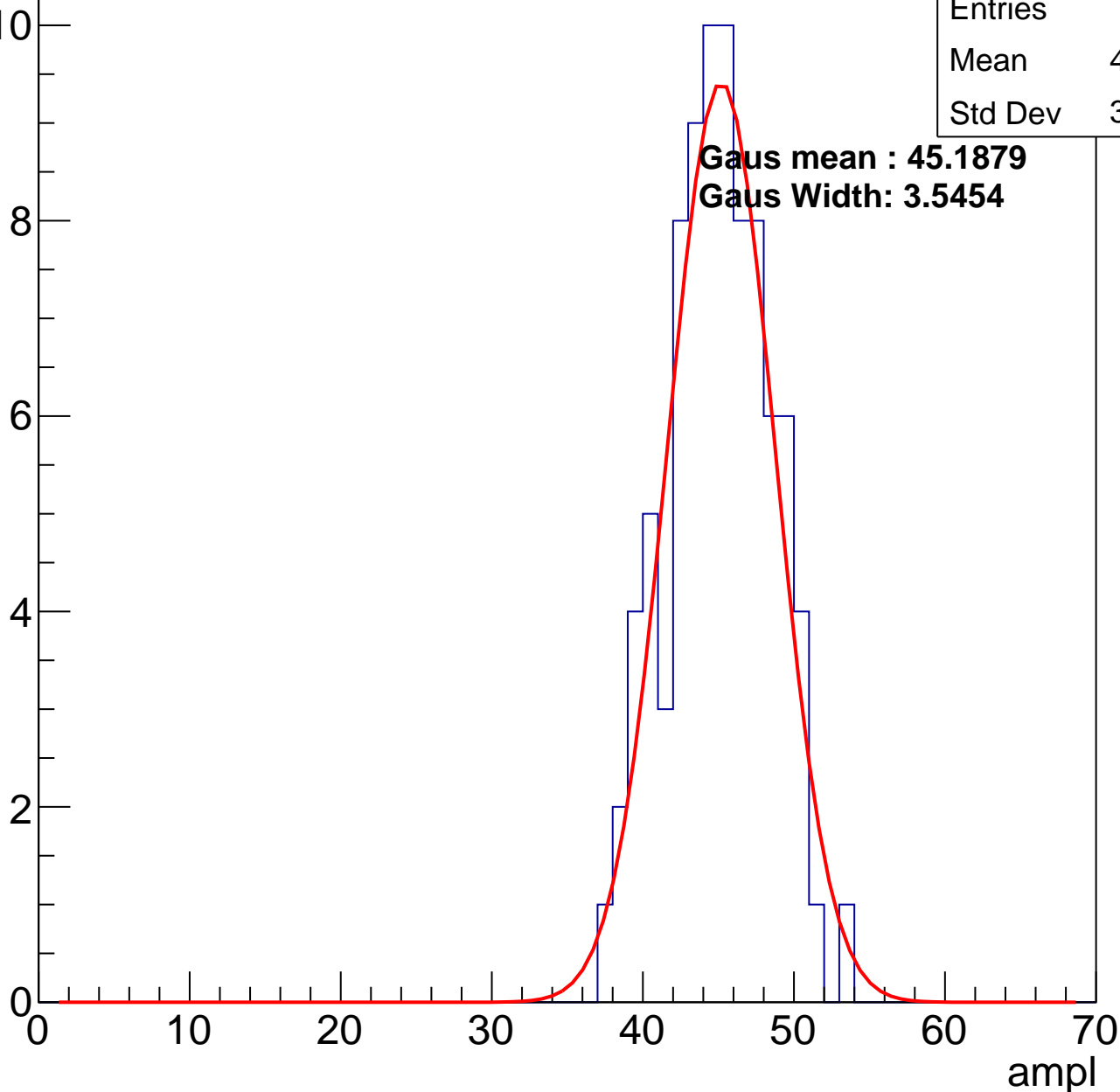
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	44.59
Std Dev	3.363

Gaus mean : 45.1879

Gaus Width: 3.5454

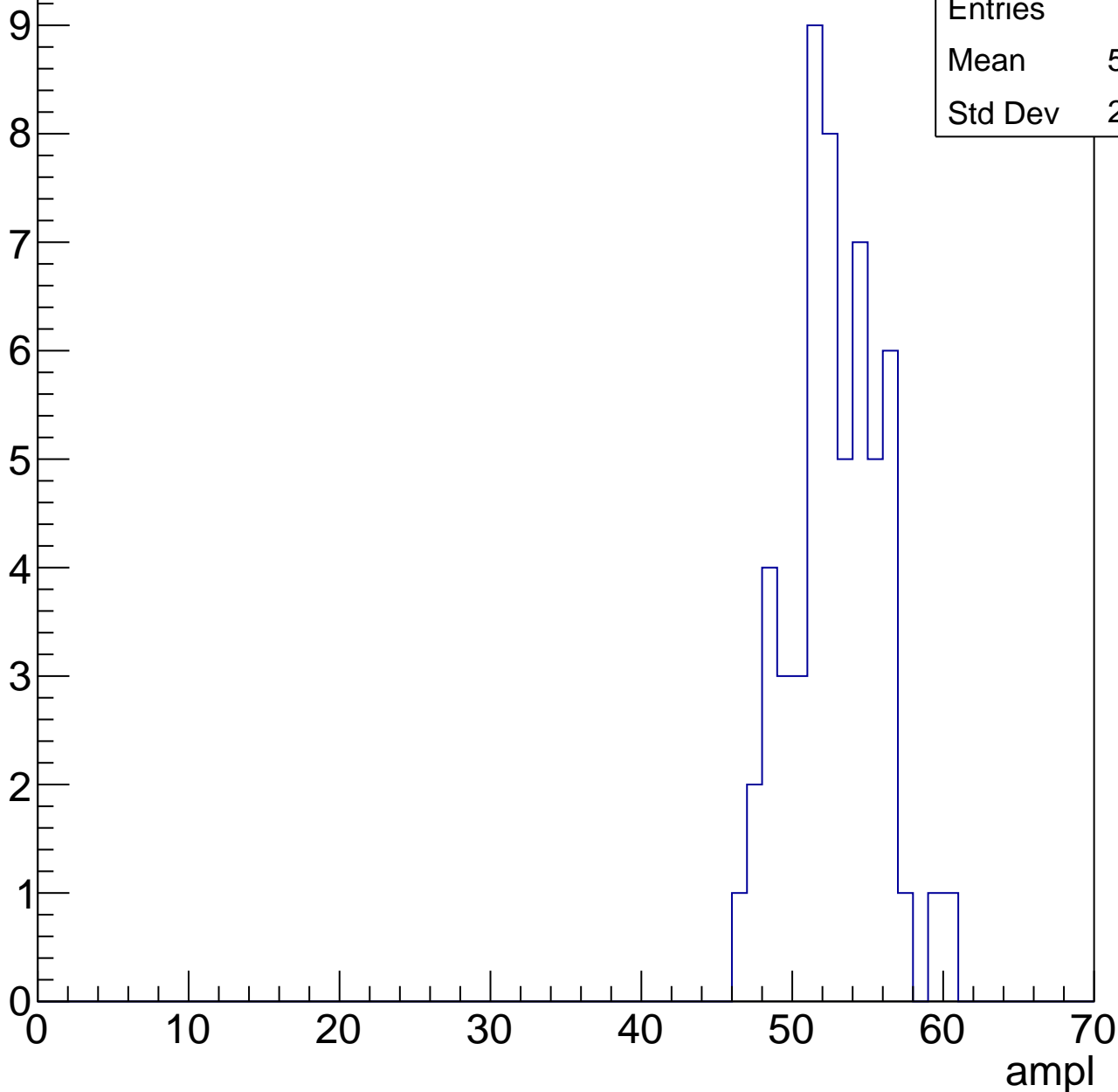


B0L002S, U4-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

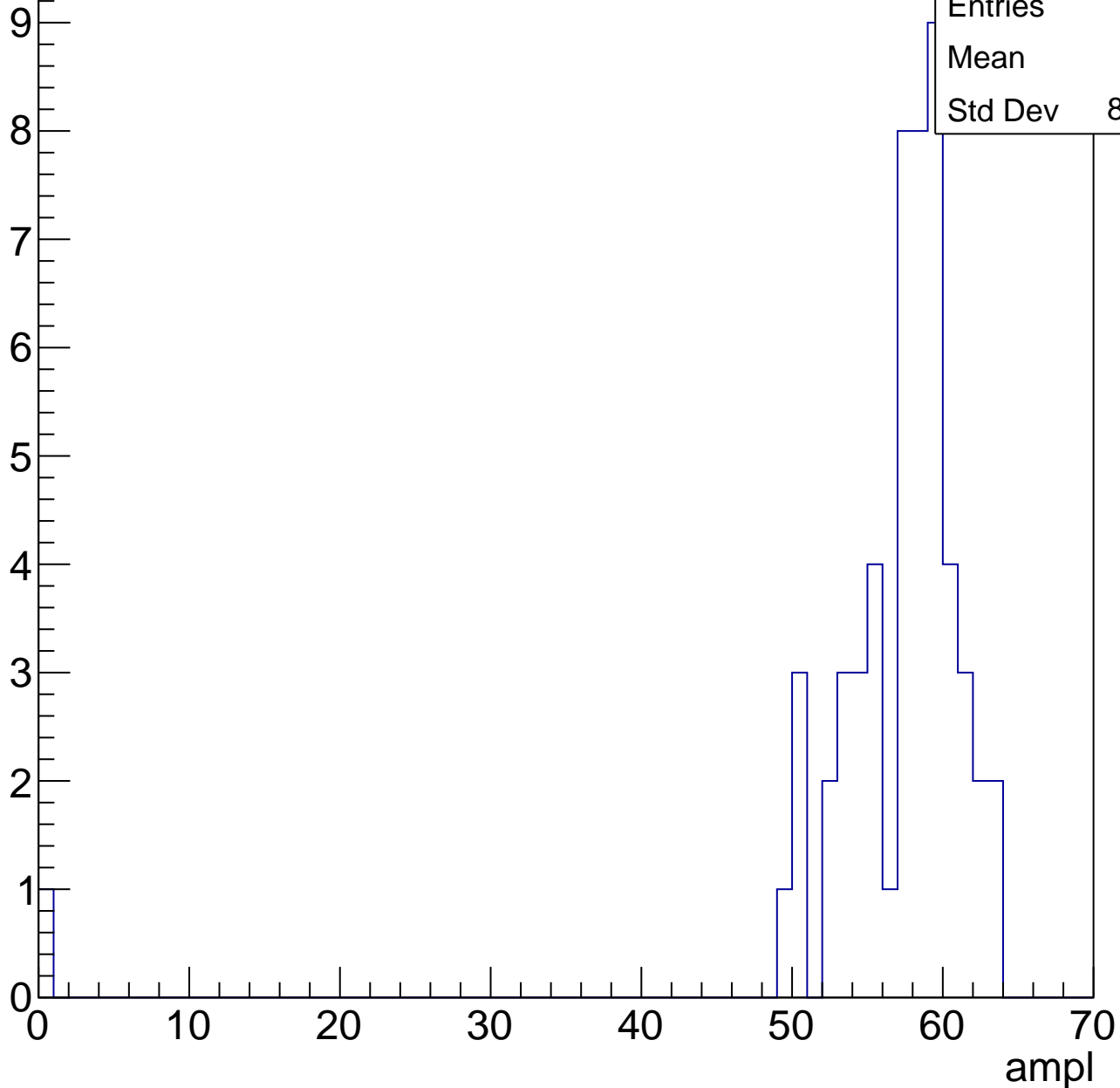
Entries	56
Mean	52.39
Std Dev	2.992



B0L002S, U4-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

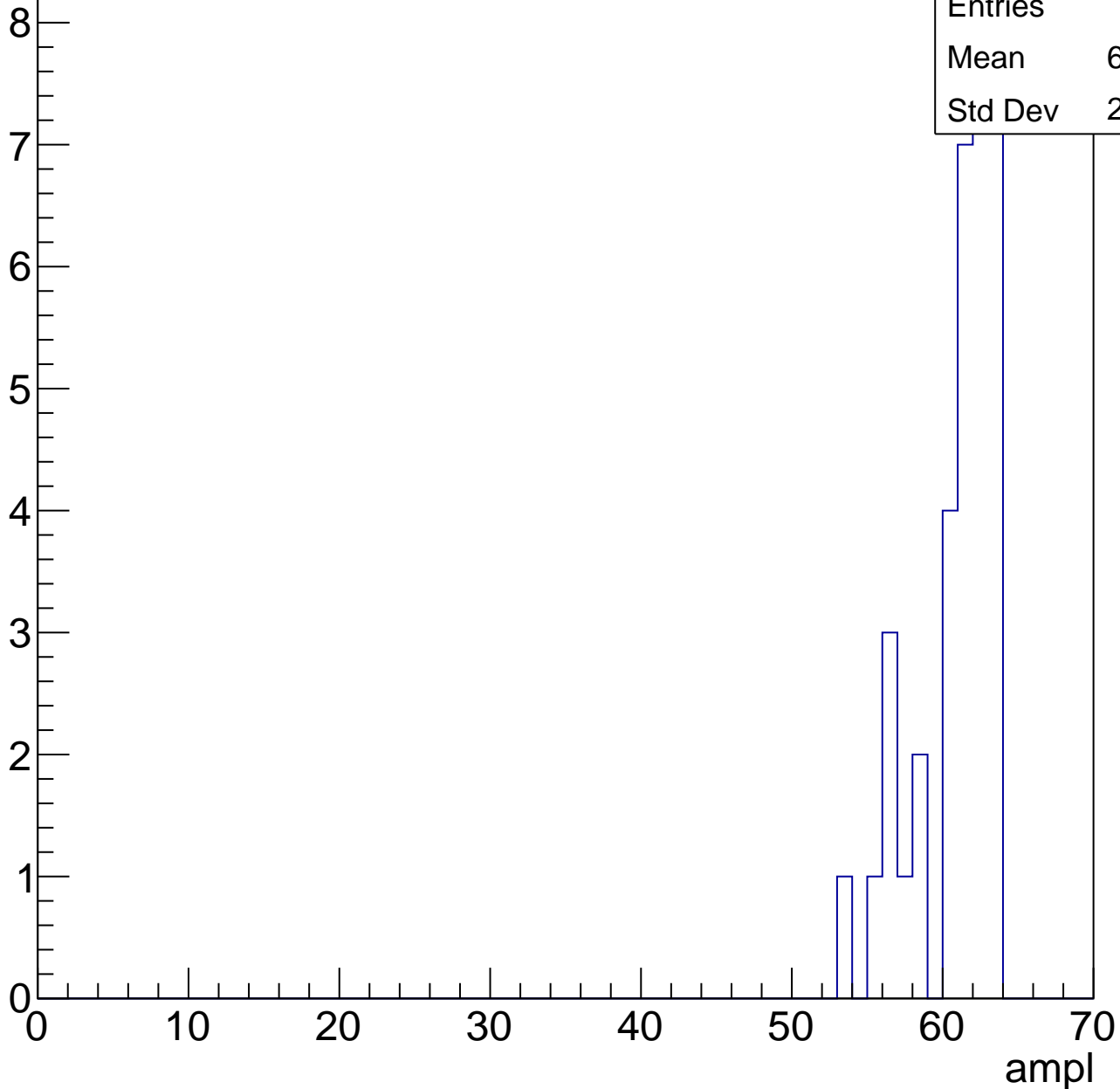


B0L002S, U4-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.46
Std Dev	2.633



B0L002S, U4-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L002S, U4-ch26, adc0

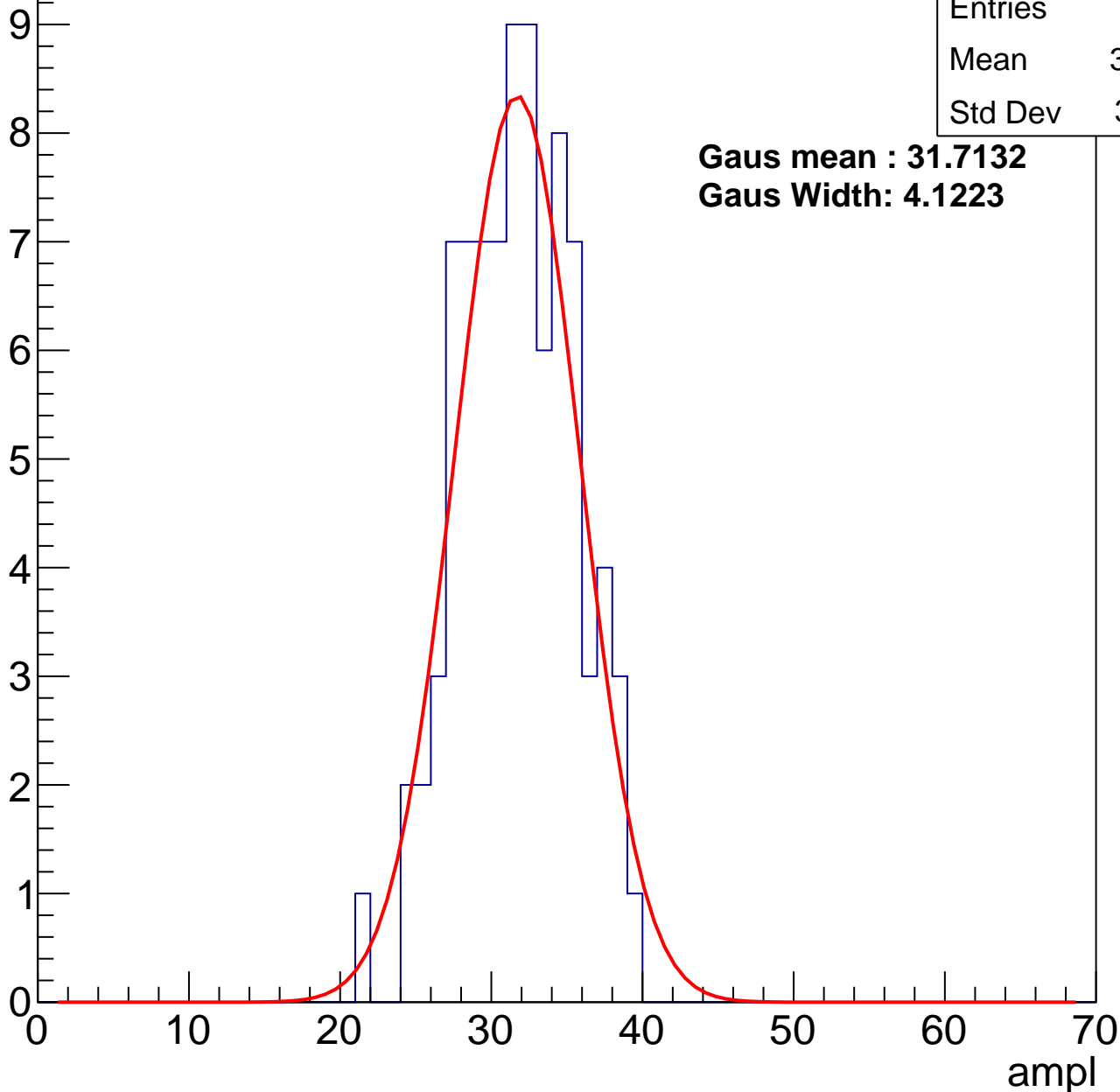
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	31.23
Std Dev	3.731

Gaus mean : 31.7132

Gaus Width: 4.1223



B0L002S, U4-ch26, adc1

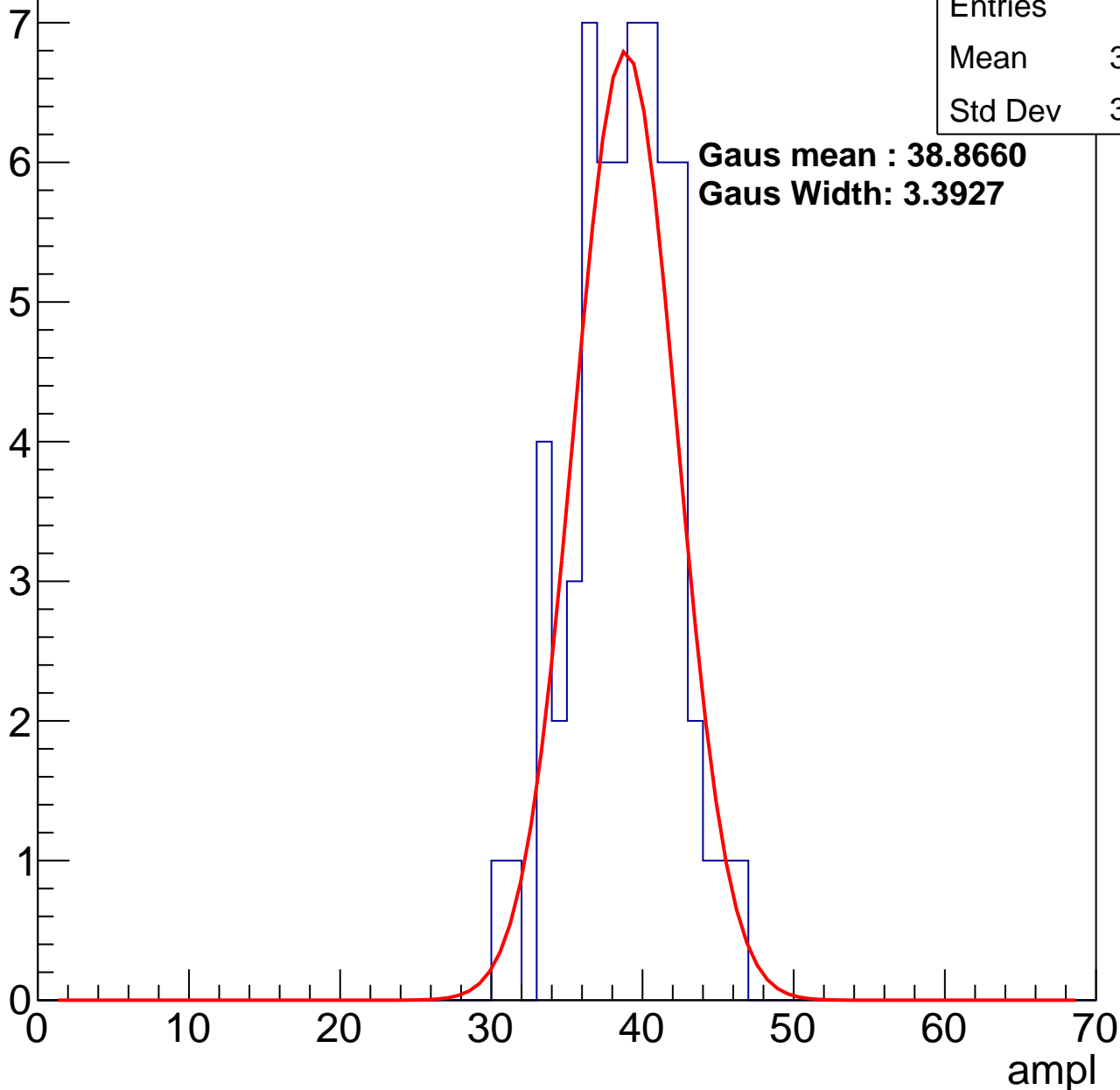
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.36
Std Dev	3.344

Gaus mean : 38.8660

Gaus Width: 3.3927

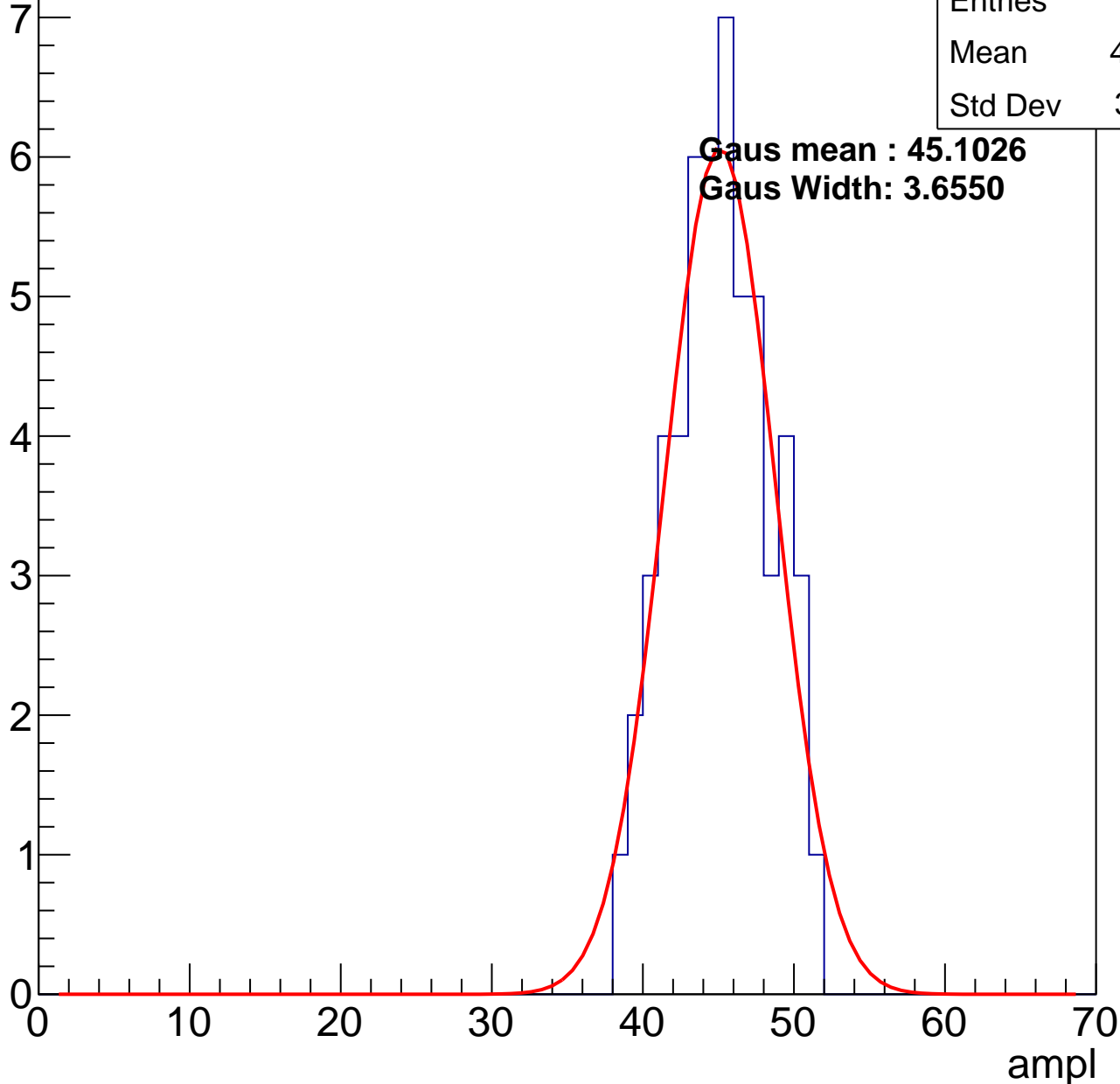


B0L002S, U4-ch26, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	44.65
Std Dev	3.181

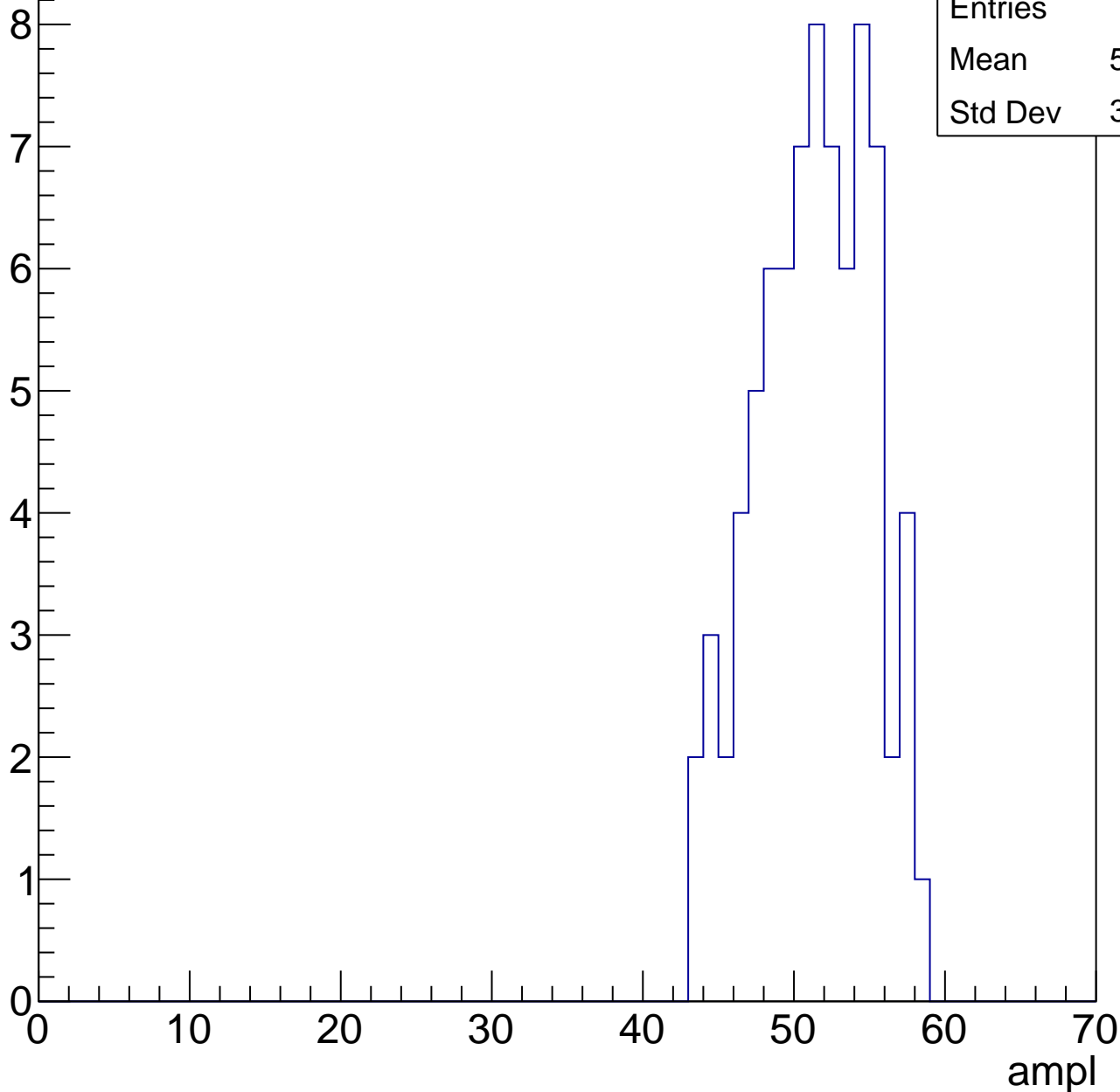


B0L002S, U4-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

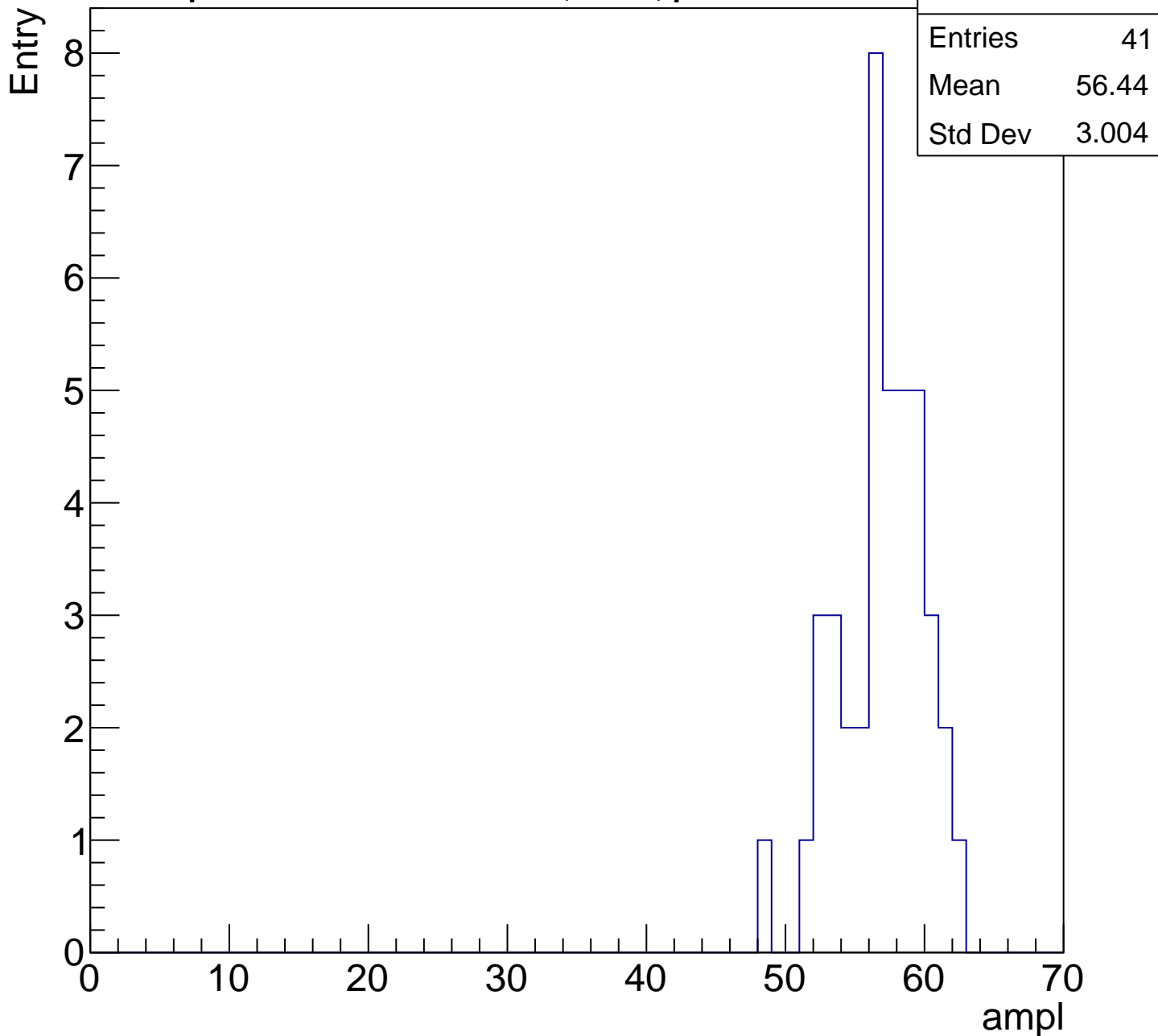
Entry

Entries	78
Mean	50.82
Std Dev	3.699



B0L002S, U4-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

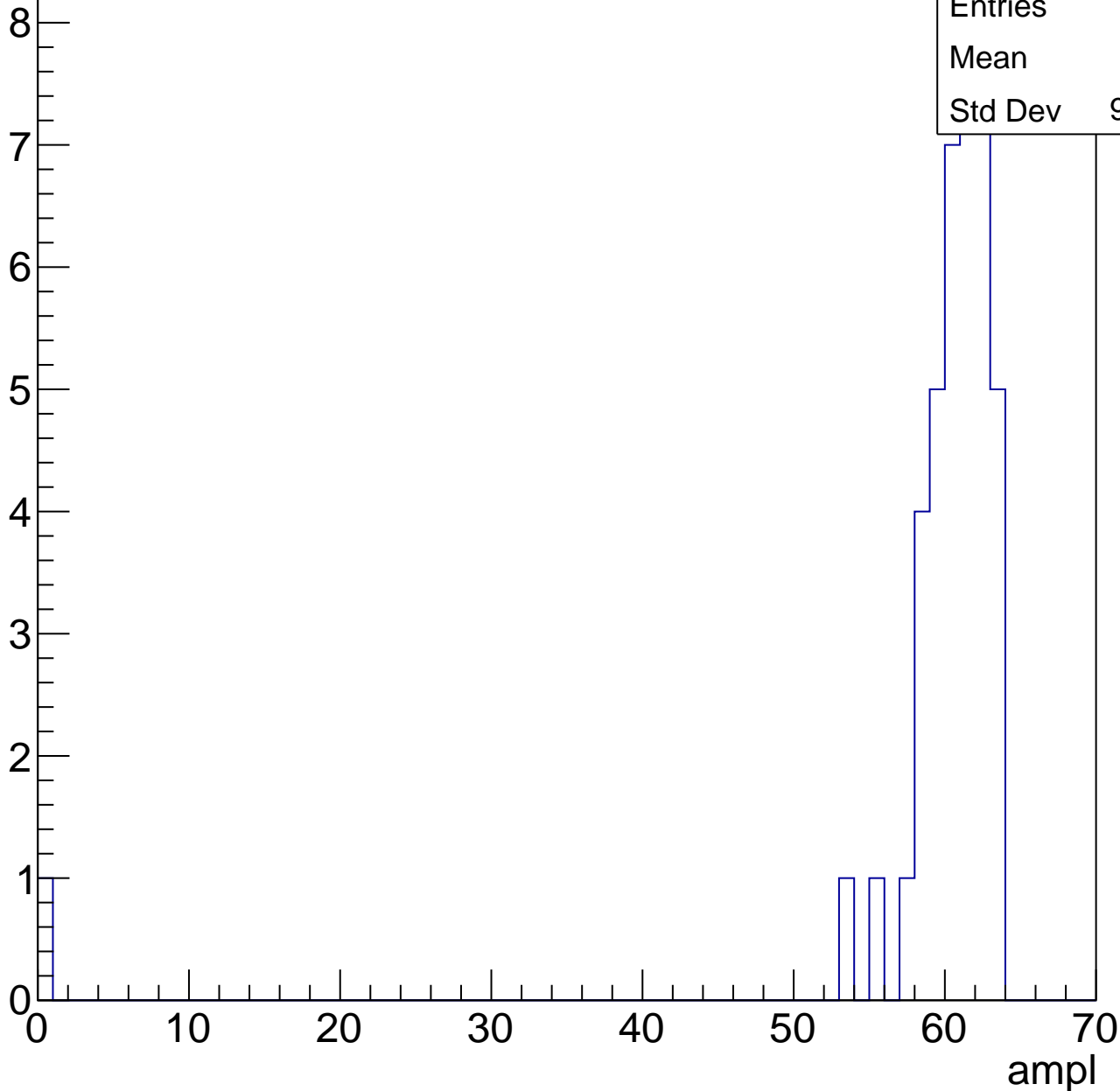


B0L002S, U4-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.8
Std Dev	9.539

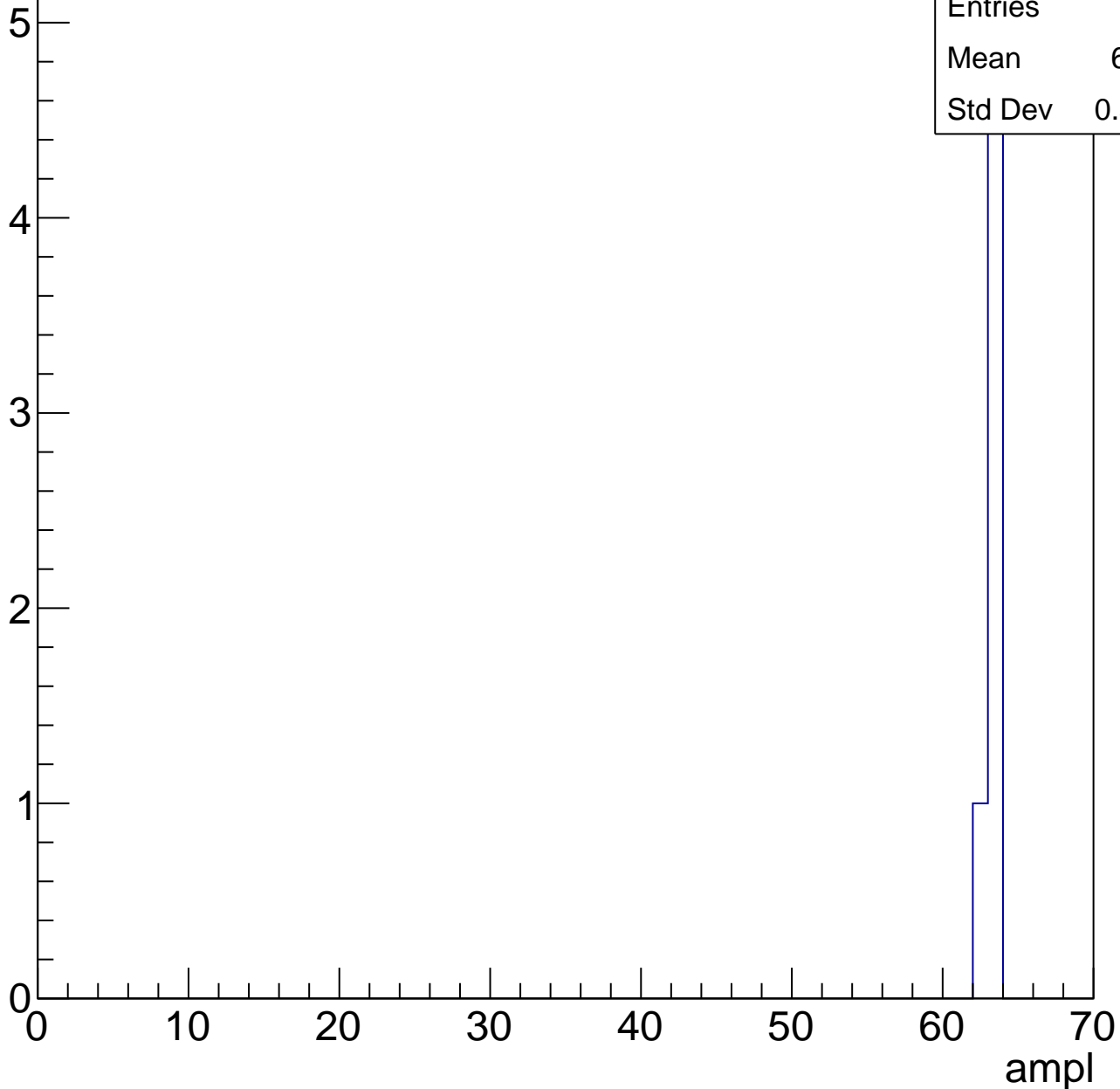


B0L002S, U4-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	6
Mean	62.83
Std Dev	0.3727



B0L002S, U4-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch27, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	30.05
Std Dev	3.727

Gaus mean : 30.2676

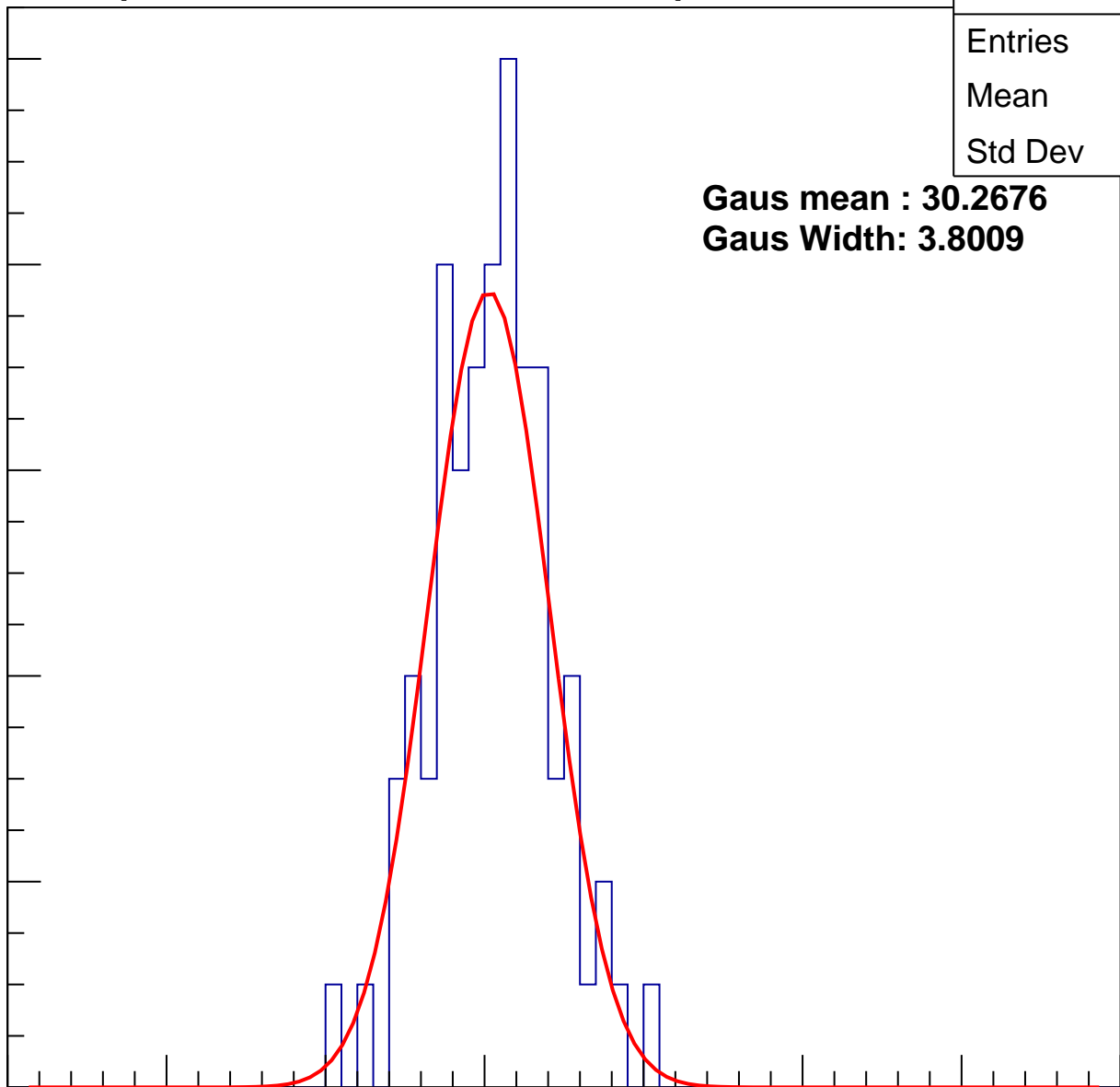
Gaus Width: 3.8009

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch27, adc1

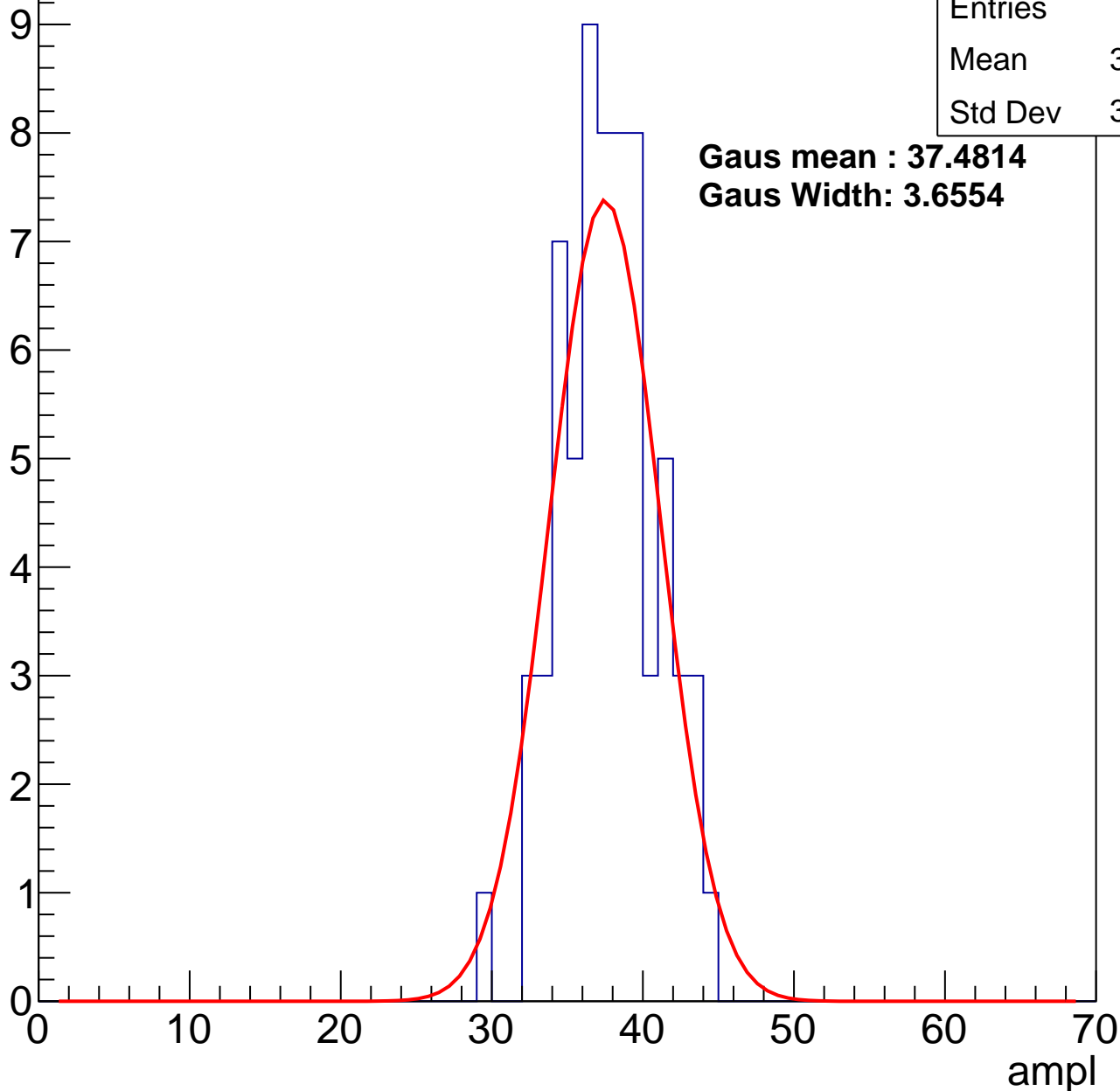
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.27
Std Dev	3.122

Gaus mean : 37.4814

Gaus Width: 3.6554



B0L002S, U4-ch27, adc2

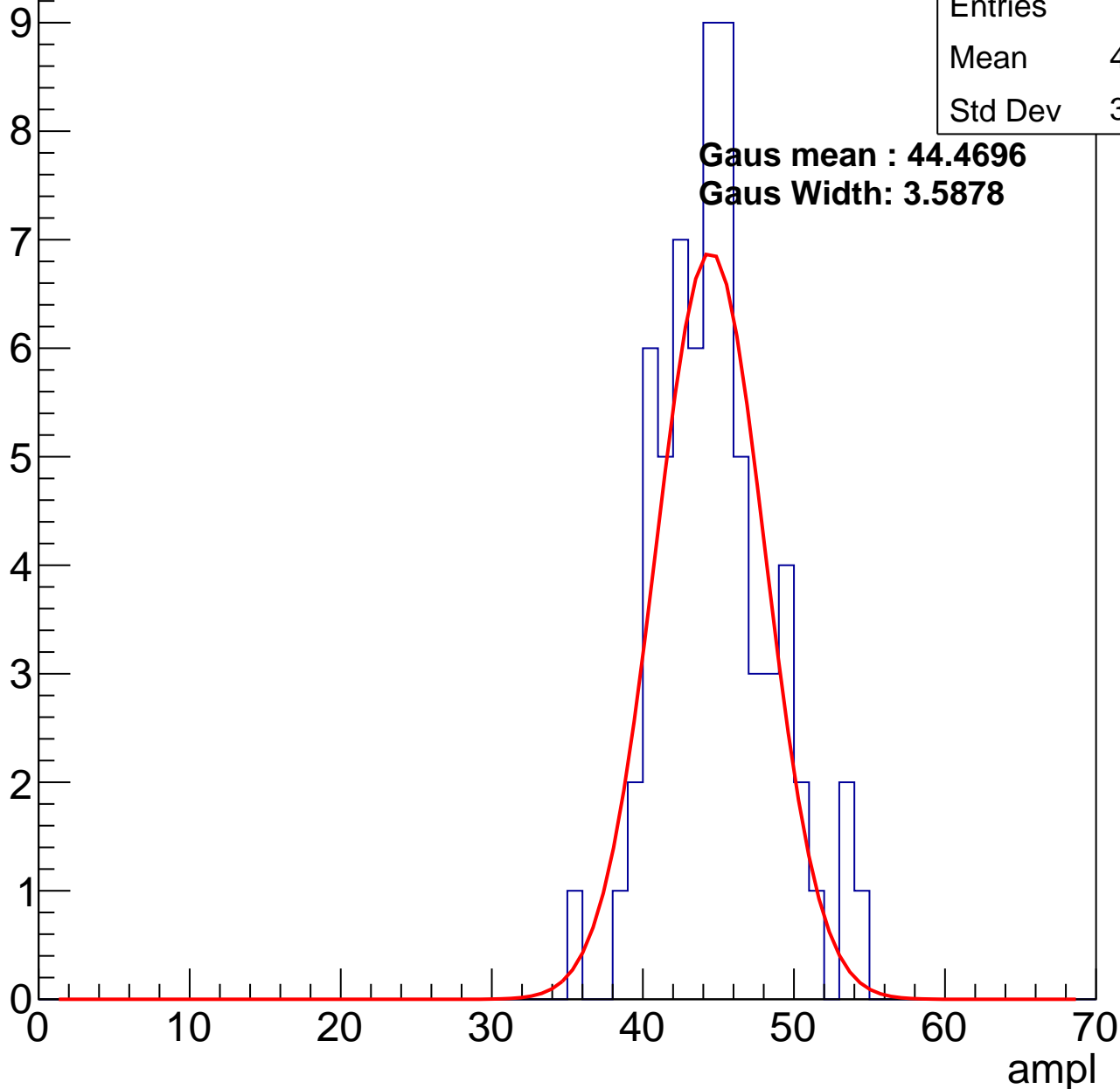
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.34
Std Dev	3.708

Gaus mean : 44.4696

Gaus Width: 3.5878

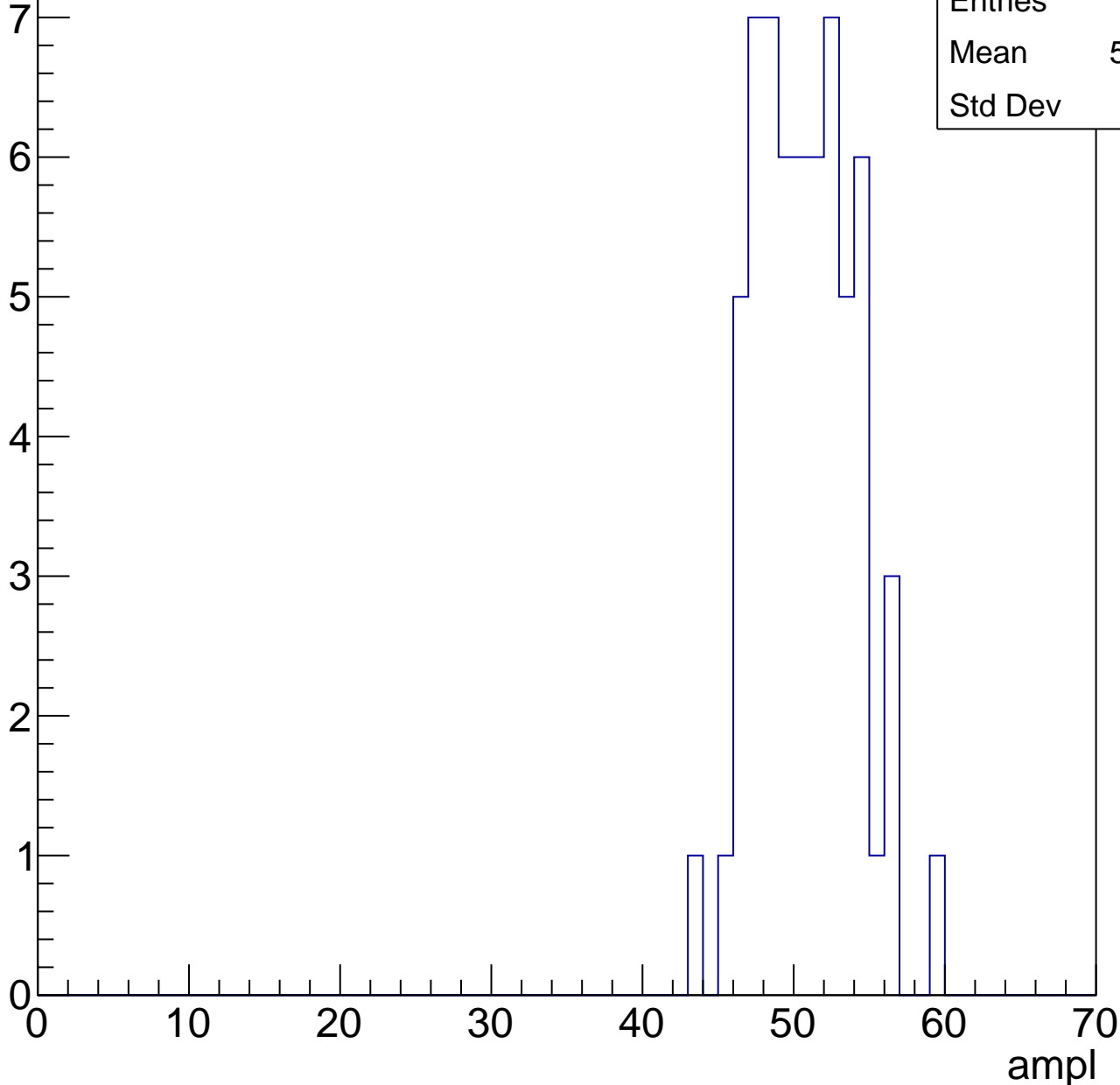


B0L002S, U4-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	50.29
Std Dev	3.2

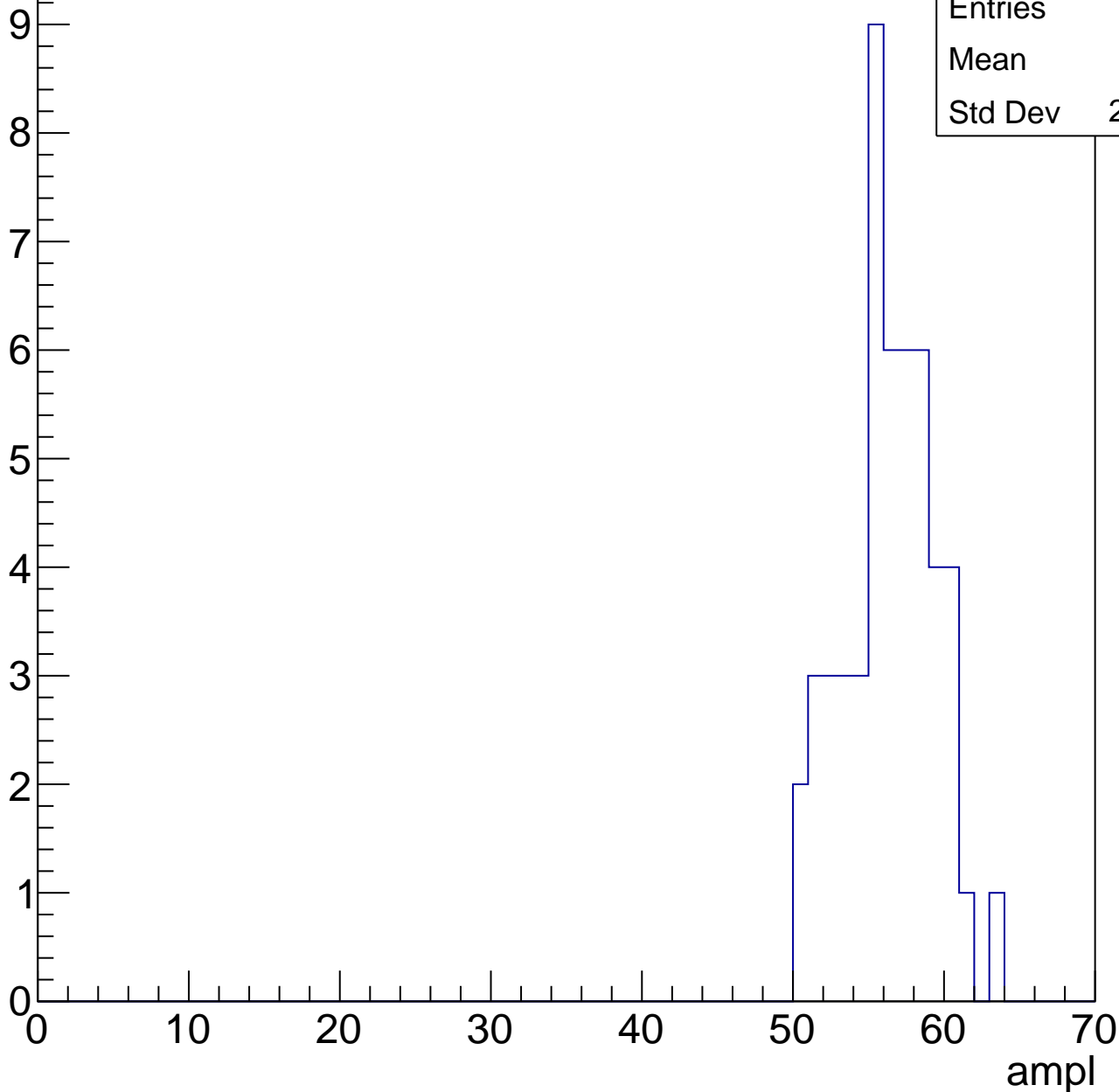


B0L002S, U4-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	55.9
Std Dev	2.959



B0L002S, U4-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	49
Mean	59.33
Std Dev	8.784

0

B0L002S, U4-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch28, adc0

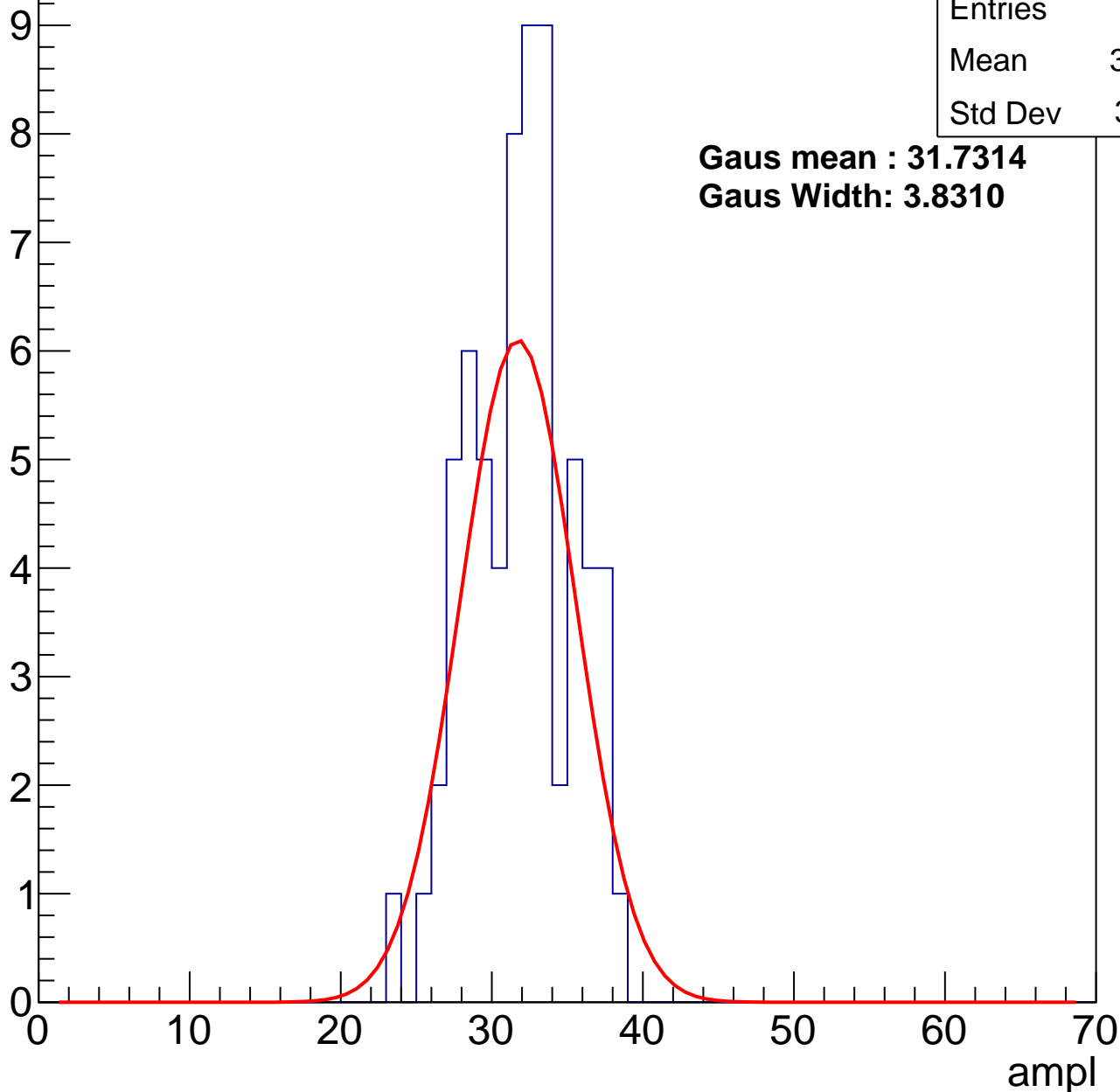
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.42
Std Dev	3.331

Gaus mean : 31.7314

Gaus Width: 3.8310



B0L002S, U4-ch28, adc1

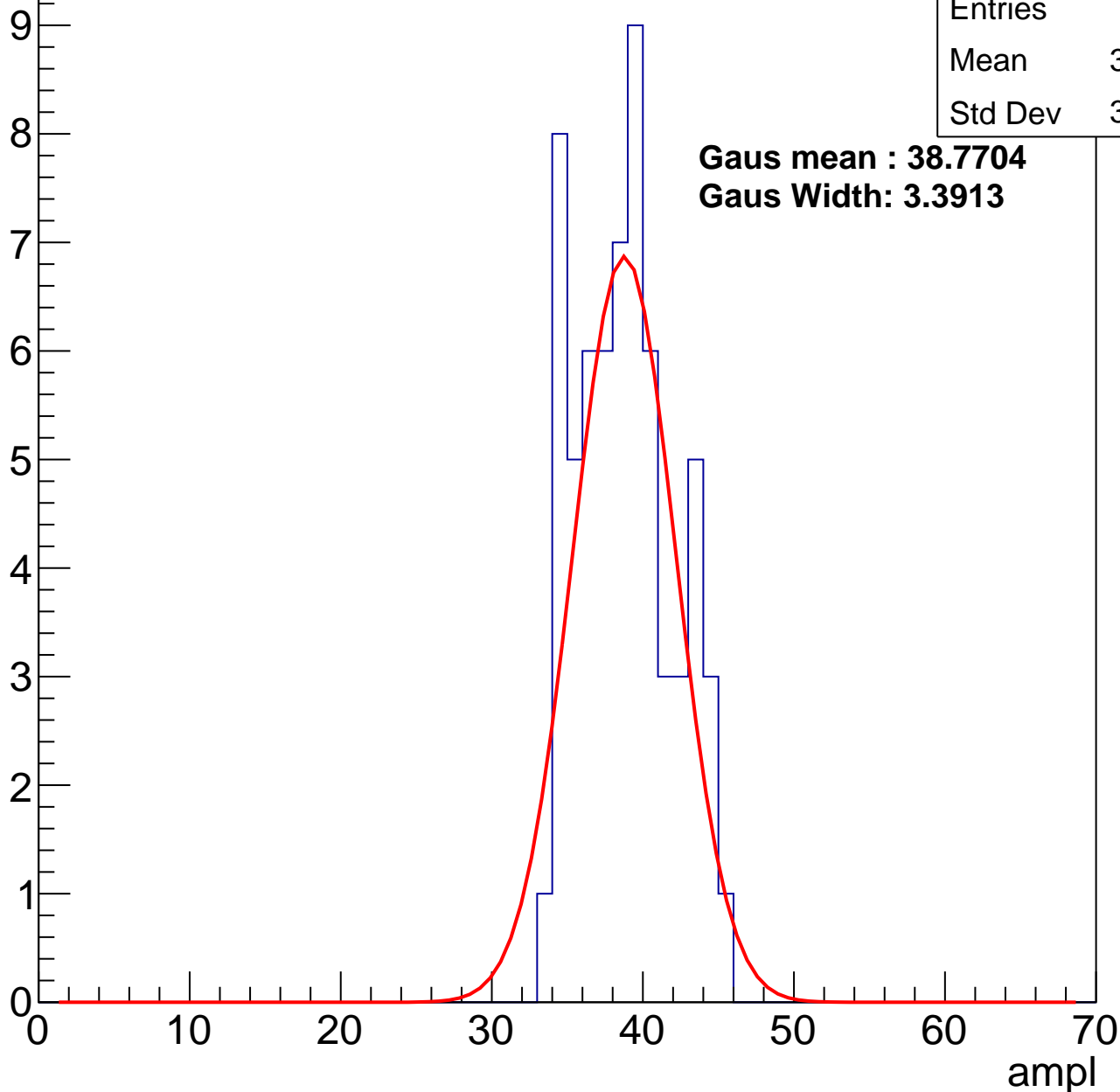
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.35
Std Dev	3.112

Gaus mean : 38.7704

Gaus Width: 3.3913



B0L002S, U4-ch28, adc2

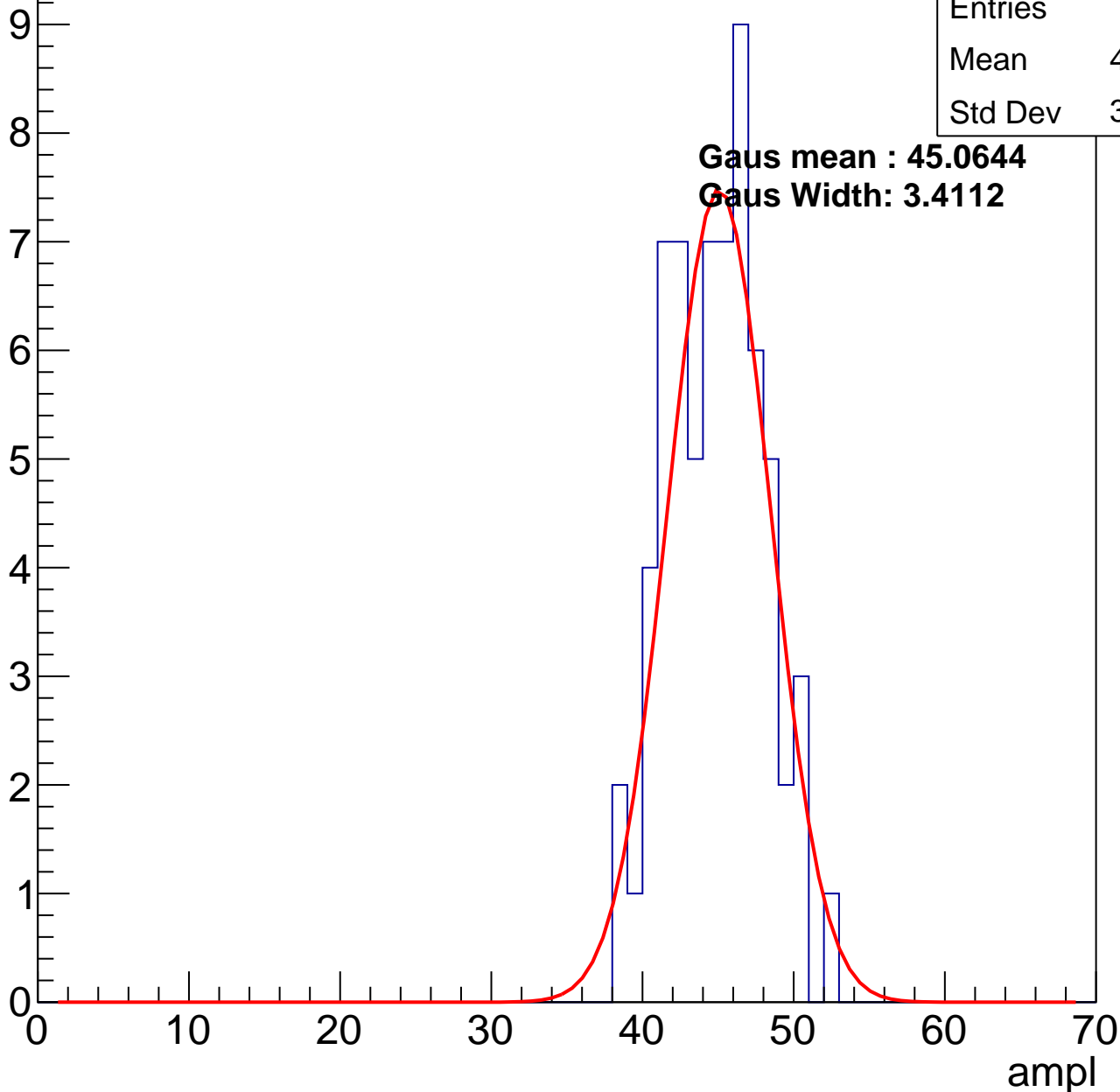
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.39
Std Dev	3.128

Gaus mean : 45.0644

Gaus Width: 3.4112

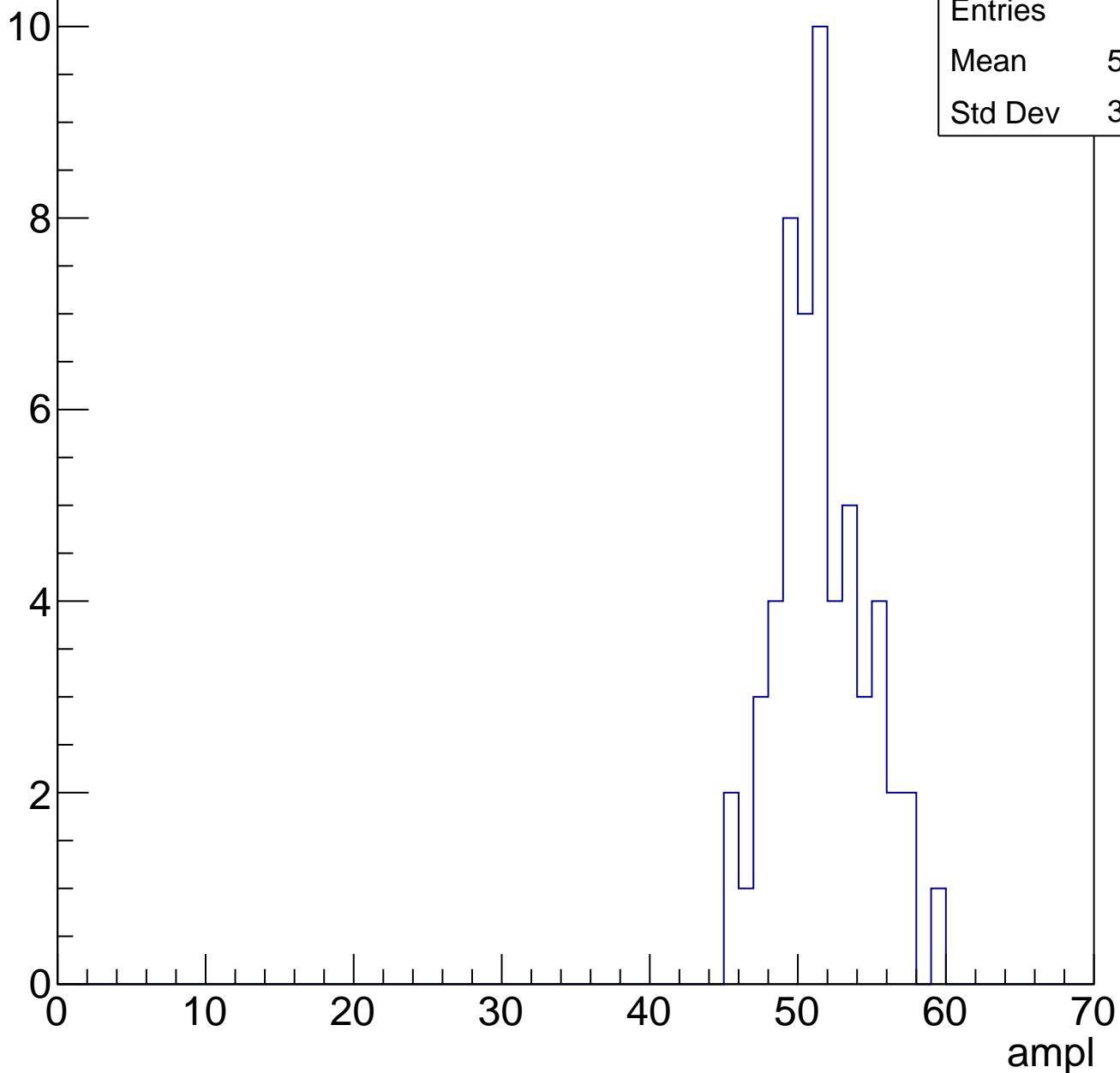


B0L002S, U4-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	51.09
Std Dev	3.049

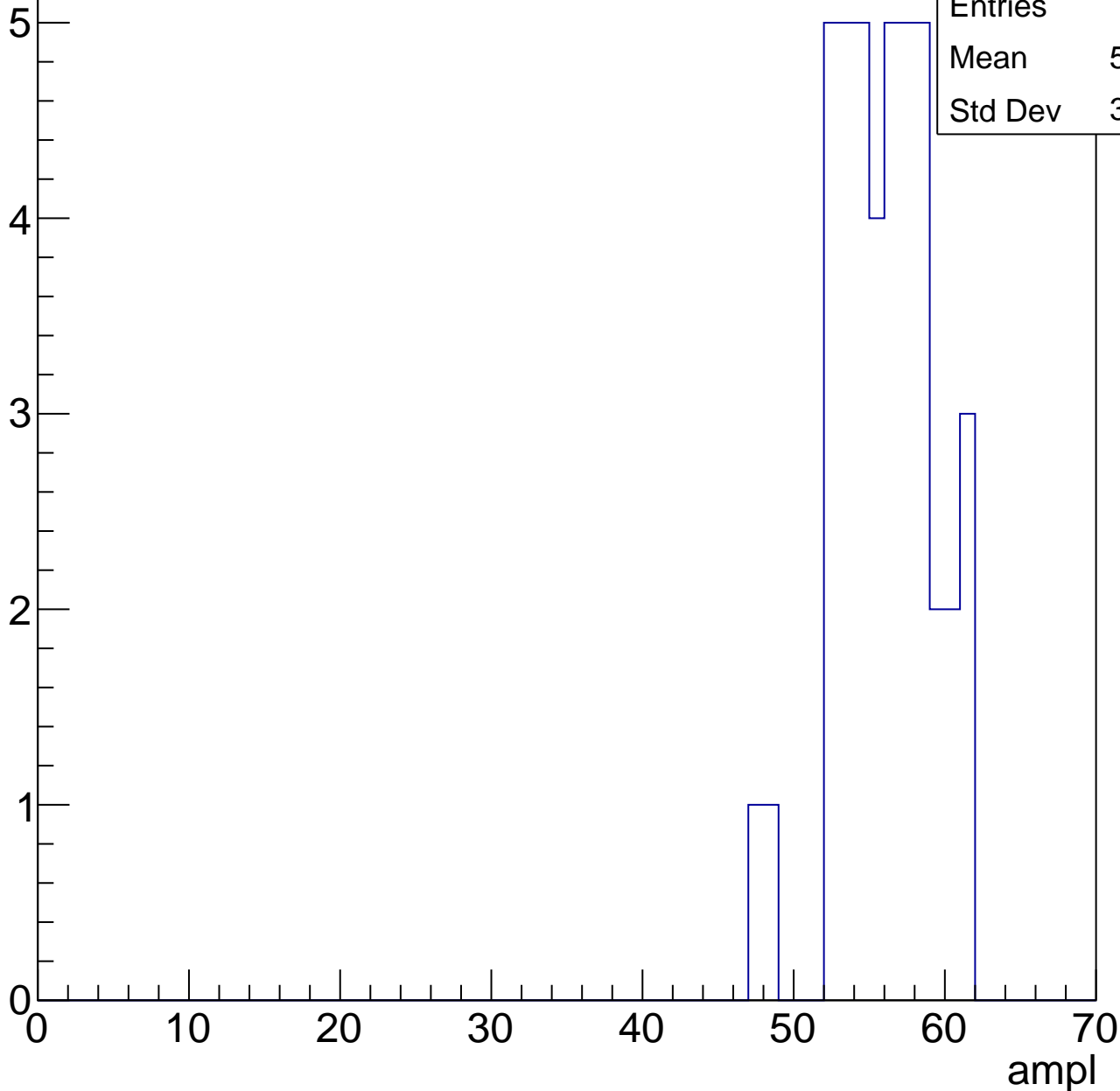


B0L002S, U4-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	55.49
Std Dev	3.172

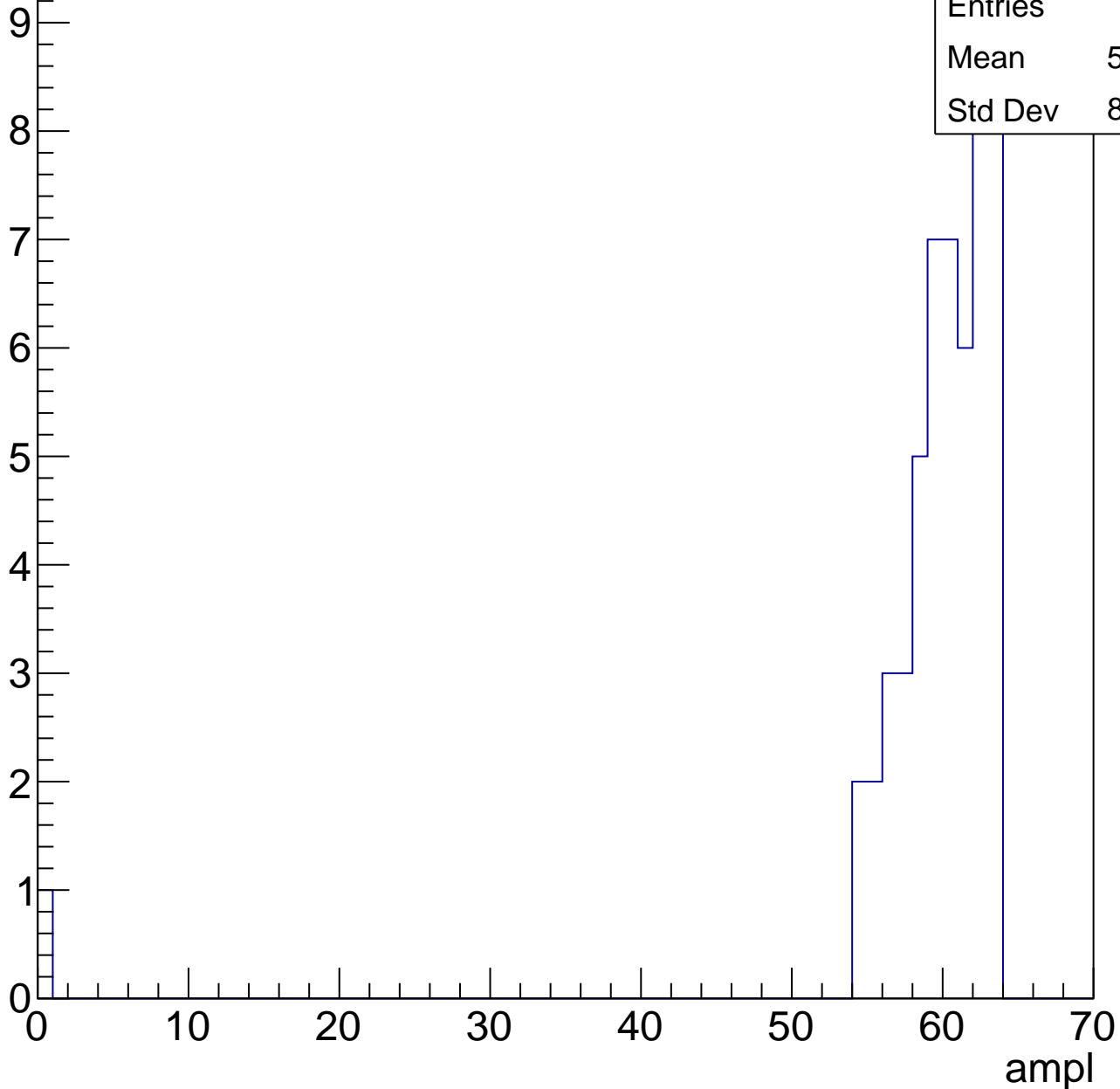


B0L002S, U4-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	58.64
Std Dev	8.512



B0L002S, U4-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

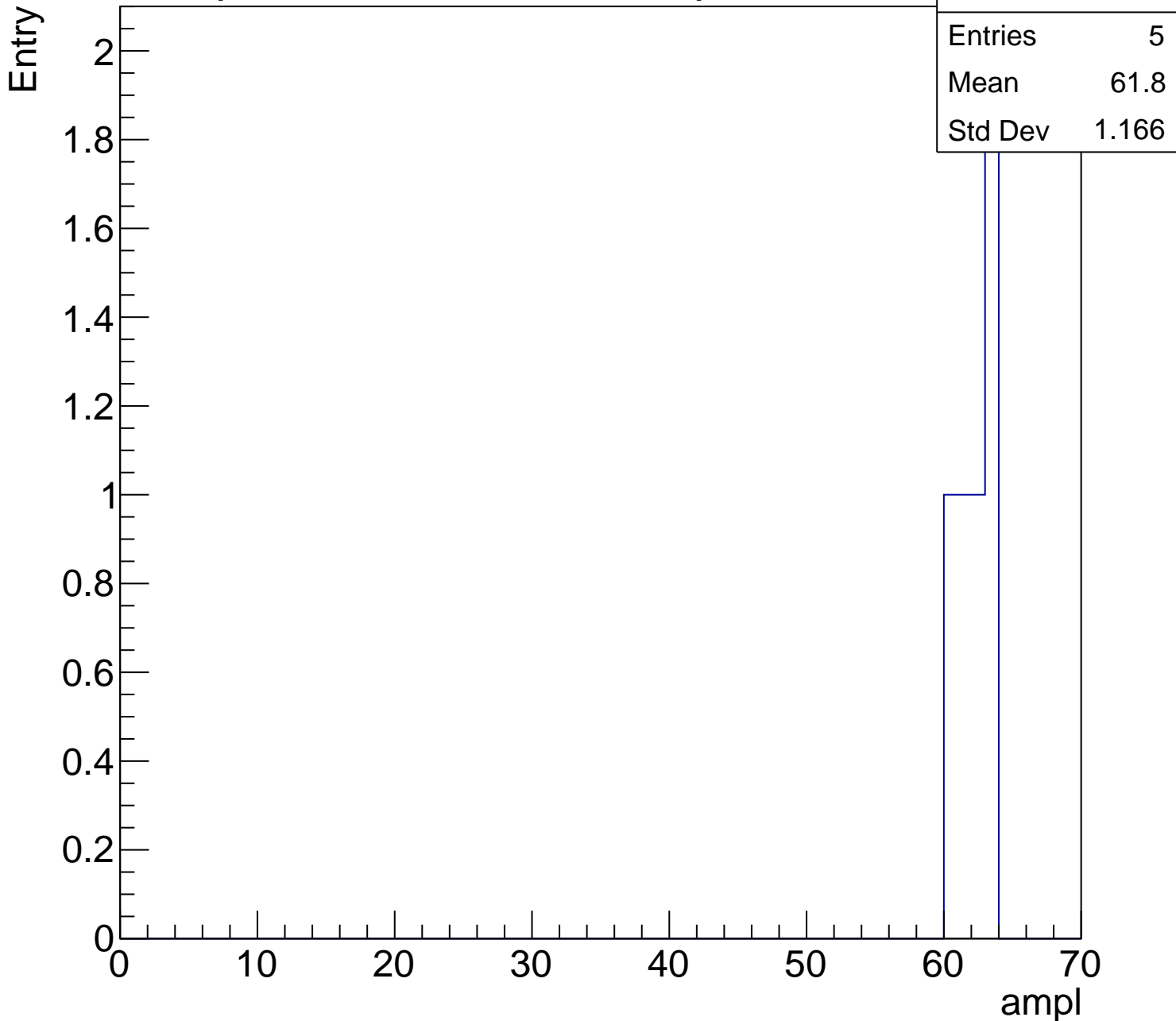
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U4-ch29, adc0

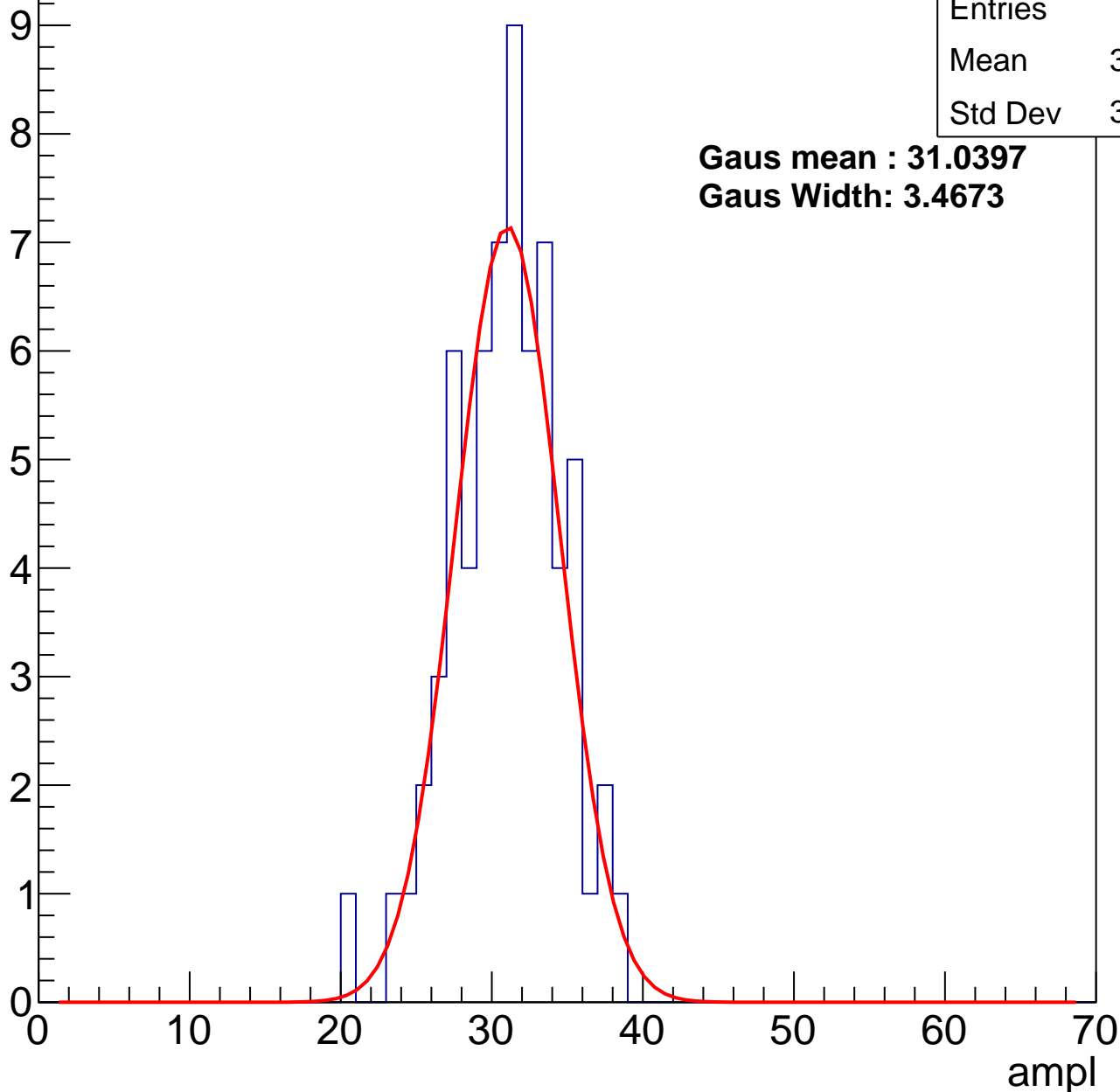
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.52
Std Dev	3.534

Gaus mean : 31.0397

Gaus Width: 3.4673



B0L002S, U4-ch29, adc1

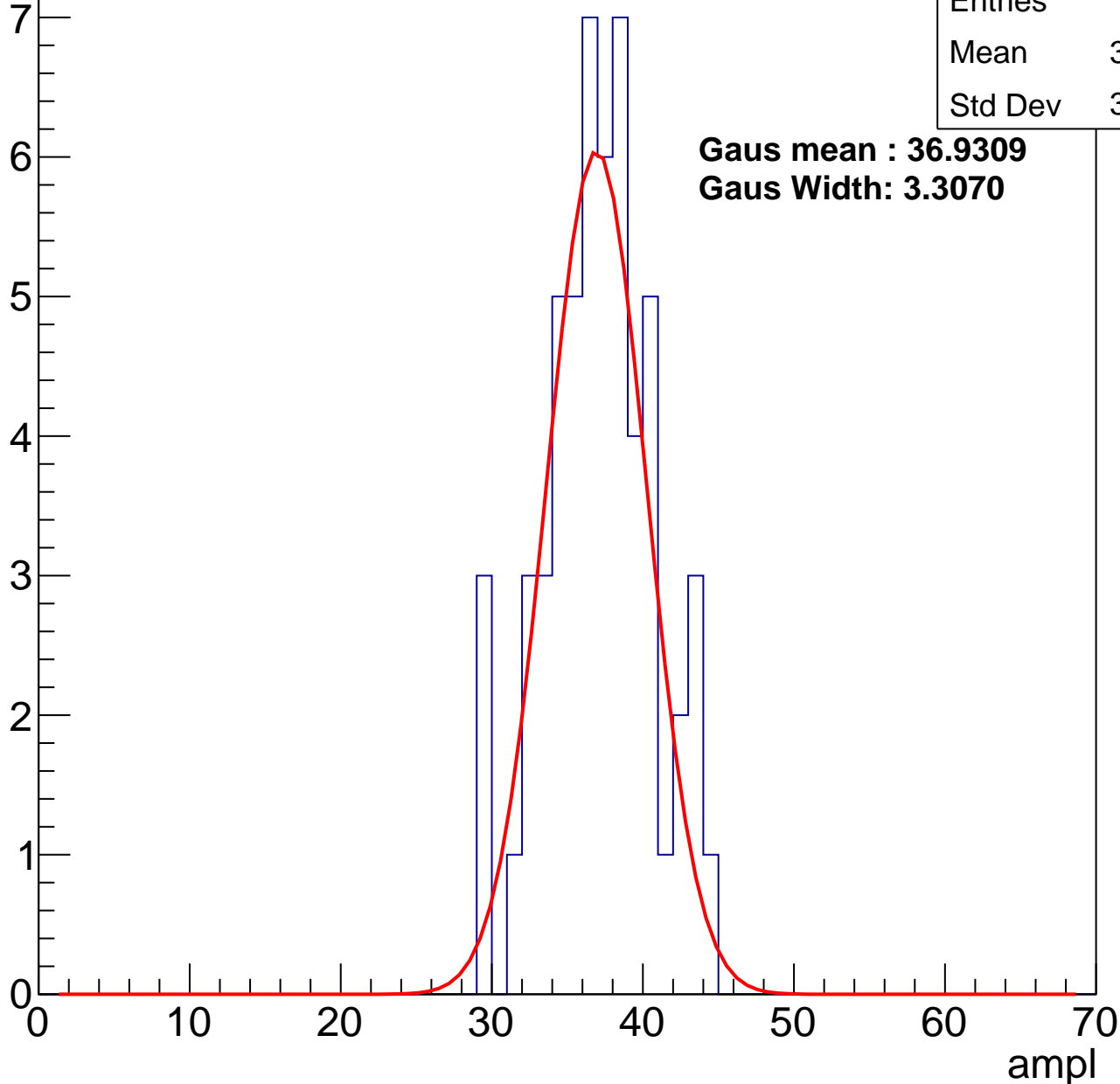
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.64
Std Dev	3.553

Gaus mean : 36.9309

Gaus Width: 3.3070



B0L002S, U4-ch29, adc2

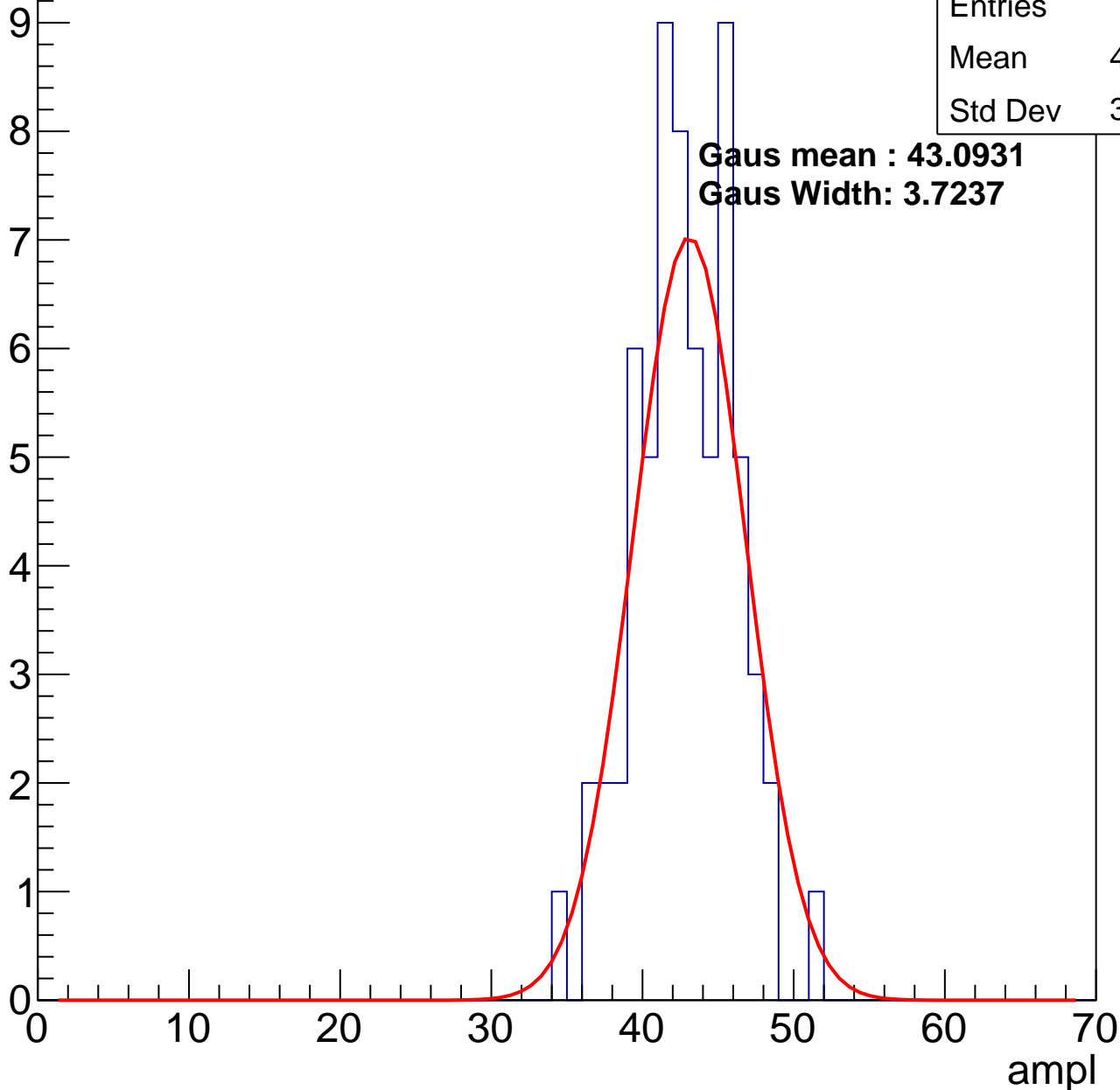
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	42.36
Std Dev	3.274

Gaus mean : 43.0931

Gaus Width: 3.7237

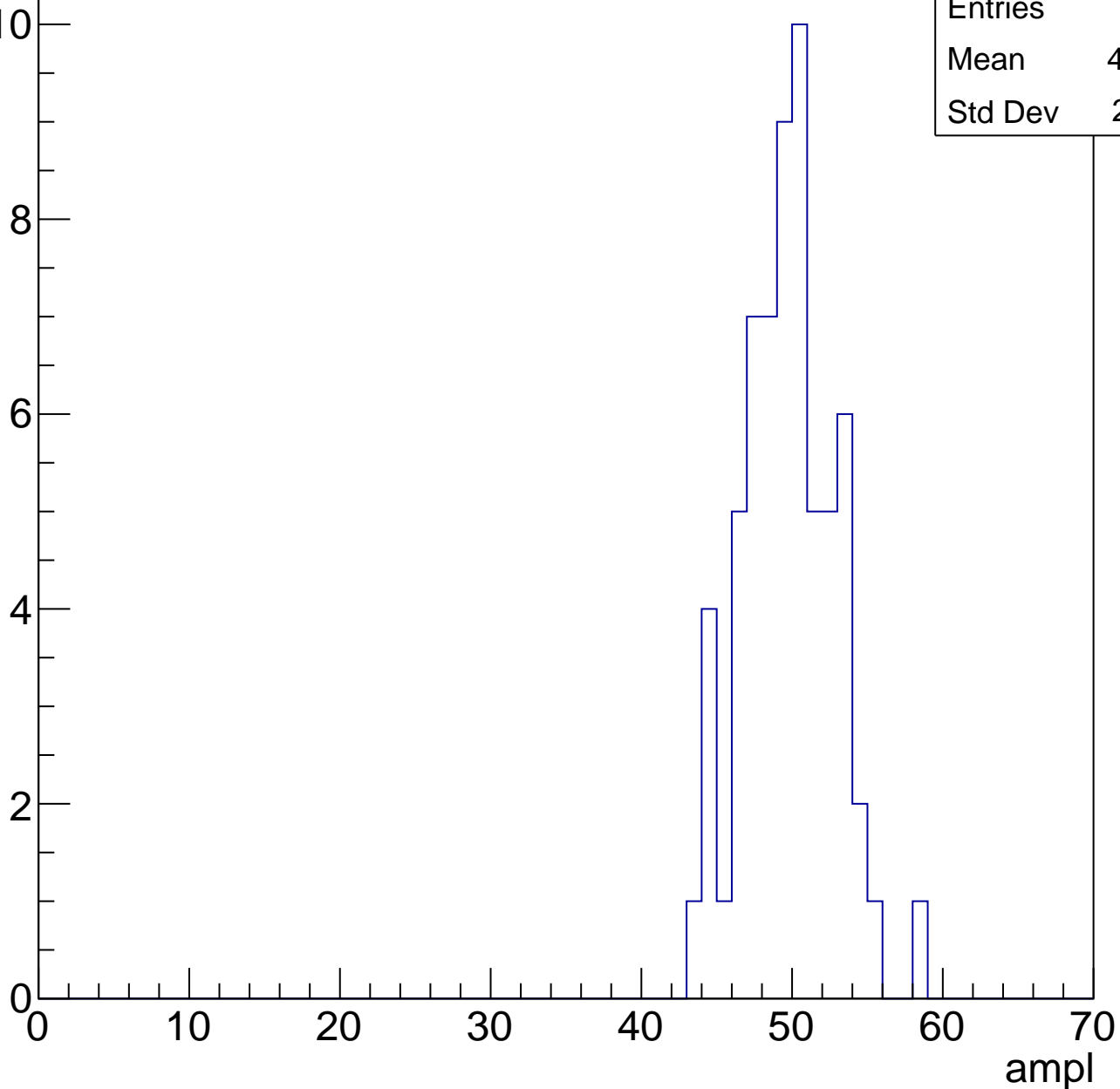


B0L002S, U4-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	49.28
Std Dev	2.971

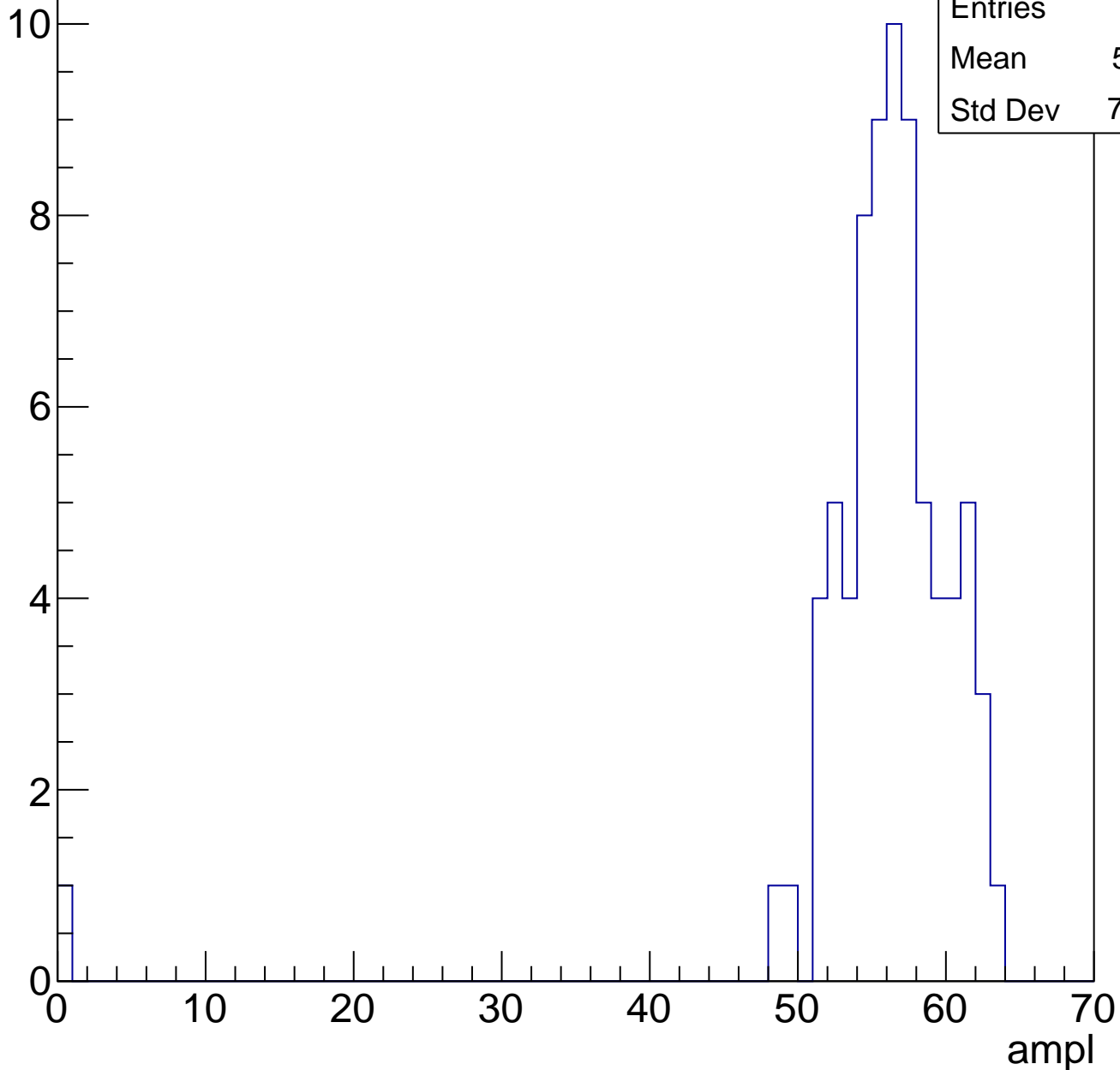


B0L002S, U4-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	55.31
Std Dev	7.243

Entry

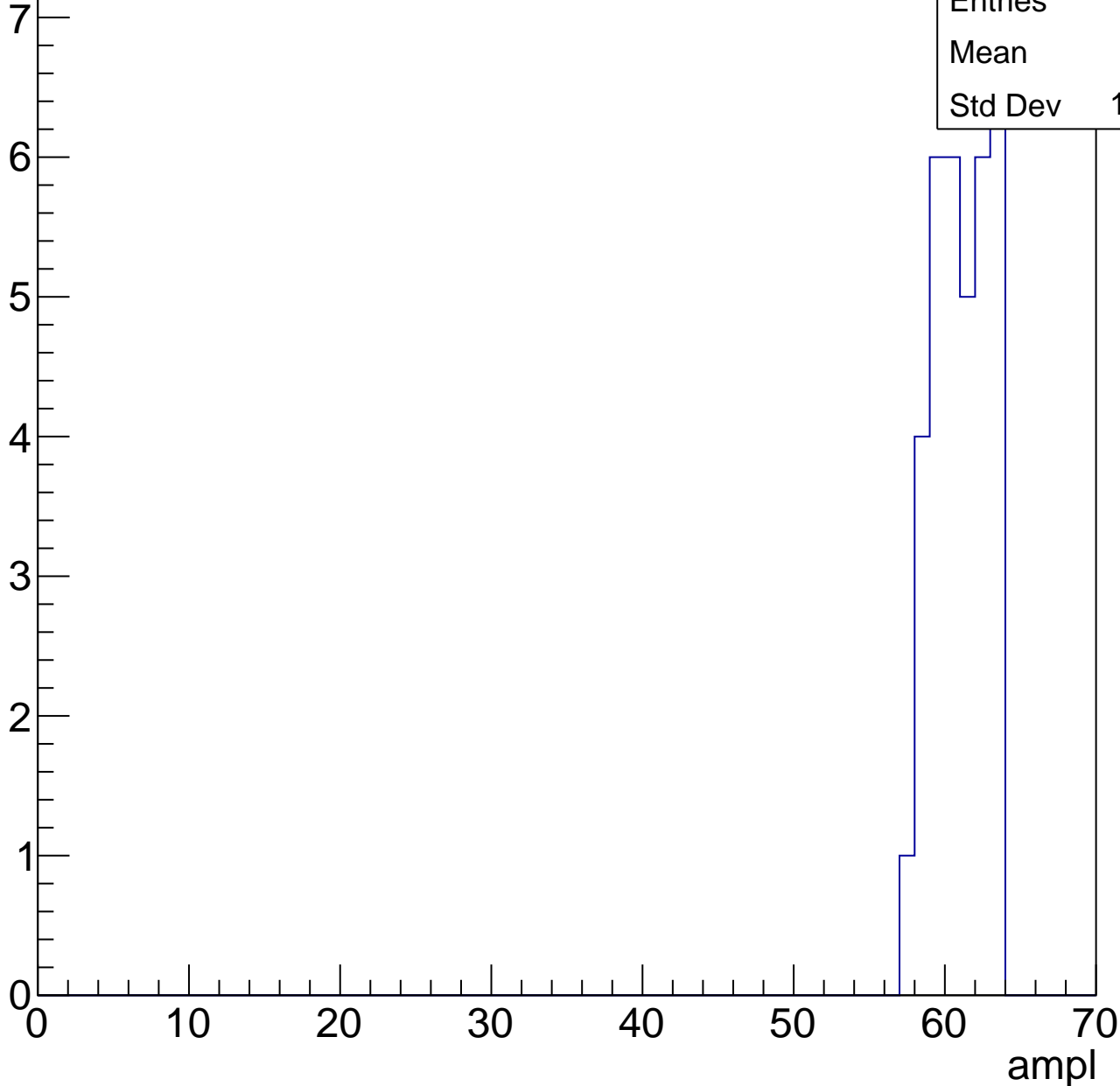


B0L002S, U4-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.6
Std Dev	1.776



B0L002S, U4-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch30, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	28.97
Std Dev	6.318

Gaus mean : 30.6330

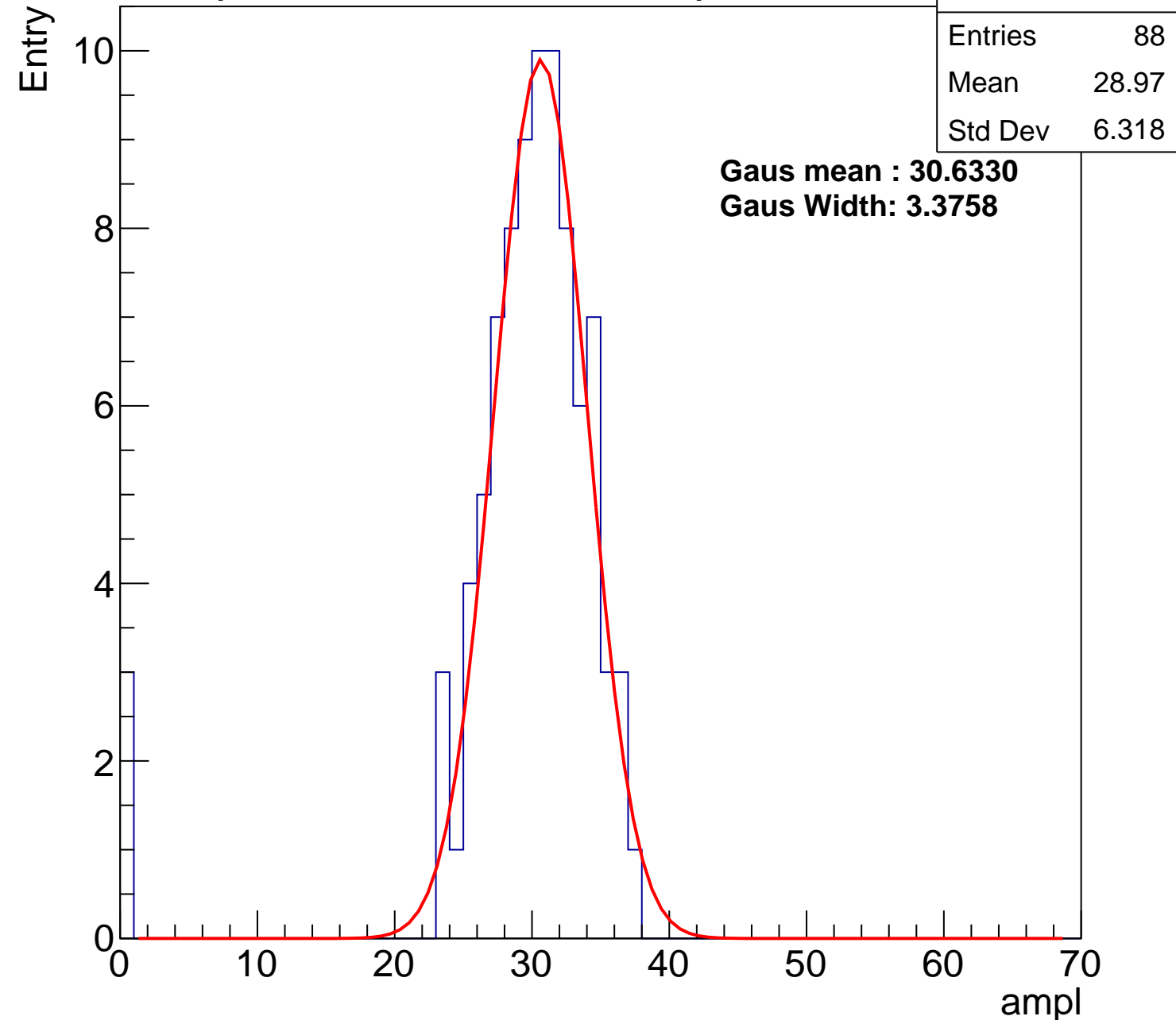
Gaus Width: 3.3758

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch30, adc1

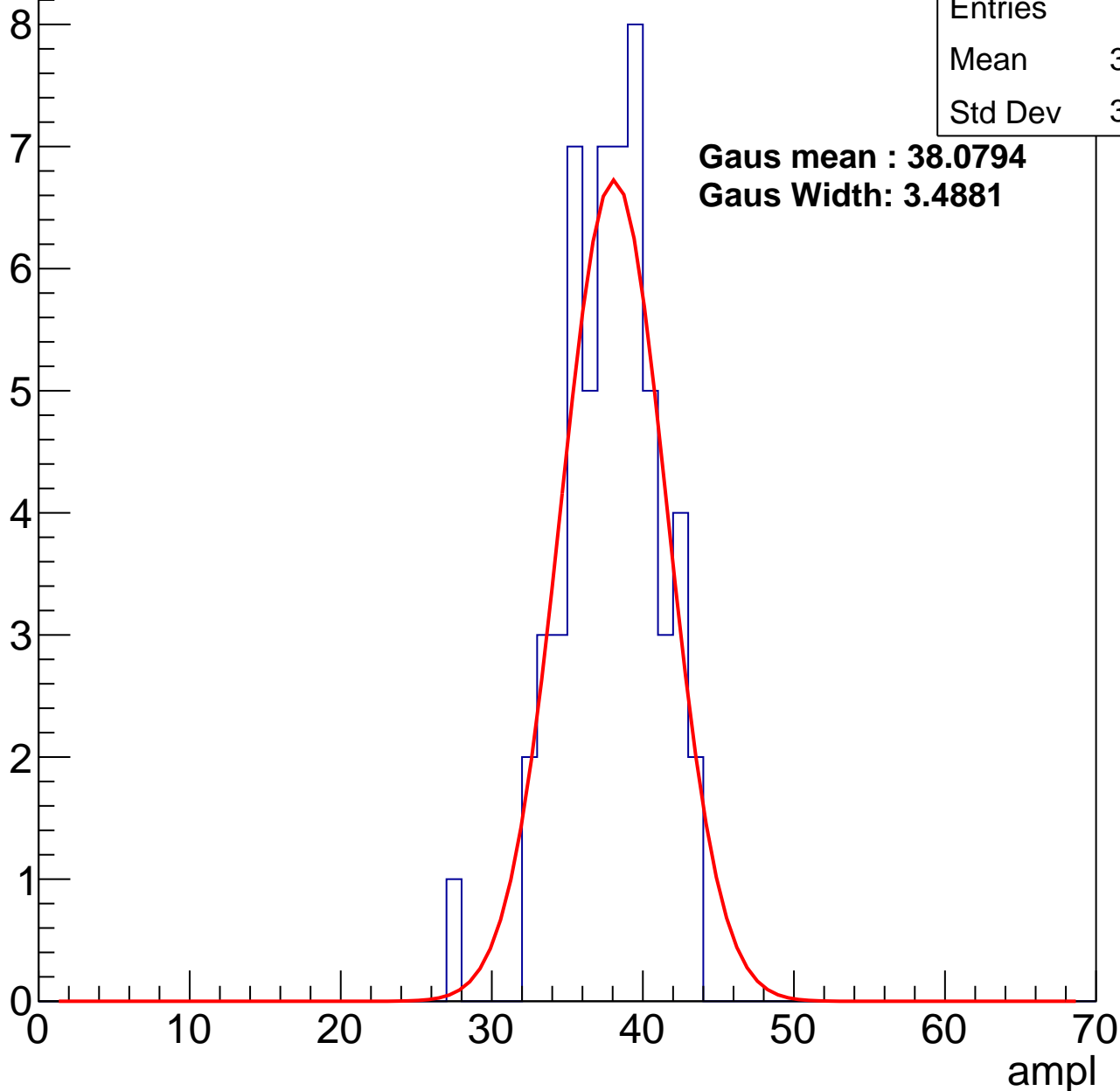
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	37.39
Std Dev	3.116

Gaus mean : 38.0794

Gaus Width: 3.4881



B0L002S, U4-ch30, adc2

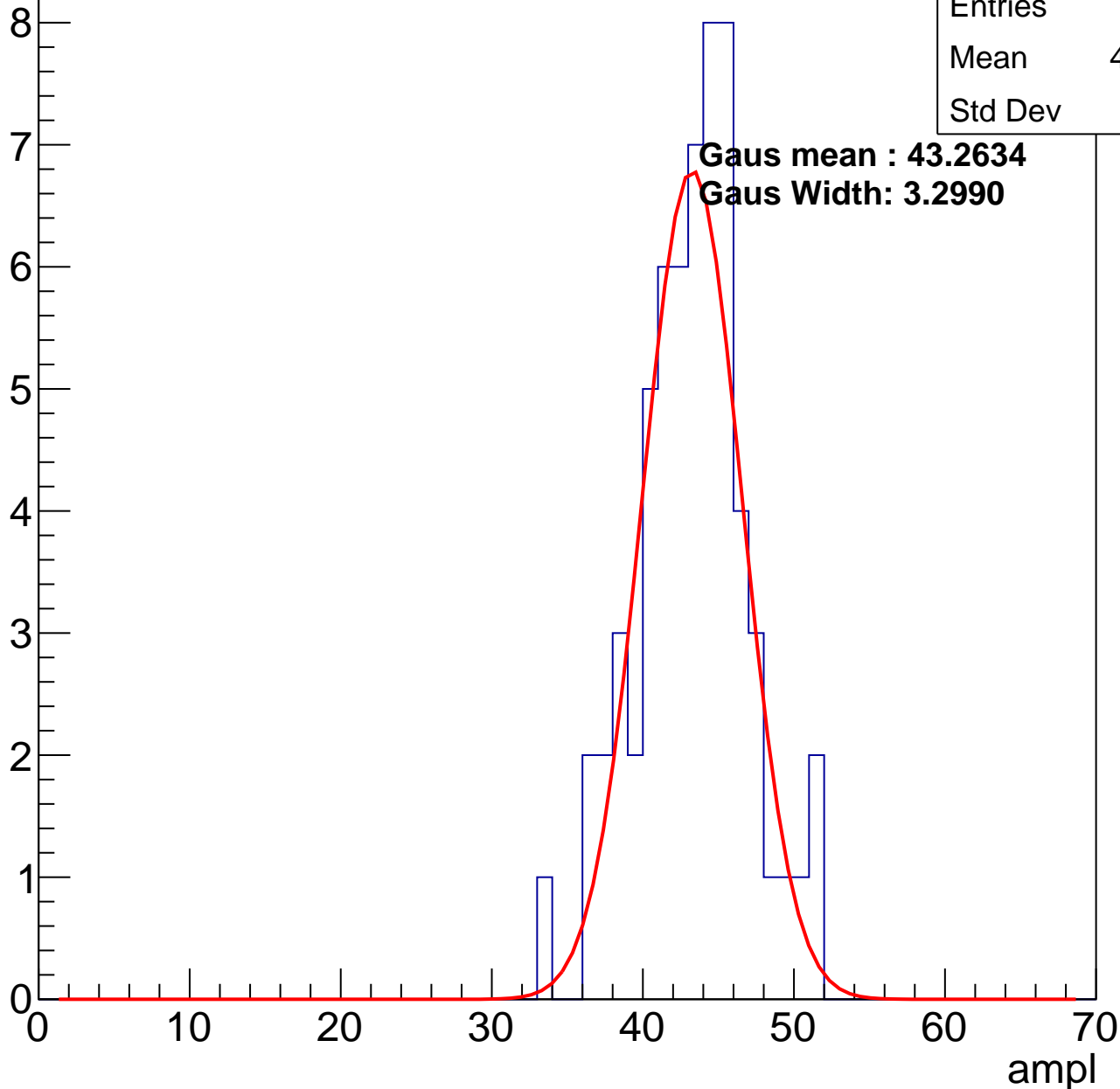
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.84
Std Dev	3.62

Gaus mean : 43.2634

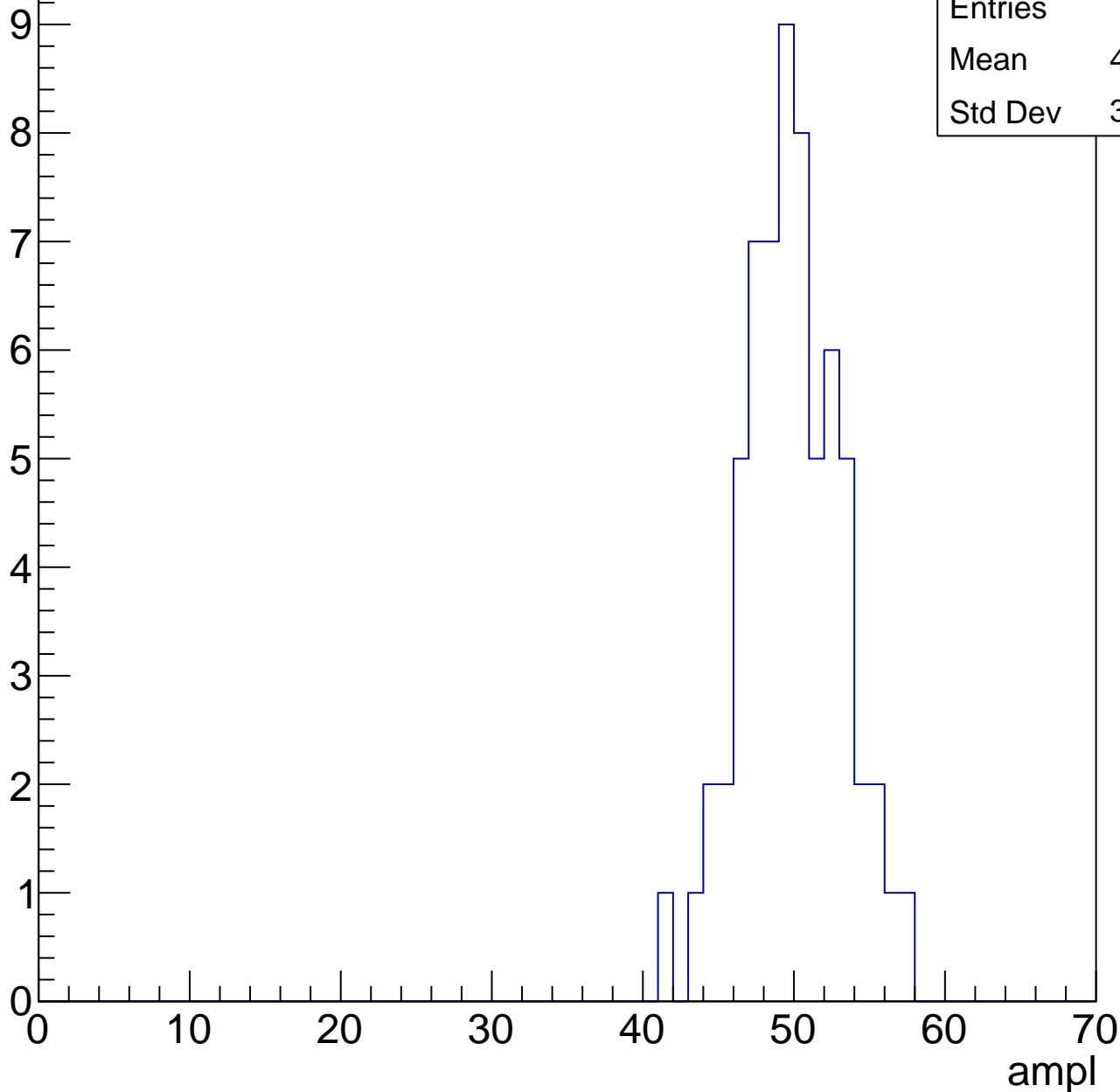
Gaus Width: 3.2990



B0L002S, U4-ch30, adc3

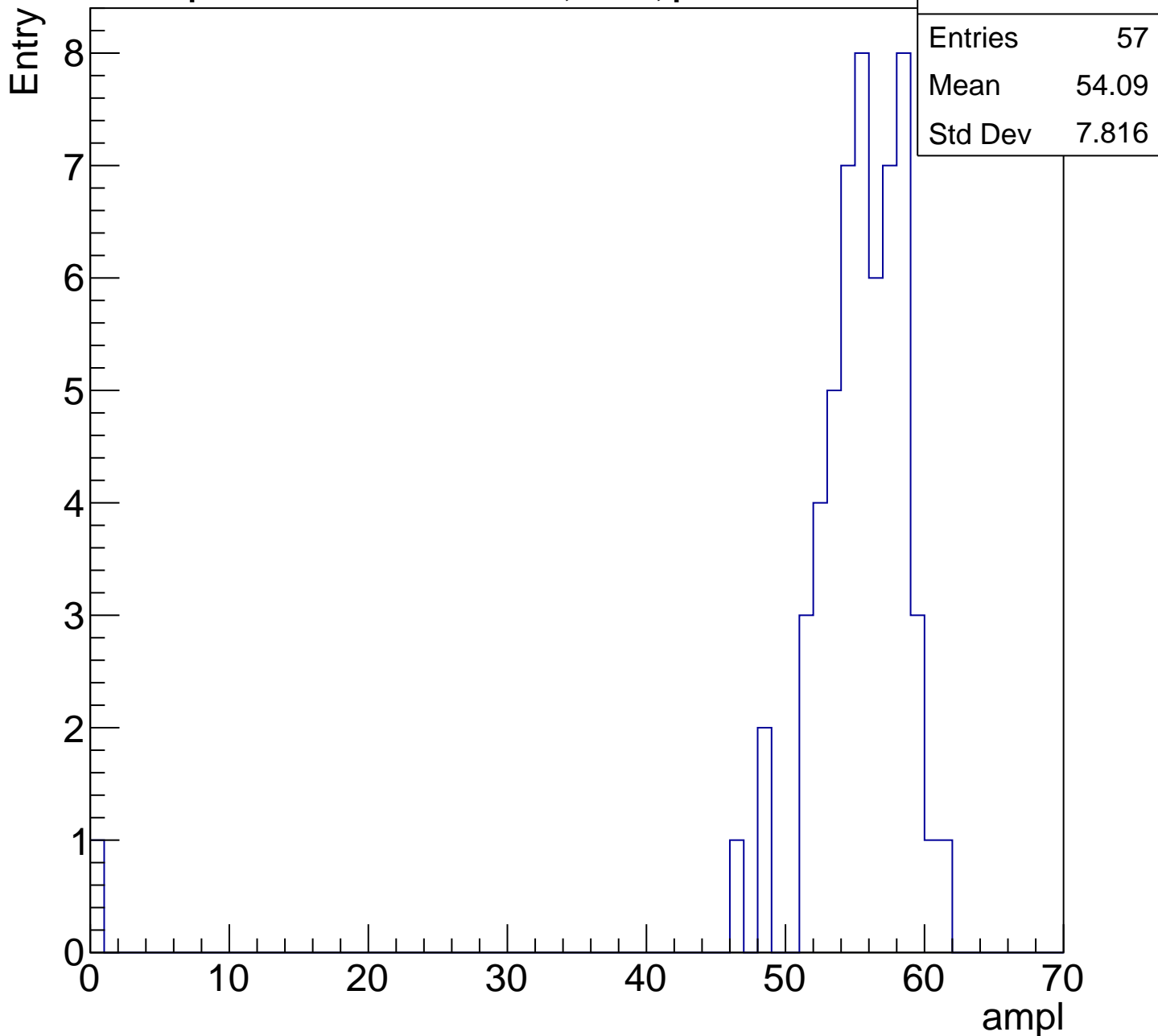
calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

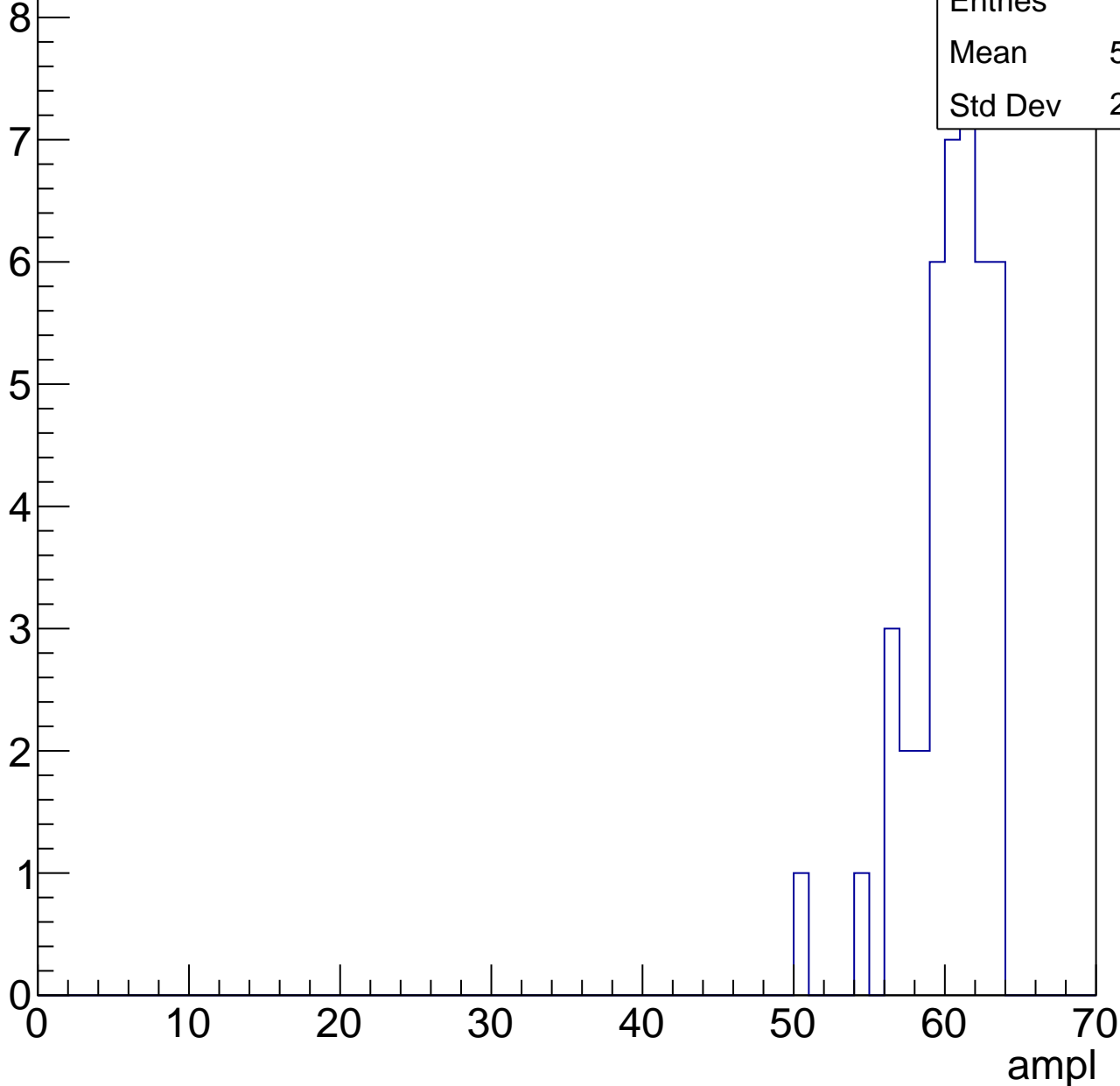


B0L002S, U4-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

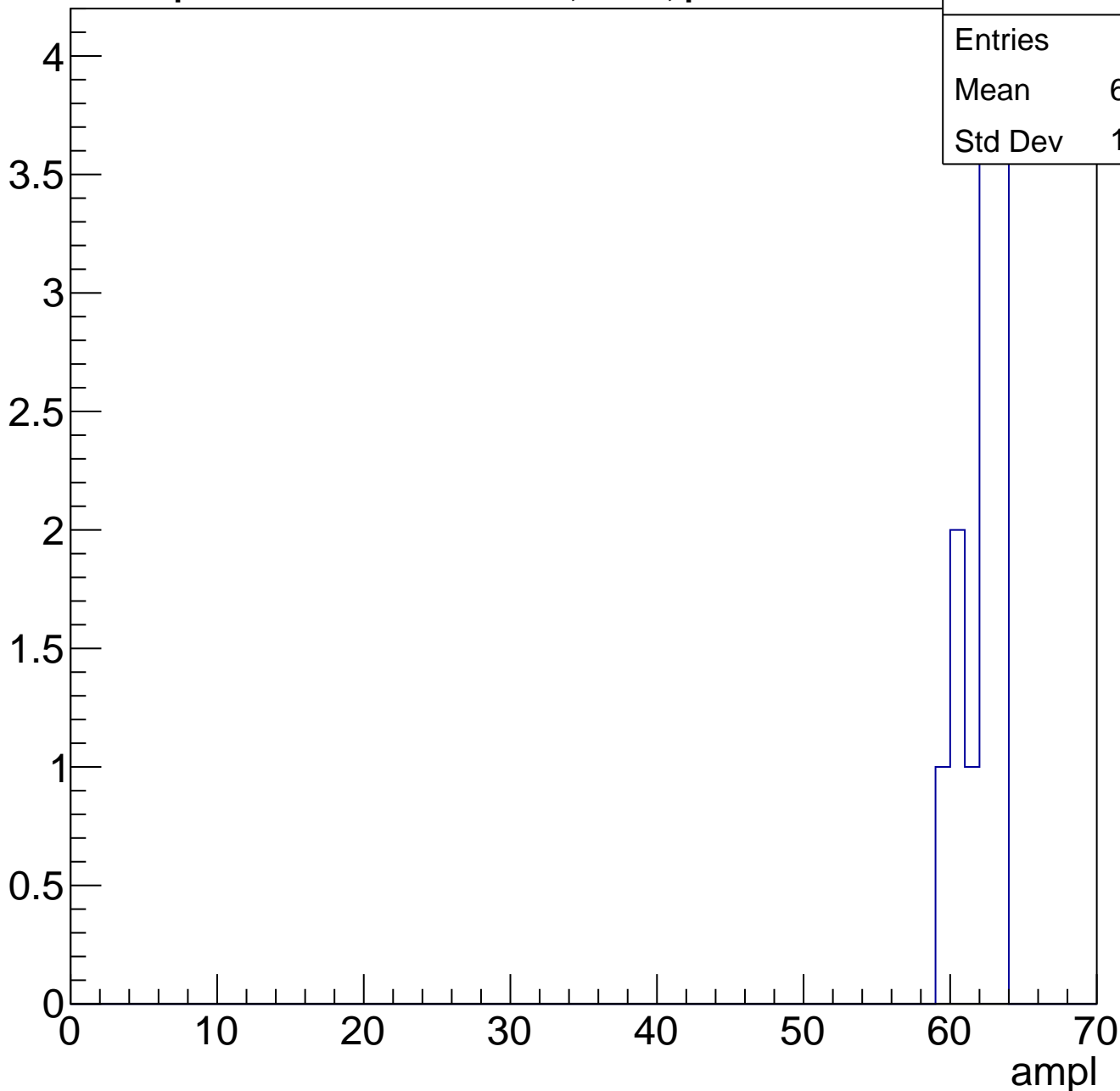
Entries	42
Mean	59.86
Std Dev	2.678



B0L002S, U4-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U4-ch31, adc0

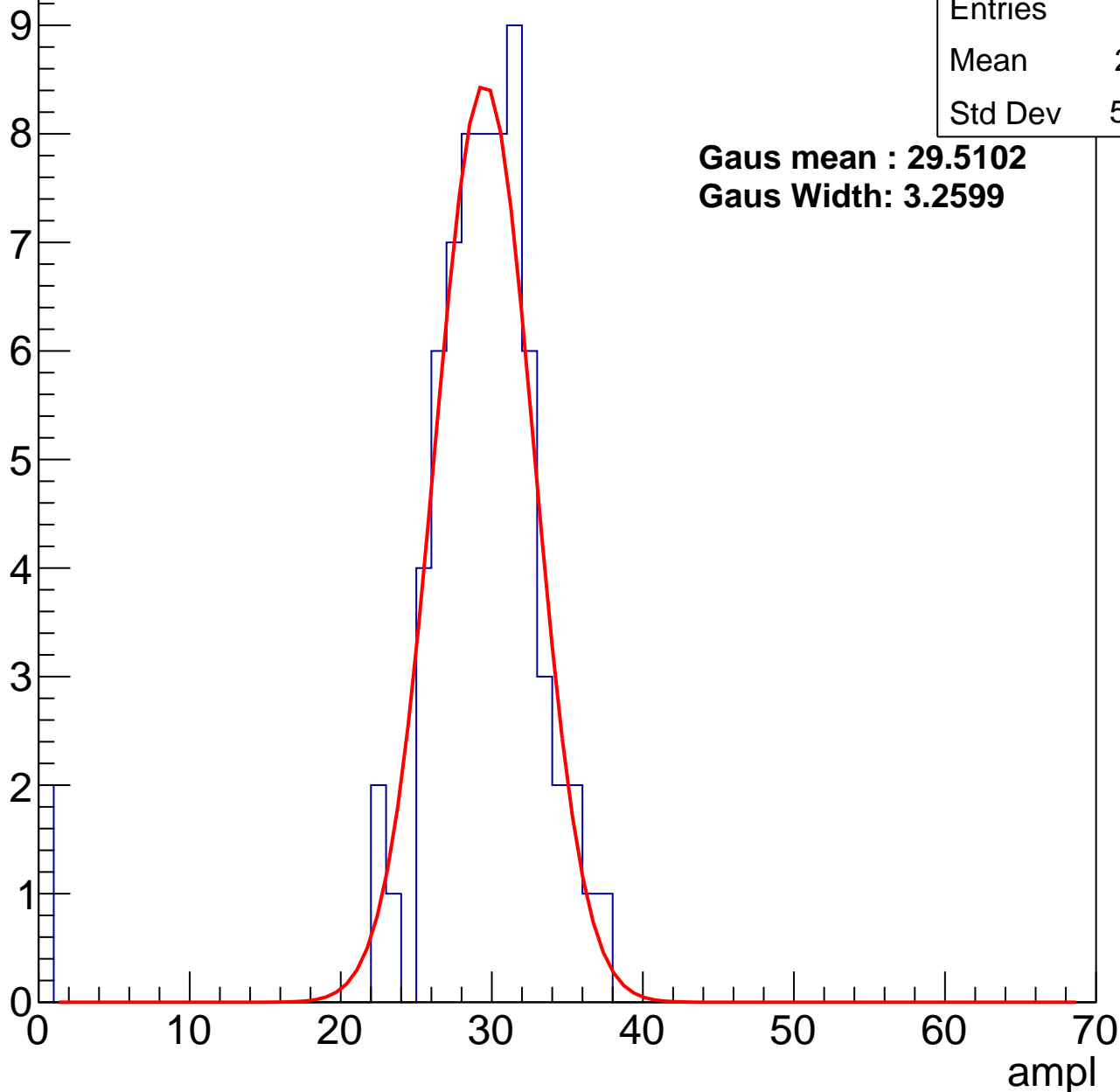
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.41
Std Dev	5.766

Gaus mean : 29.5102

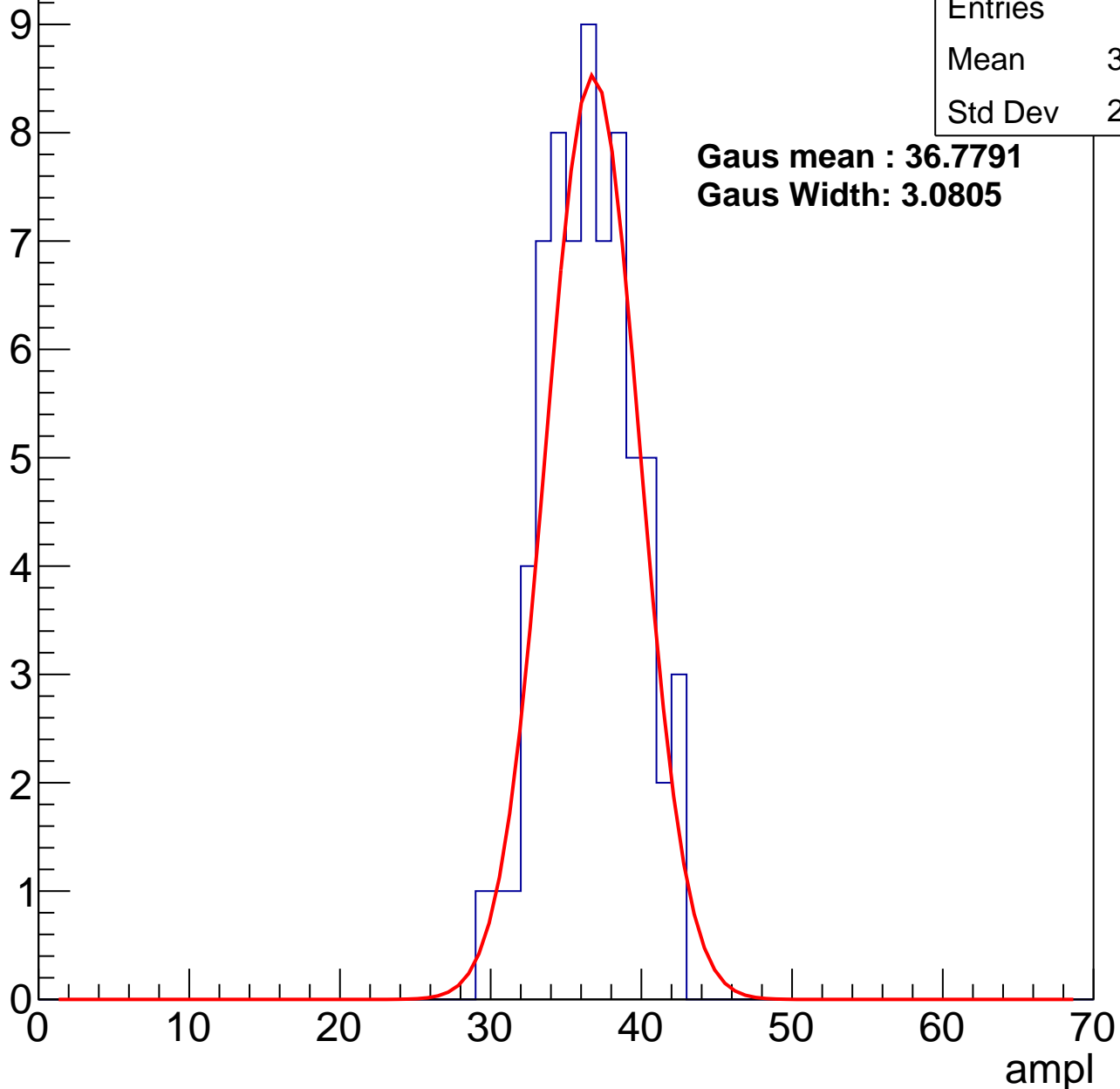
Gaus Width: 3.2599



B0L002S, U4-ch31, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	68
Mean	36.12
Std Dev	2.963

B0L002S, U4-ch31, adc2

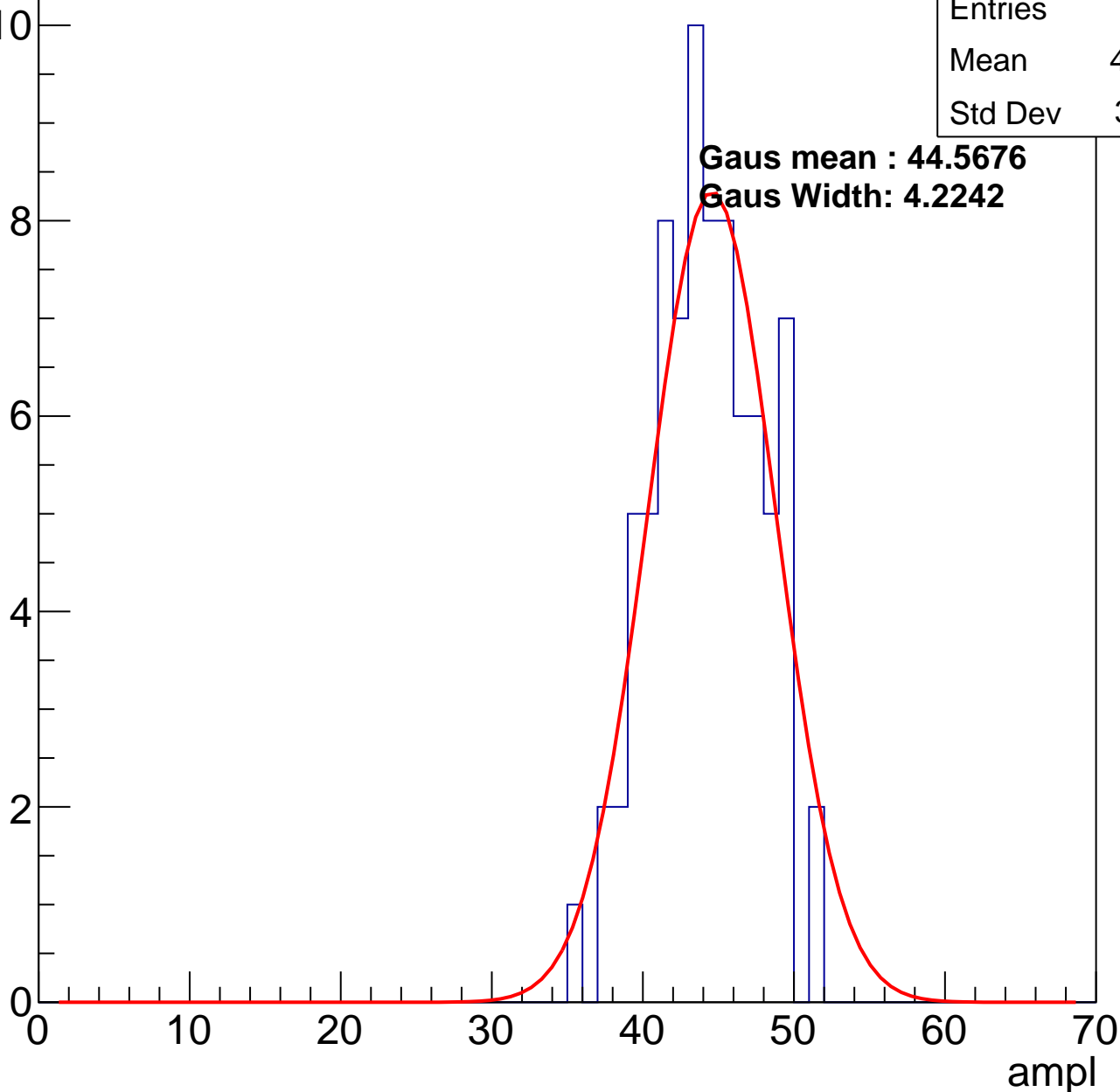
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	43.74
Std Dev	3.491

Gaus mean : 44.5676

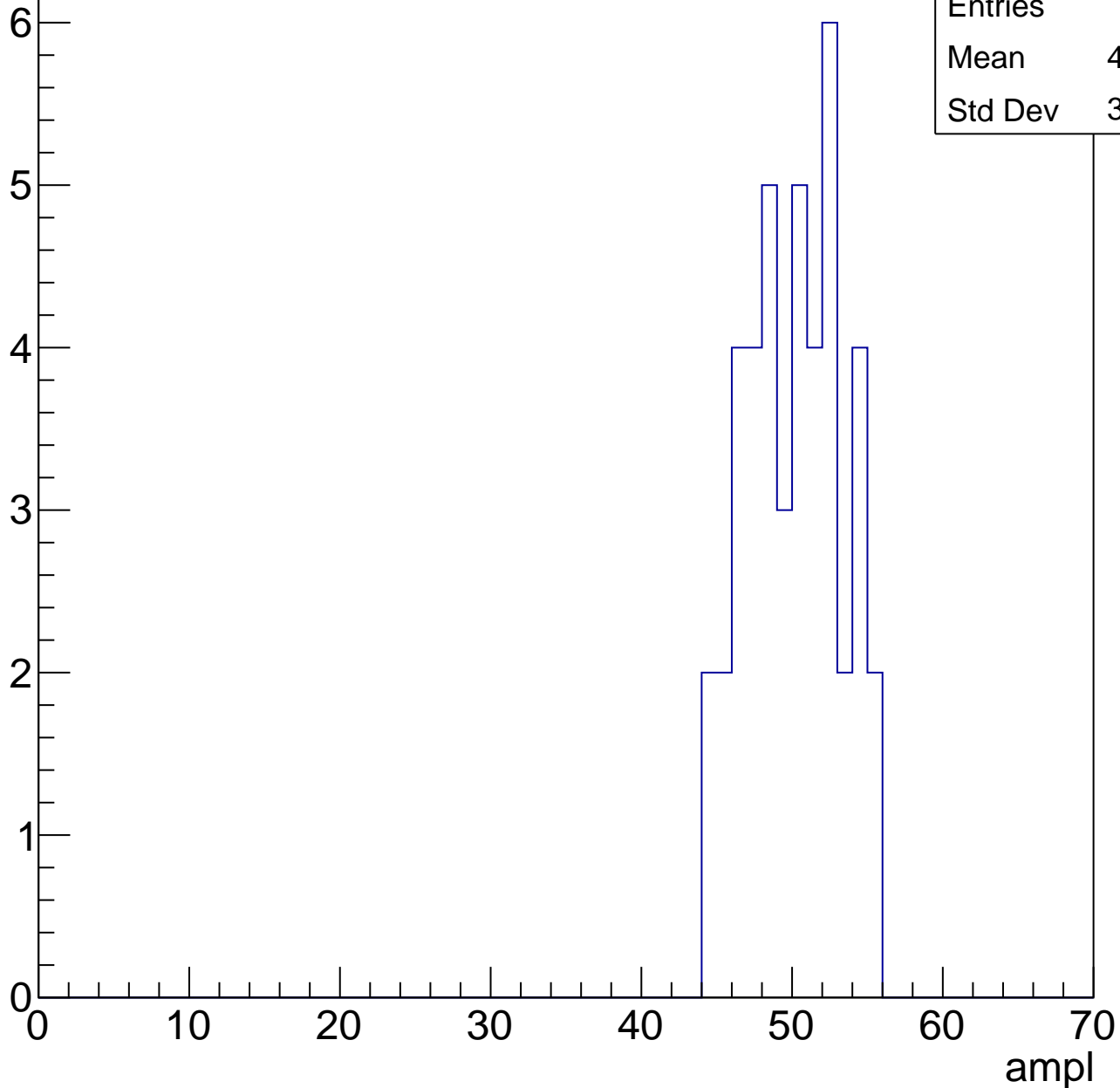
Gaus Width: 4.2242



B0L002S, U4-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



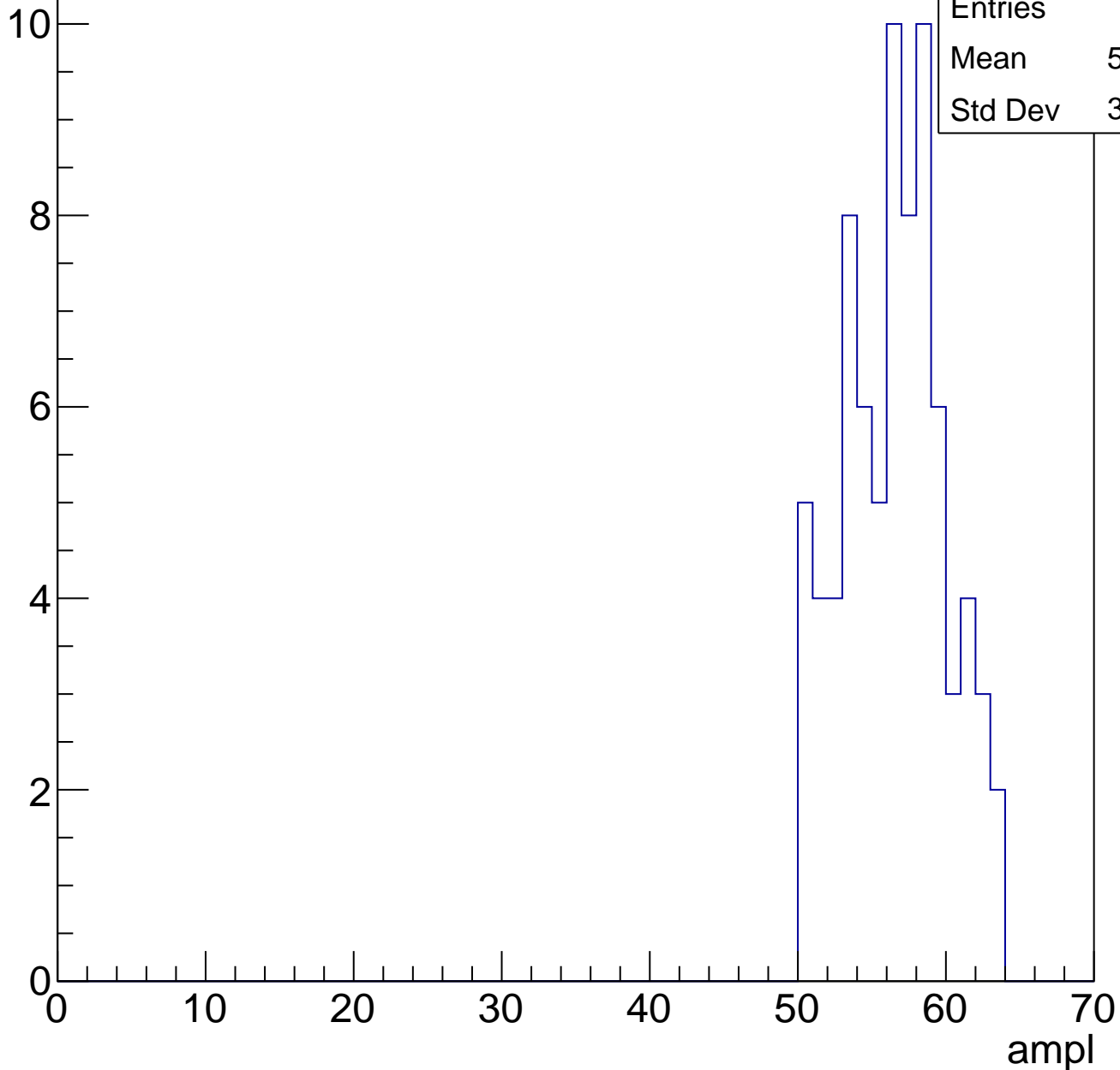
Entries	43
Mean	49.65
Std Dev	3.049

B0L002S, U4-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	56.04
Std Dev	3.399

Entry

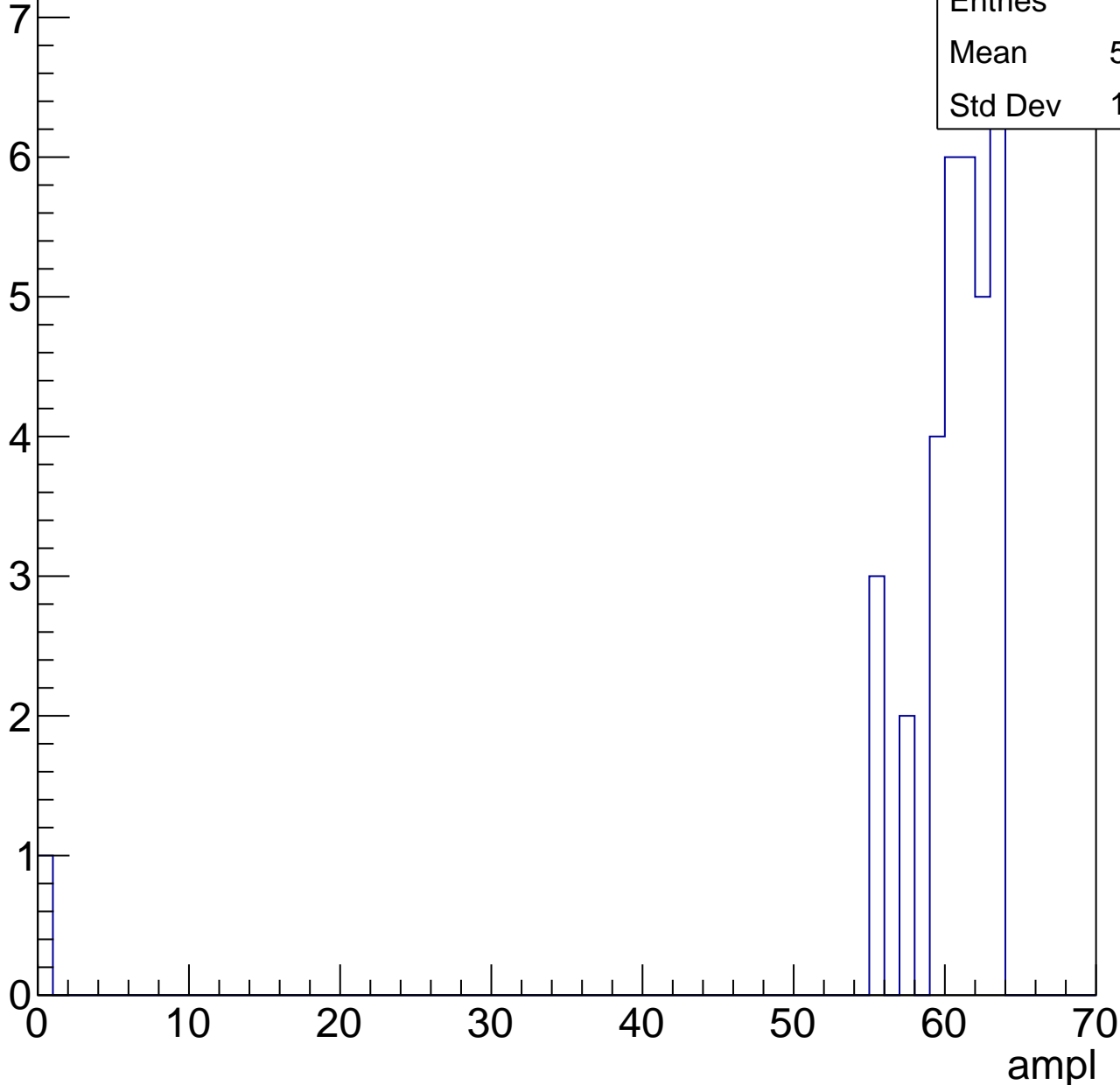


B0L002S, U4-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.59
Std Dev	10.46



B0L002S, U4-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

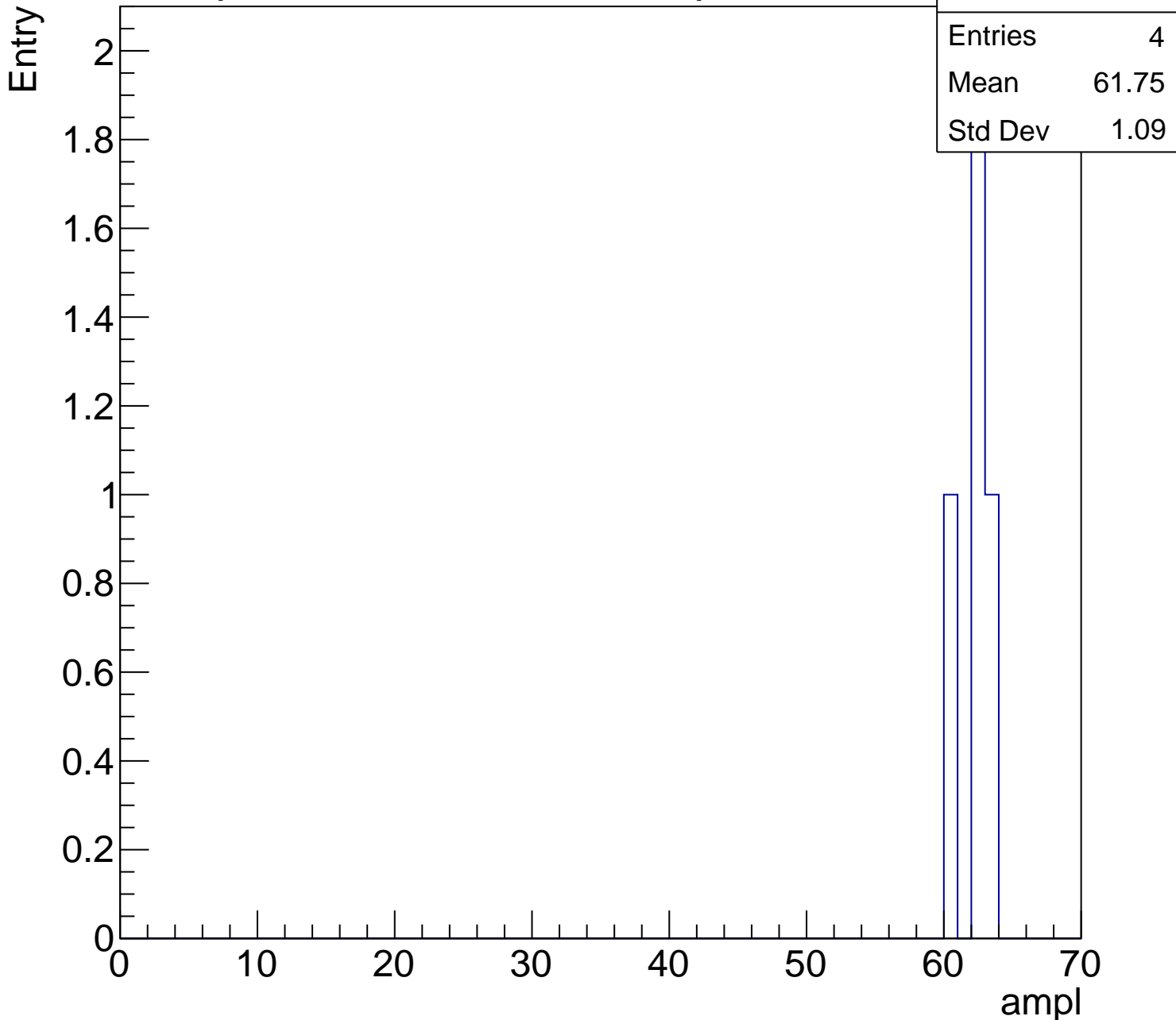
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch32, adc0

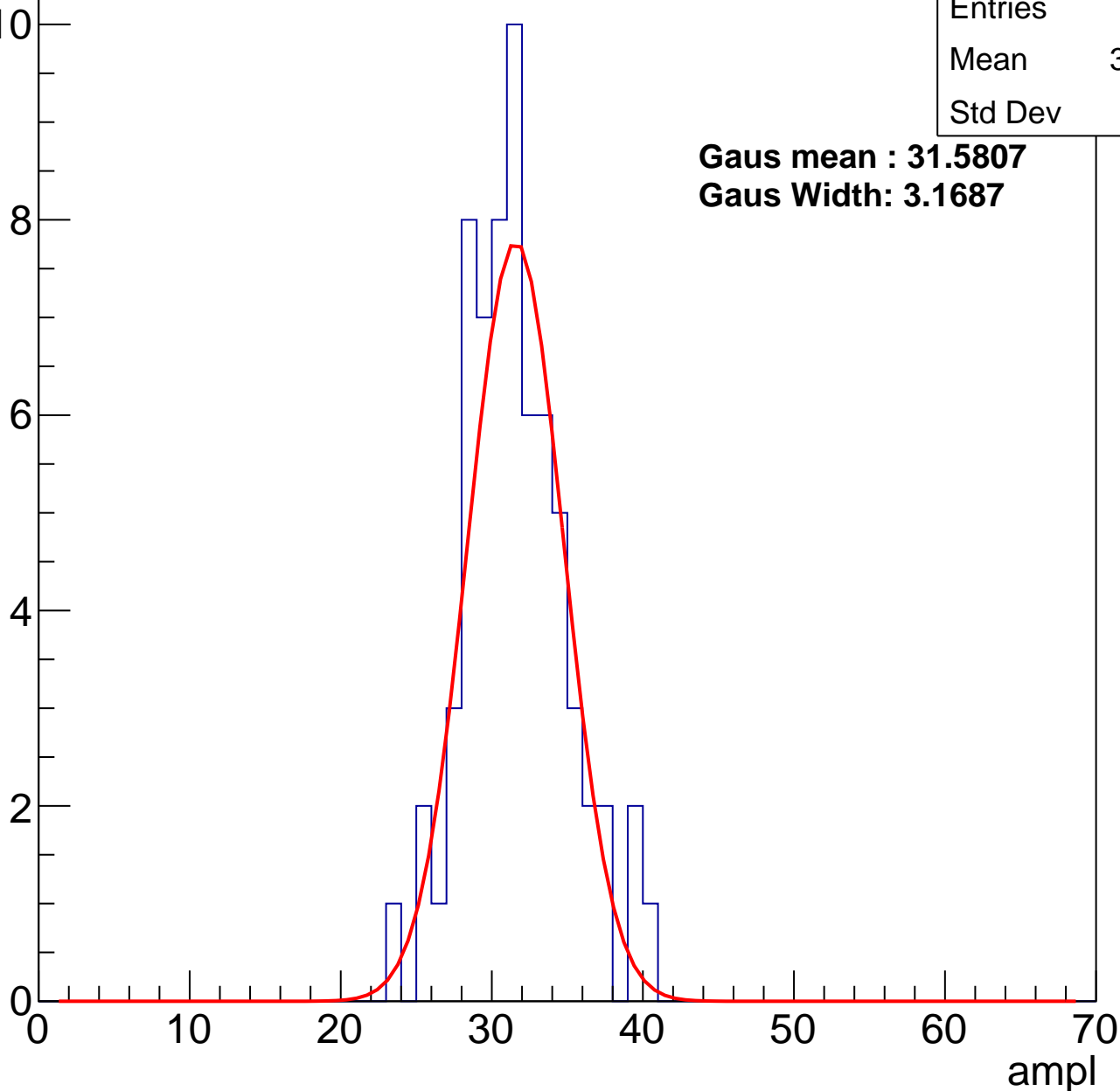
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	31.13
Std Dev	3.39

Gaus mean : 31.5807

Gaus Width: 3.1687



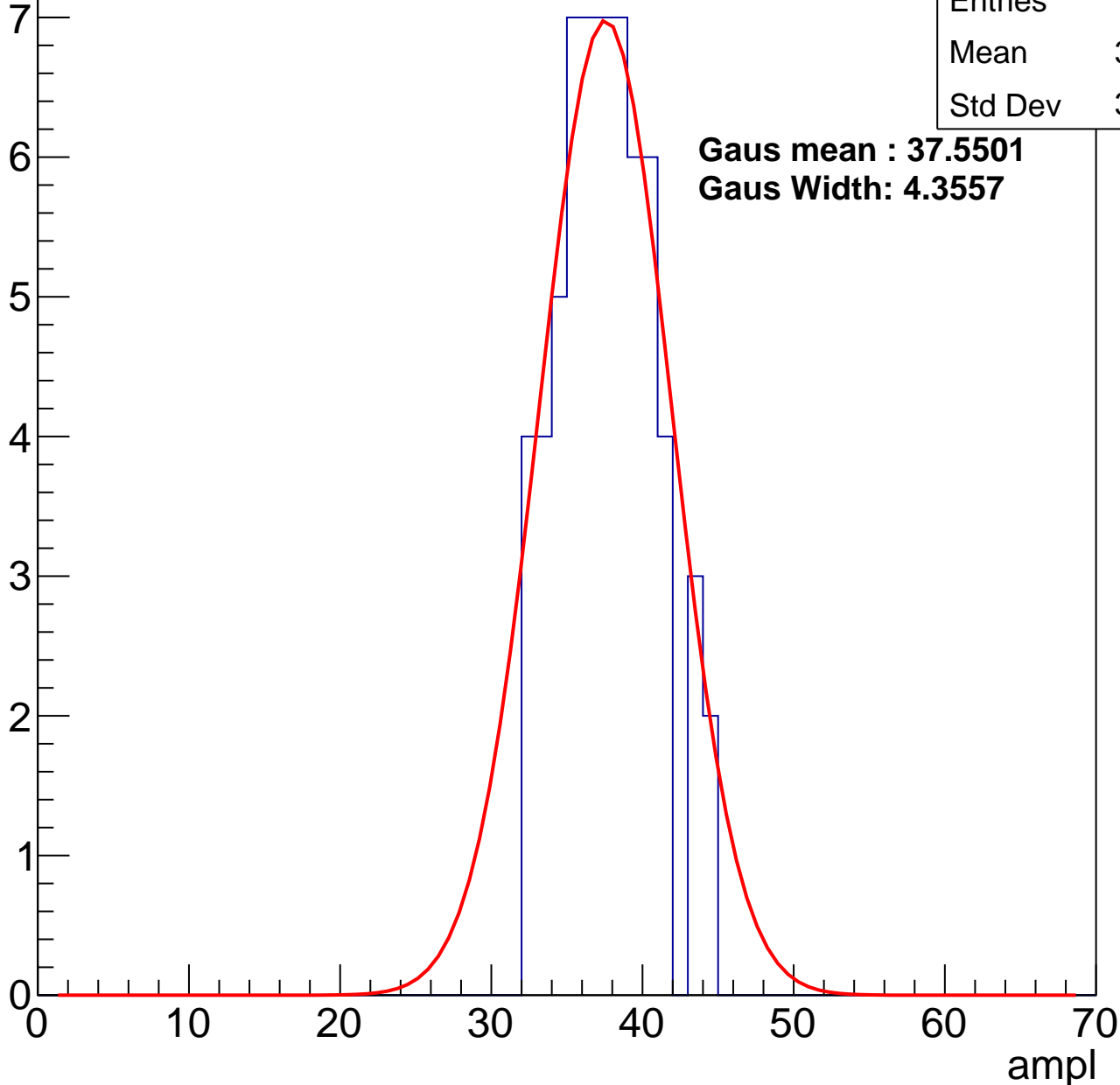
B0L002S, U4-ch32, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.21
Std Dev	3.101

Gaus mean : 37.5501
Gaus Width: 4.3557



B0L002S, U4-ch32, adc2

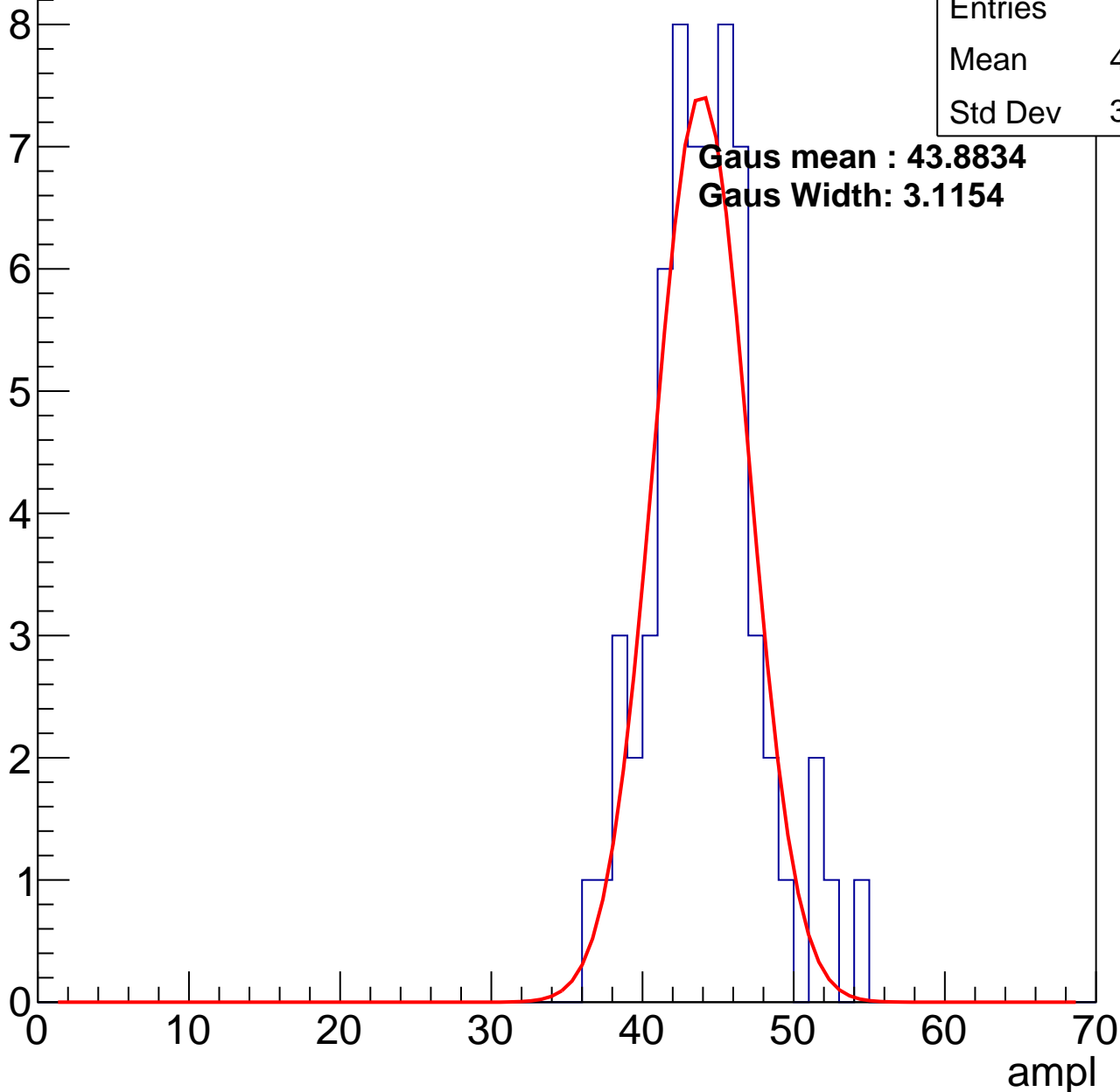
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.68
Std Dev	3.536

Gaus mean : 43.8834

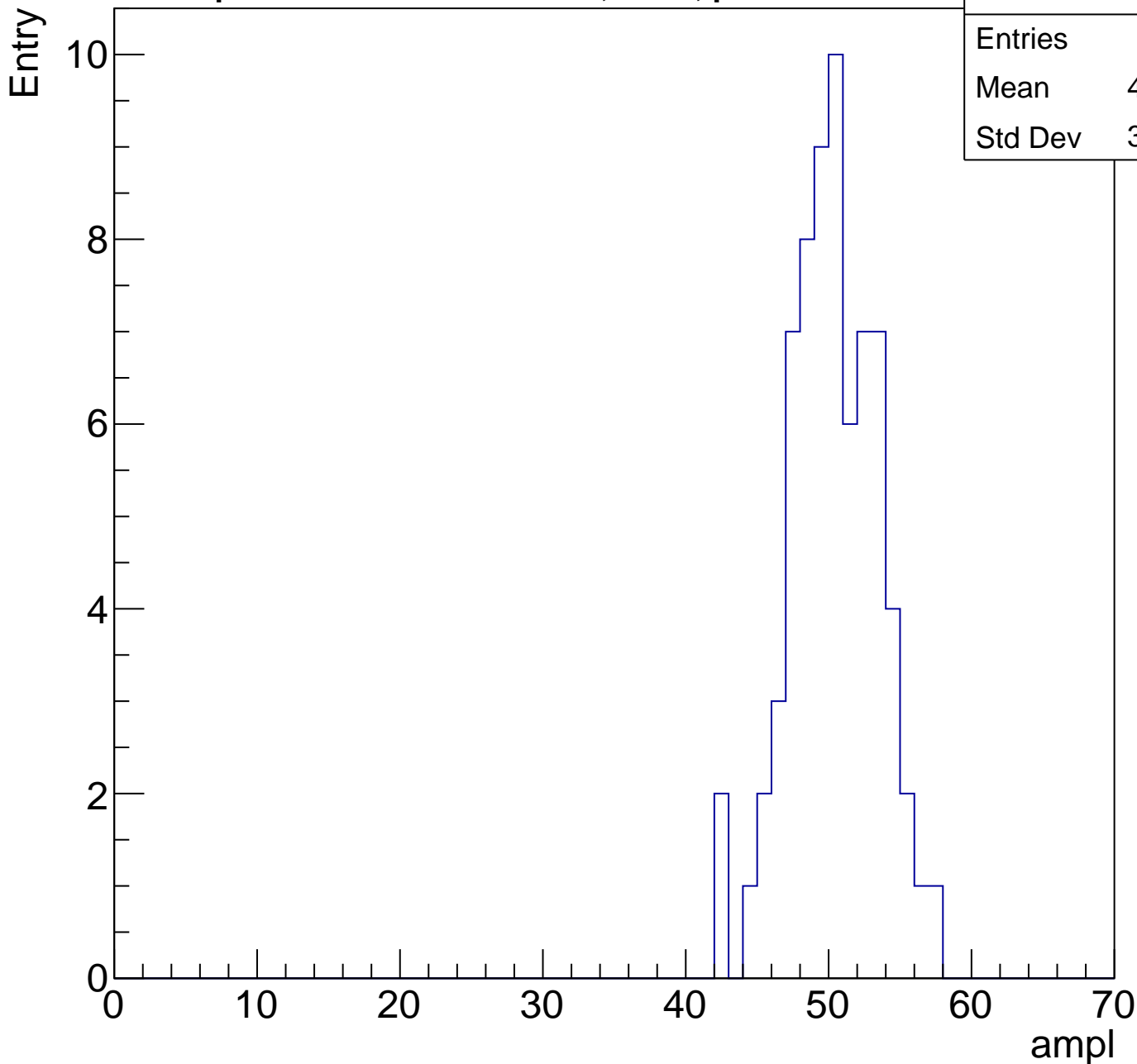
Gaus Width: 3.1154



B0L002S, U4-ch32, adc3

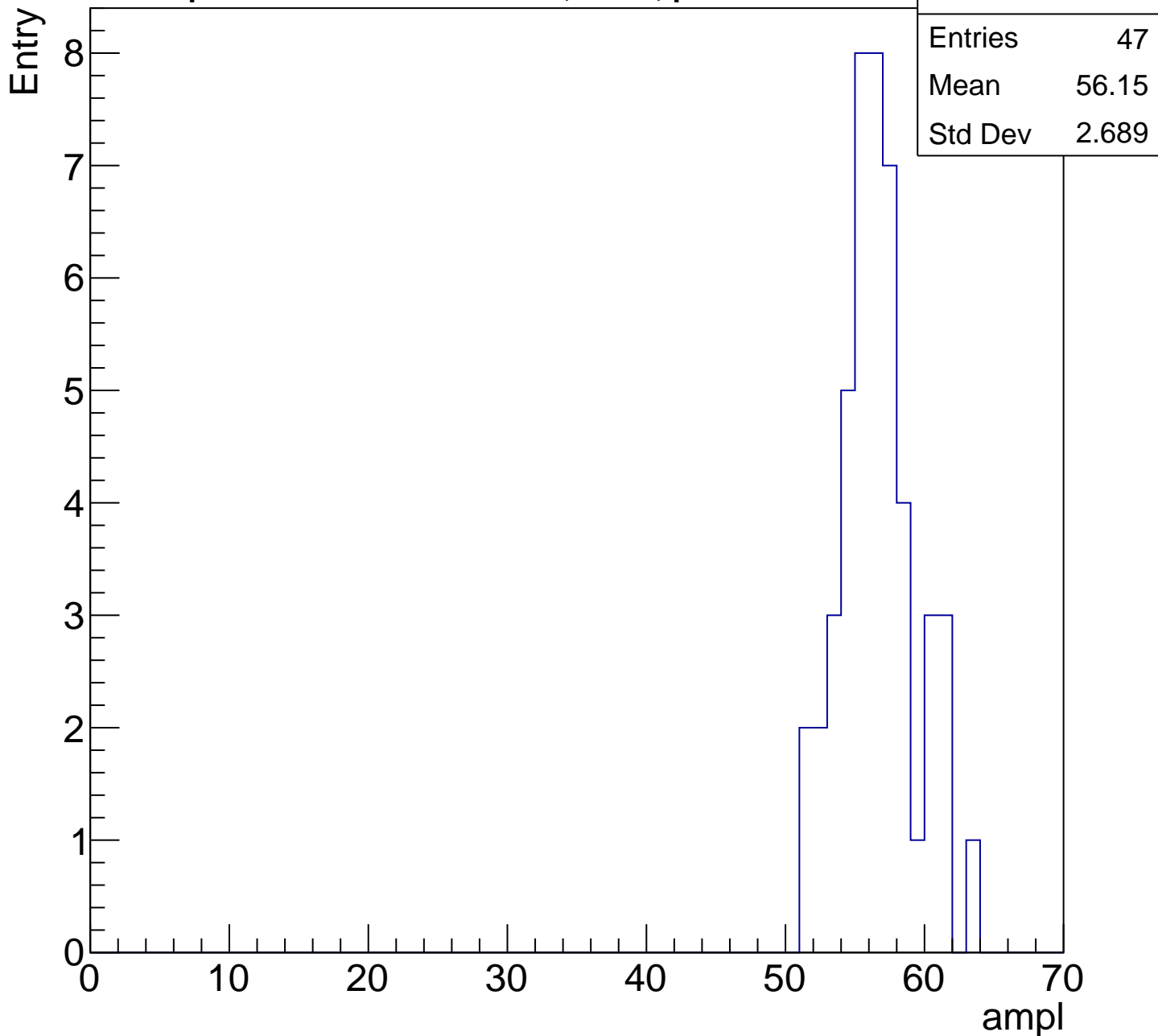
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	49.86
Std Dev	3.072



B0L002S, U4-ch32, adc4

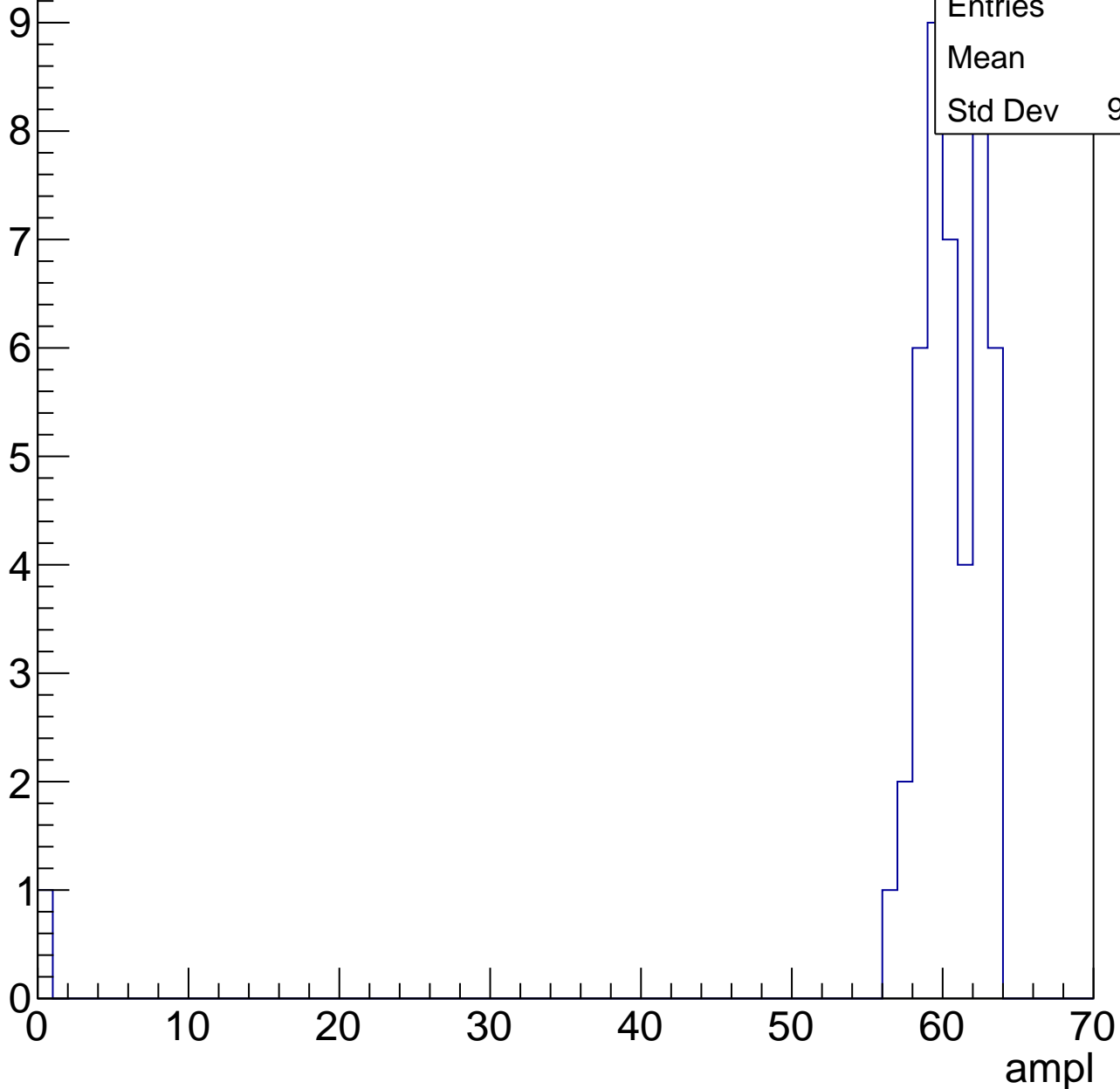
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

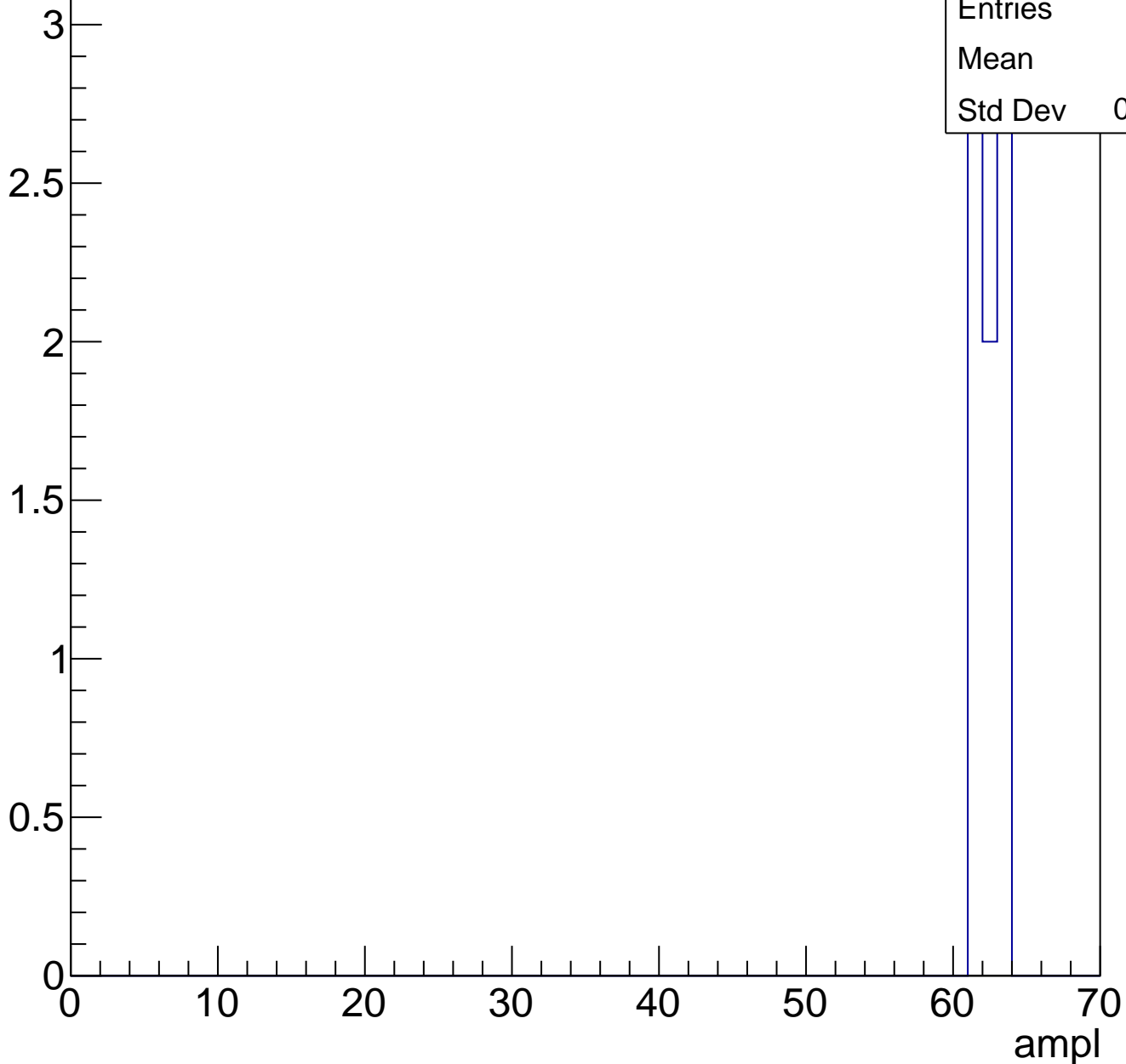
Entry



B0L002S, U4-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch33, adc0

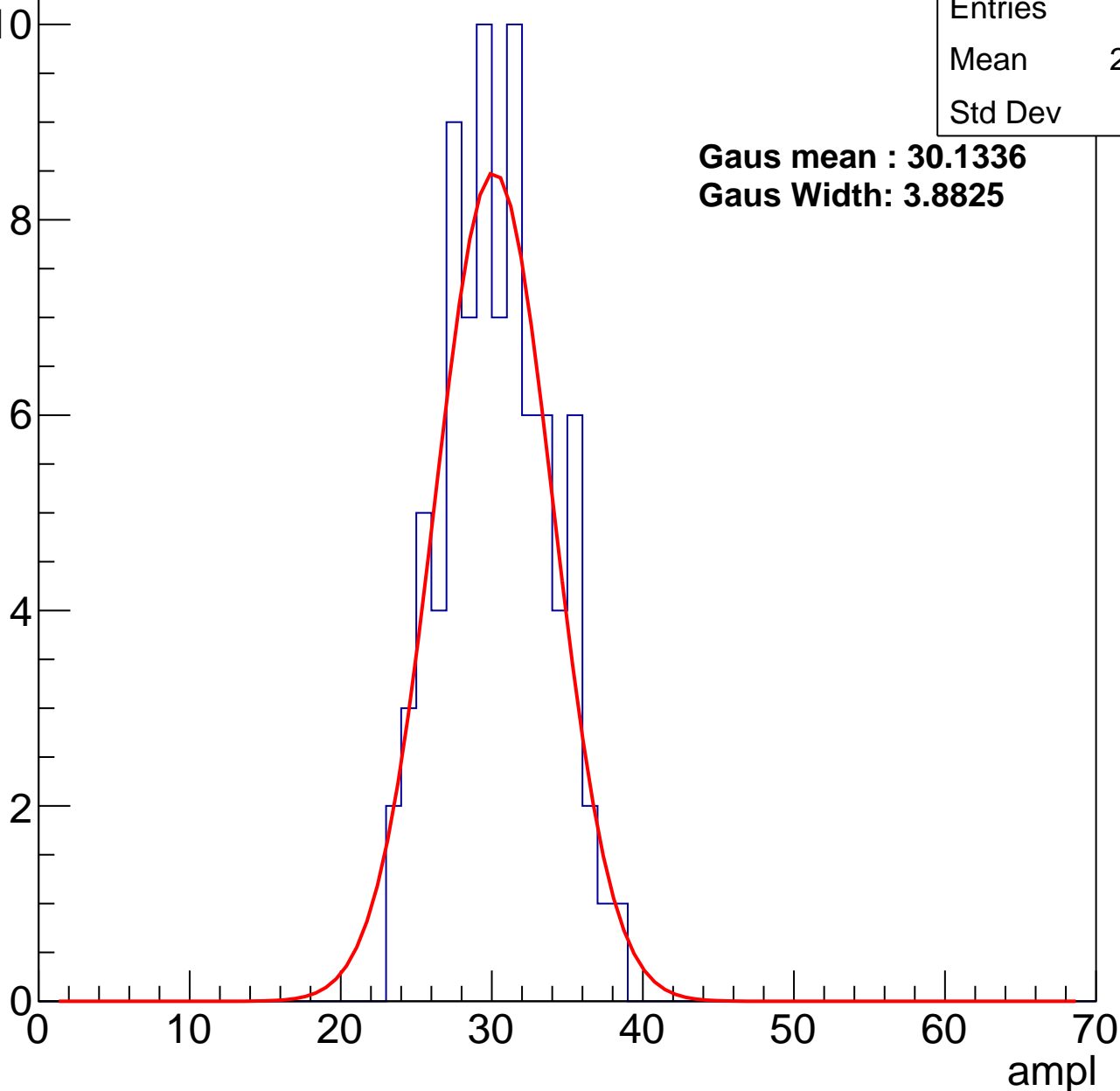
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	29.87
Std Dev	3.47

Gaus mean : 30.1336

Gaus Width: 3.8825



B0L002S, U4-ch33, adc1

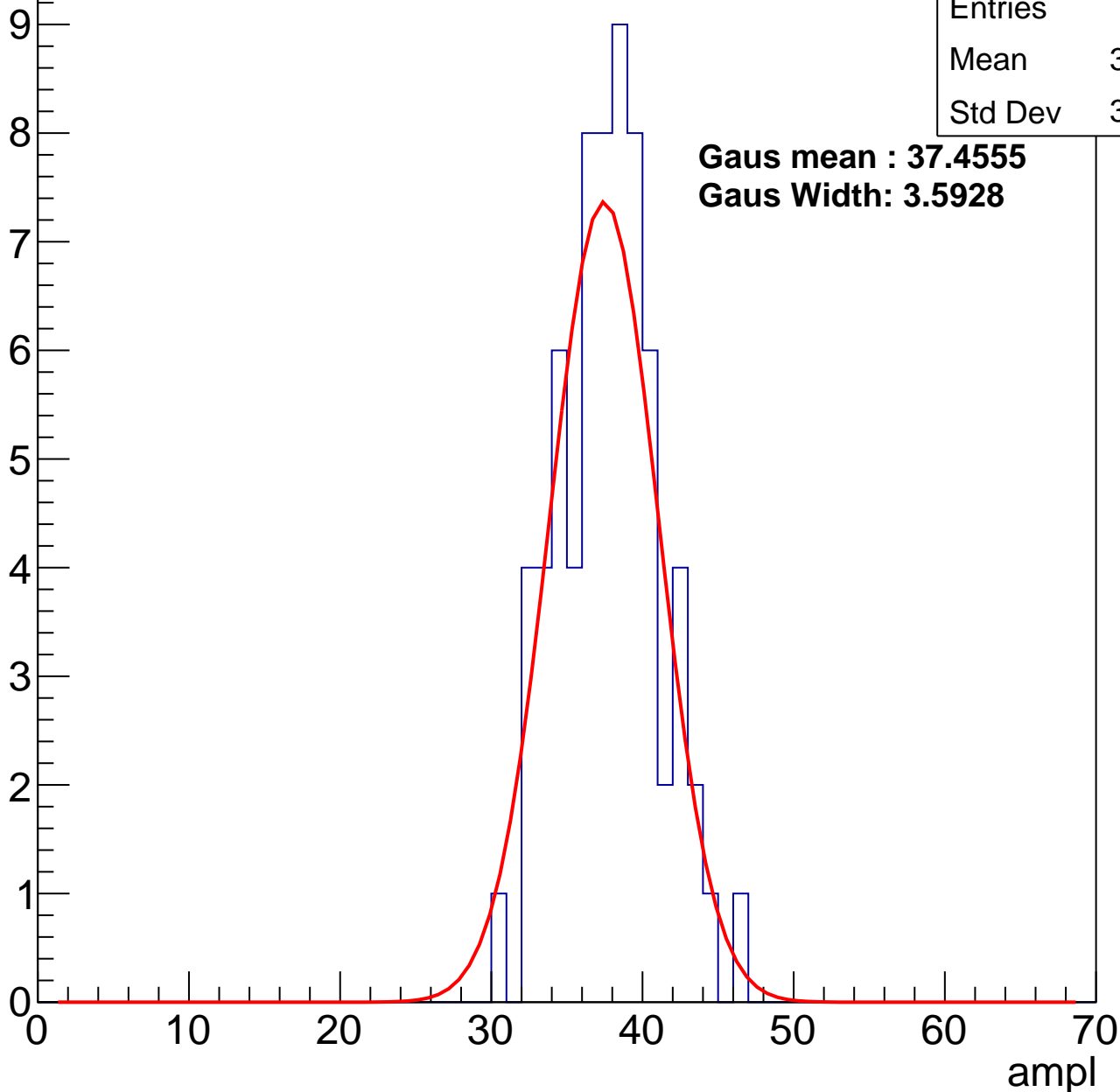
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.32
Std Dev	3.238

Gaus mean : 37.4555

Gaus Width: 3.5928



B0L002S, U4-ch33, adc2

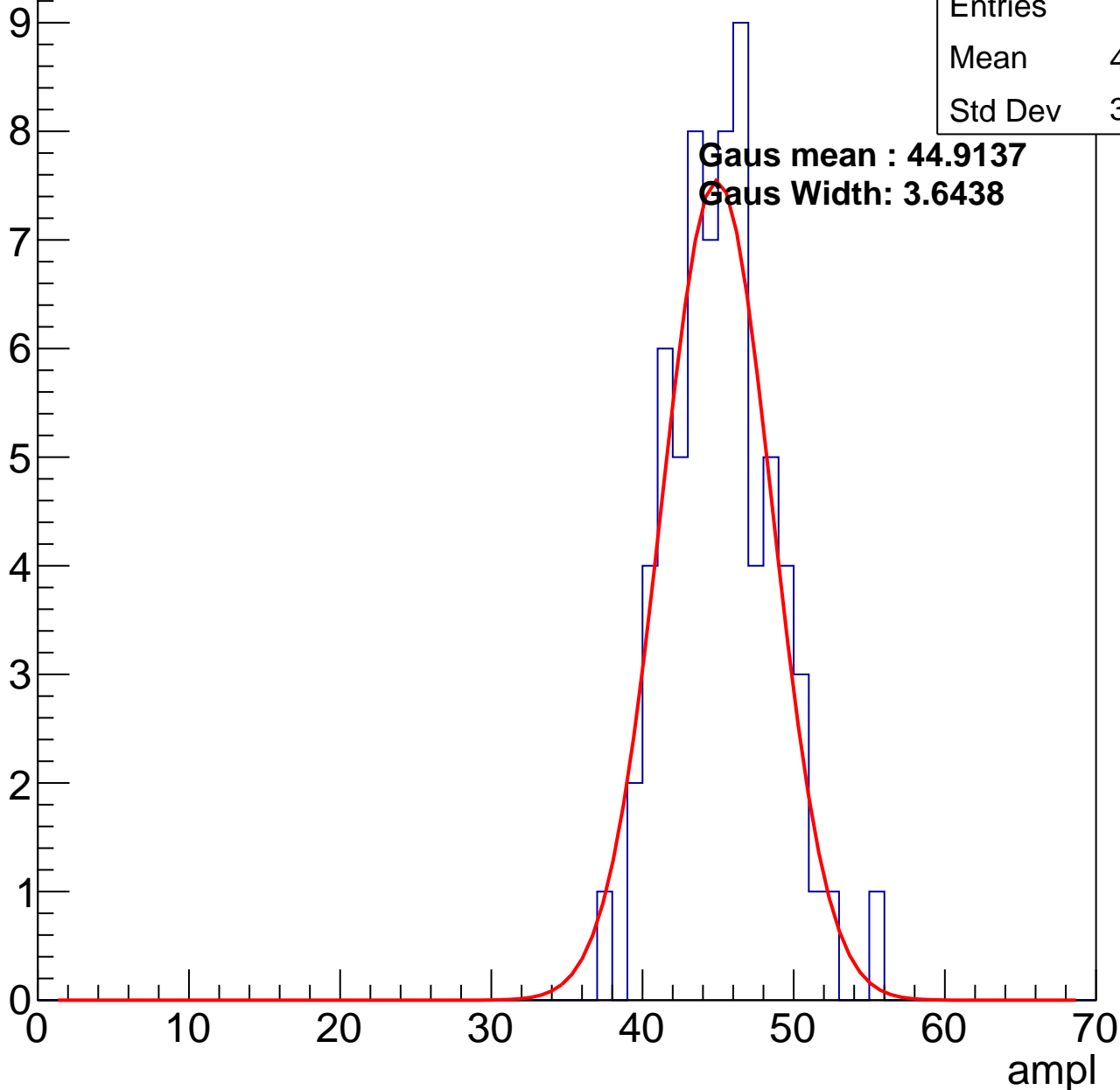
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	44.77
Std Dev	3.418

Gaus mean : 44.9137

Gaus Width: 3.6438

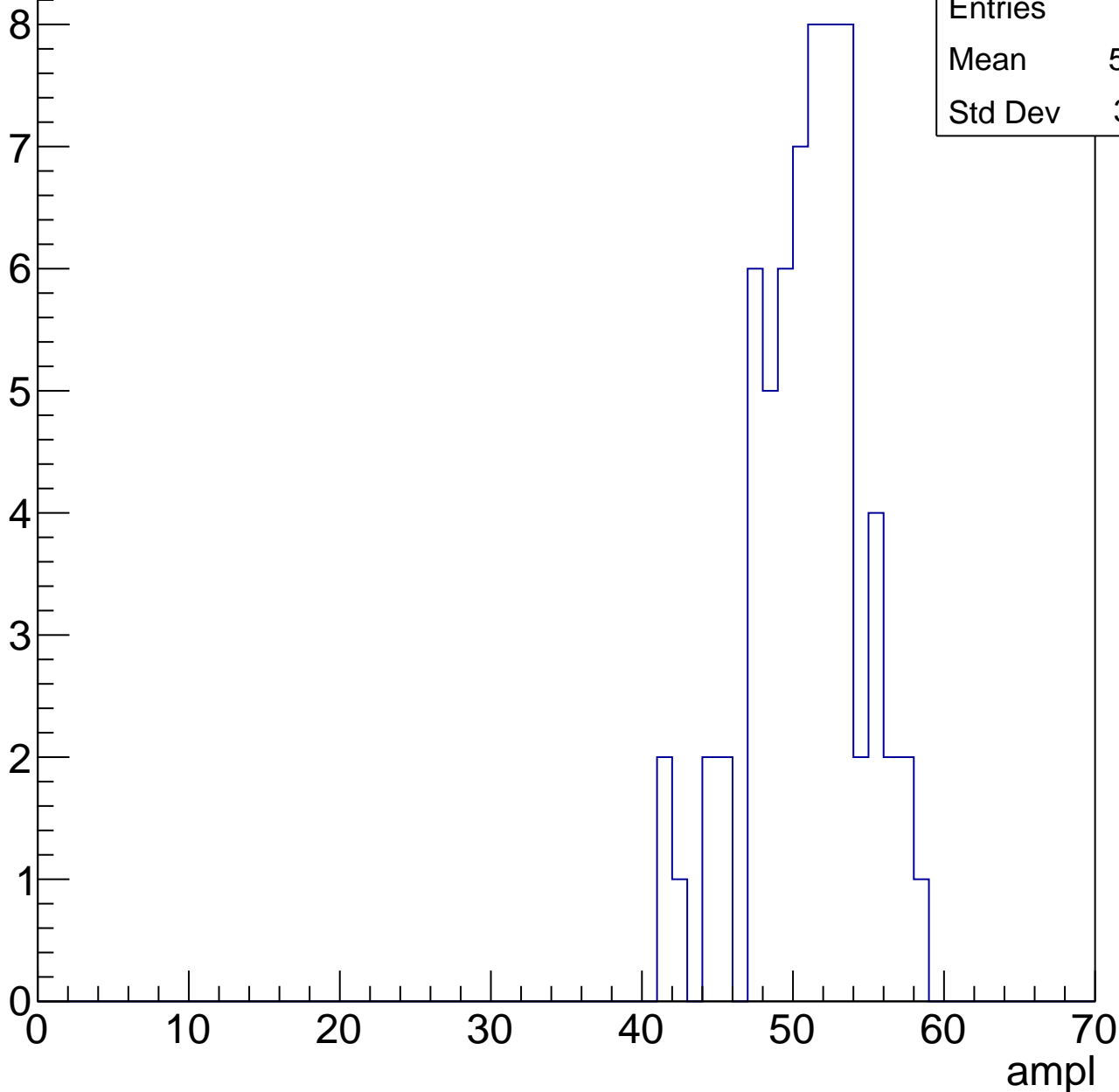


B0L002S, U4-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.42
Std Dev	3.681

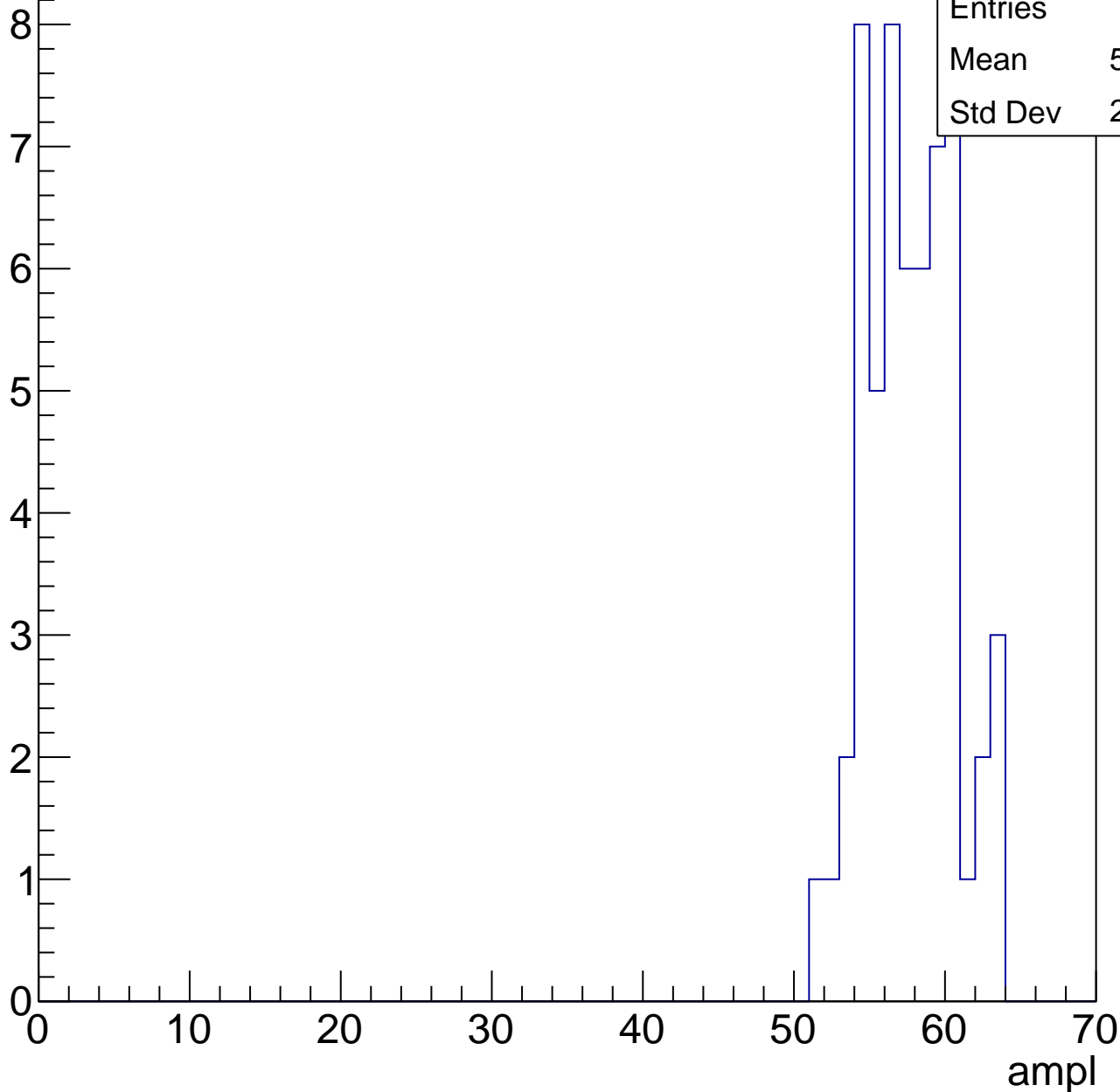


B0L002S, U4-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.26
Std Dev	2.844



B0L002S, U4-ch33, adc5

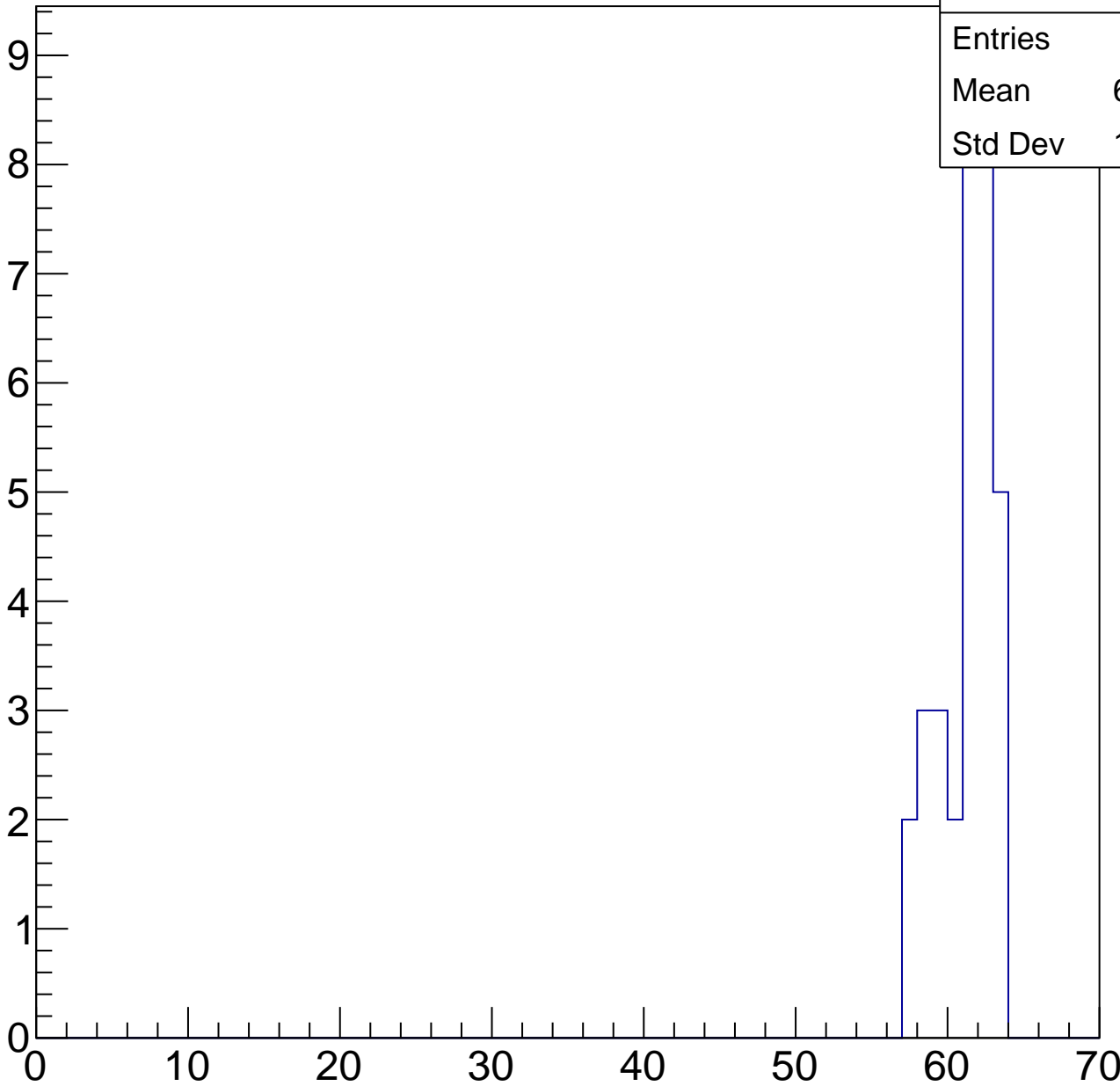
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.78
Std Dev	1.763

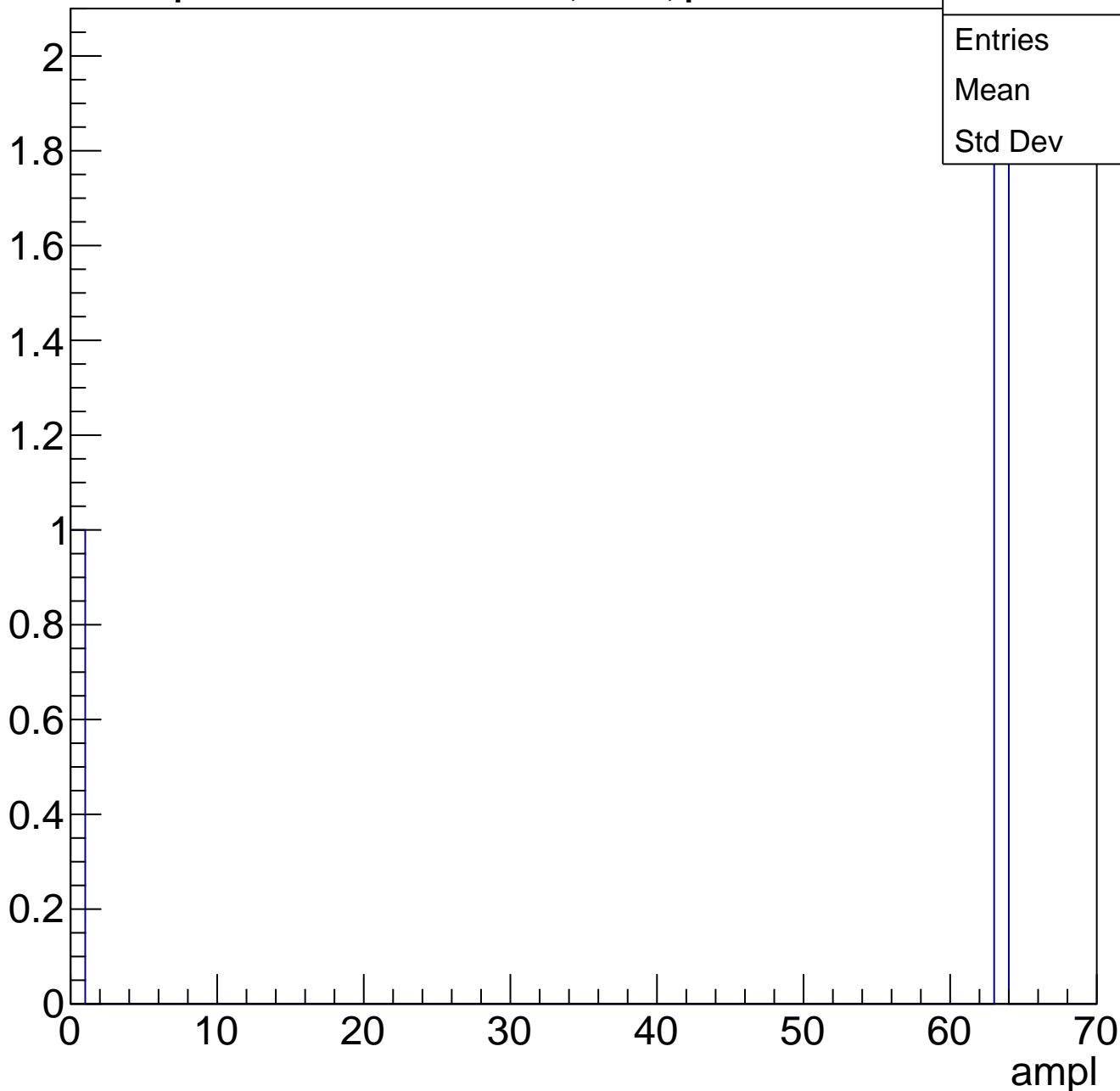
ampl



B0L002S, U4-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch34, adc0

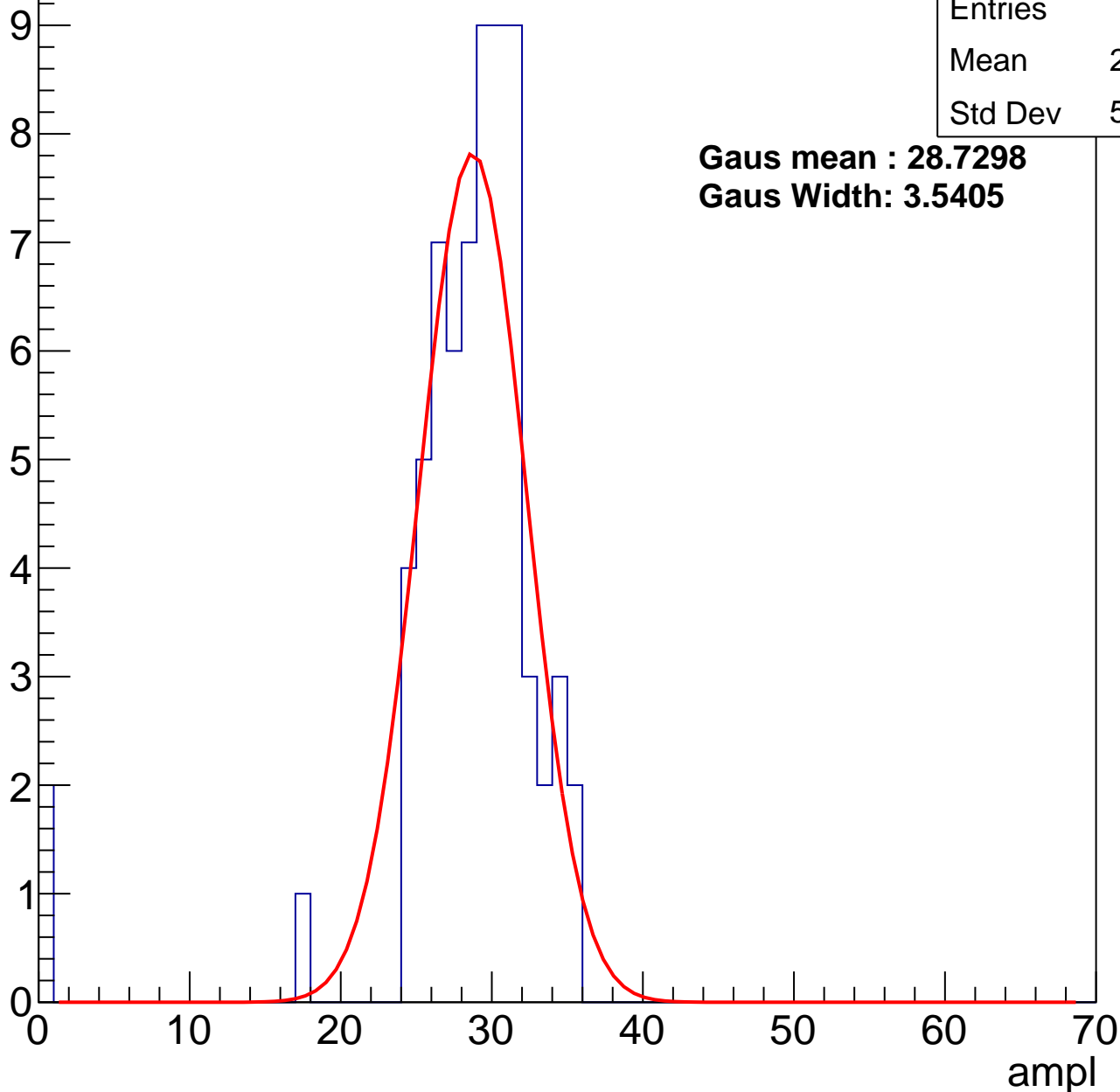
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	27.86
Std Dev	5.724

Gaus mean : 28.7298

Gaus Width: 3.5405



B0L002S, U4-ch34, adc1

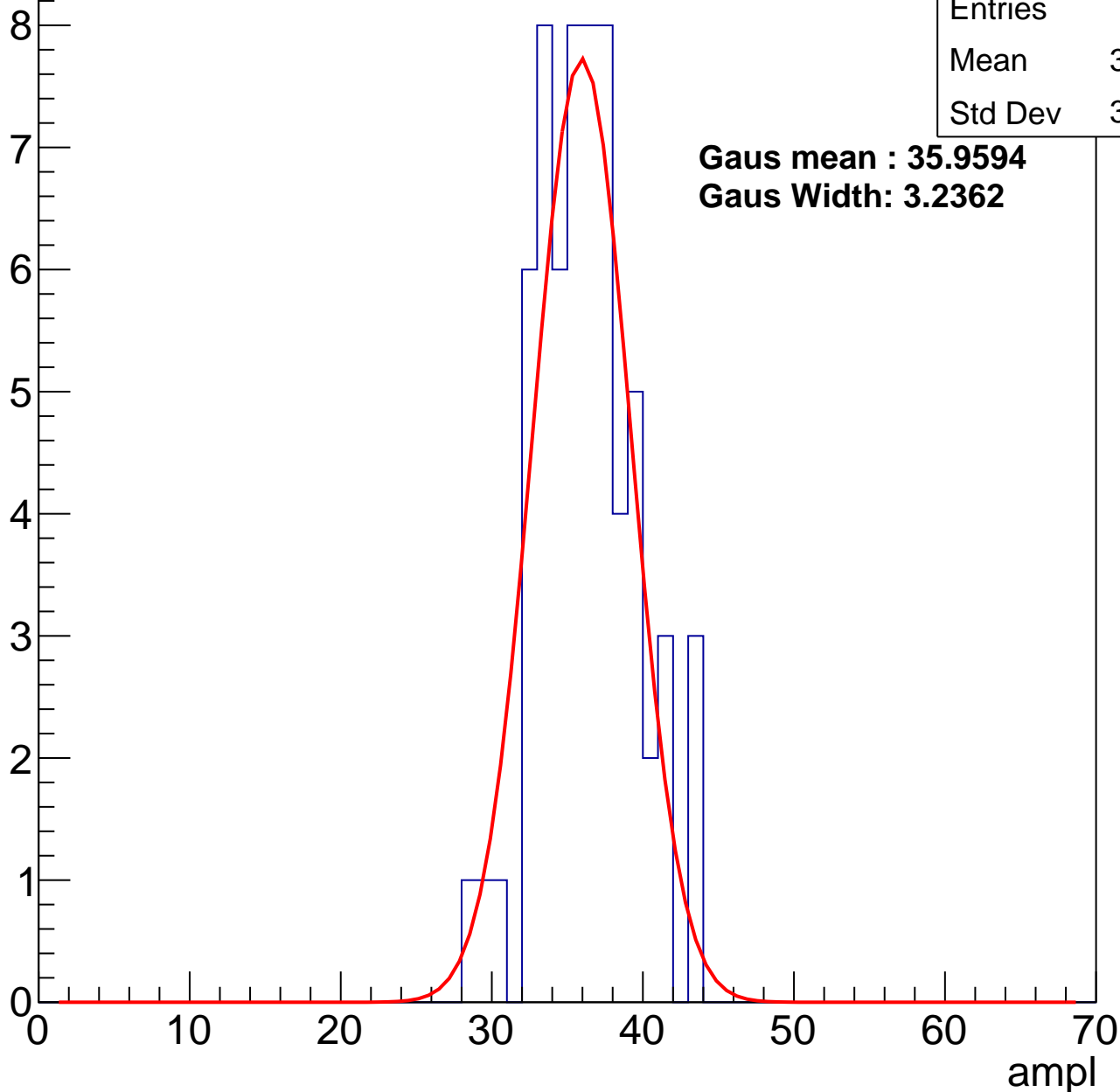
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	35.78
Std Dev	3.233

Gaus mean : 35.9594

Gaus Width: 3.2362



B0L002S, U4-ch34, adc2

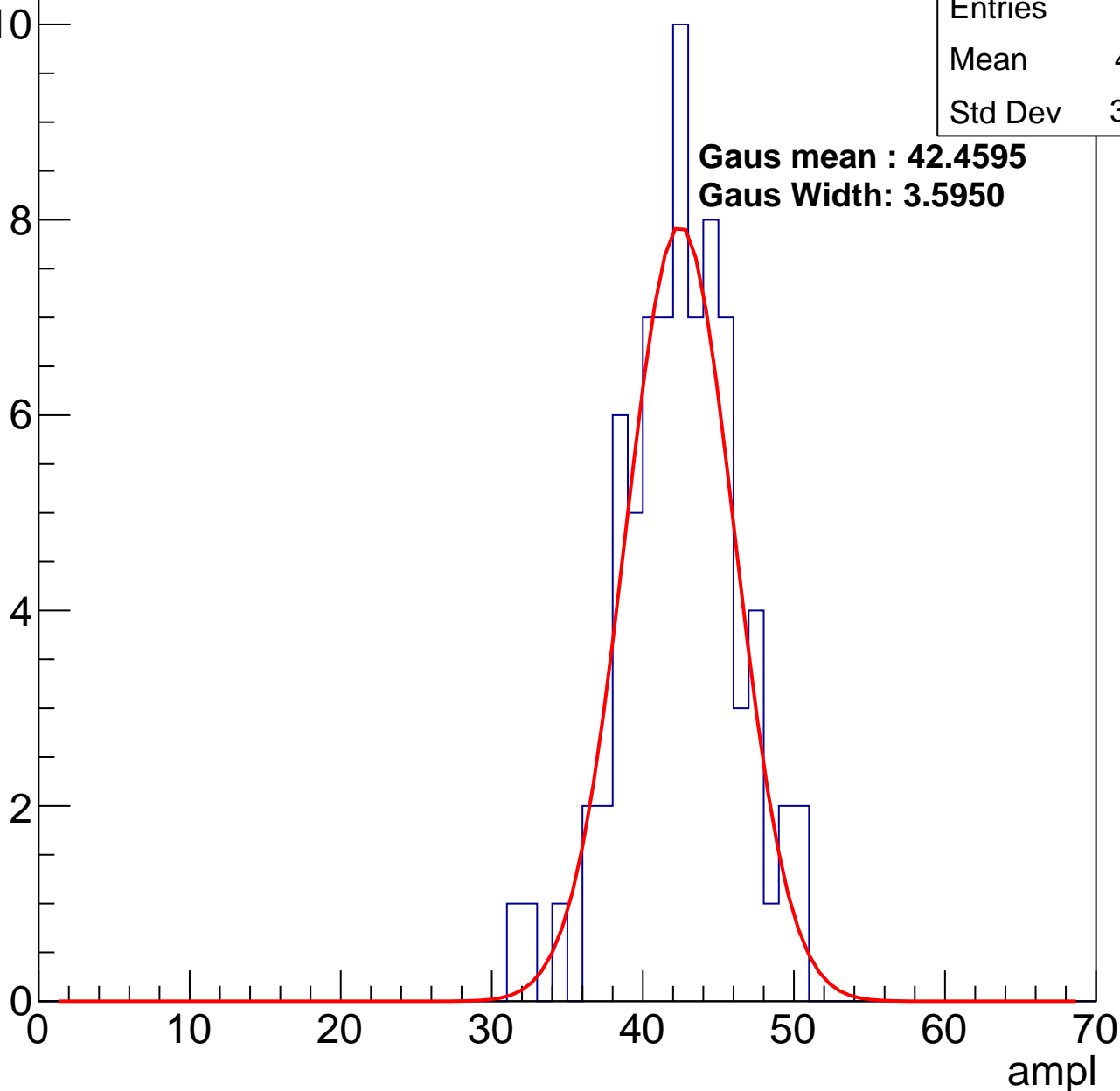
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	42.01
Std Dev	3.796

Gaus mean : 42.4595

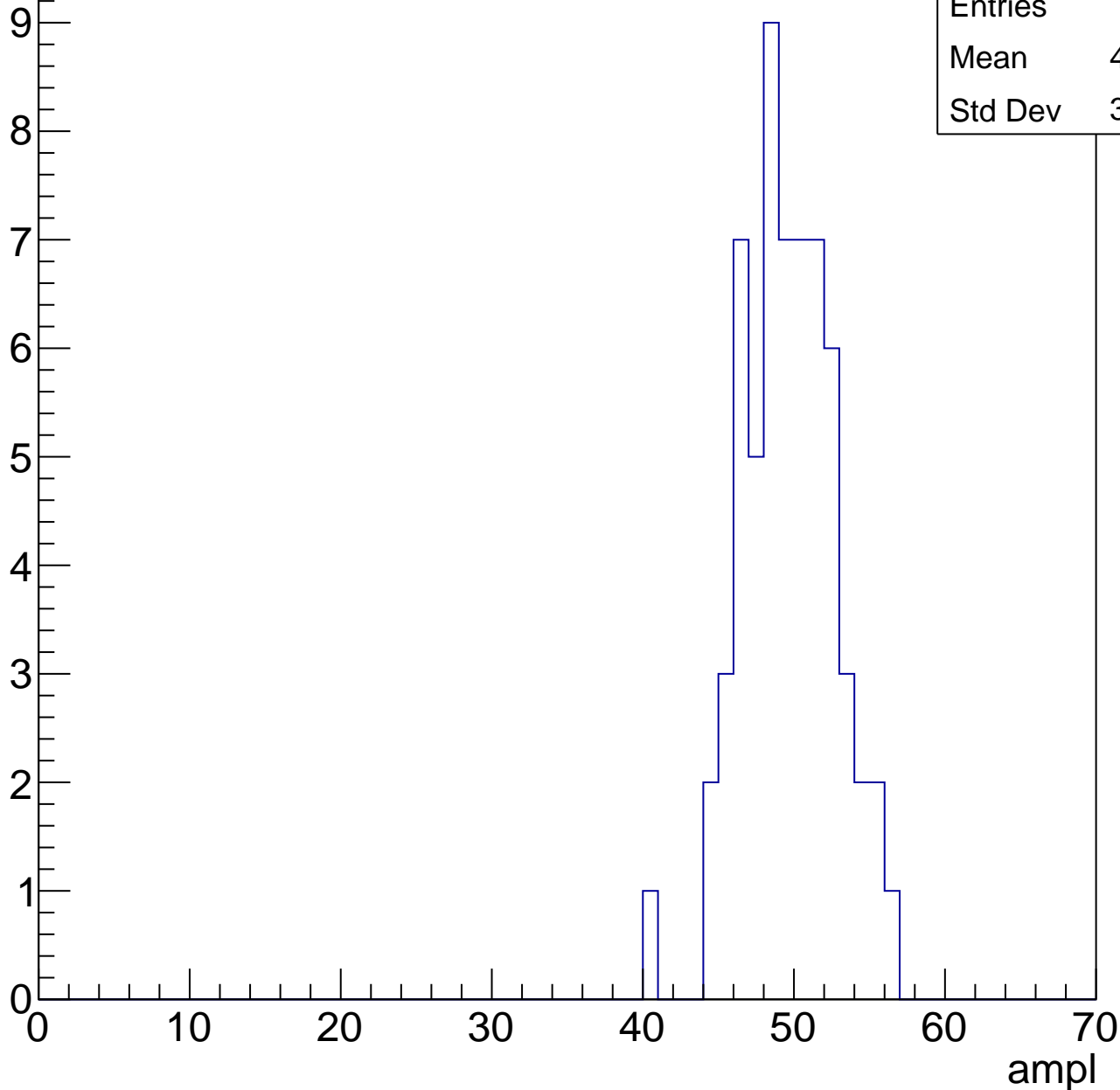
Gaus Width: 3.5950



B0L002S, U4-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

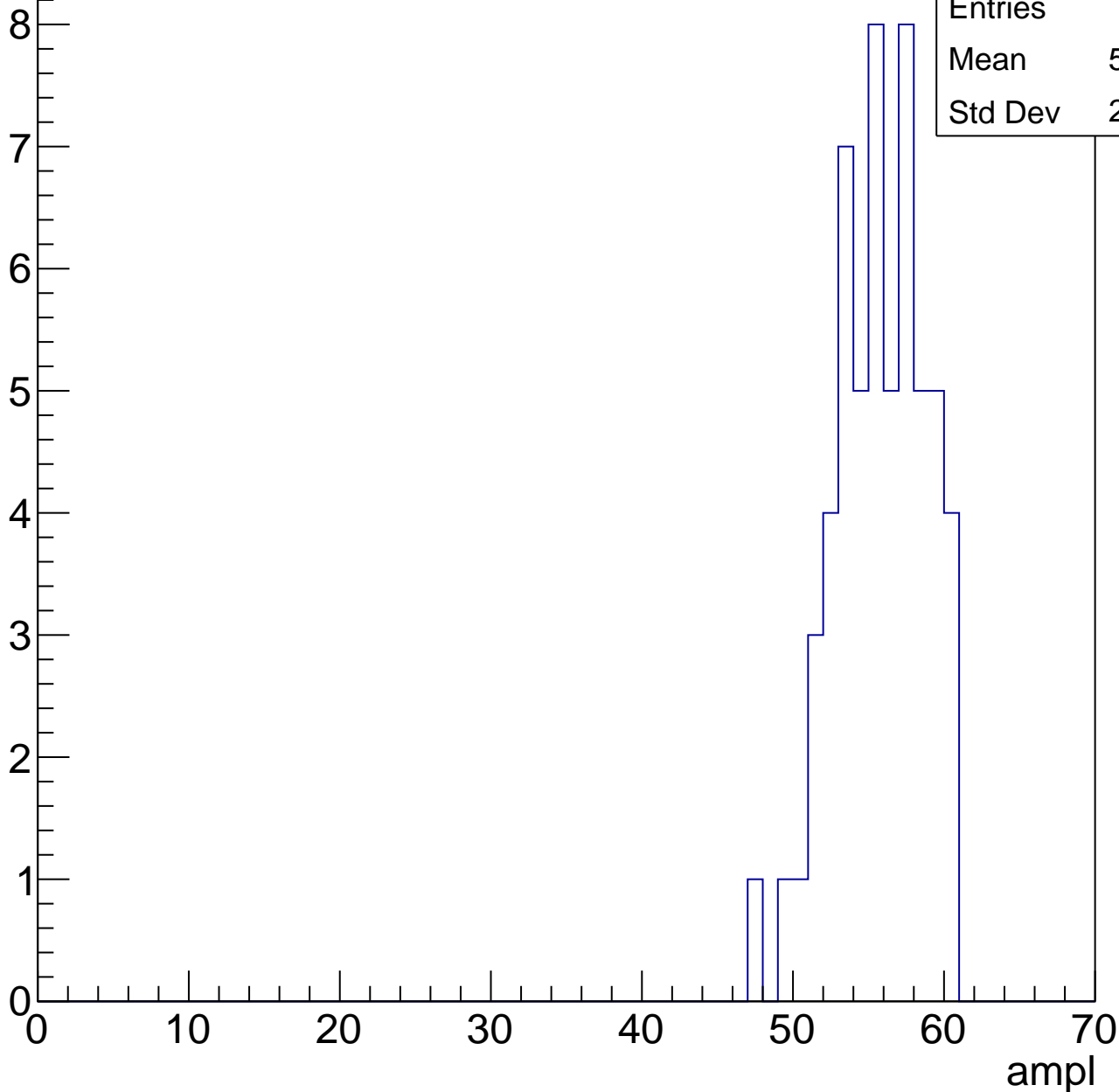


B0L002S, U4-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	55.25
Std Dev	2.963

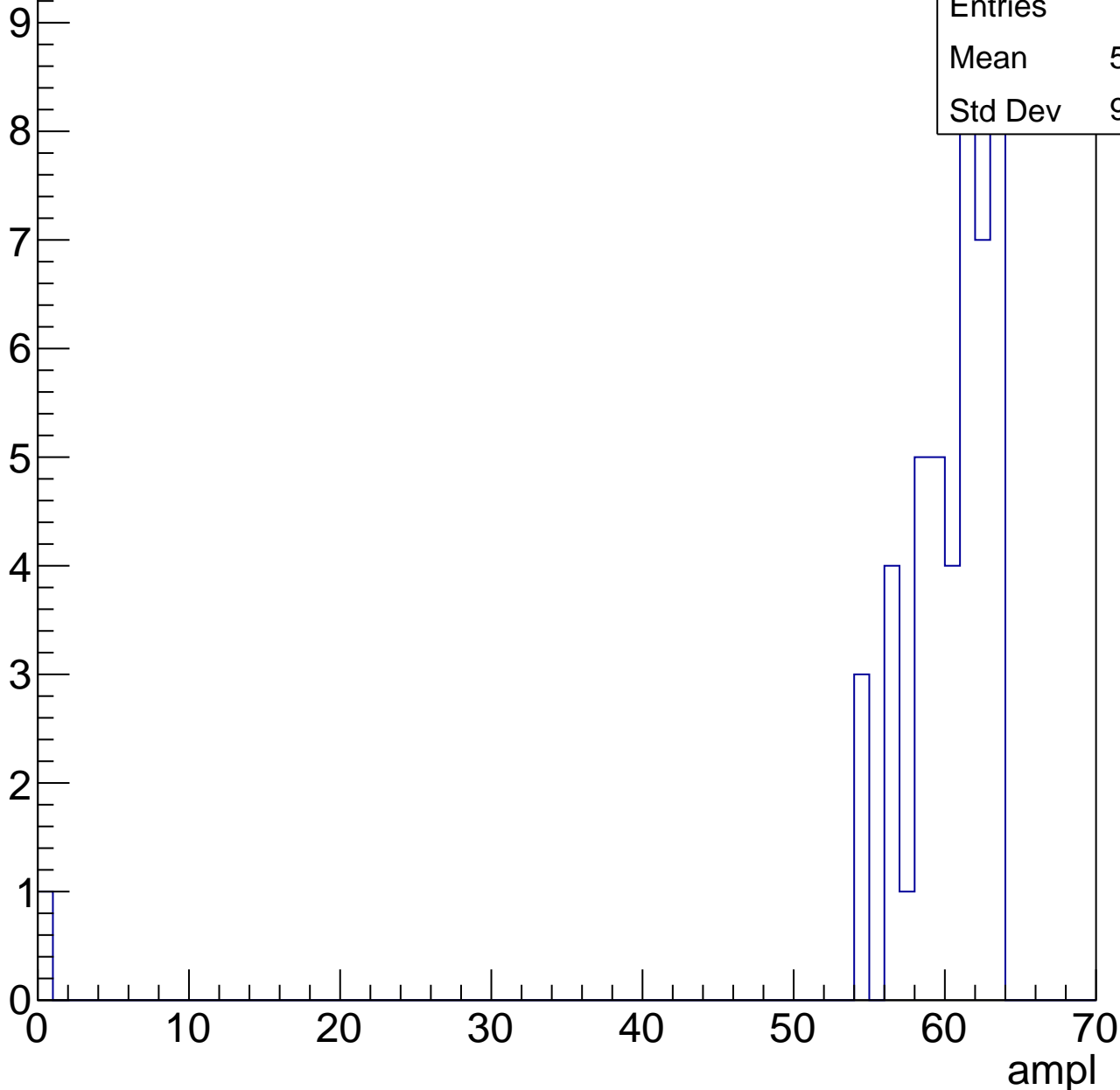


B0L002S, U4-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

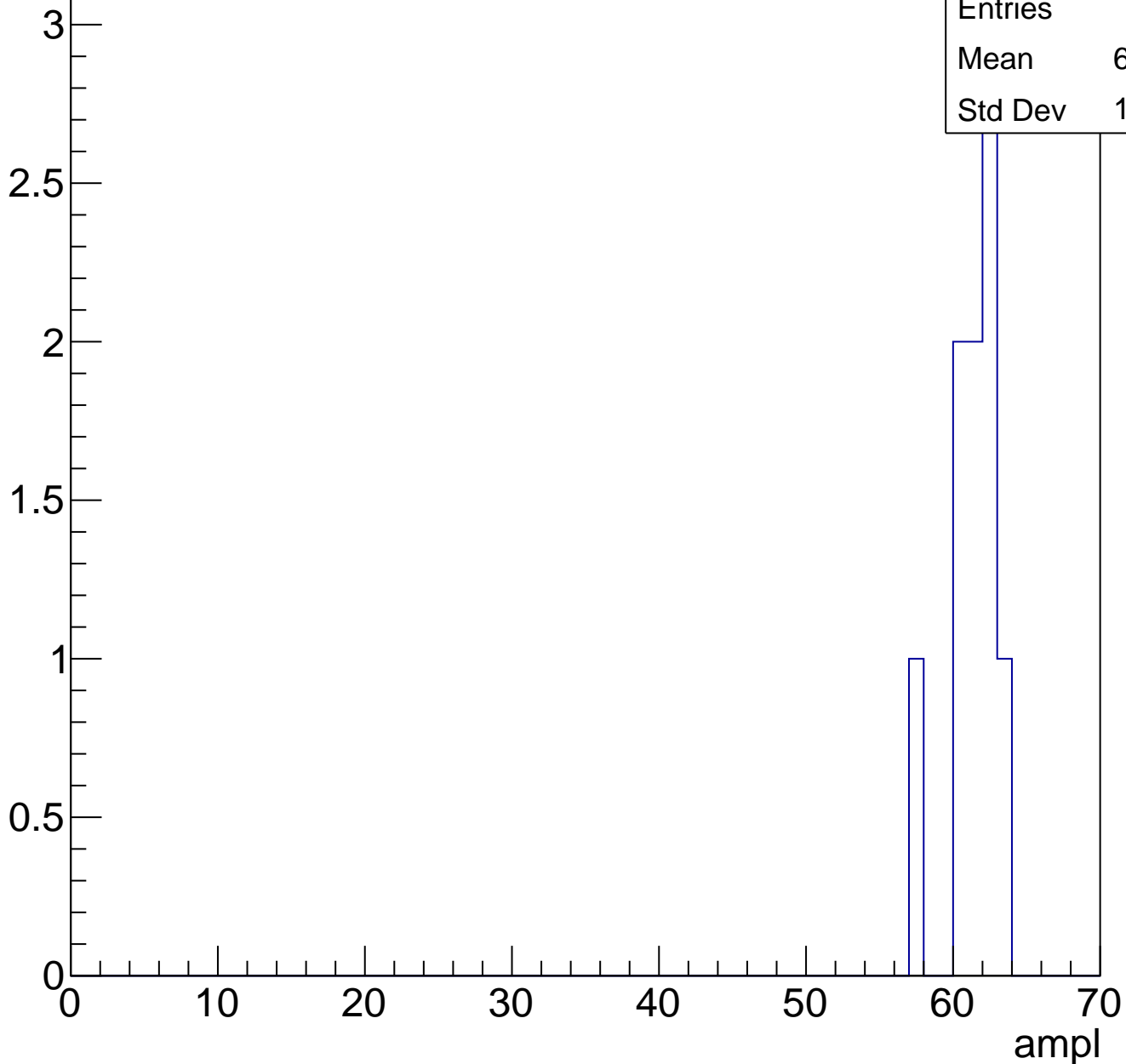
Entries	47
Mean	58.66
Std Dev	9.037



B0L002S, U4-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch35, adc0

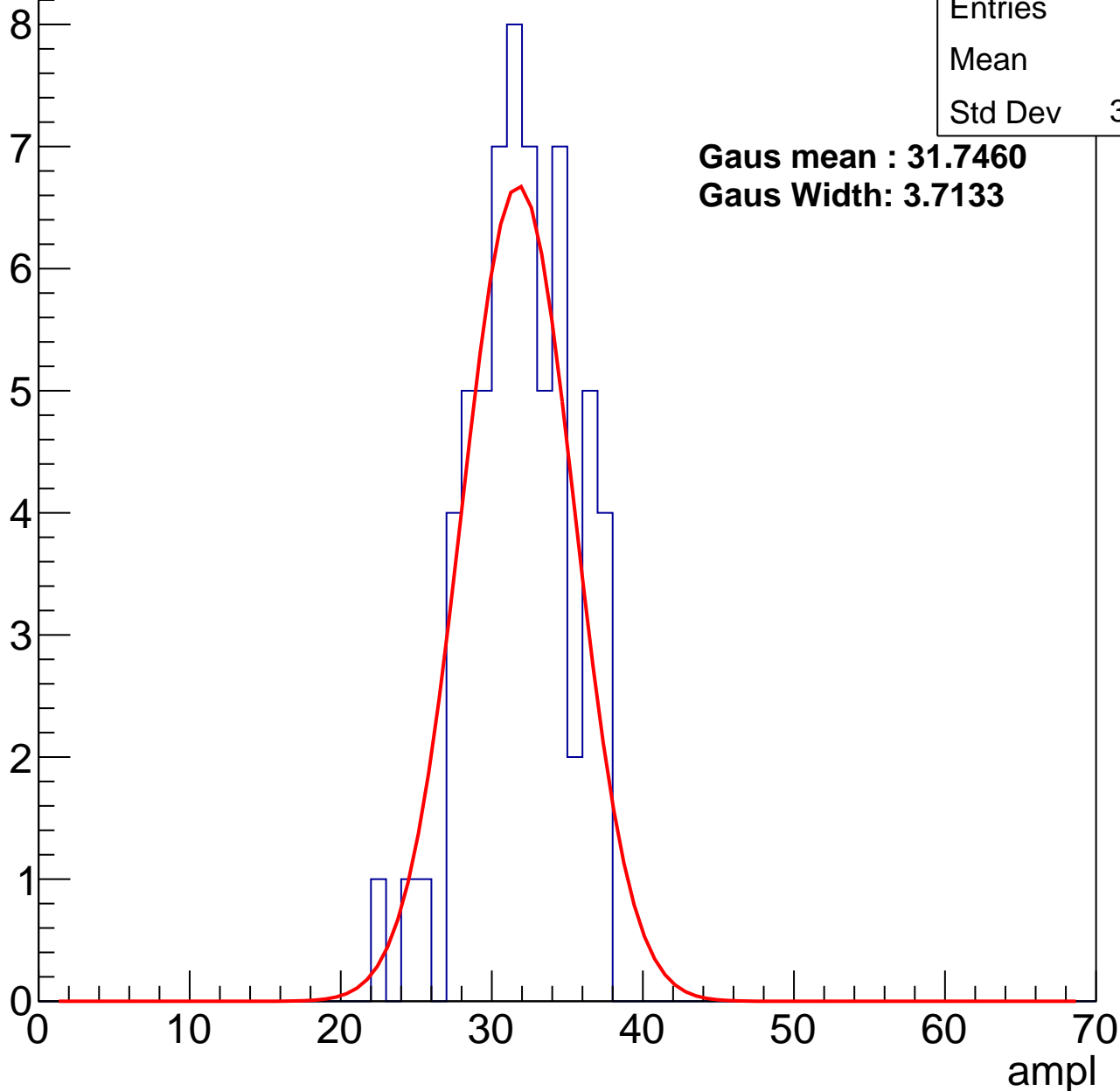
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.4
Std Dev	3.319

Gaus mean : 31.7460

Gaus Width: 3.7133



B0L002S, U4-ch35, adc1

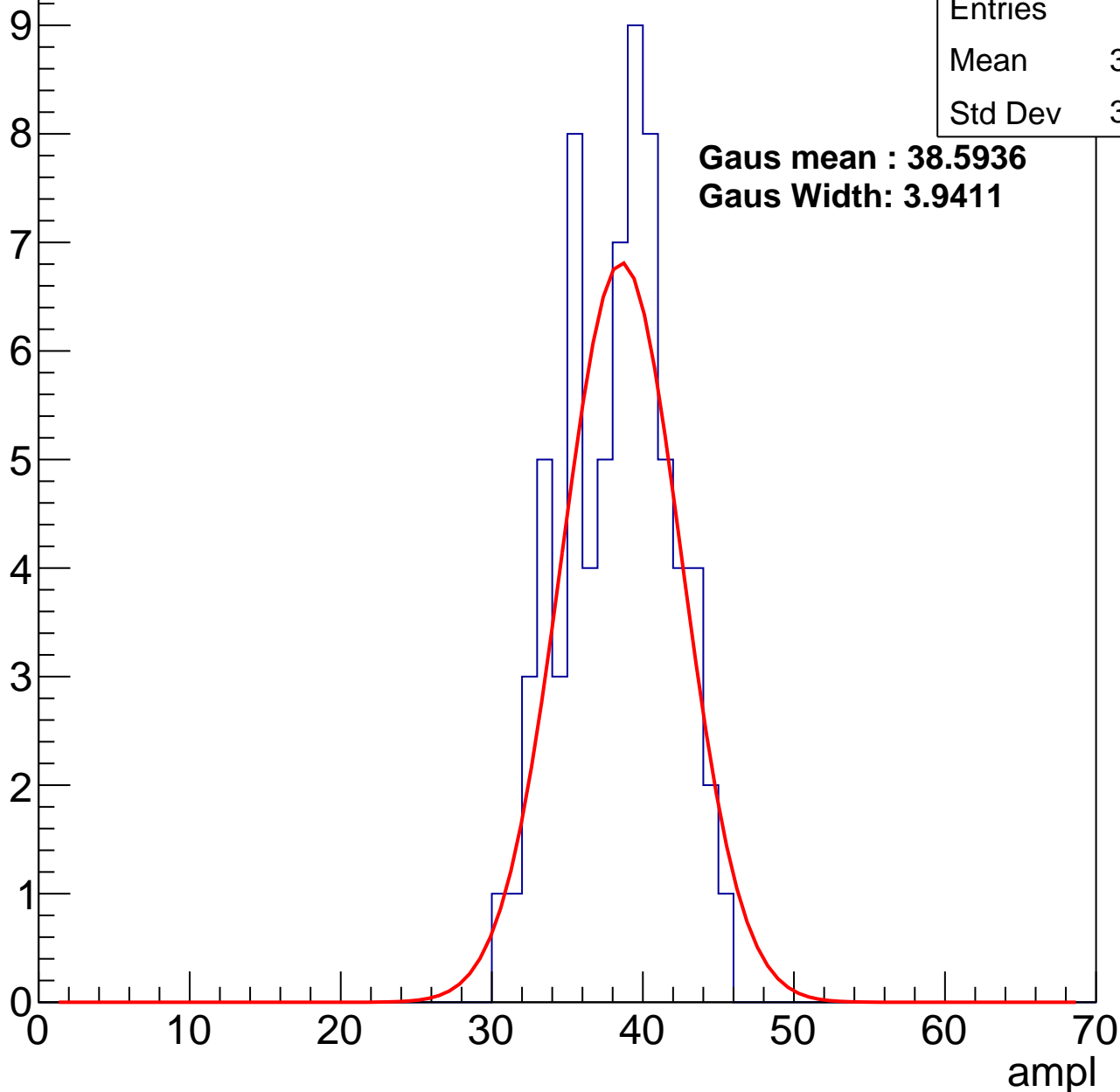
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.83
Std Dev	3.497

Gaus mean : 38.5936

Gaus Width: 3.9411



B0L002S, U4-ch35, adc2

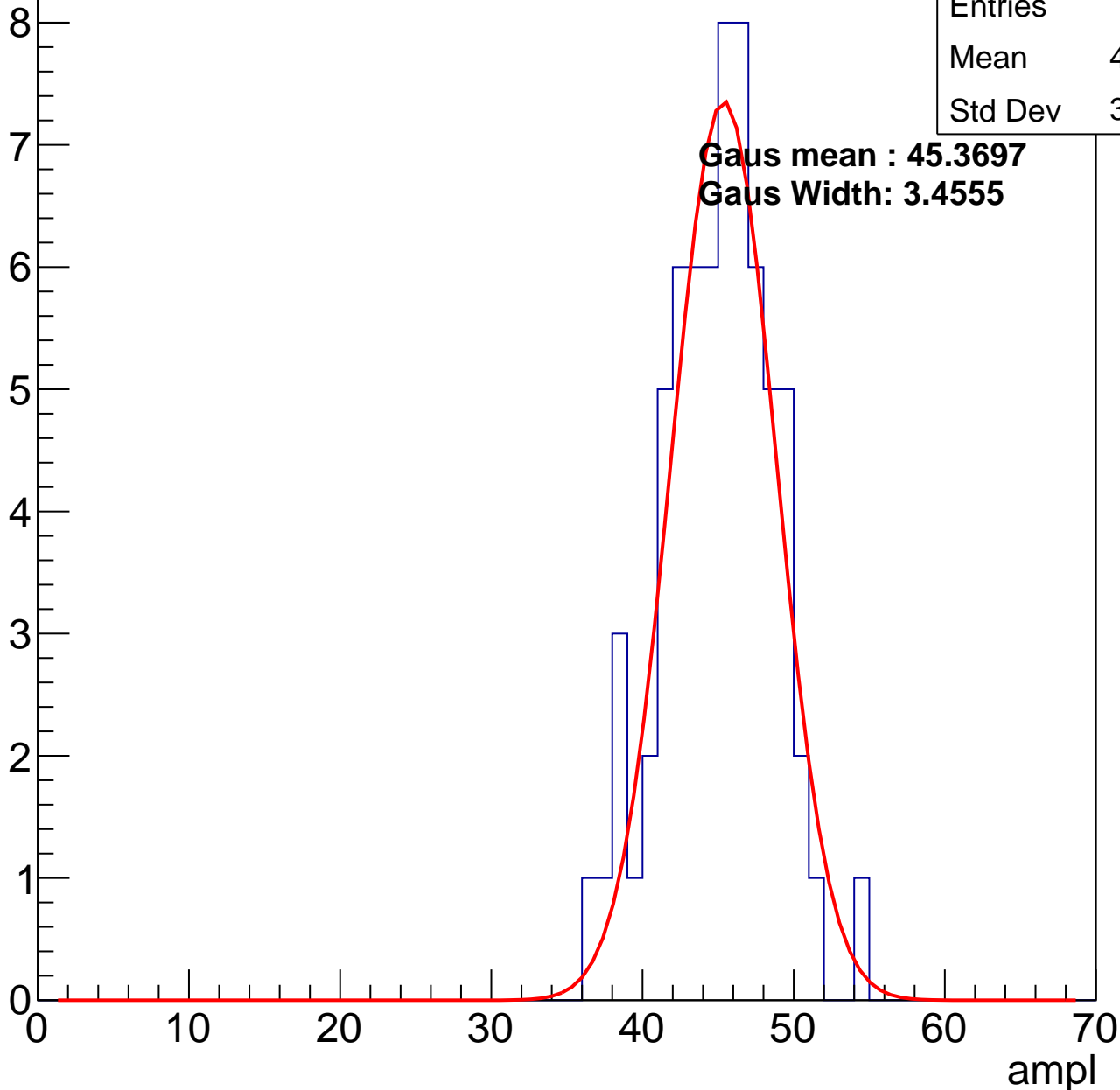
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.55
Std Dev	3.567

Gaus mean : 45.3697

Gaus Width: 3.4555

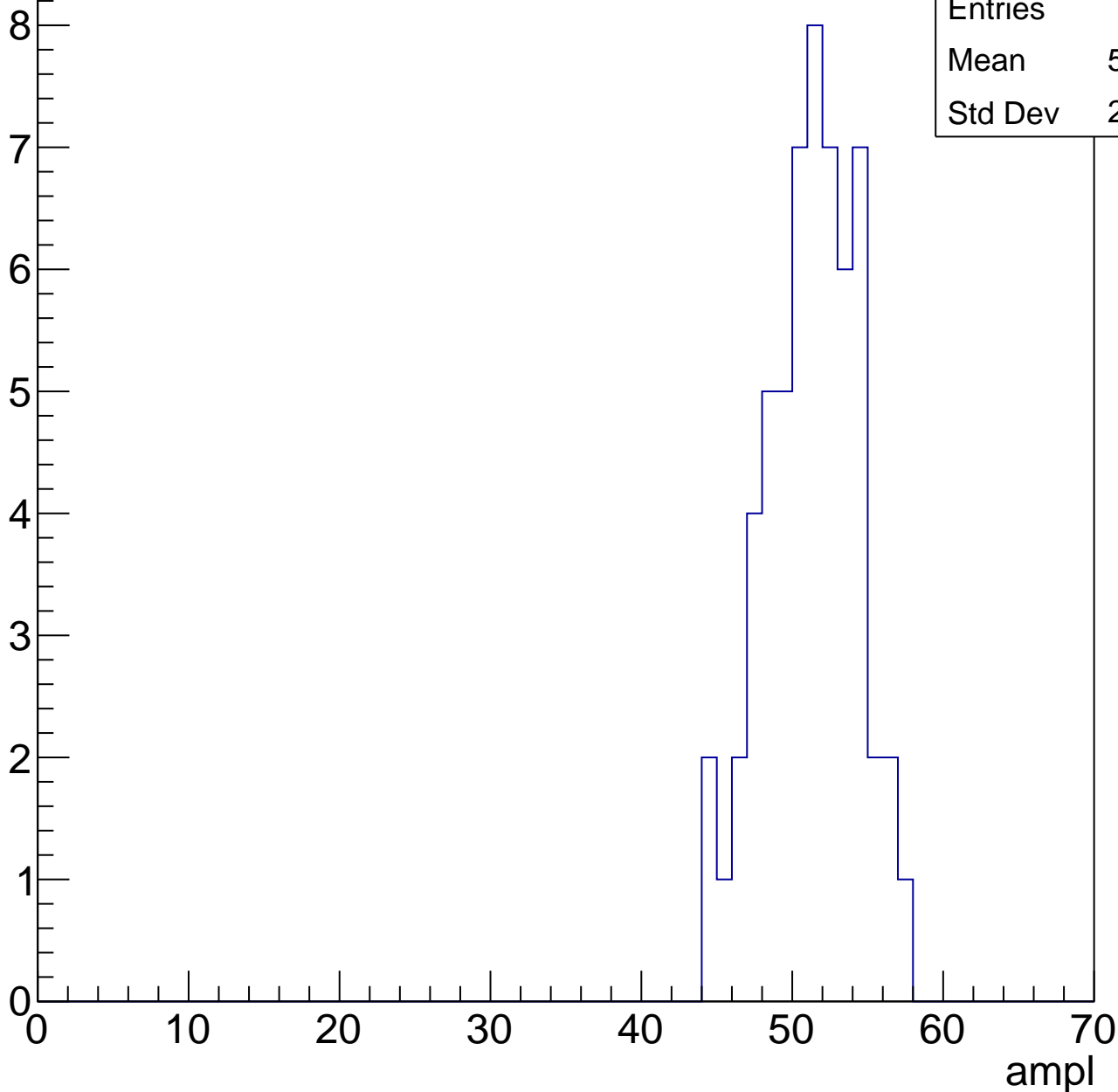


B0L002S, U4-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

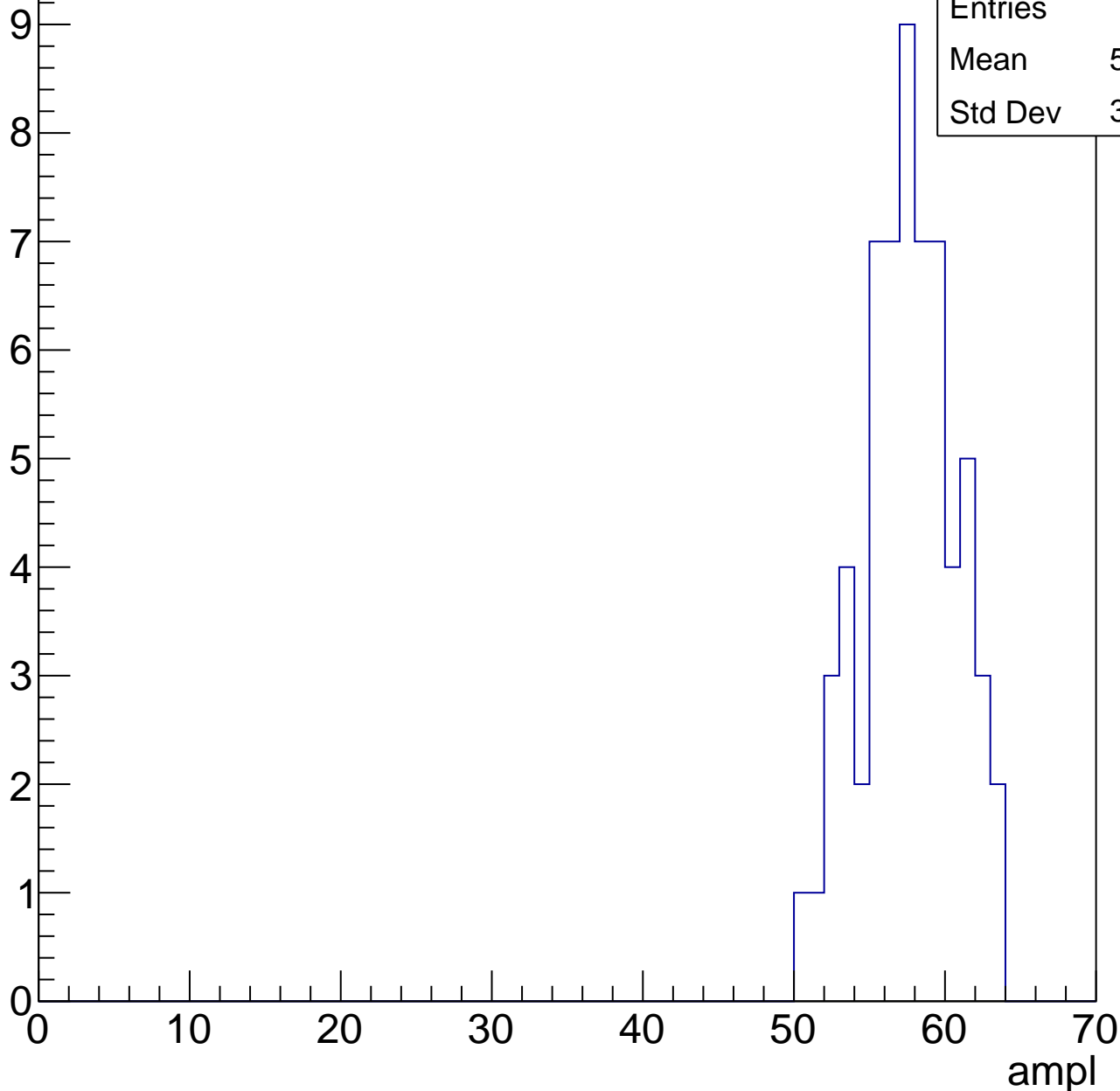
Entries	59
Mean	50.76
Std Dev	2.993



B0L002S, U4-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



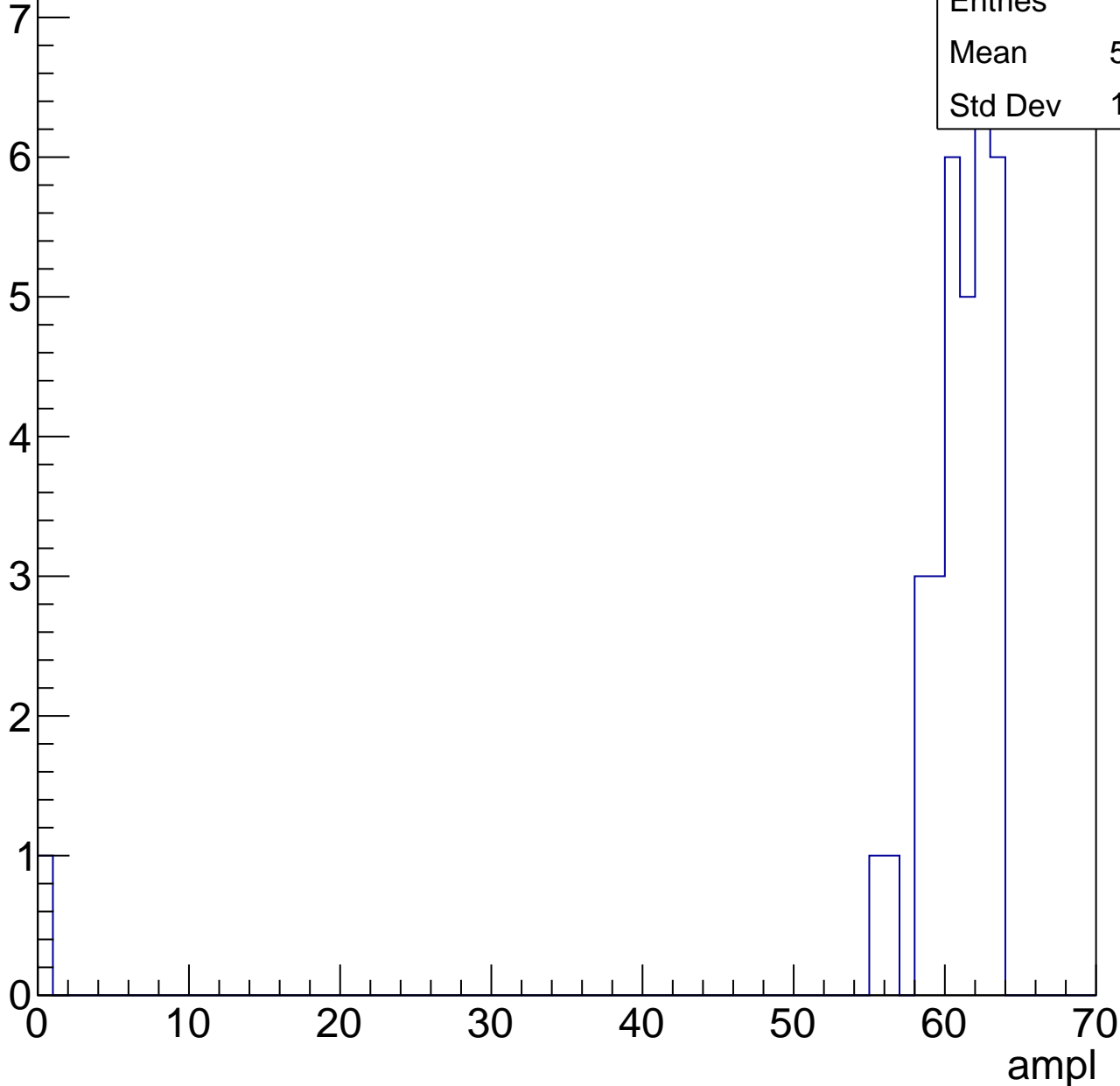
Entries	62
Mean	57.15
Std Dev	3.042

B0L002S, U4-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.76
Std Dev	10.58



B0L002S, U4-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L002S, U4-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U4-ch36, adc0

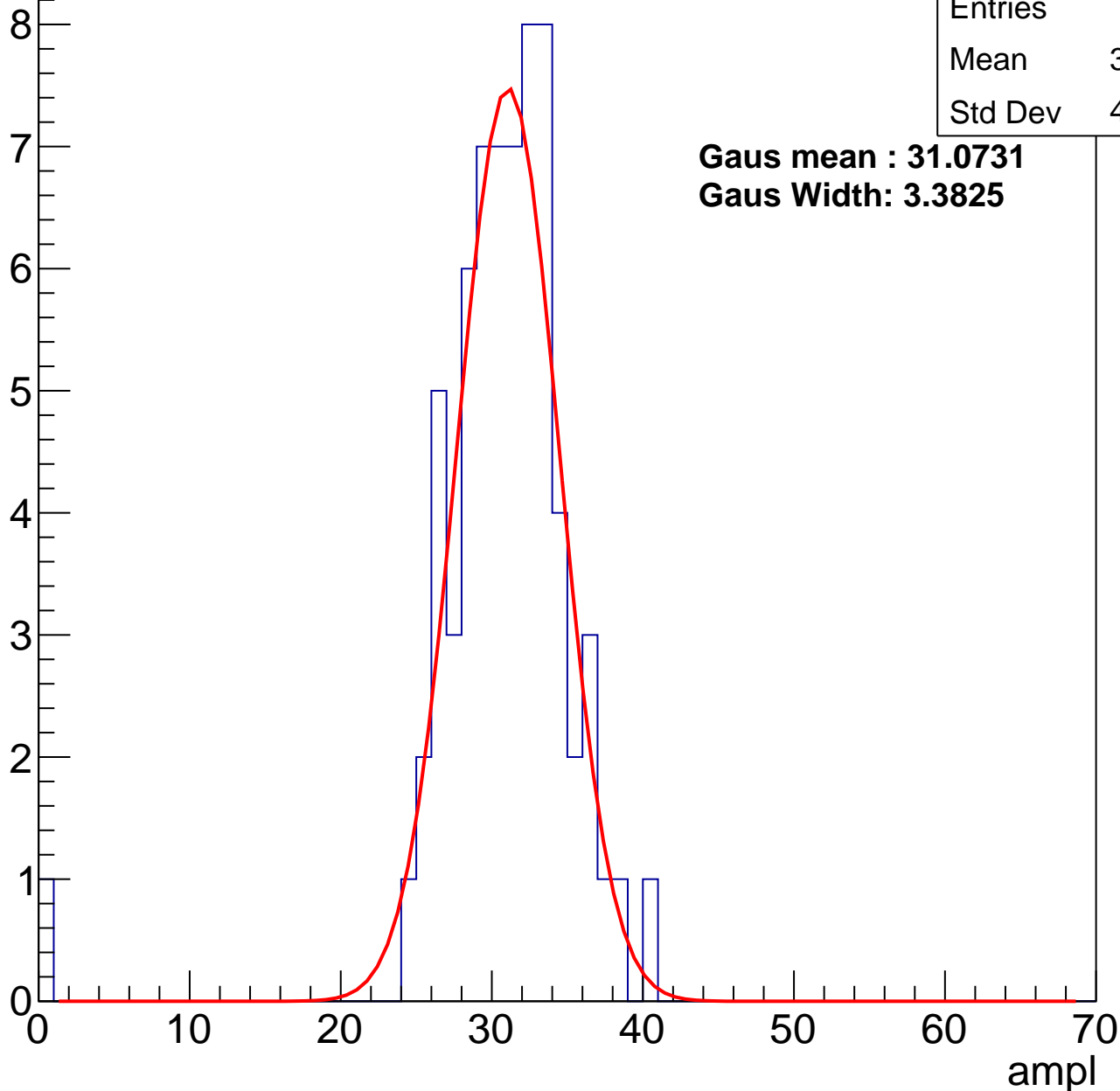
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	30.33
Std Dev	4.982

Gaus mean : 31.0731

Gaus Width: 3.3825



B0L002S, U4-ch36, adc1

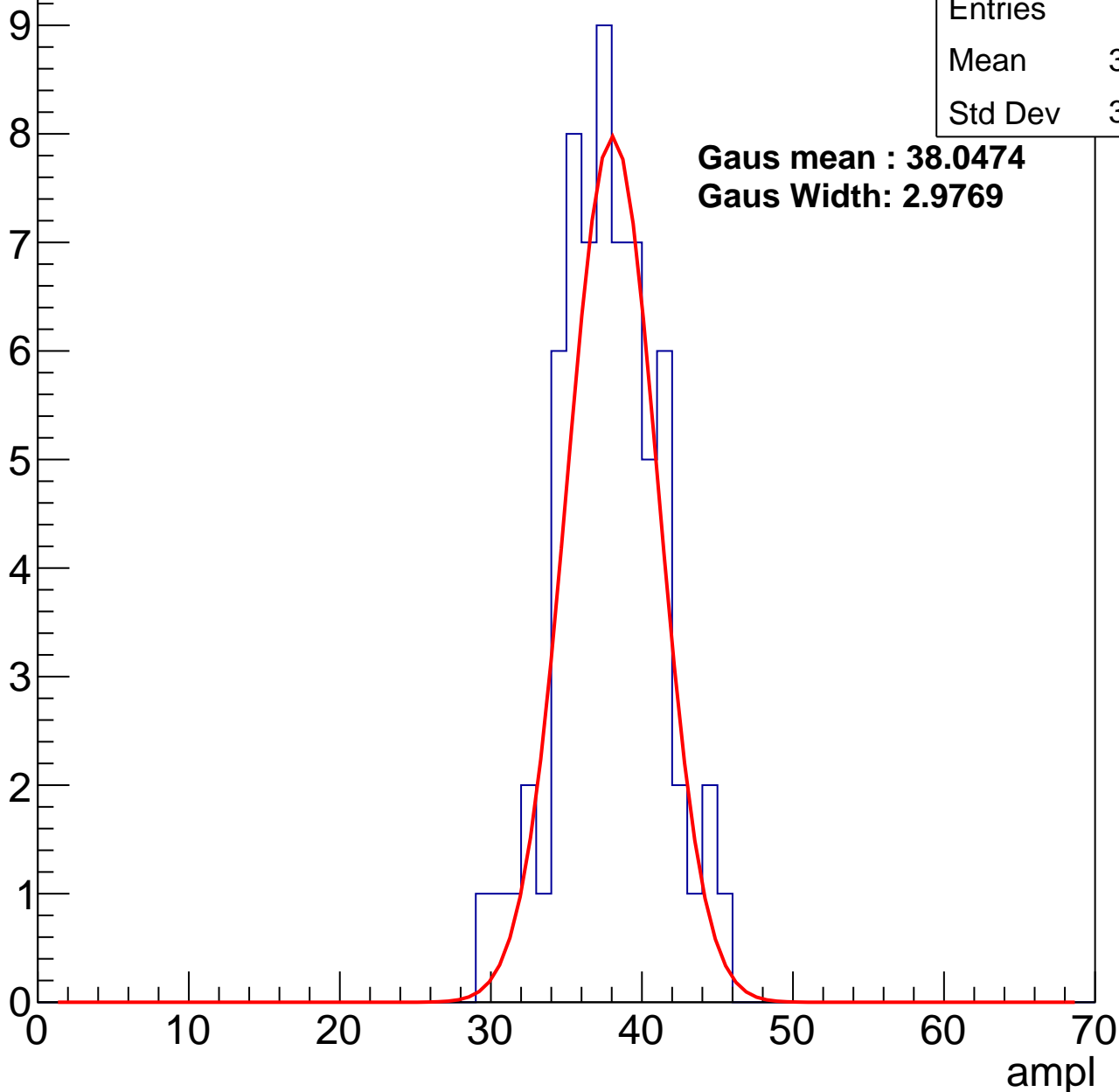
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.33
Std Dev	3.289

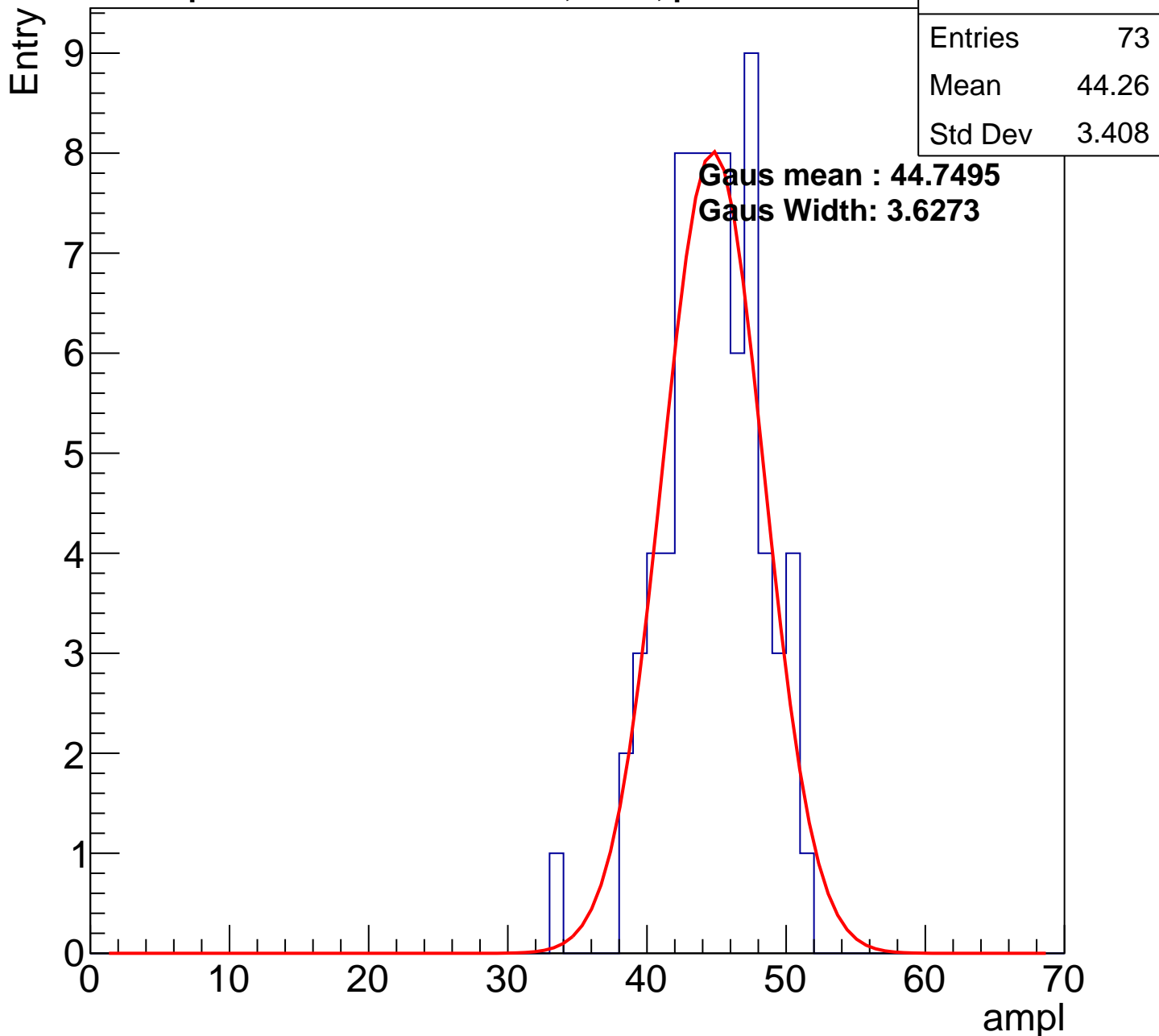
Gaus mean : 38.0474

Gaus Width: 2.9769



B0L002S, U4-ch36, adc2

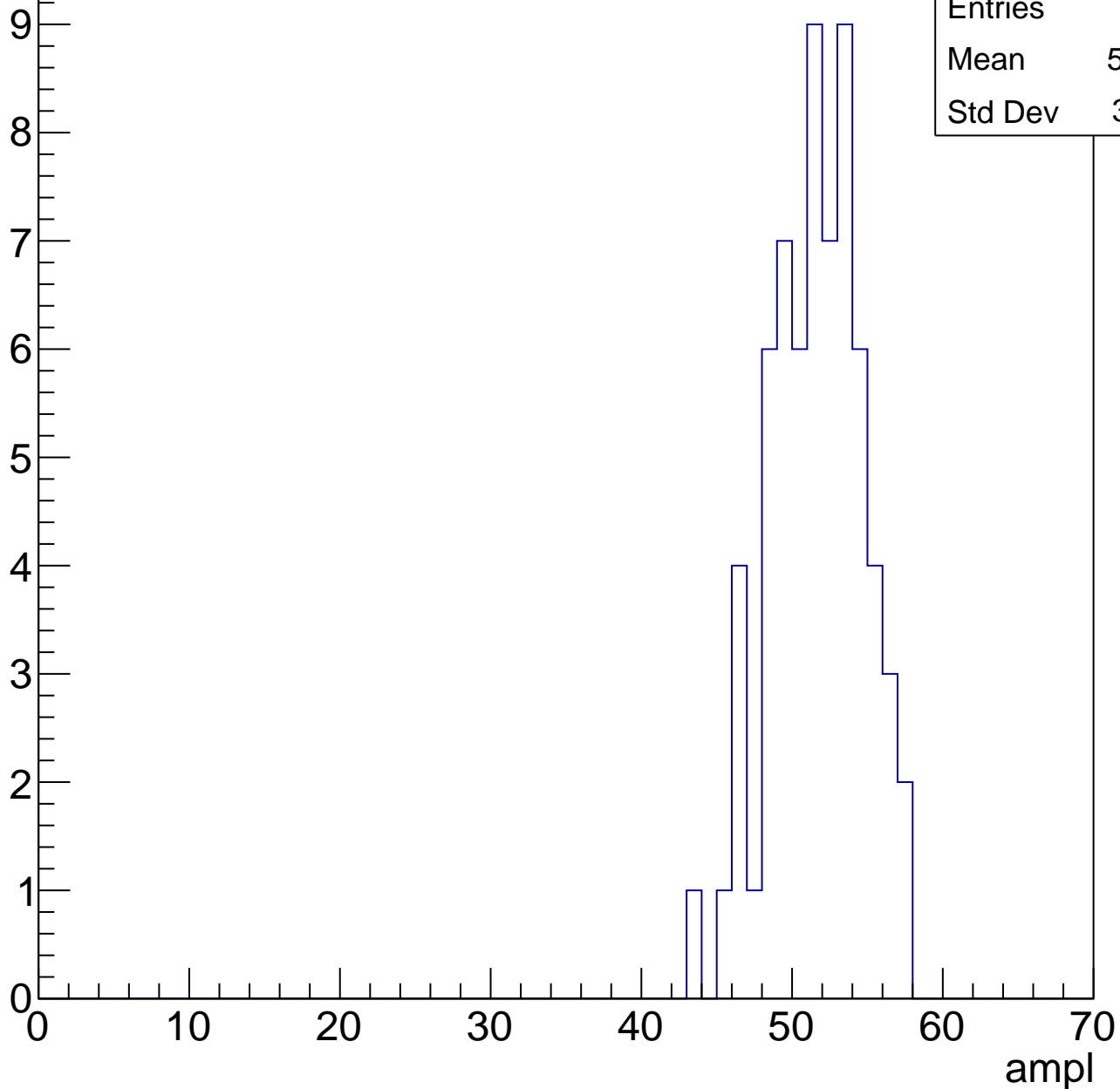
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



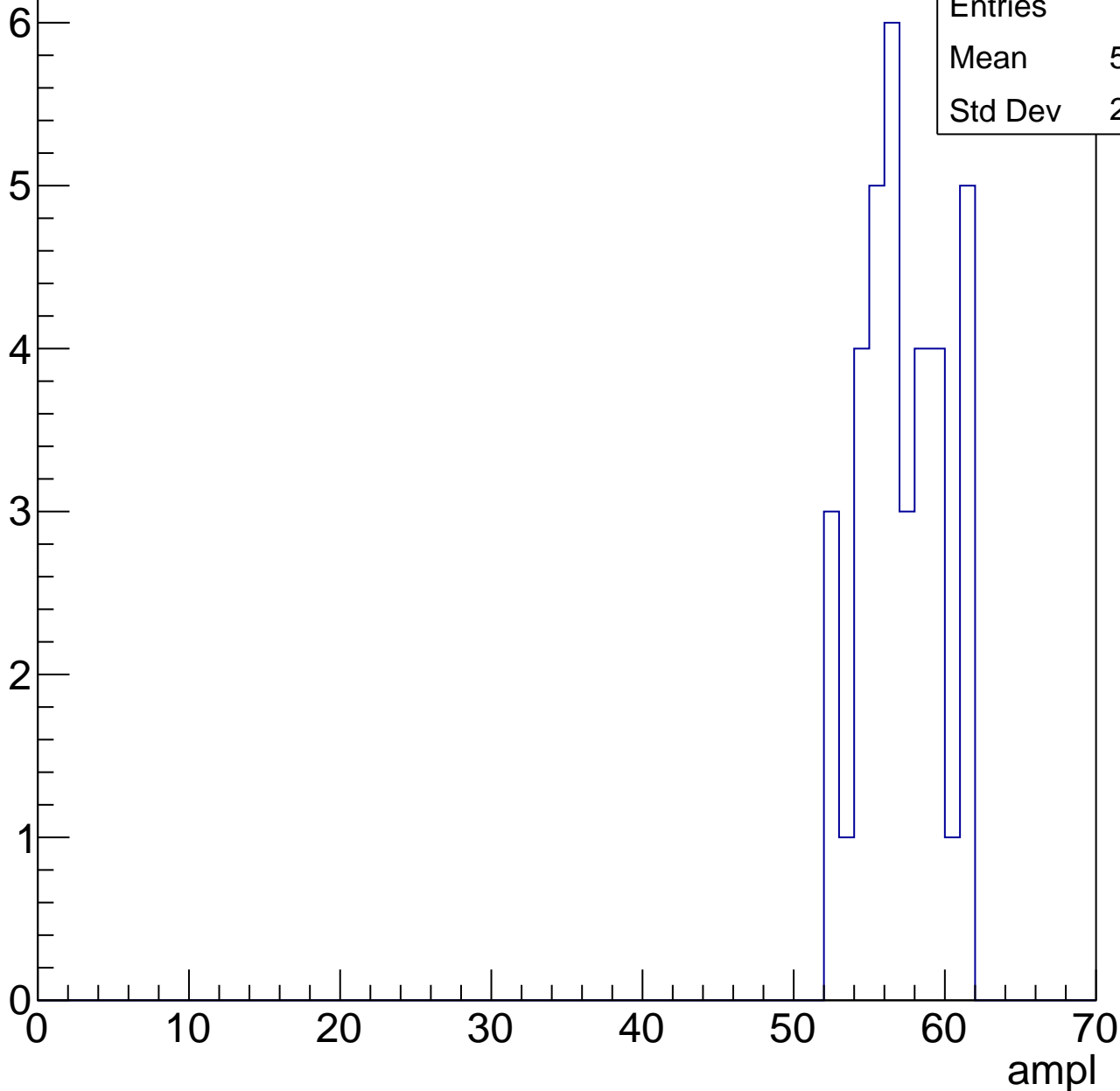
Entries	66
Mean	51.15
Std Dev	3.041

B0L002S, U4-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

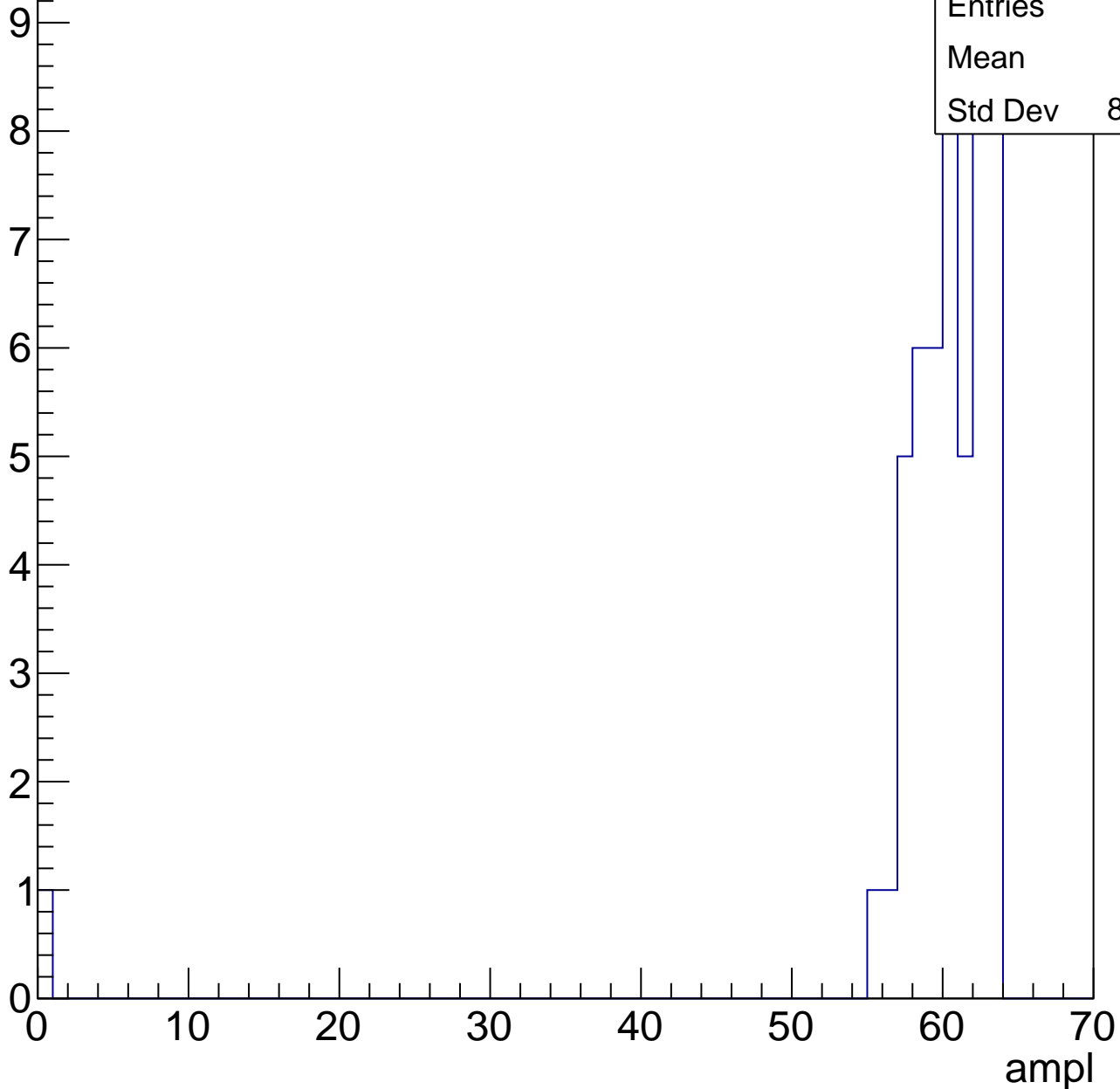
Entries	36
Mean	56.67
Std Dev	2.677



B0L002S, U4-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U4-ch37, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	29.23
Std Dev	4.514

Gaus mean : 30.2243

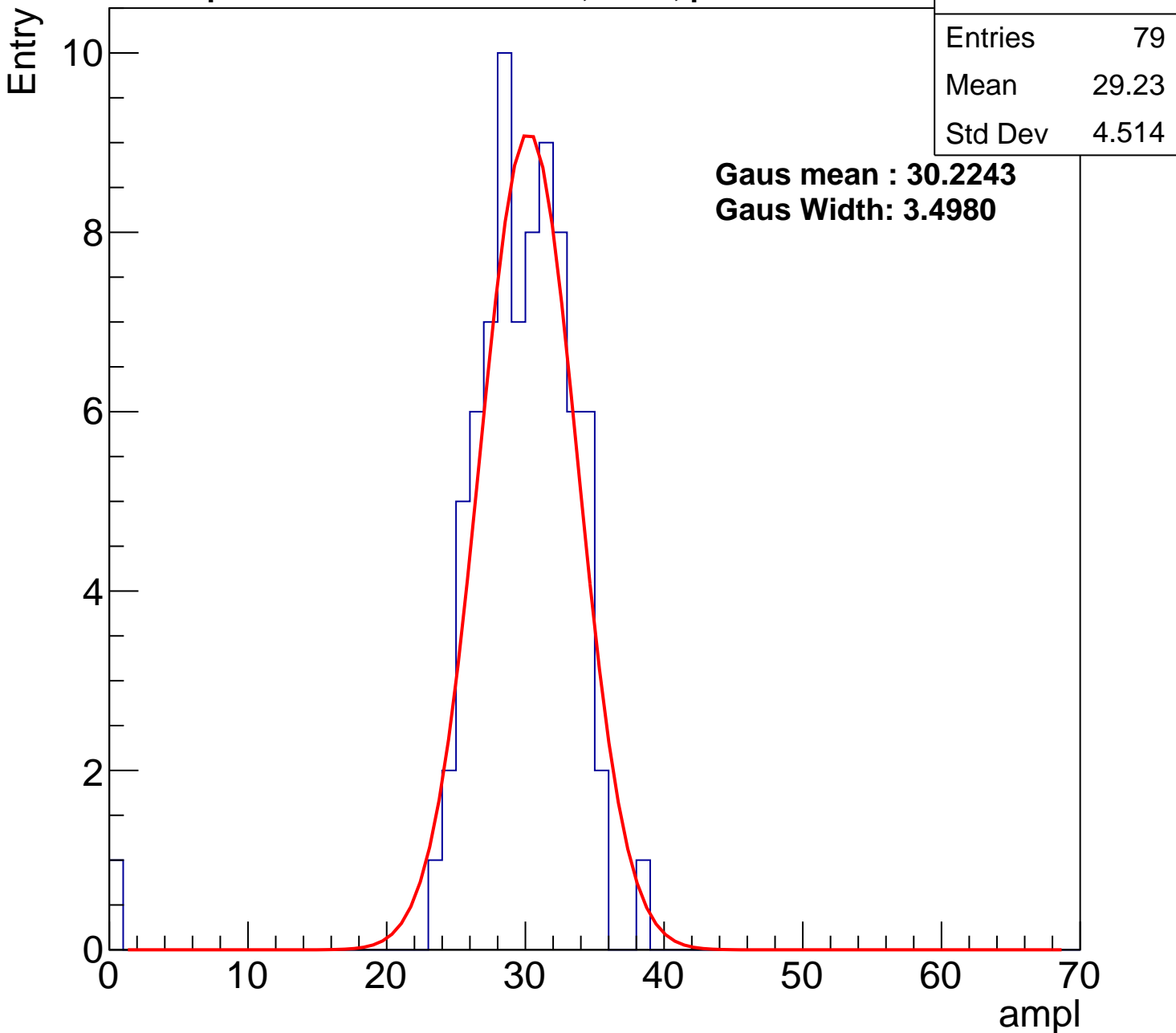
Gaus Width: 3.4980

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



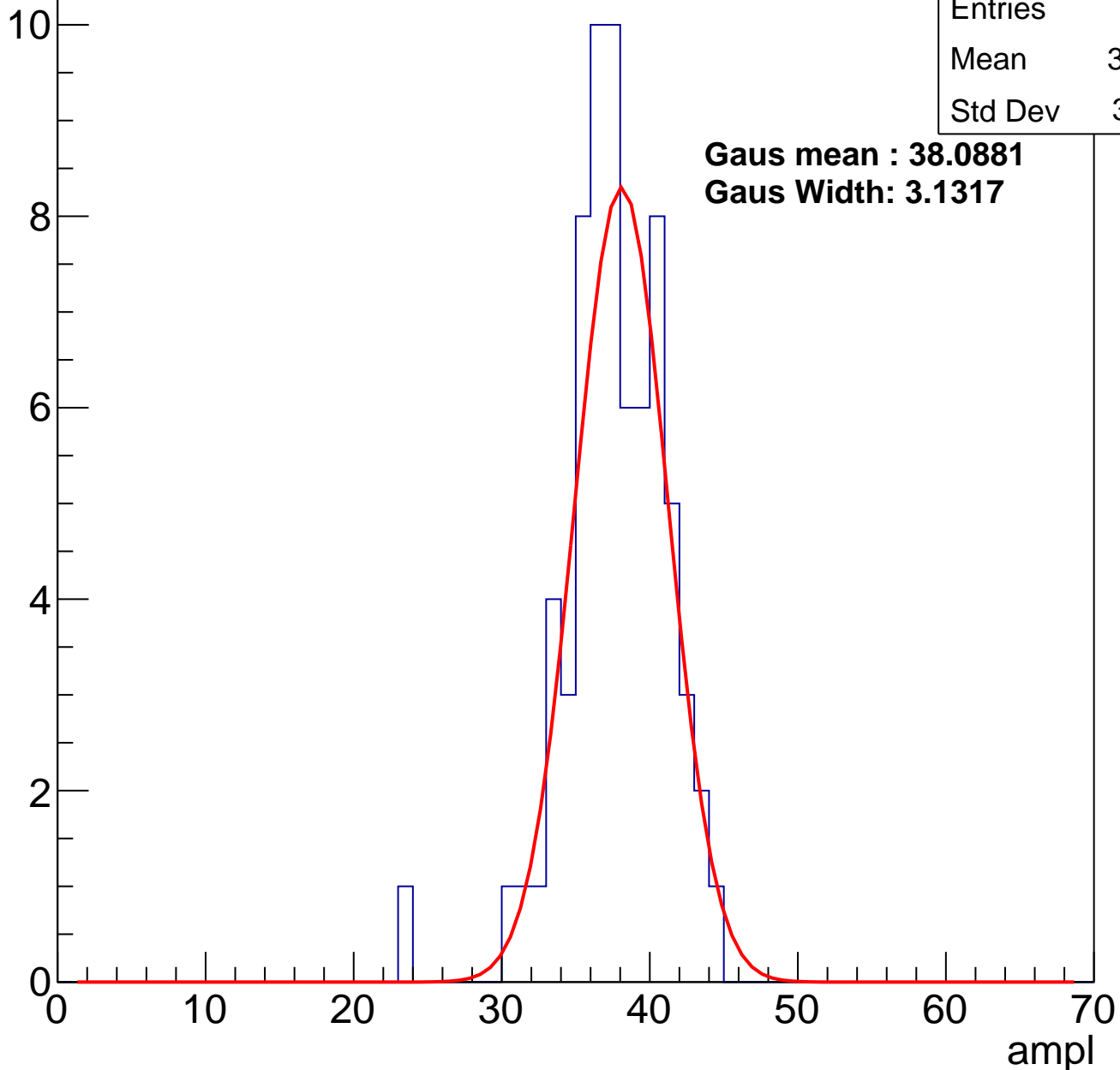
B0L002S, U4-ch37, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	37.19
Std Dev	3.411

Gaus mean : 38.0881
Gaus Width: 3.1317

Entry



B0L002S, U4-ch37, adc2

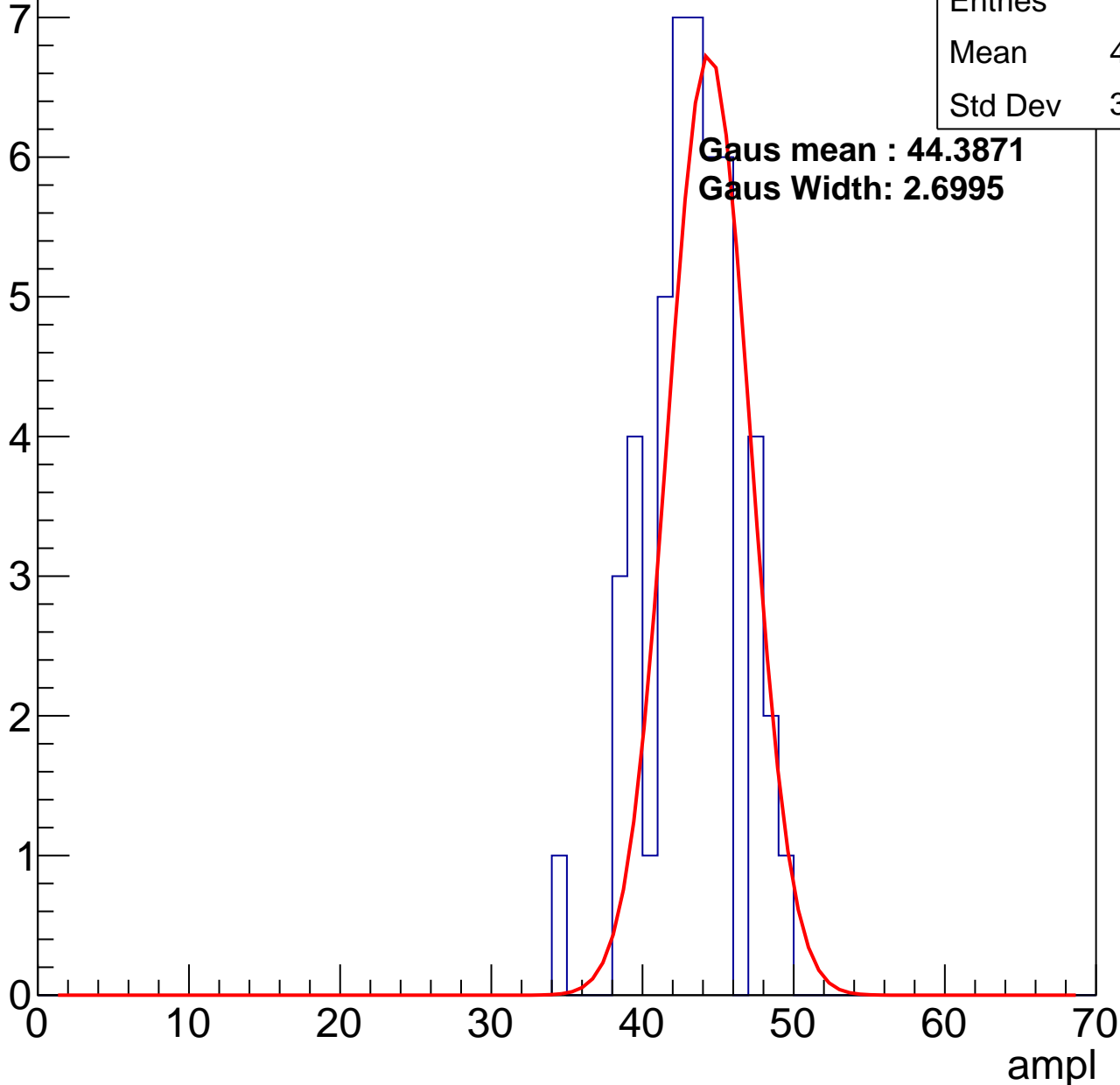
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	42.79
Std Dev	3.038

Gaus mean : 44.3871

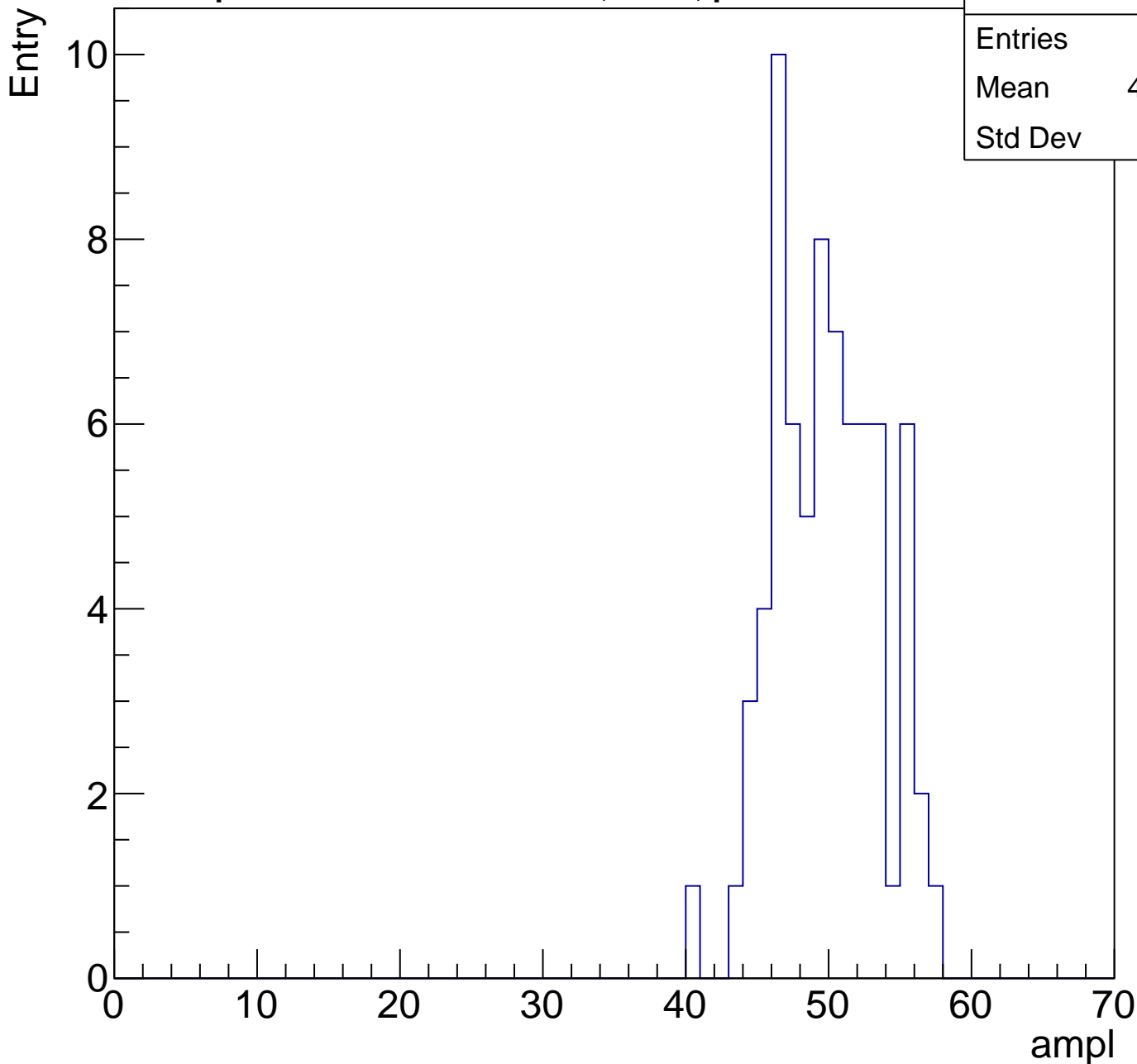
Gaus Width: 2.6995



B0L002S, U4-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	49.42
Std Dev	3.6

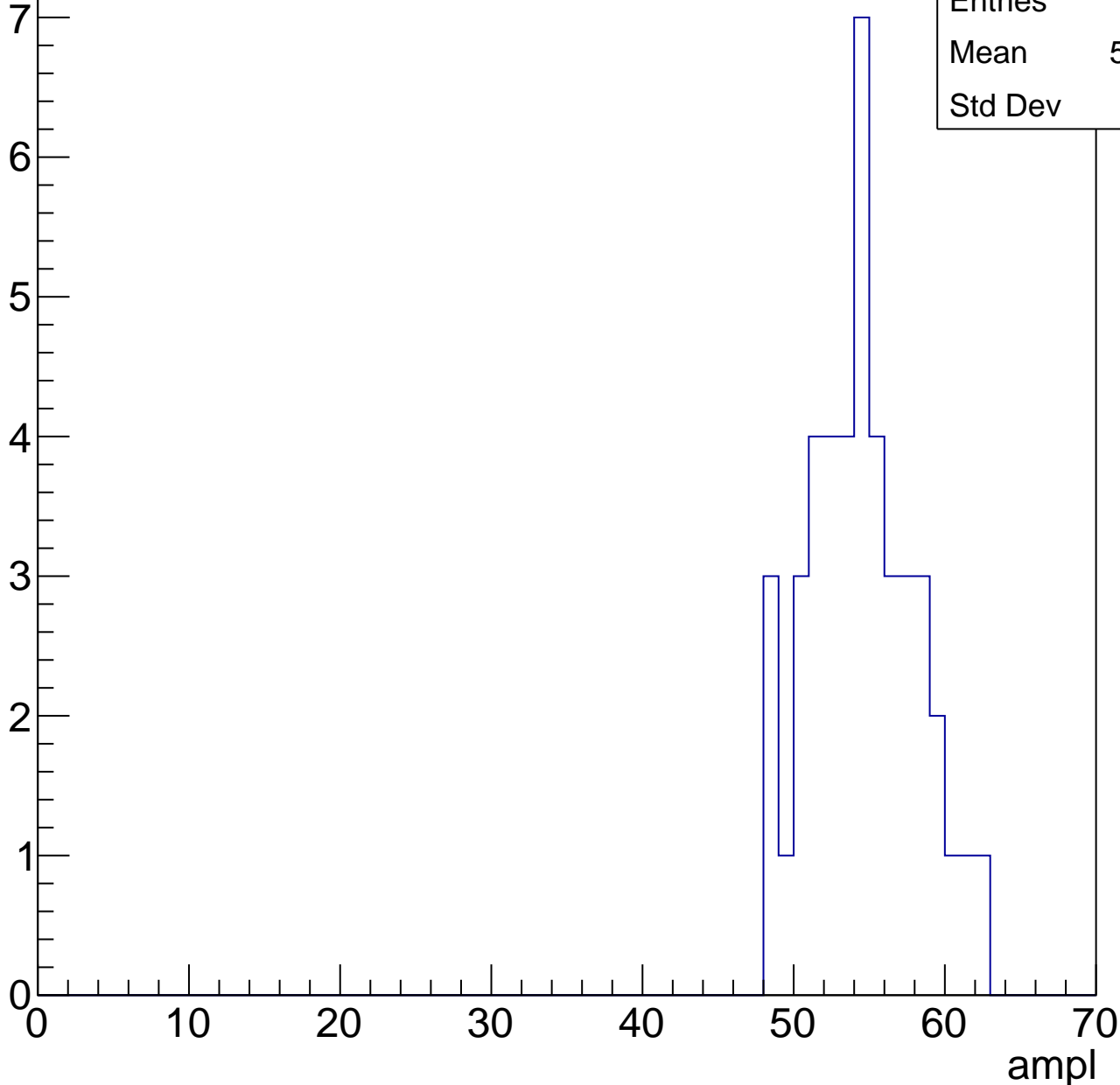


B0L002S, U4-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

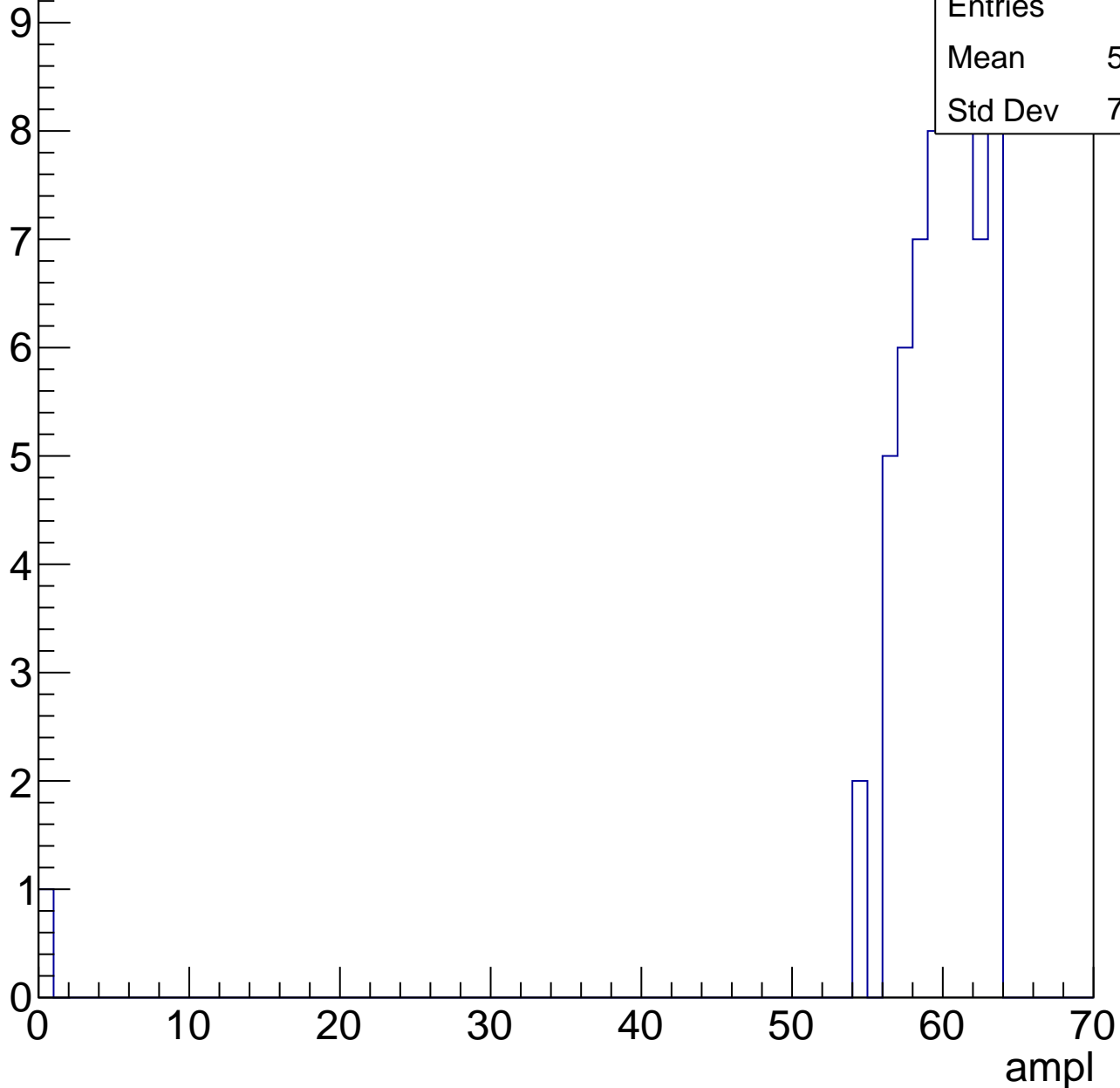
Entries	44
Mean	54.07
Std Dev	3.46



B0L002S, U4-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch38, adc0

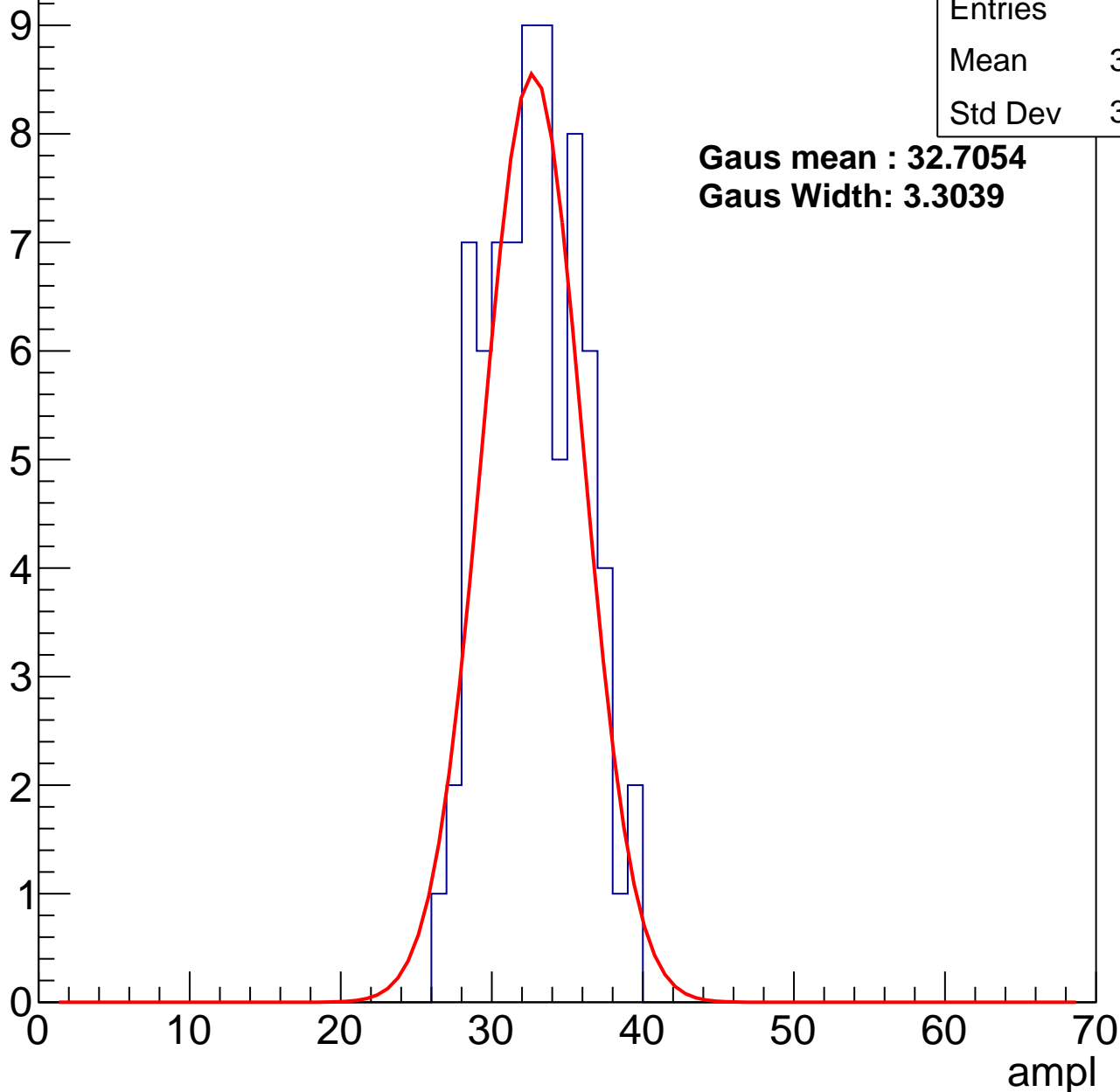
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	32.32
Std Dev	3.098

Gaus mean : 32.7054

Gaus Width: 3.3039



B0L002S, U4-ch38, adc1

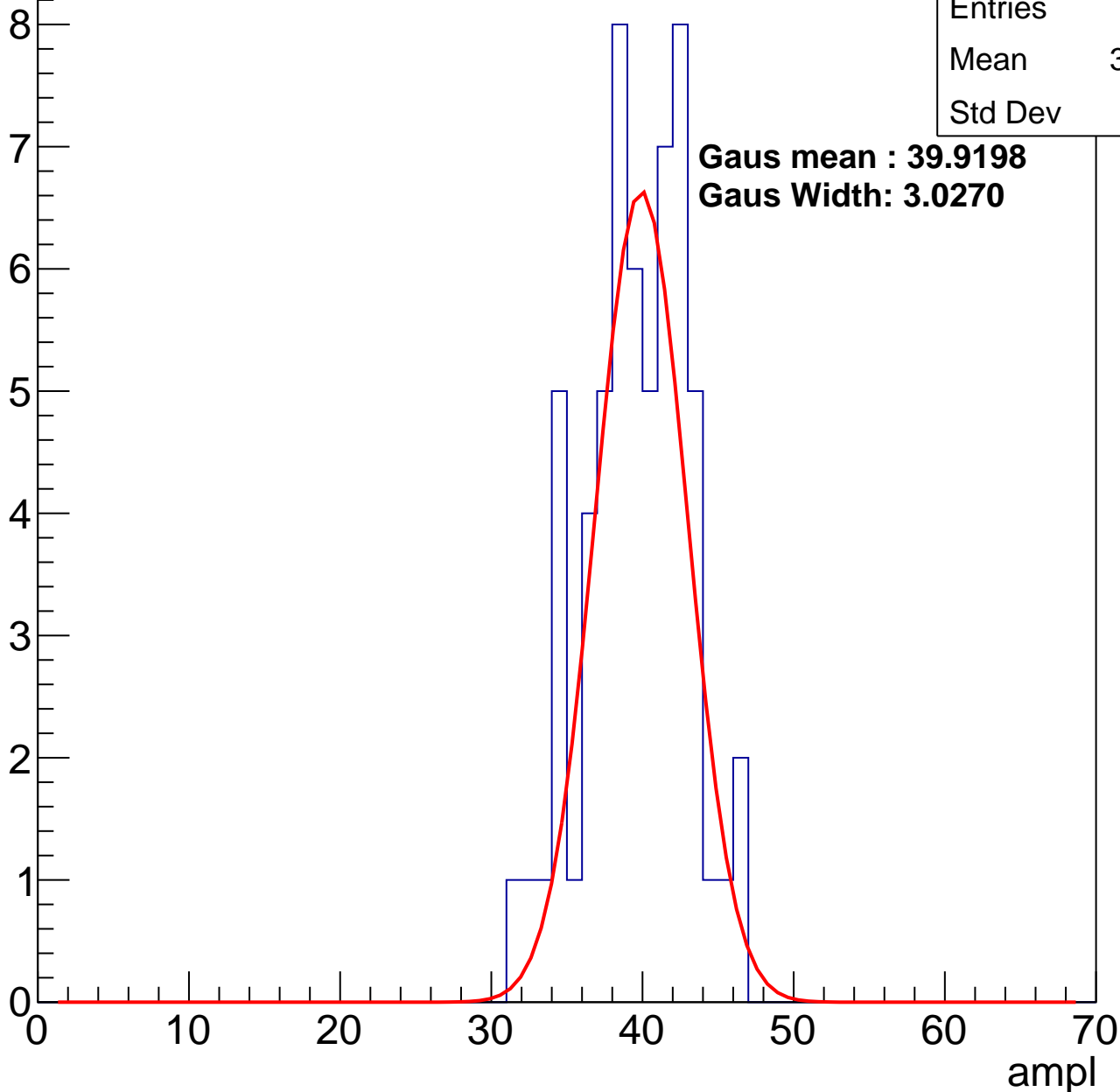
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	39.13
Std Dev	3.38

Gaus mean : 39.9198

Gaus Width: 3.0270



B0L002S, U4-ch38, adc2

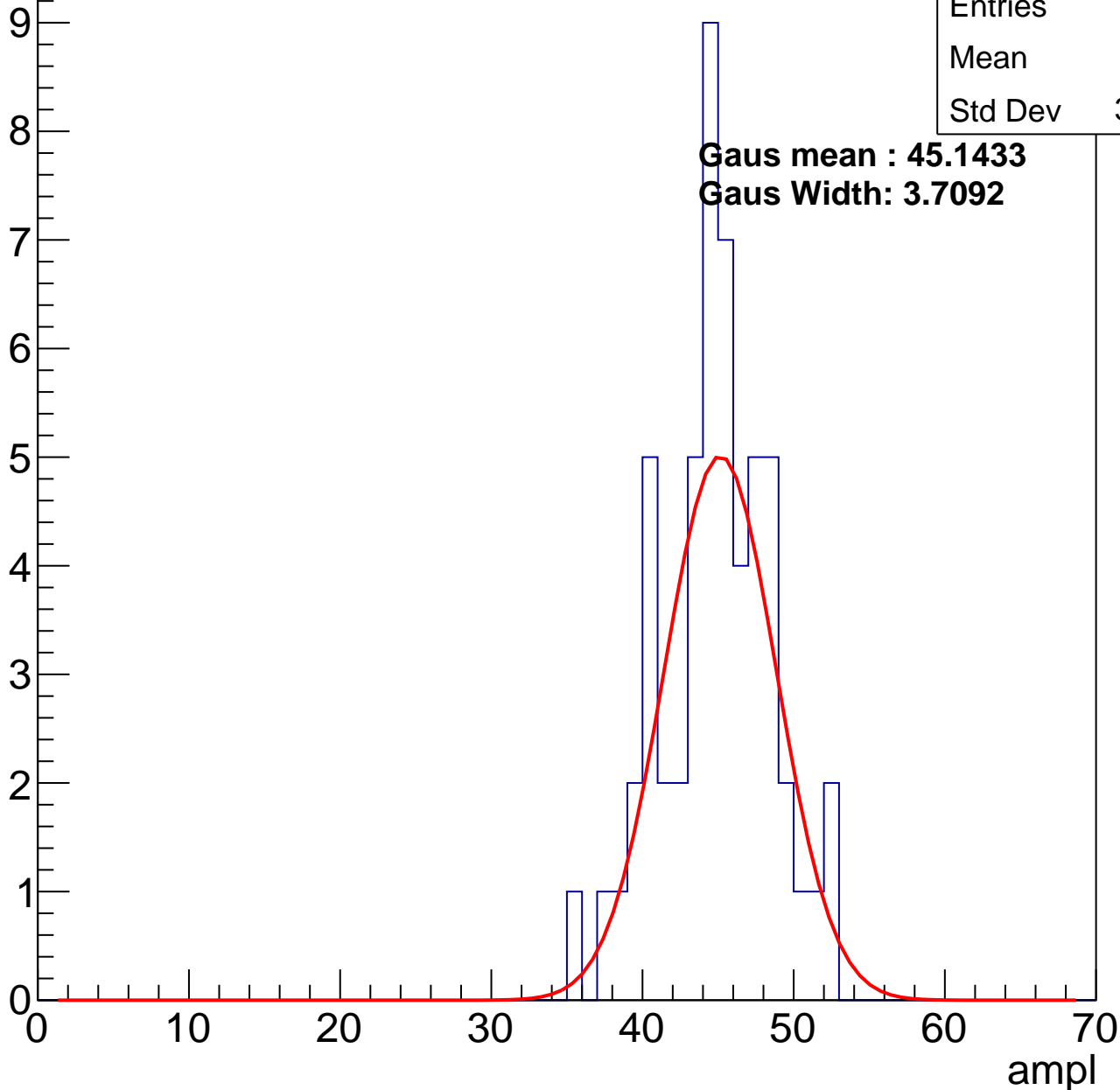
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.4
Std Dev	3.641

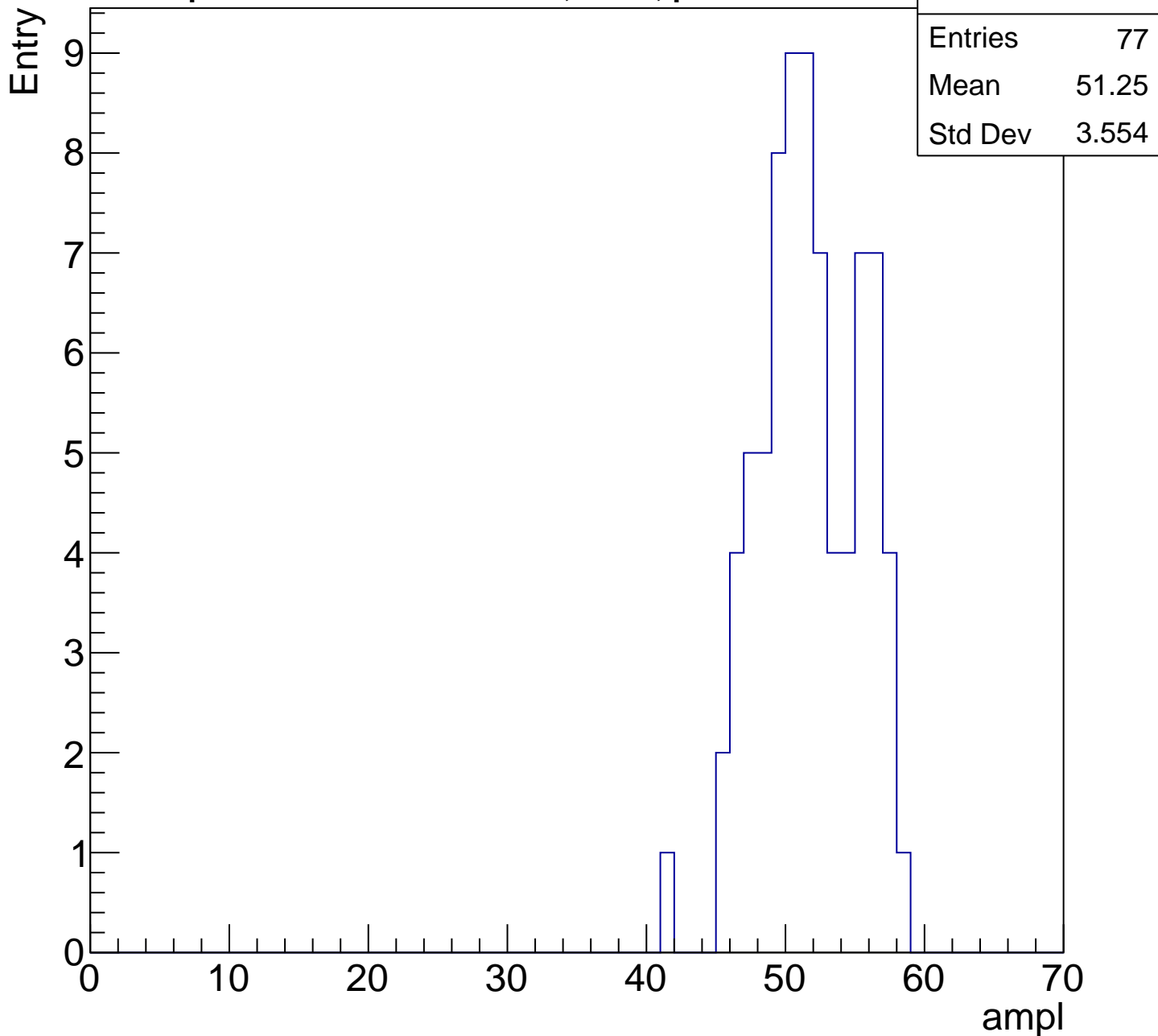
Gaus mean : 45.1433

Gaus Width: 3.7092



B0L002S, U4-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

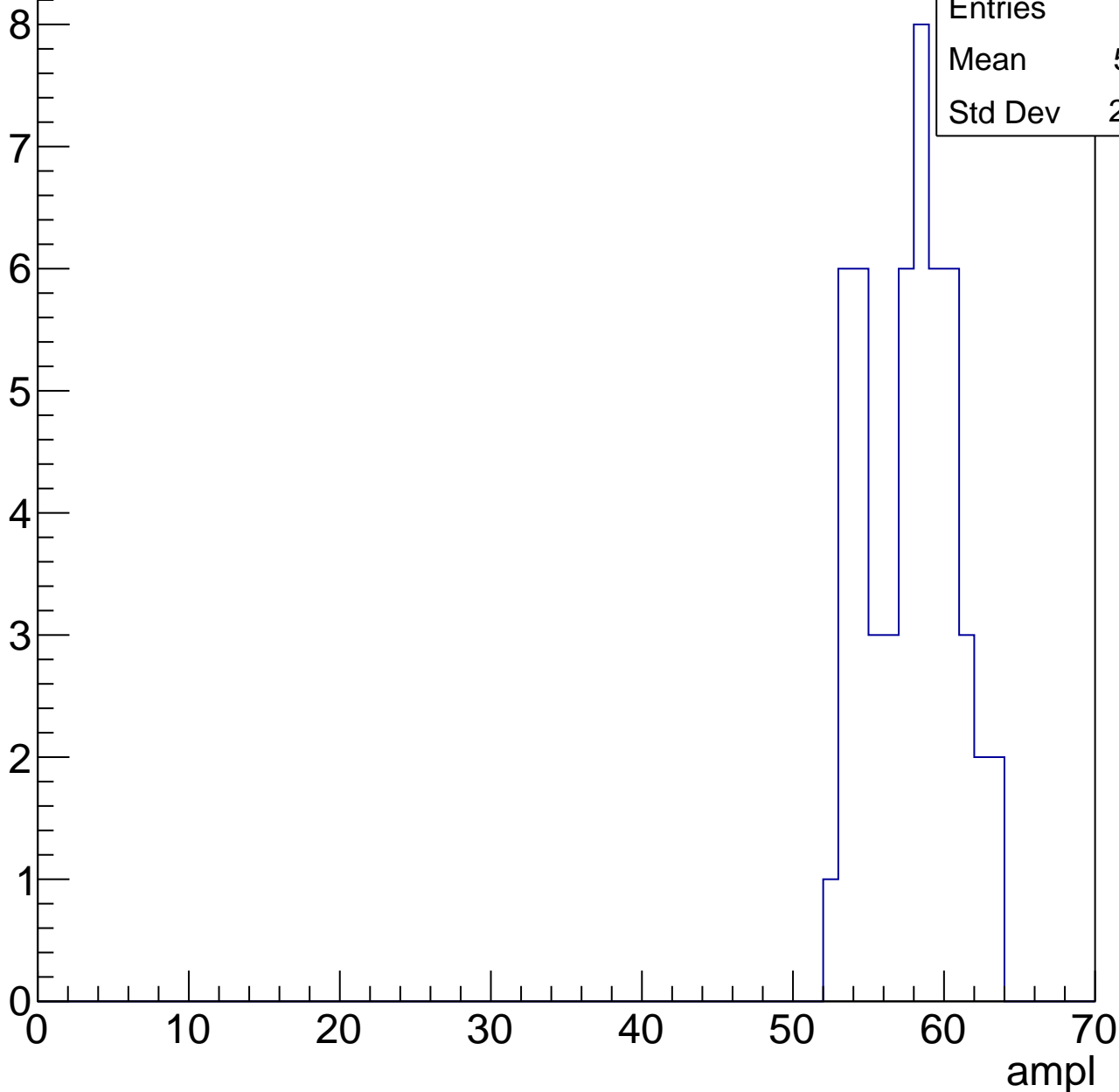


B0L002S, U4-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

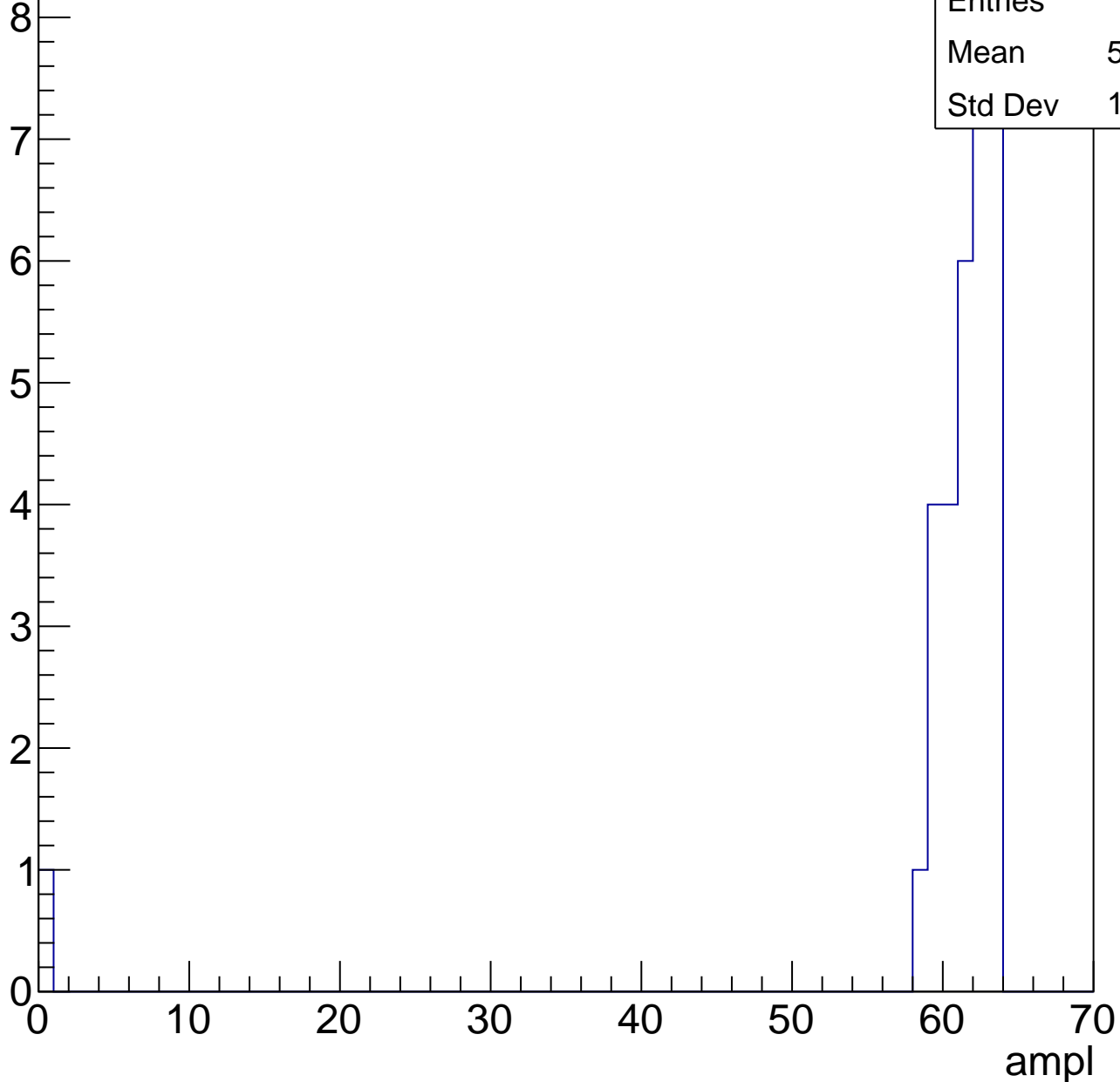
Entries	52
Mean	57.31
Std Dev	2.912



B0L002S, U4-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

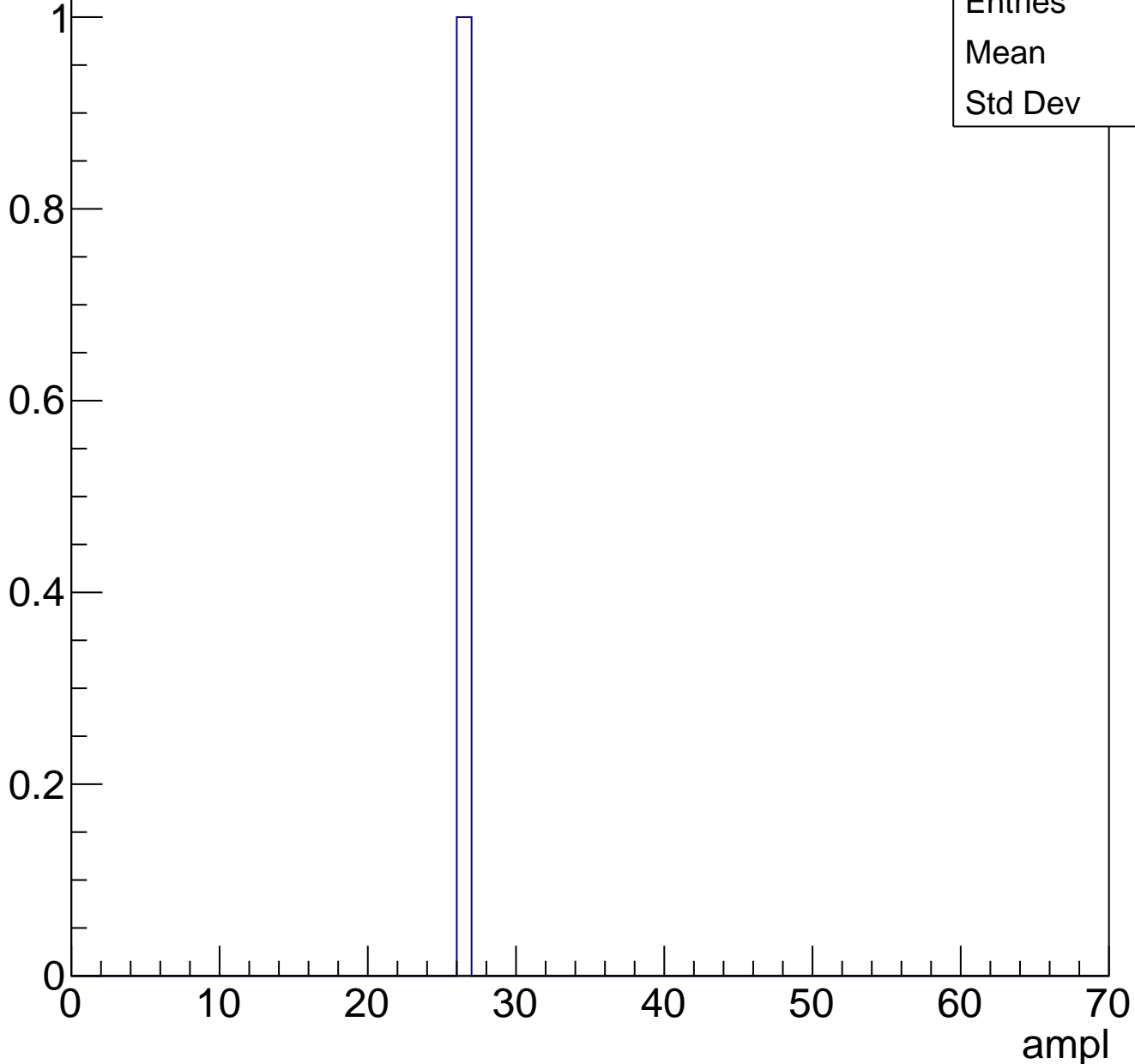
Entry



B0L002S, U4-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	26
Std Dev	0

B0L002S, U4-ch39, adc0

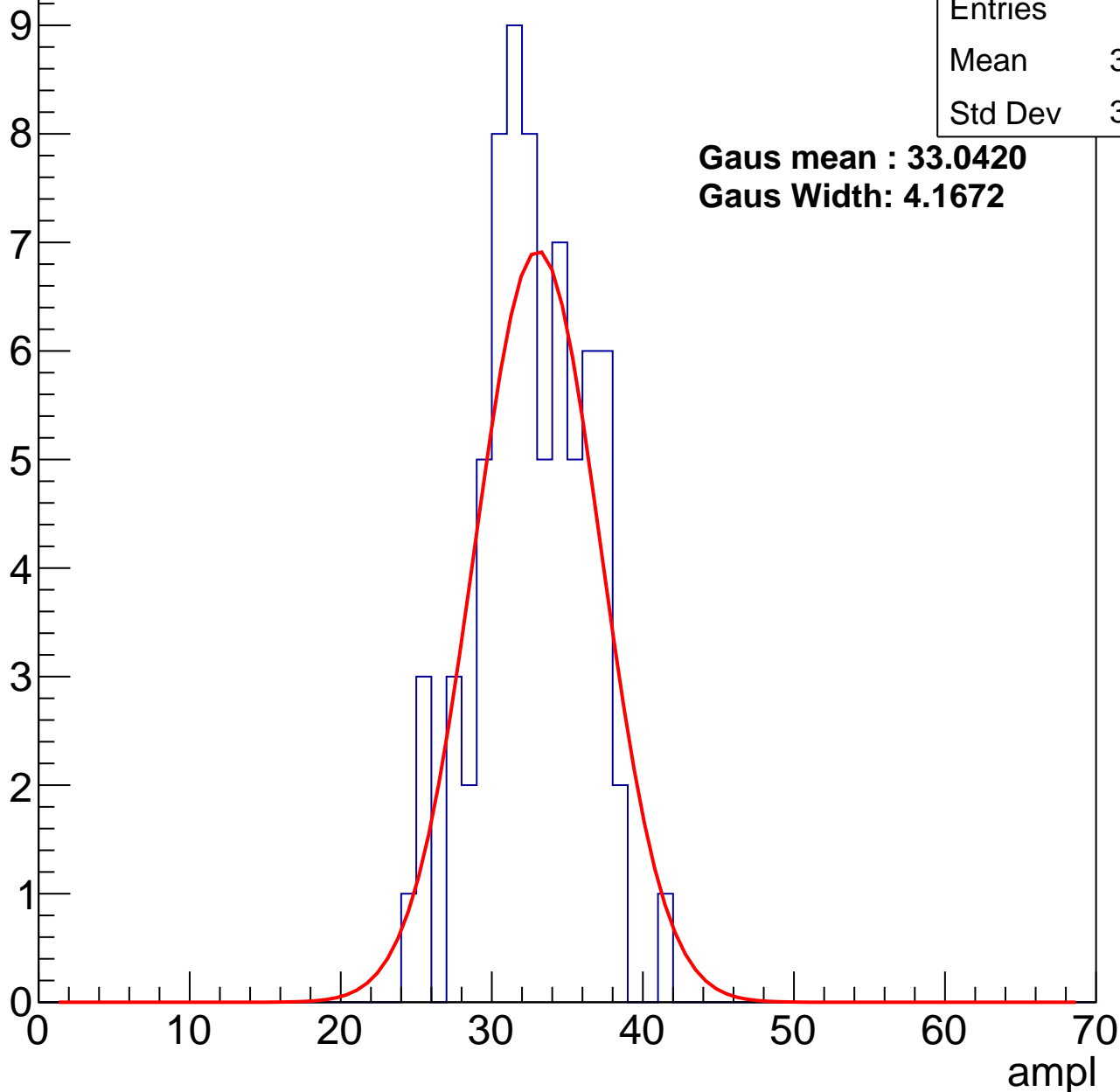
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	32.24
Std Dev	3.514

Gaus mean : 33.0420

Gaus Width: 4.1672



B0L002S, U4-ch39, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	39.39
Std Dev	3.571

Gaus mean : 40.1286

Gaus Width: 4.0476

10

8

6

4

2

0

0

10

20

30

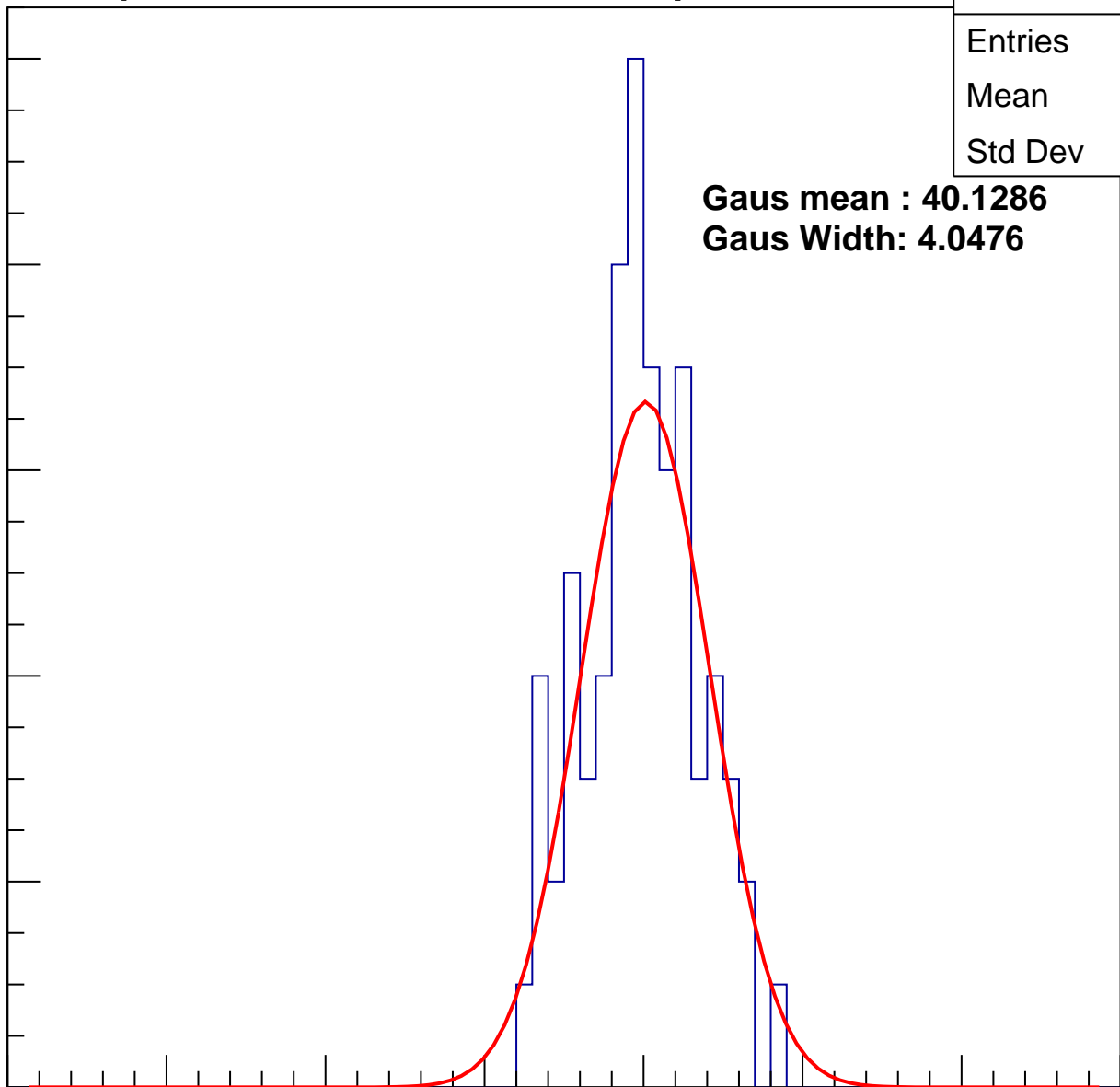
40

50

60

70

ampl



B0L002S, U4-ch39, adc2

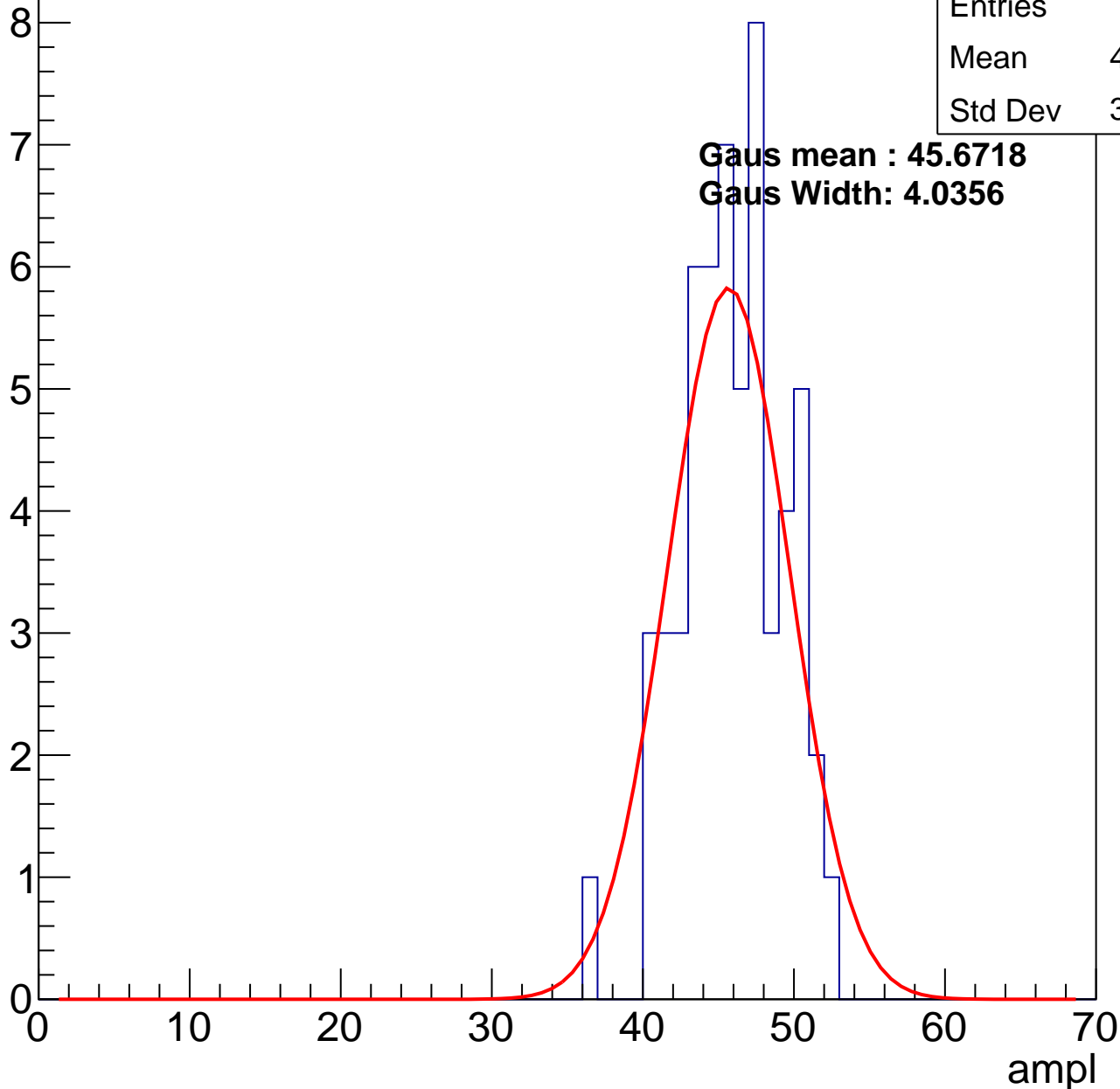
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.47
Std Dev	3.304

Gaus mean : 45.6718

Gaus Width: 4.0356

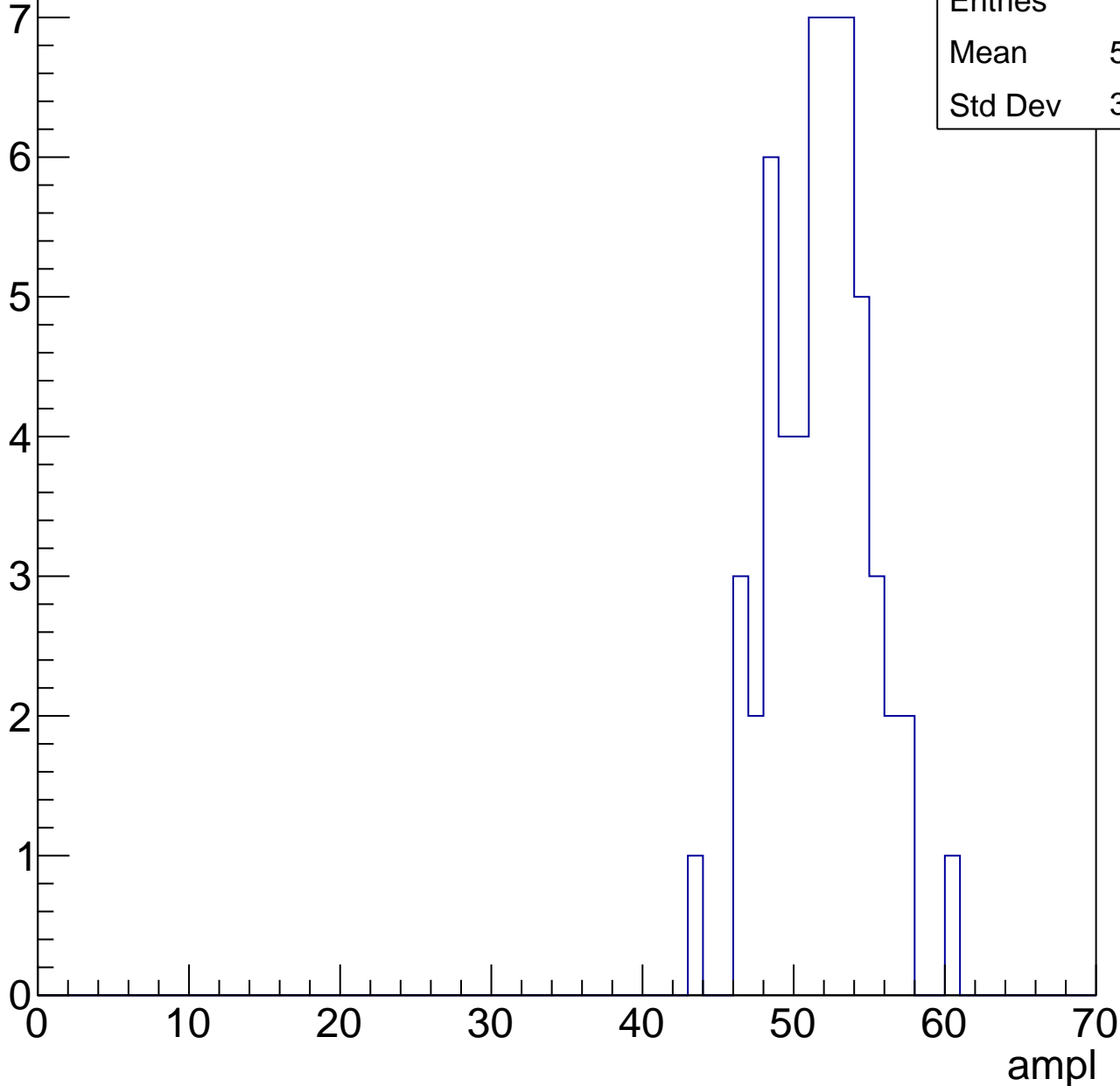


B0L002S, U4-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	51.33
Std Dev	3.249

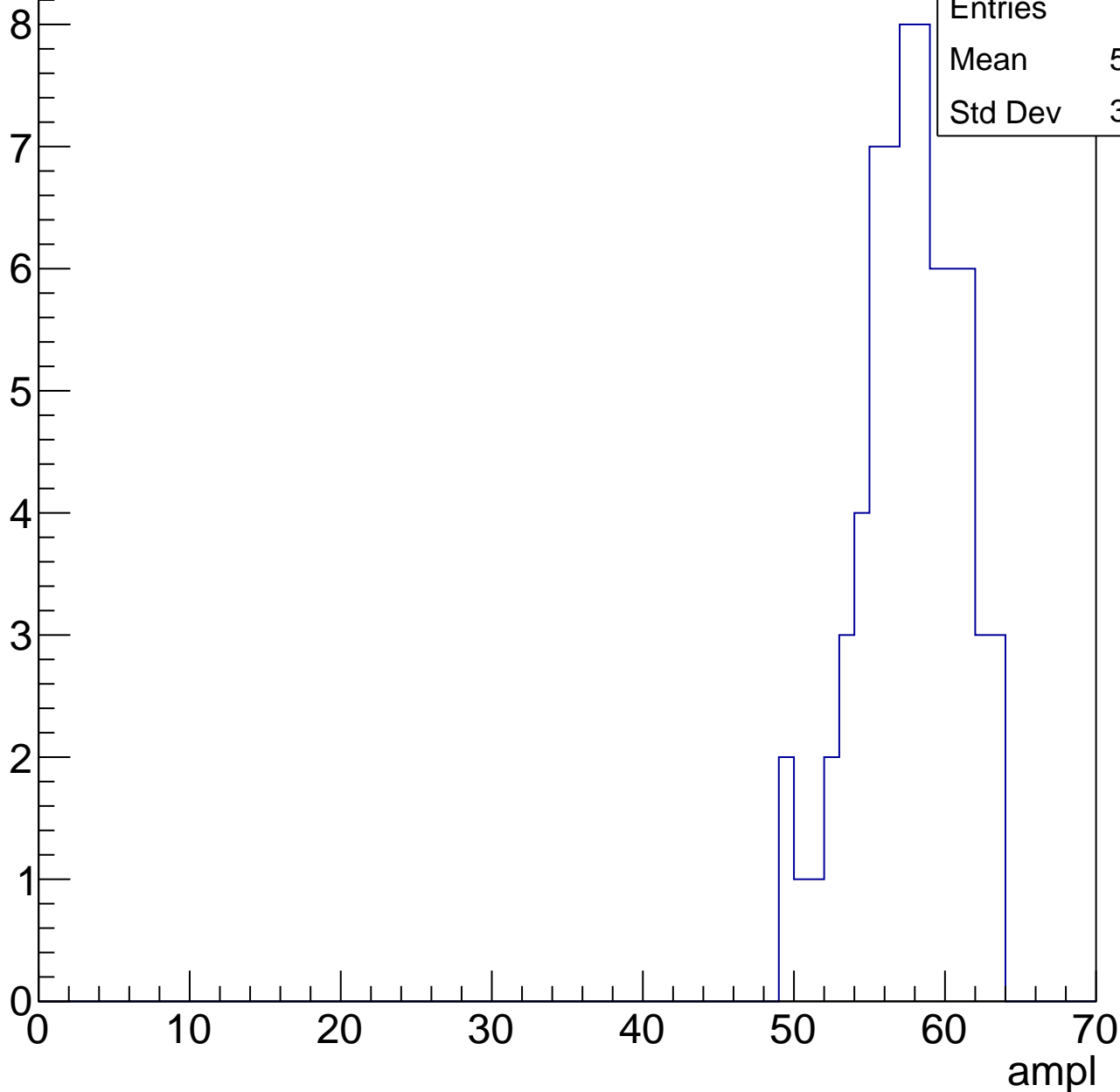


B0L002S, U4-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	57.16
Std Dev	3.335

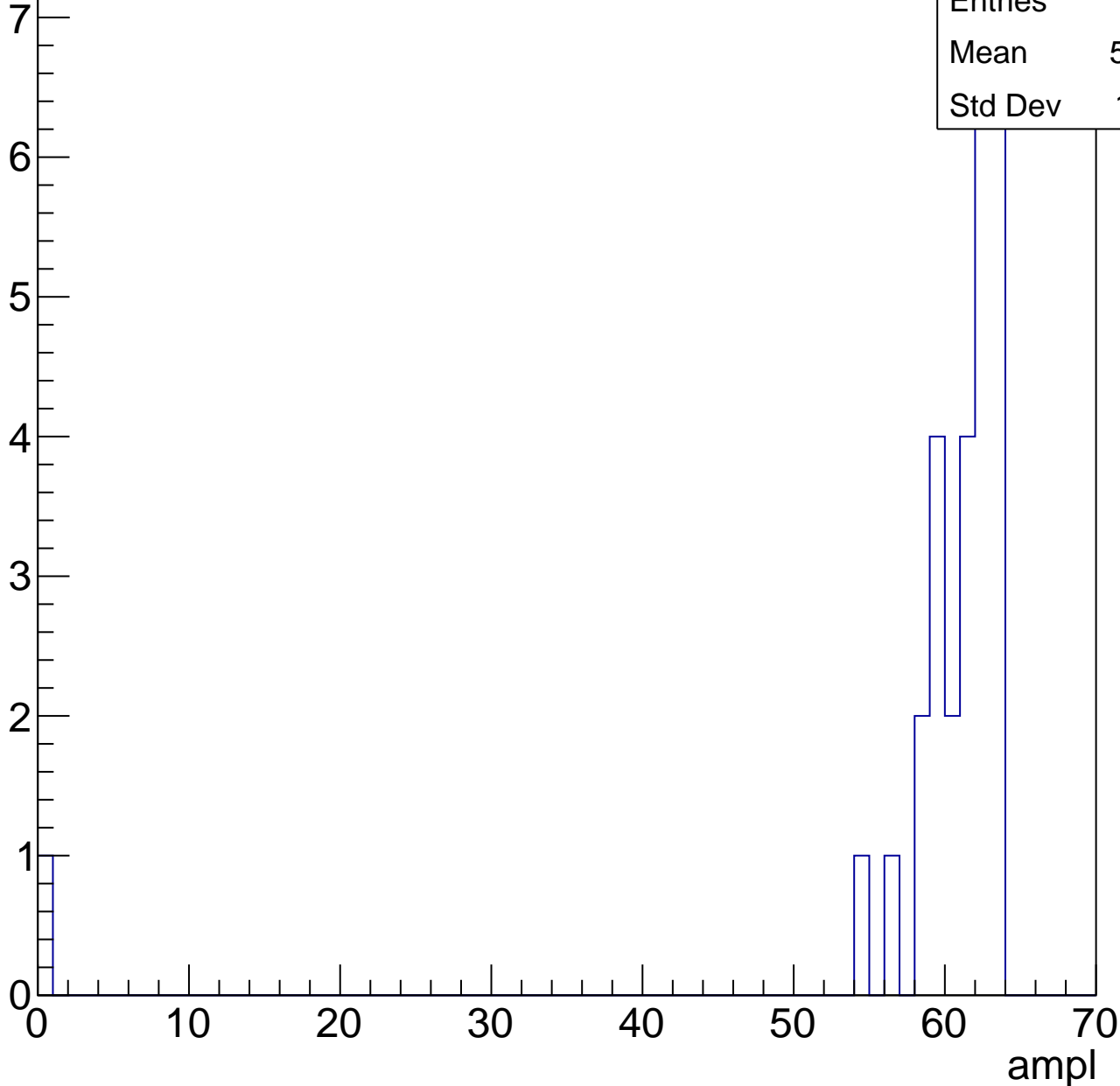


B0L002S, U4-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	29
Mean	58.66
Std Dev	11.31



B0L002S, U4-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U4-ch40, adc0

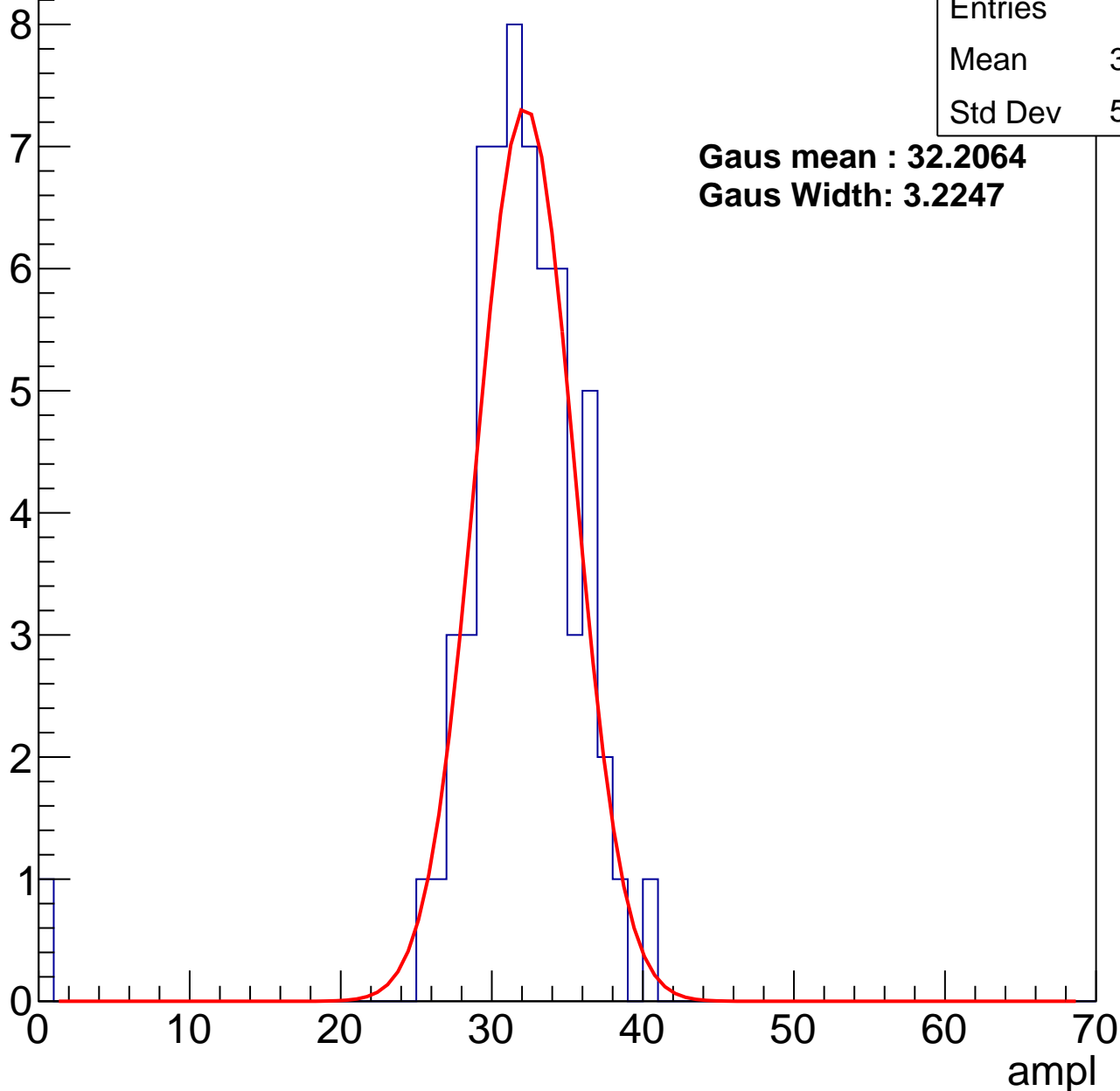
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	31.29
Std Dev	5.059

Gaus mean : 32.2064

Gaus Width: 3.2247



B0L002S, U4-ch40, adc1

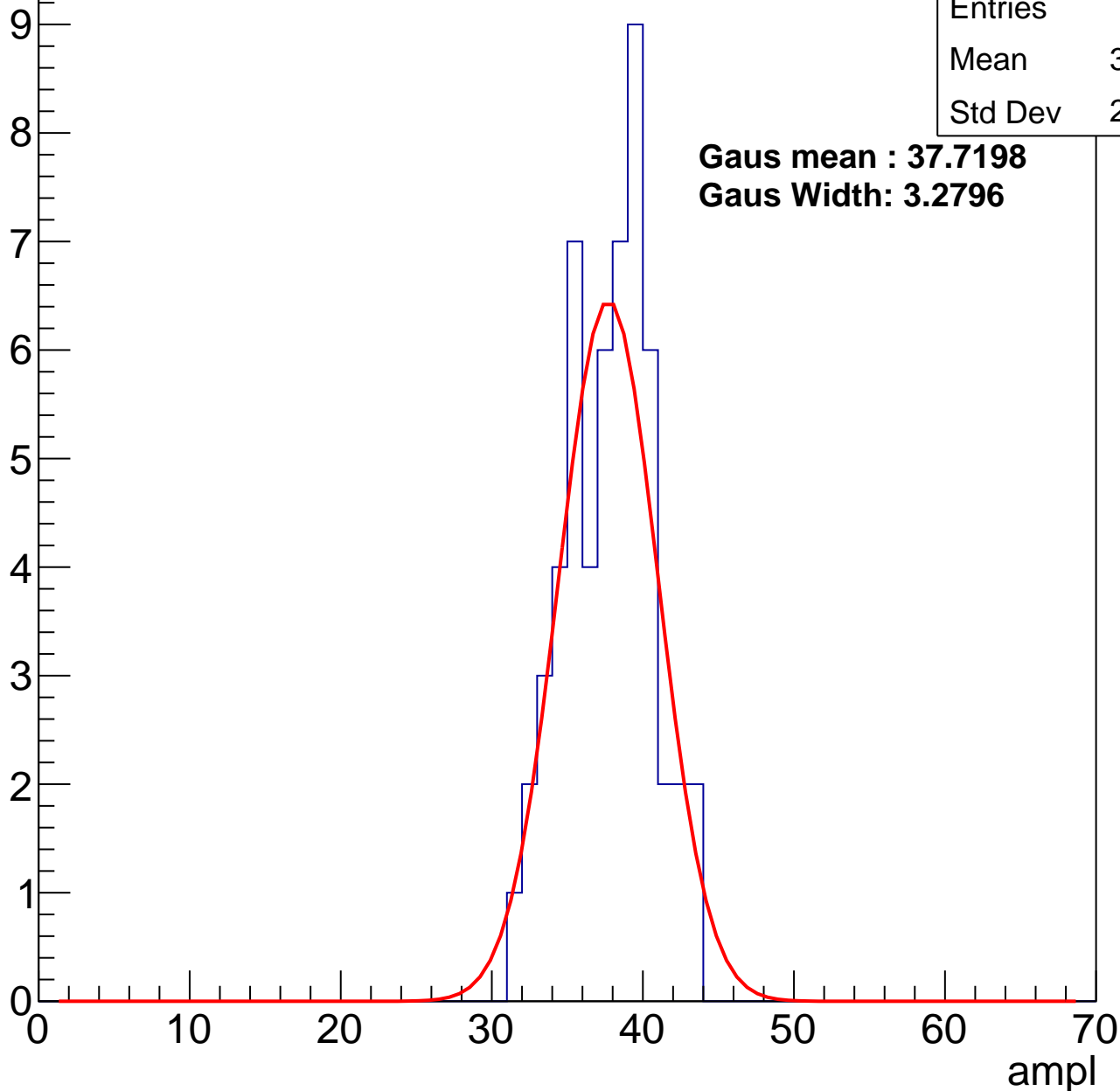
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	37.27
Std Dev	2.857

Gaus mean : 37.7198

Gaus Width: 3.2796

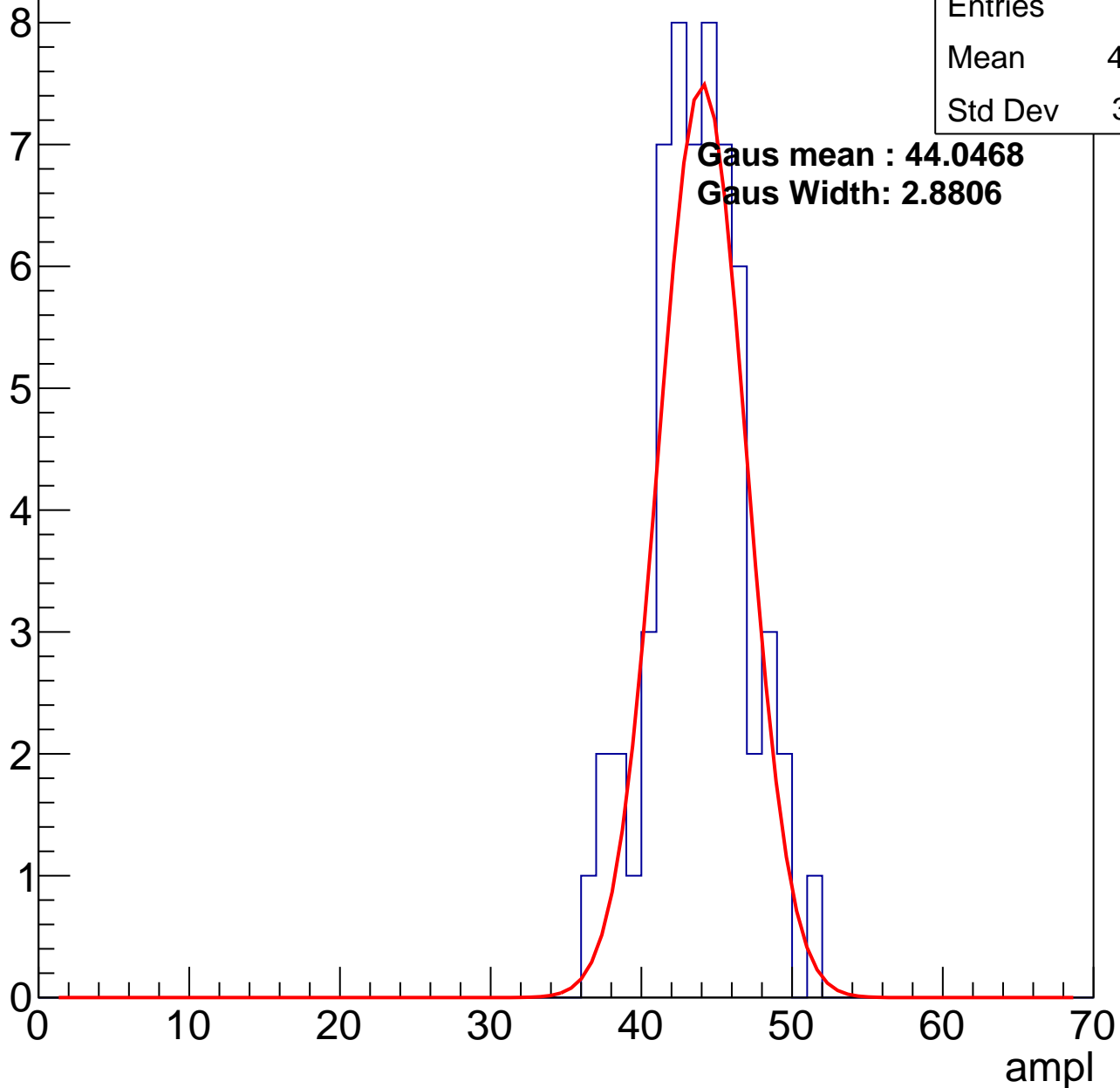


B0L002S, U4-ch40, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.32
Std Dev	3.101

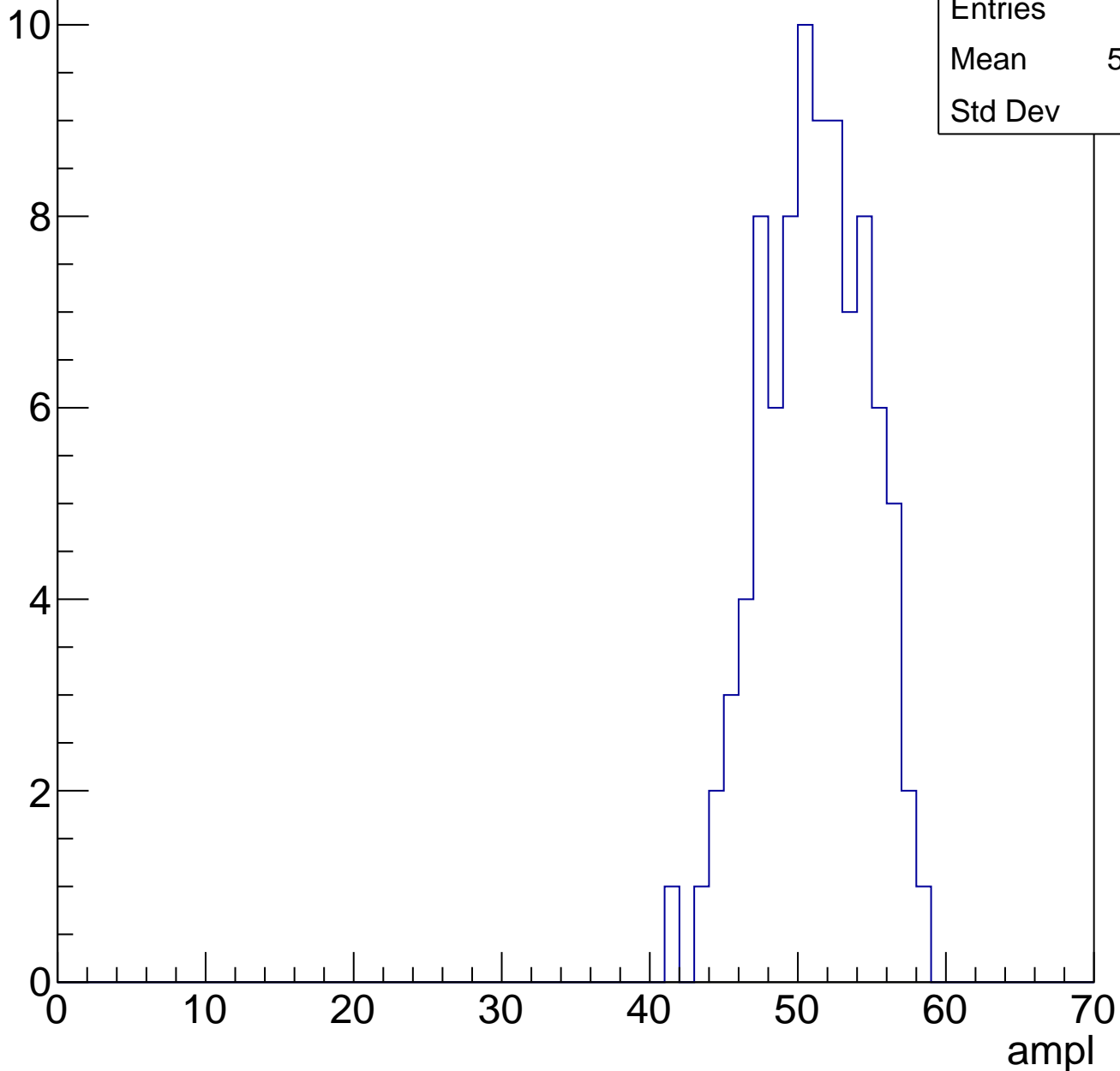


B0L002S, U4-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	90
Mean	50.66
Std Dev	3.55

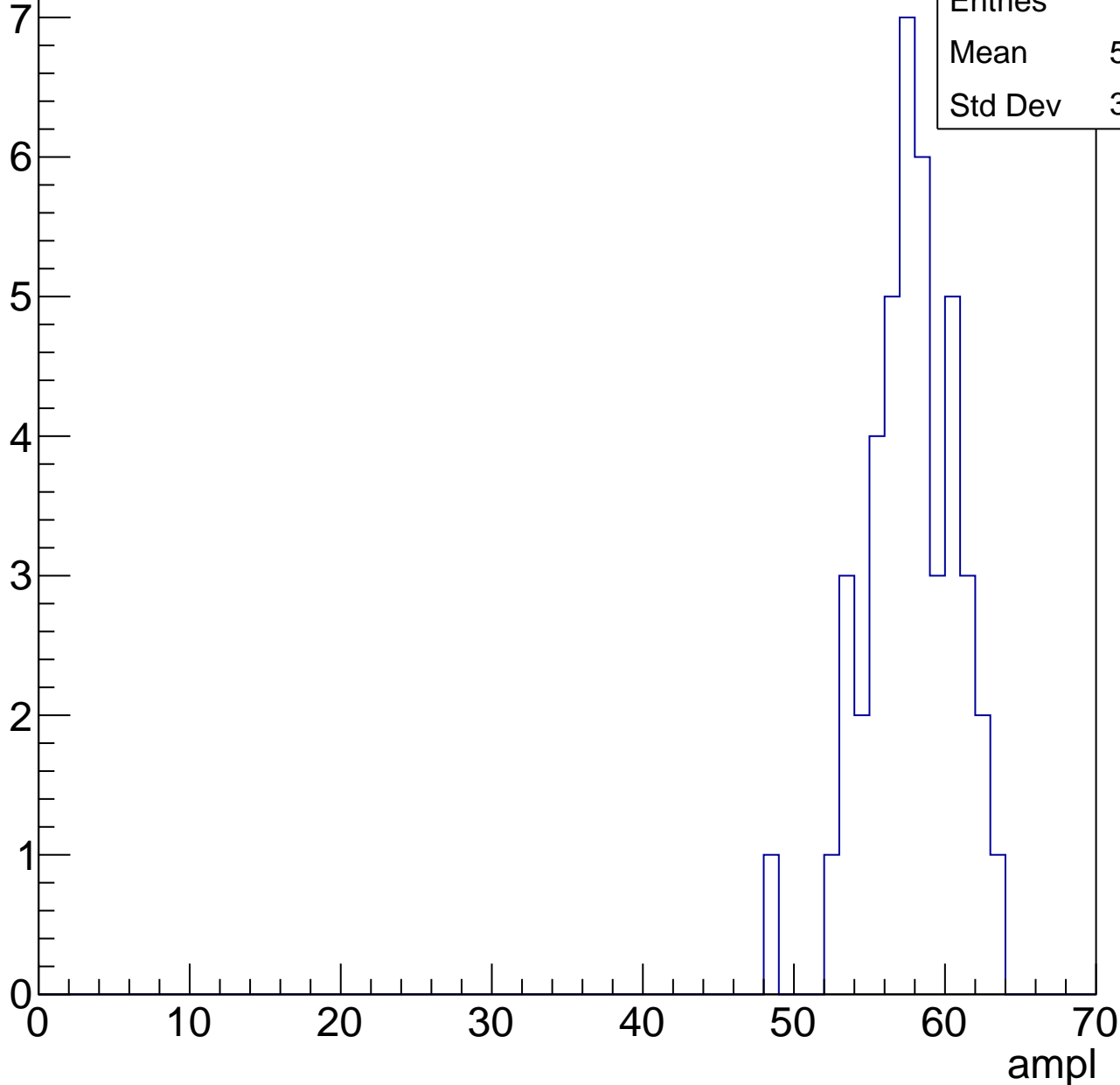
Entry



B0L002S, U4-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

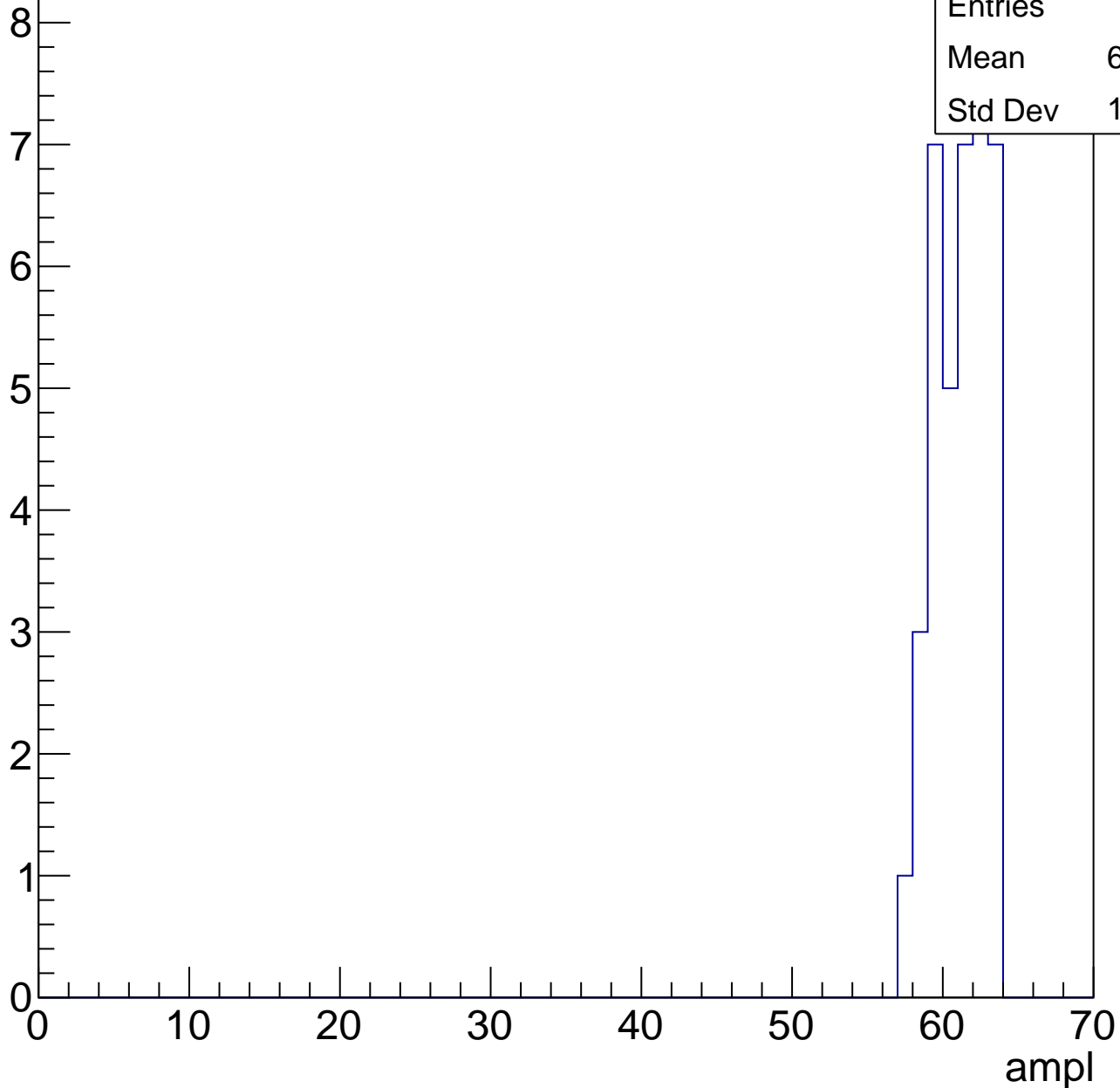
Entry



B0L002S, U4-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch41, adc0

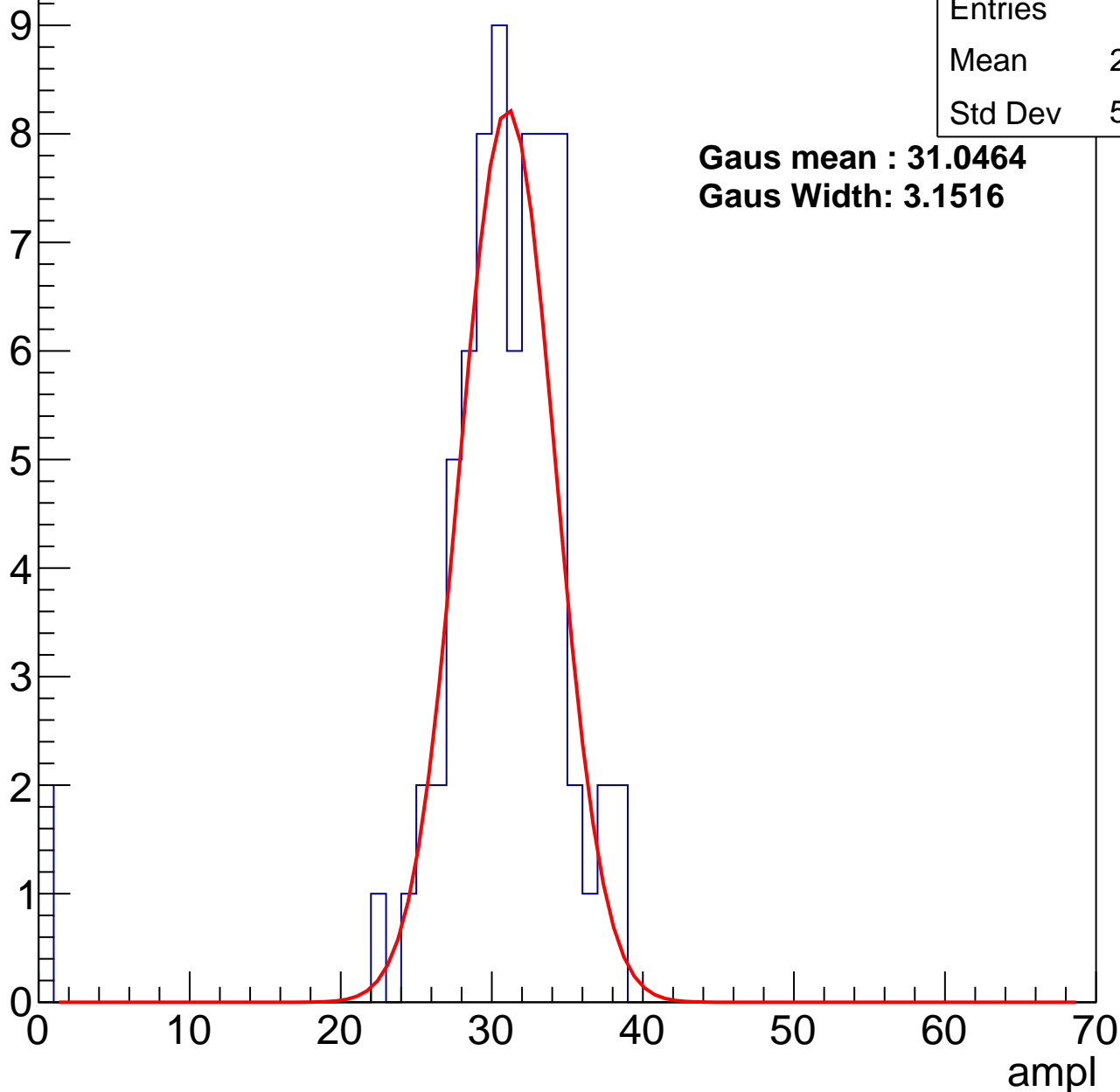
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	29.96
Std Dev	5.978

Gaus mean : 31.0464

Gaus Width: 3.1516



B0L002S, U4-ch41, adc1

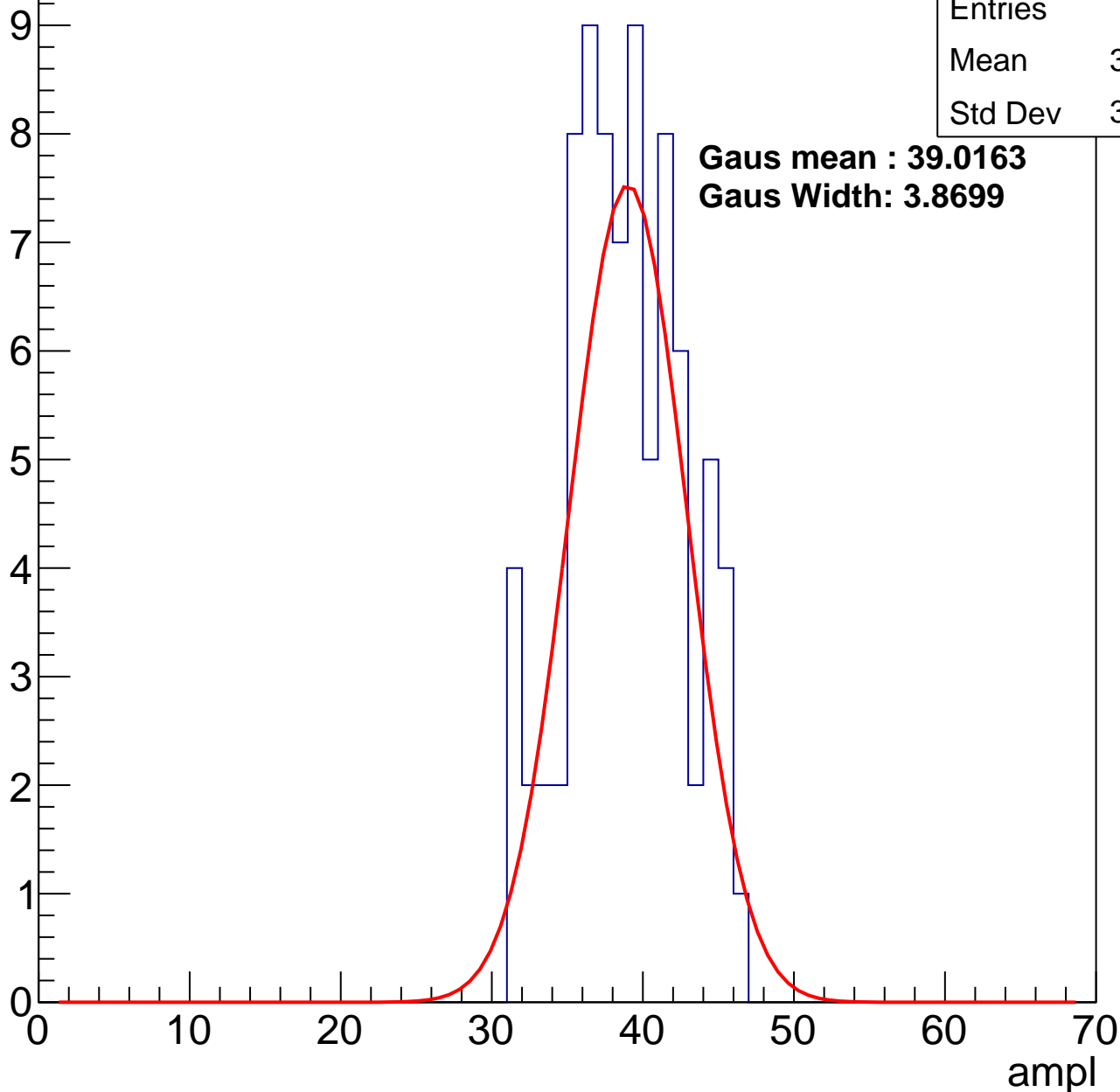
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	38.43
Std Dev	3.725

Gaus mean : 39.0163

Gaus Width: 3.8699



B0L002S, U4-ch41, adc2

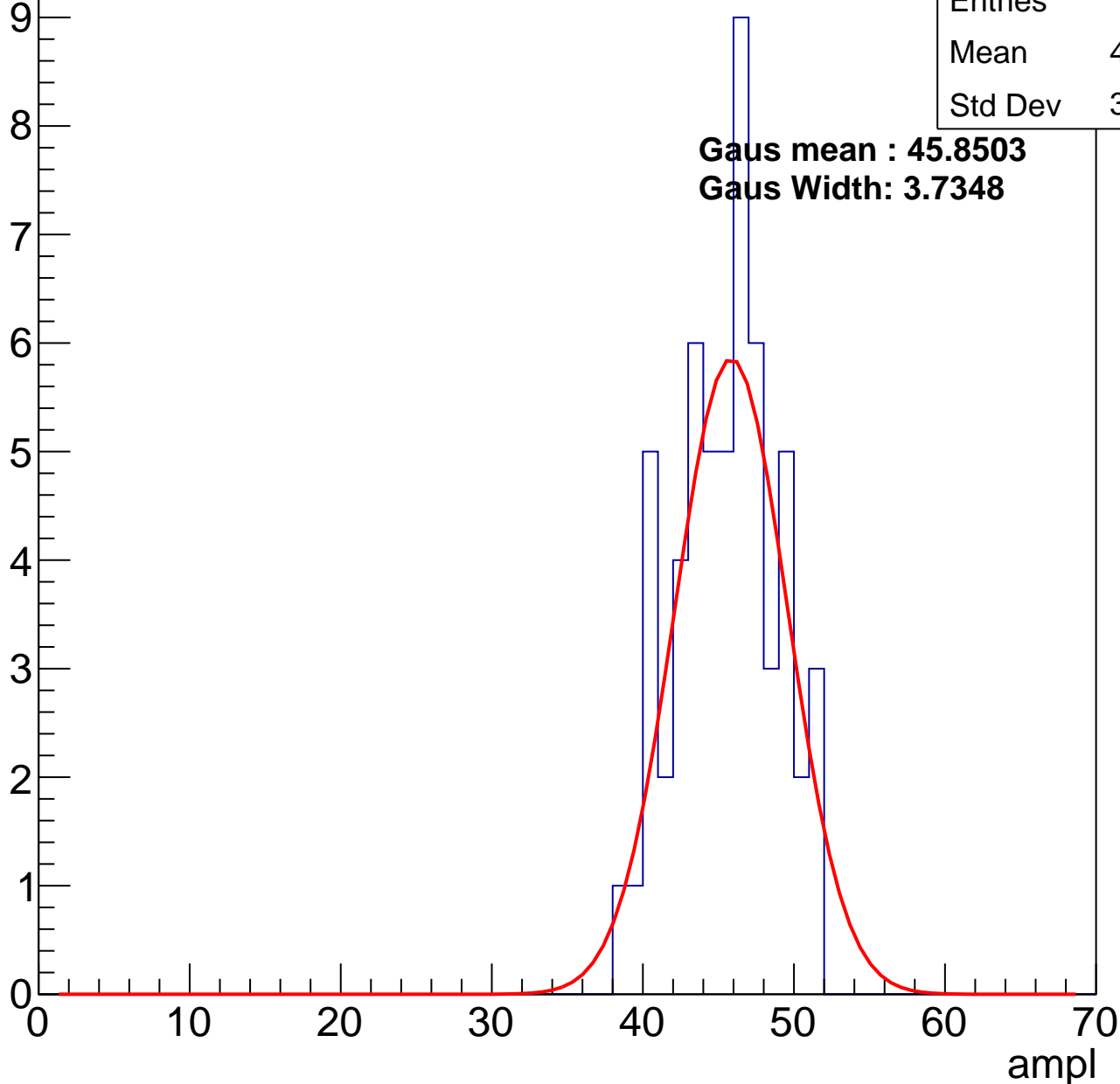
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	45.05
Std Dev	3.257

Gaus mean : 45.8503

Gaus Width: 3.7348

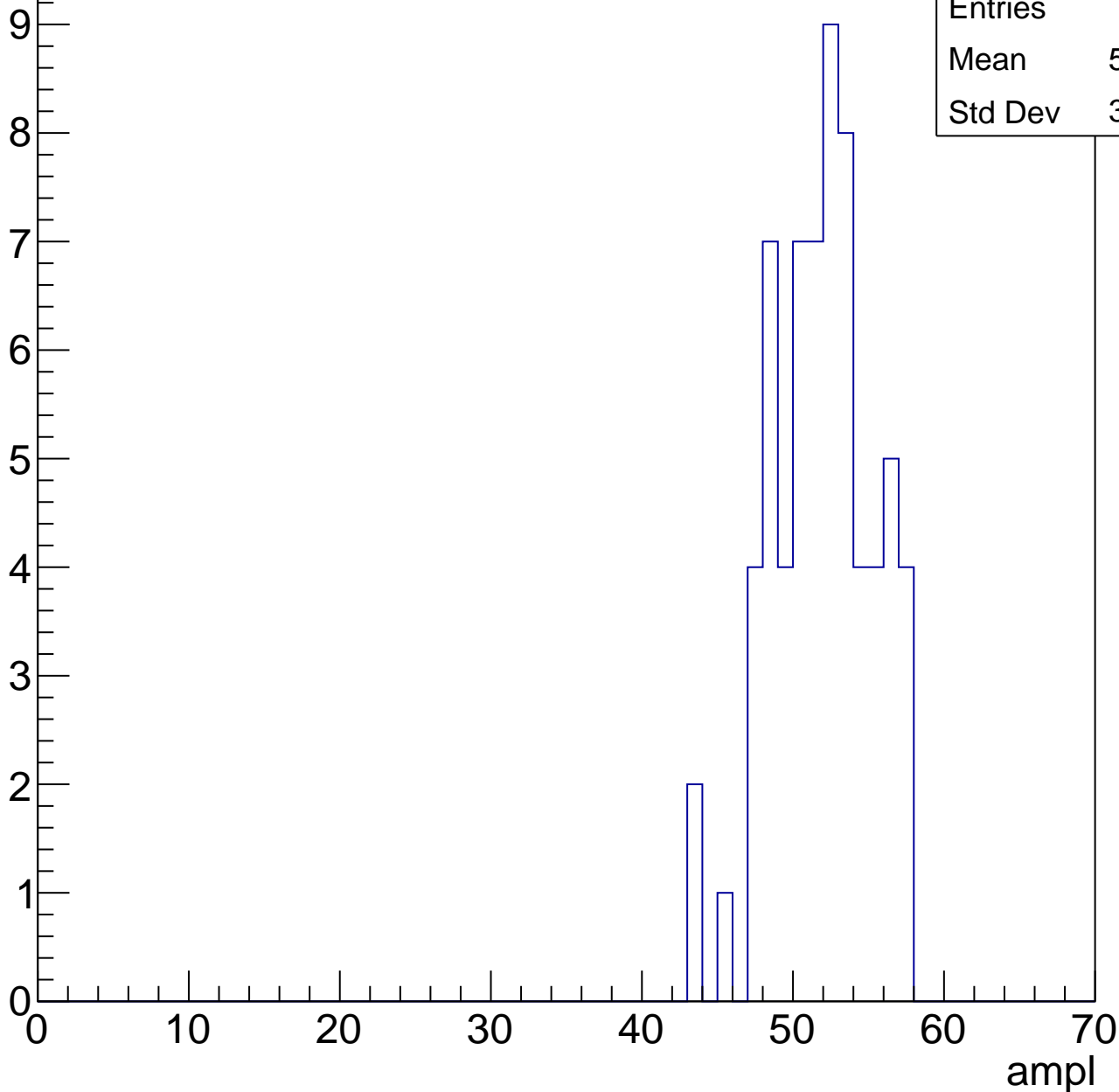


B0L002S, U4-ch41, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	51.42
Std Dev	3.285



B0L002S, U4-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

6

5

4

3

2

1

0

Entries	44
Mean	56.73
Std Dev	3.048

10

20

30

40

50

60

70

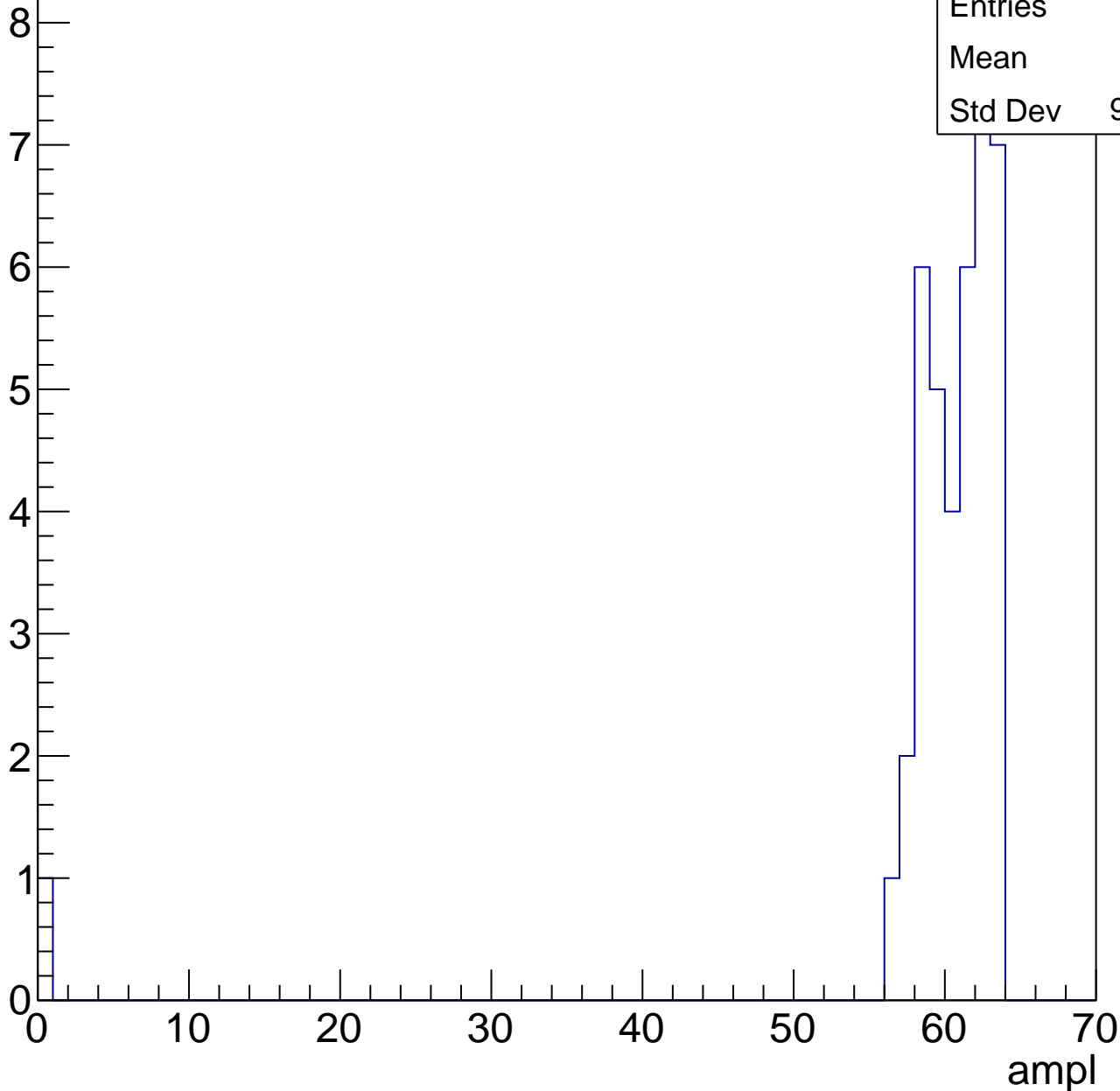
ampl

B0L002S, U4-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

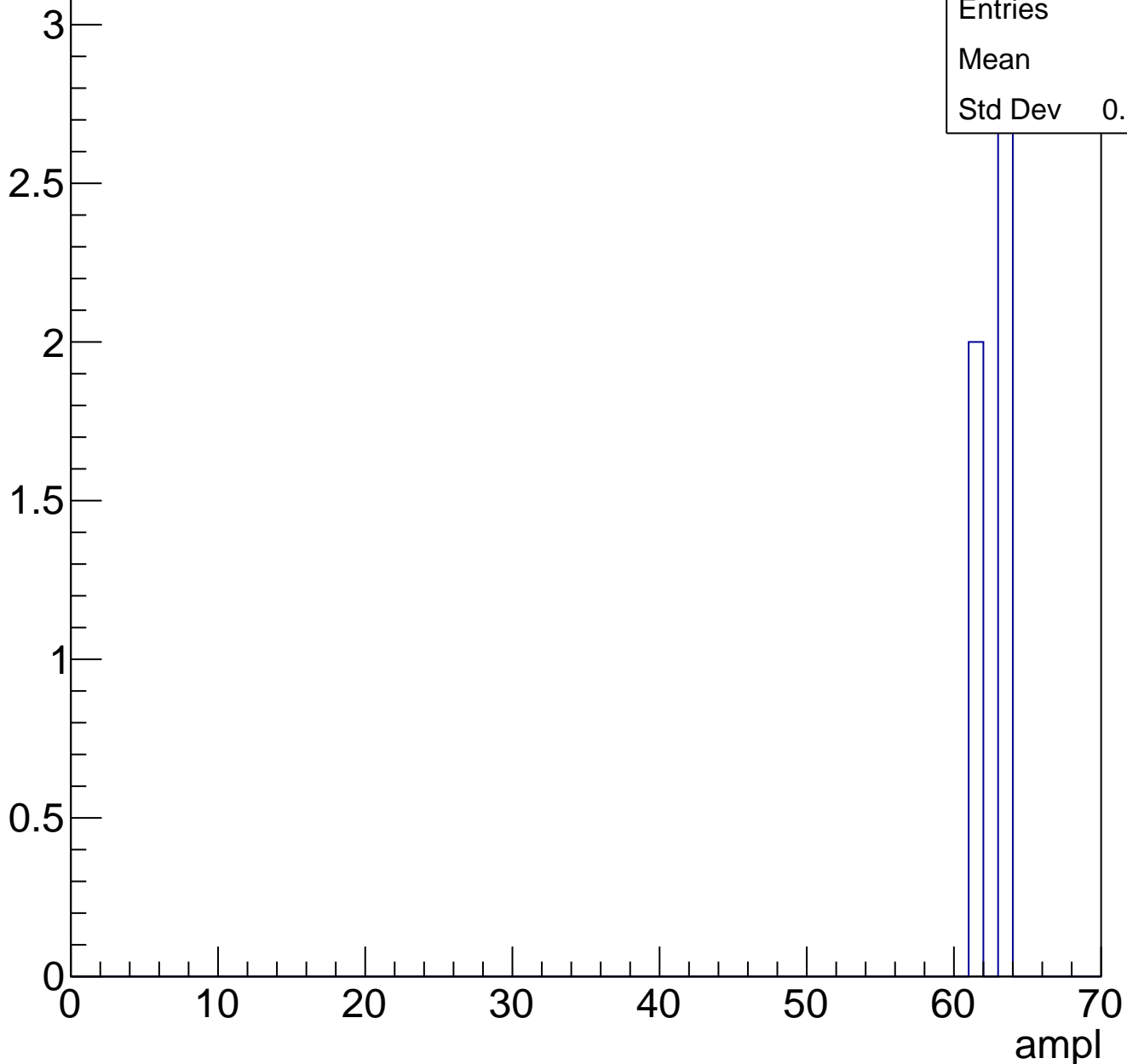
Entries	40
Mean	58.9
Std Dev	9.638



B0L002S, U4-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U4-ch42, adc0

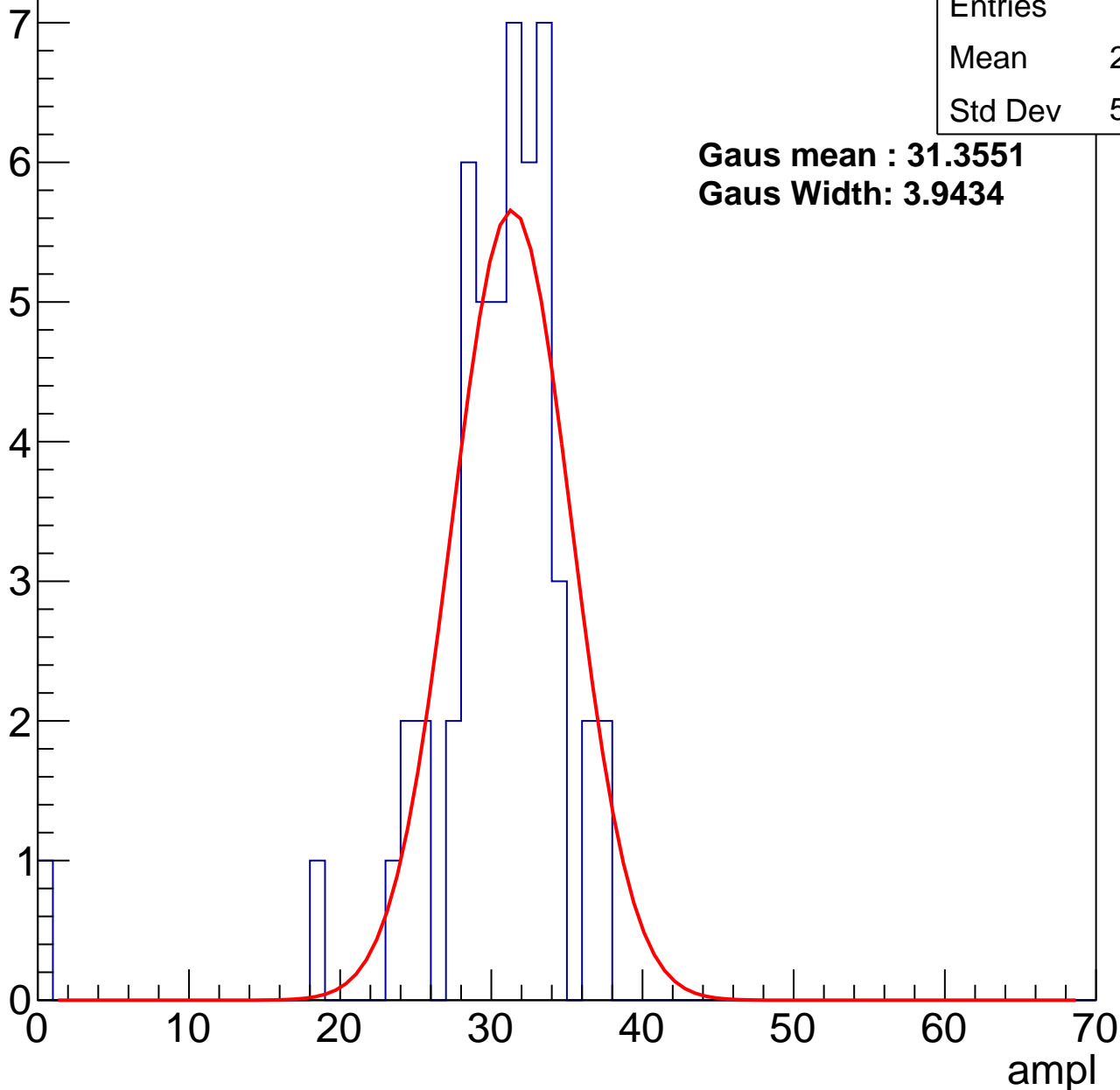
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	29.69
Std Dev	5.493

Gaus mean : 31.3551

Gaus Width: 3.9434



B0L002S, U4-ch42, adc1

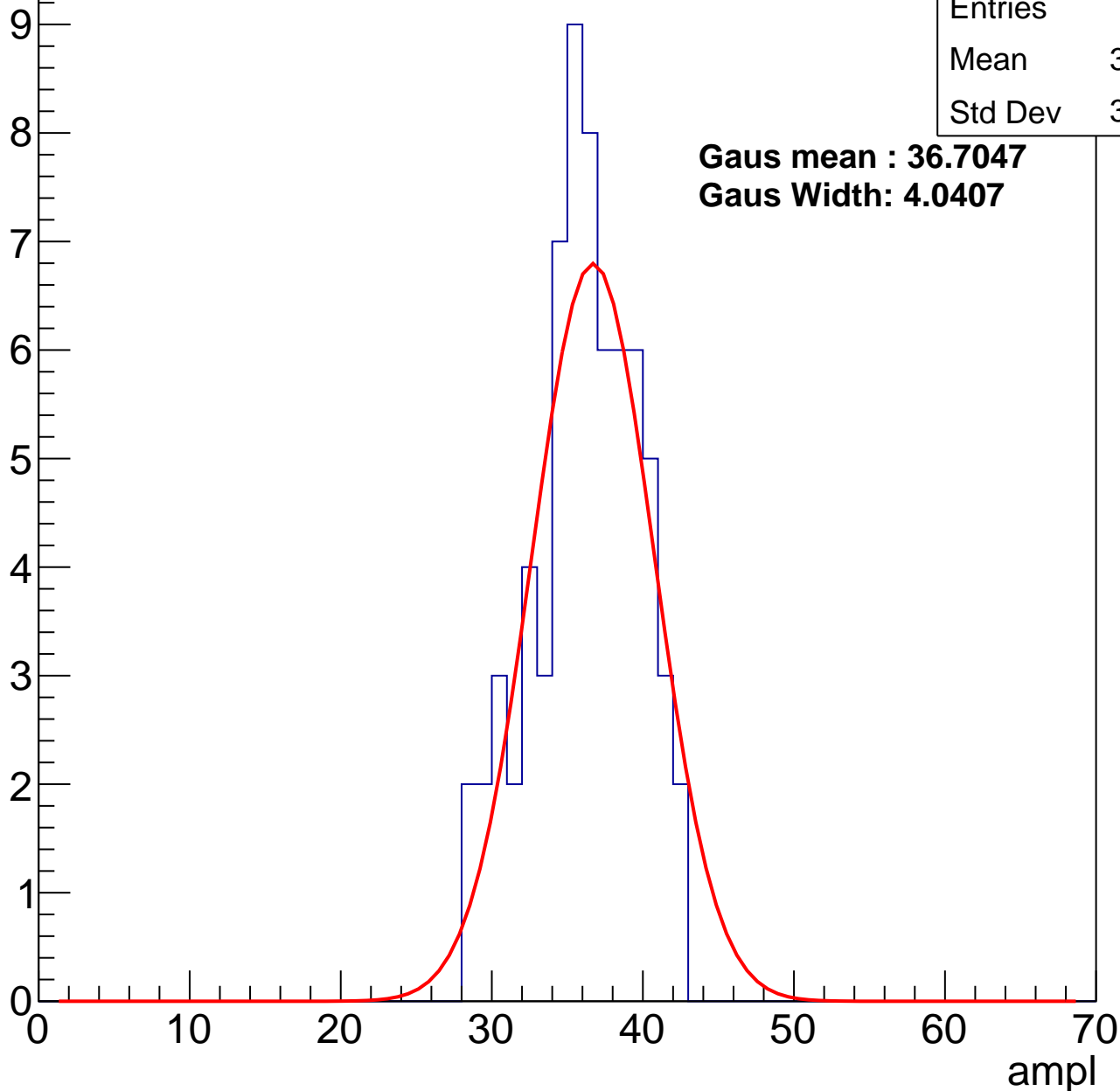
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.66
Std Dev	3.462

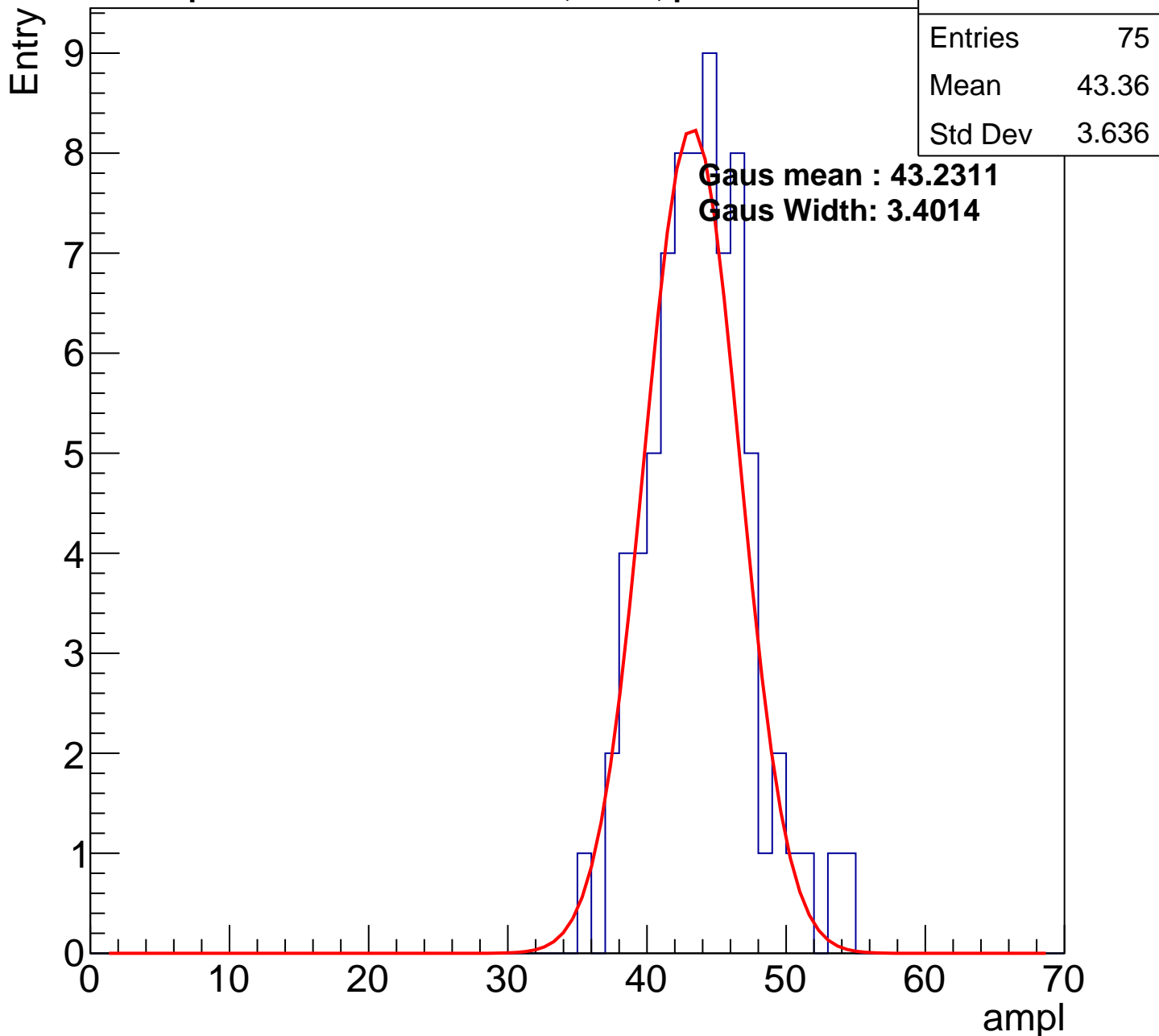
Gaus mean : 36.7047

Gaus Width: 4.0407



B0L002S, U4-ch42, adc2

calib_packv5_042523_0143.root, FC#8, port C1

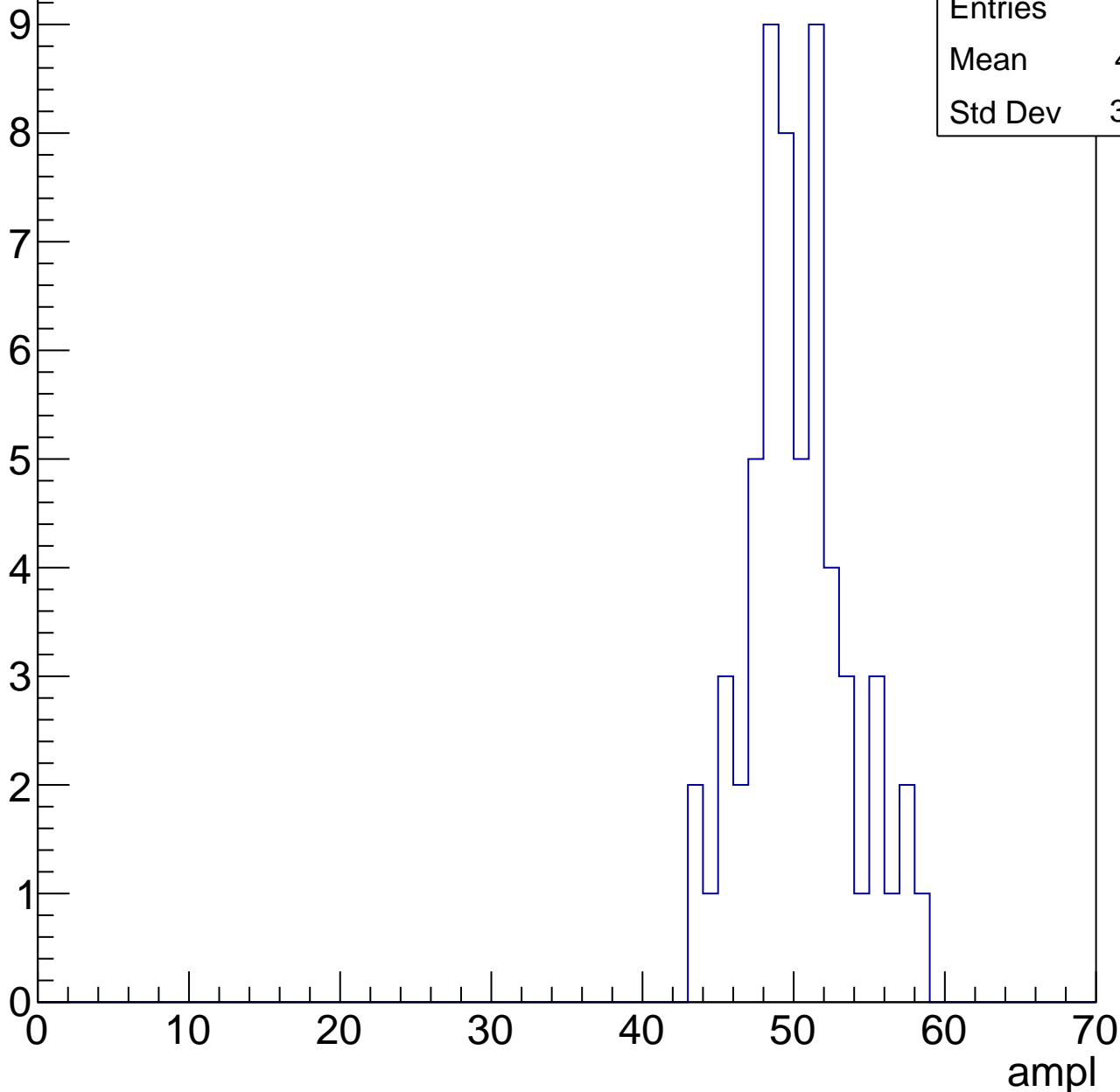


B0L002S, U4-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.81
Std Dev	3.367

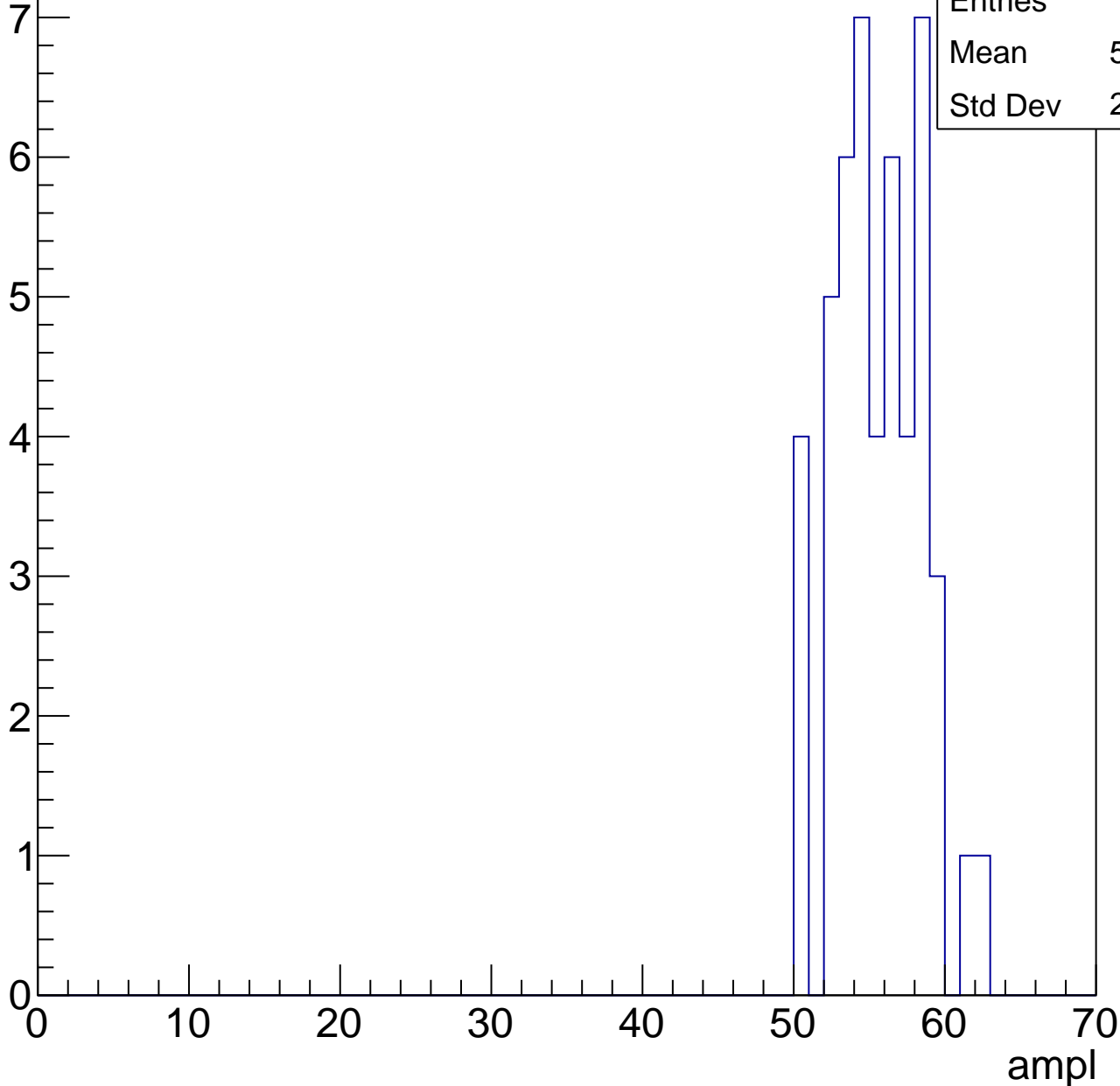


B0L002S, U4-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	55.12
Std Dev	2.862



B0L002S, U4-ch42, adc5

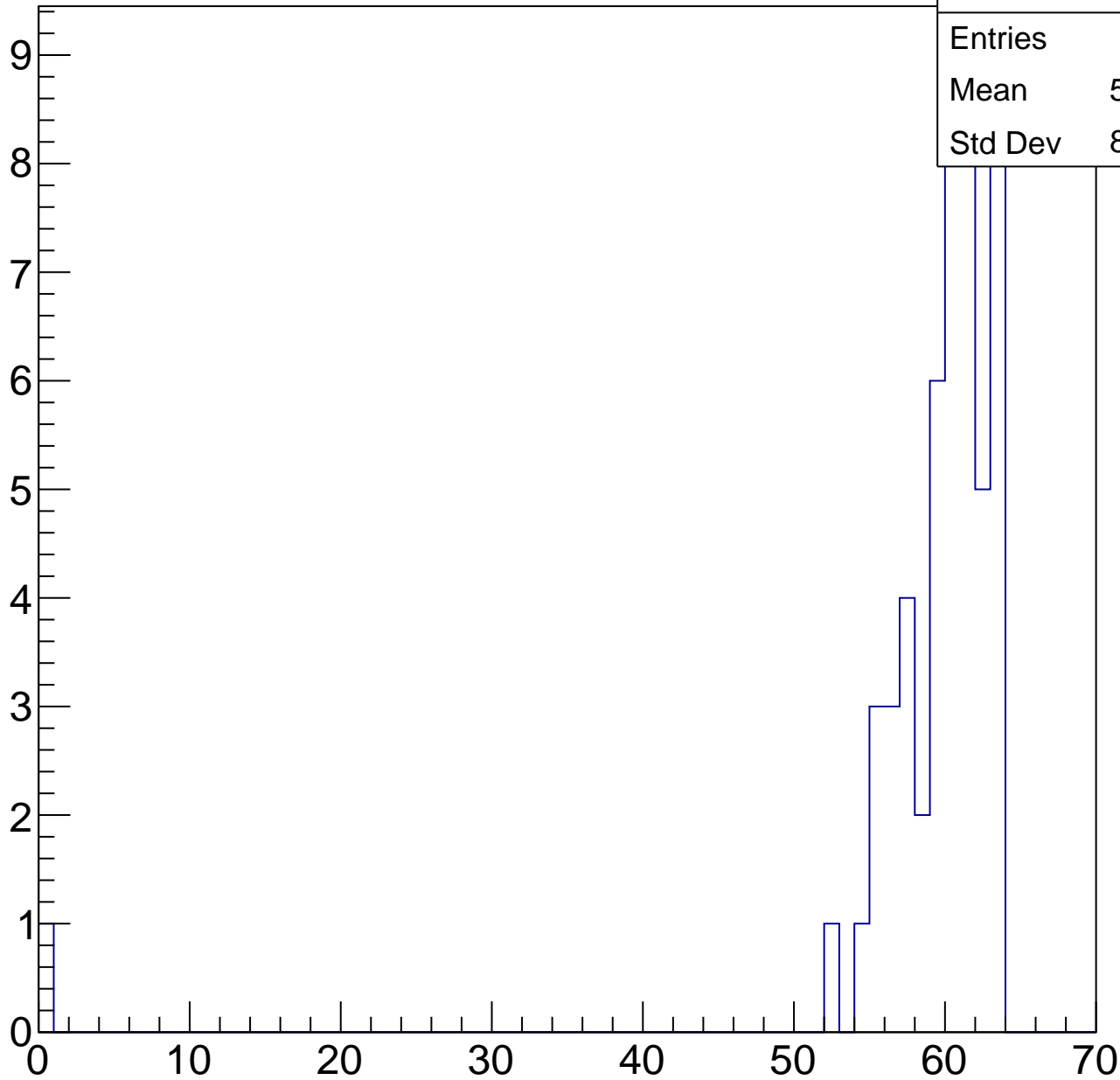
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.46
Std Dev	8.606

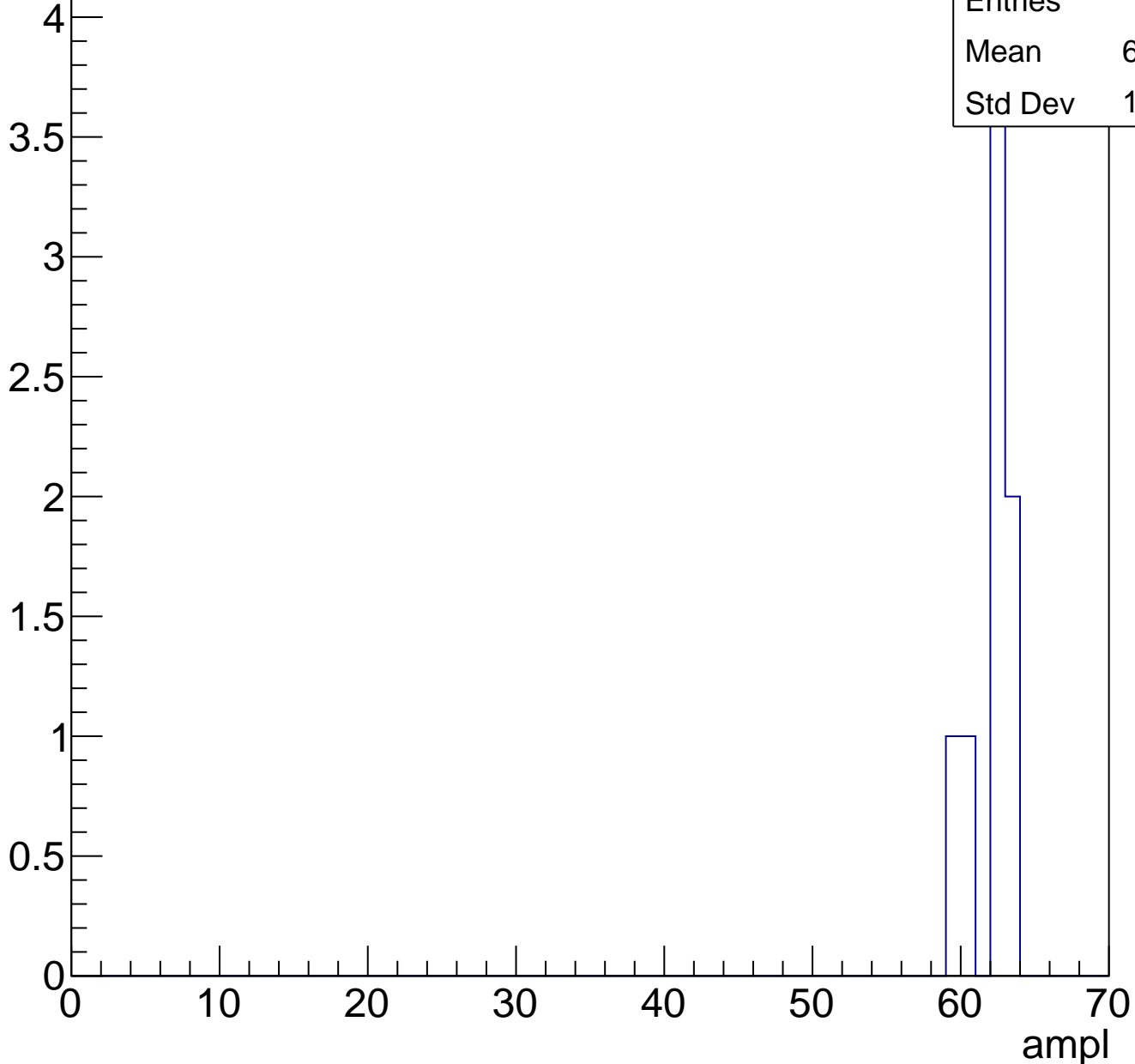
ampl



B0L002S, U4-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch43, adc0

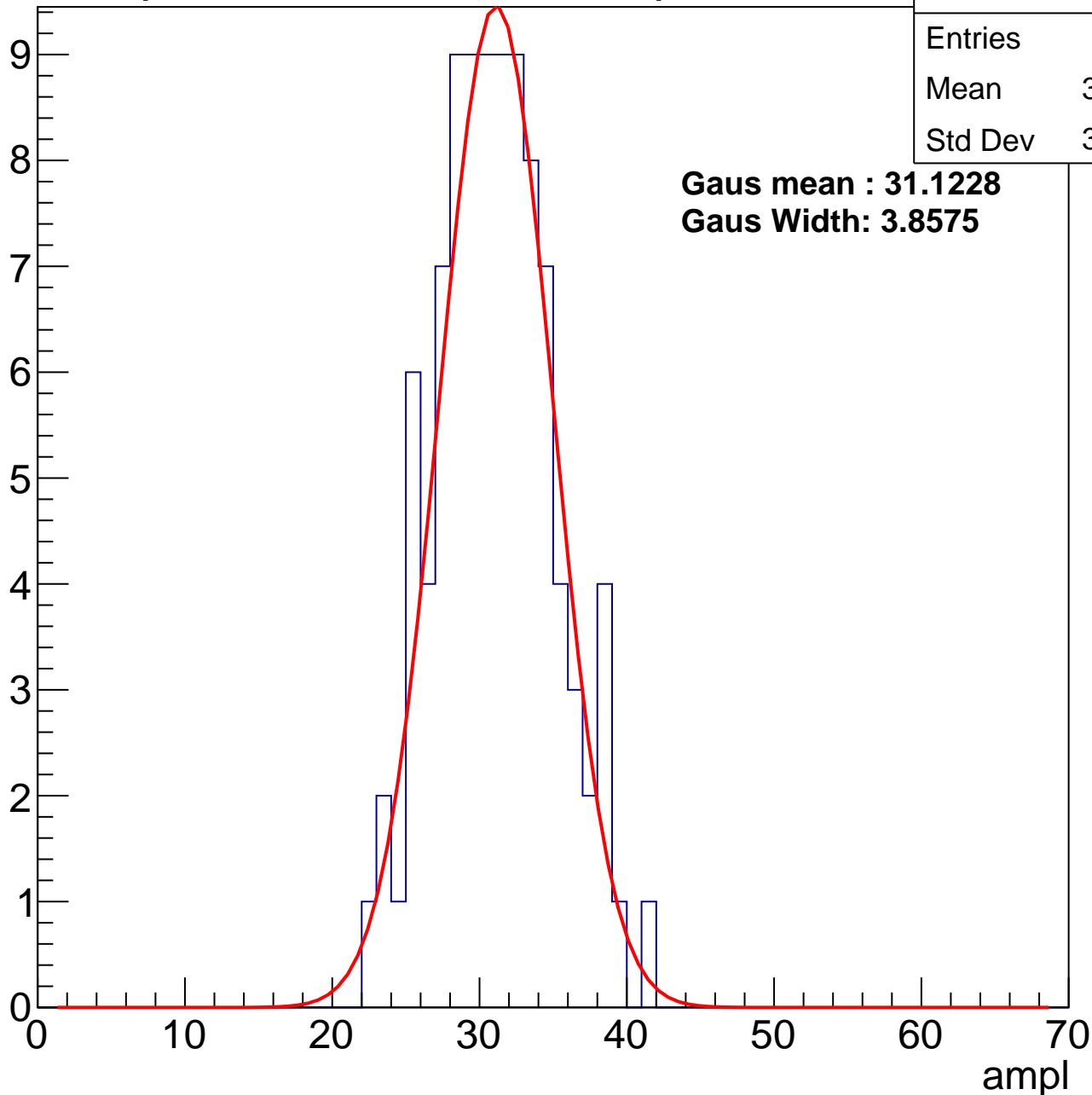
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	96
Mean	30.64
Std Dev	3.919

Gaus mean : 31.1228

Gaus Width: 3.8575



B0L002S, U4-ch43, adc1

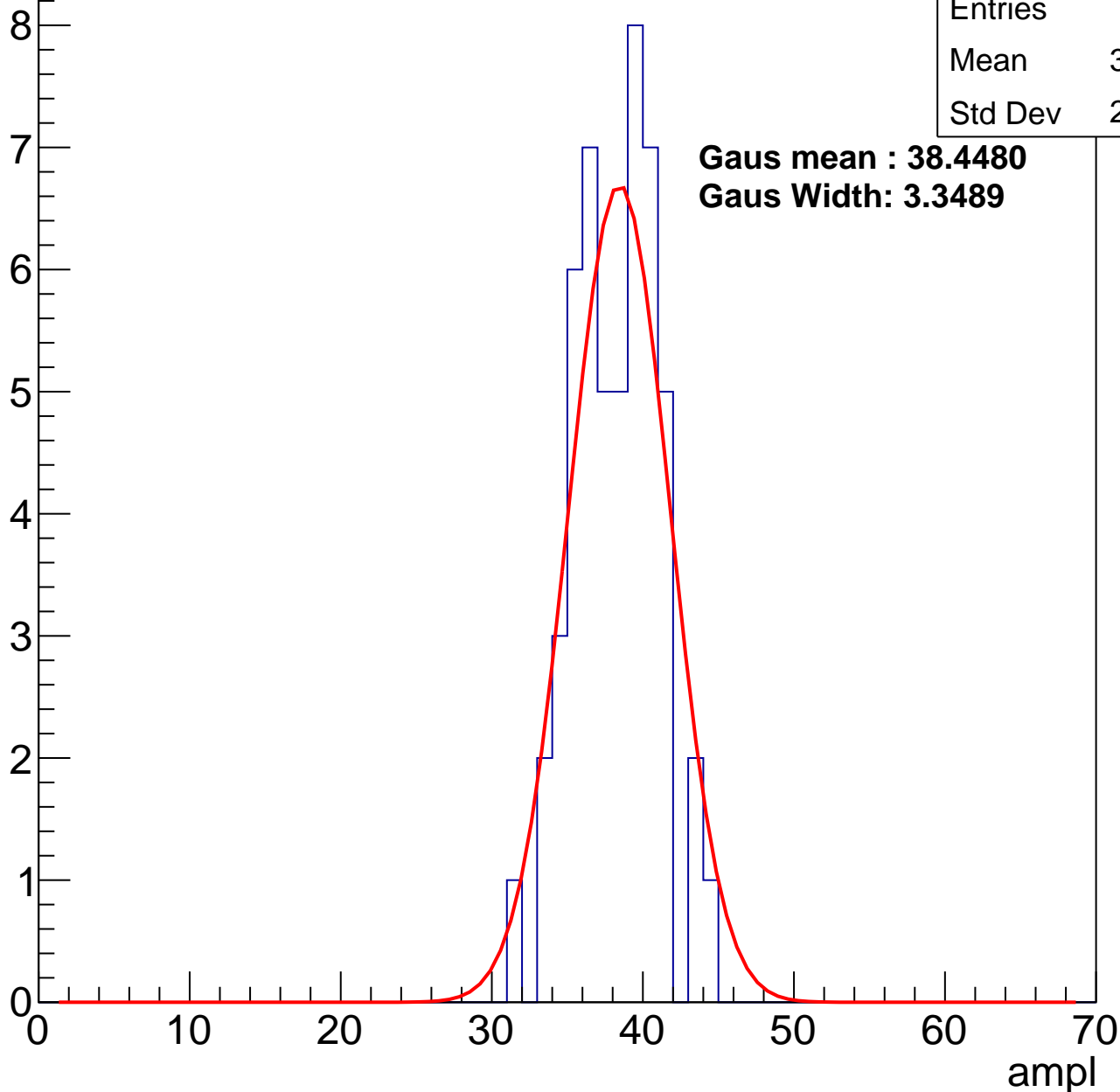
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	37.75
Std Dev	2.766

Gaus mean : 38.4480

Gaus Width: 3.3489



B0L002S, U4-ch43, adc2

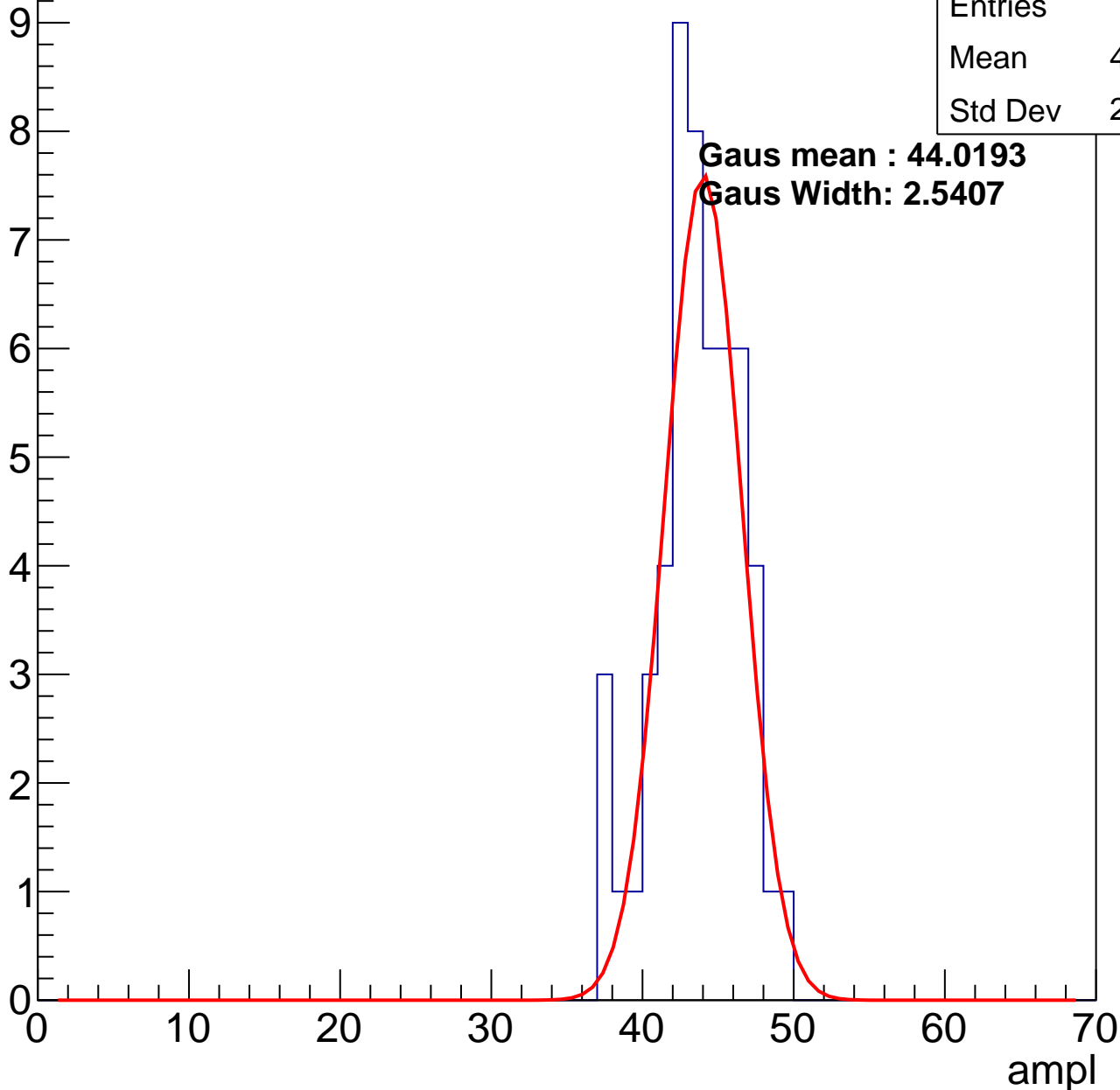
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.19
Std Dev	2.775

Gaus mean : 44.0193

Gaus Width: 2.5407



B0L002S, U4-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

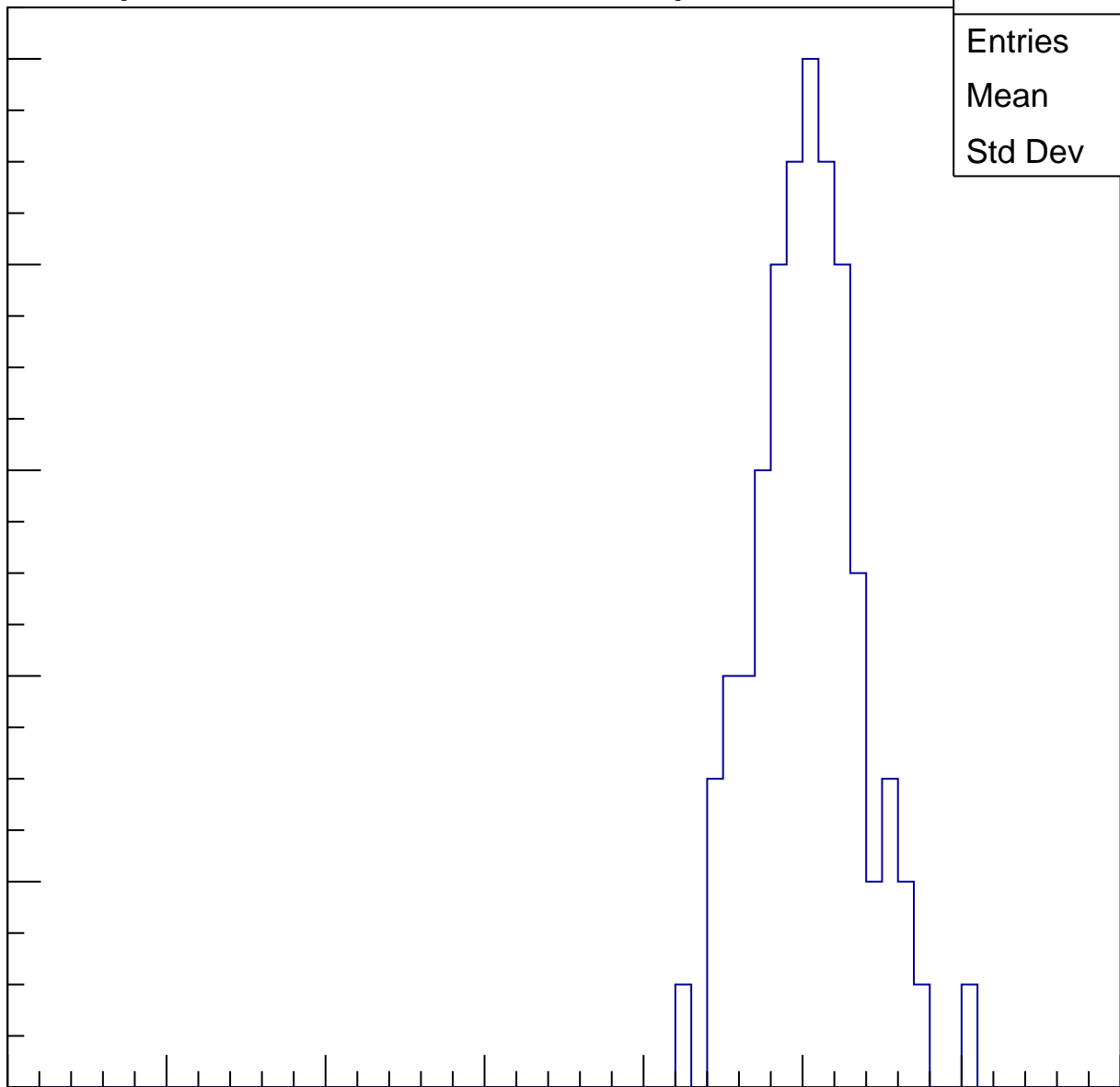
Entries	76
Mean	49.83
Std Dev	3.33

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	57.12
Std Dev	3.097

Entry

10

8

6

4

2

0

0

10

20

30

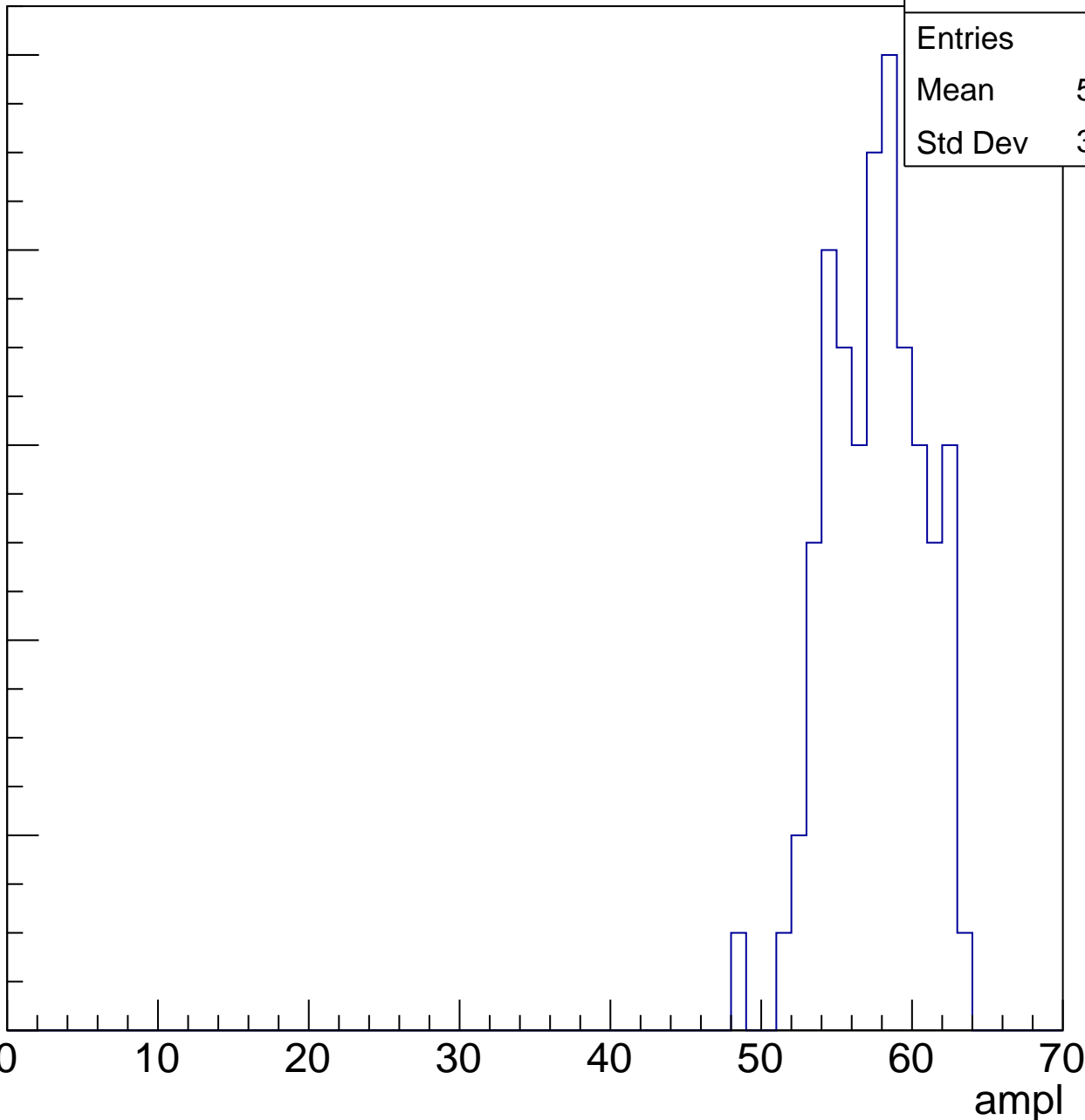
40

50

60

70

ampl

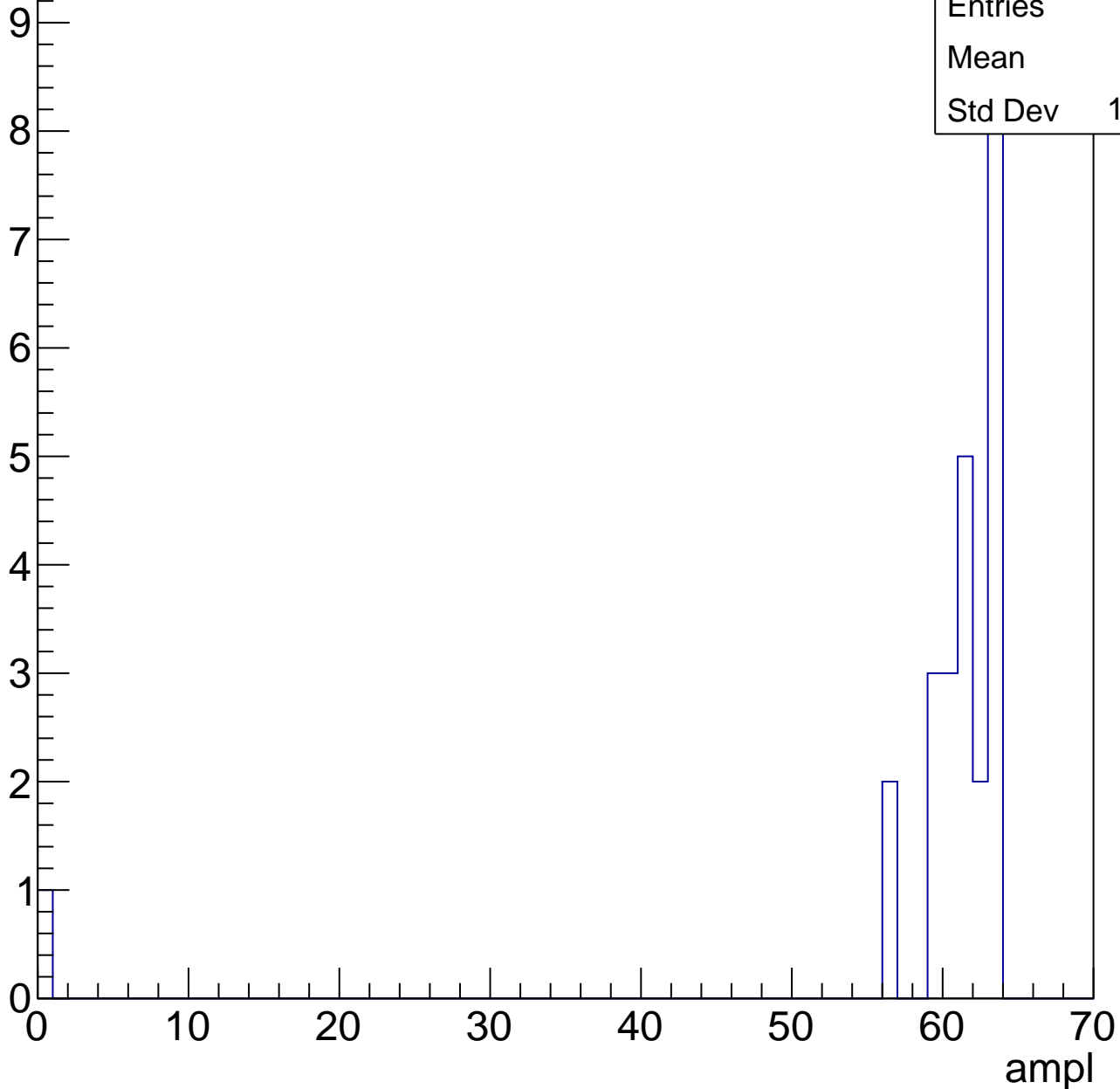


B0L002S, U4-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

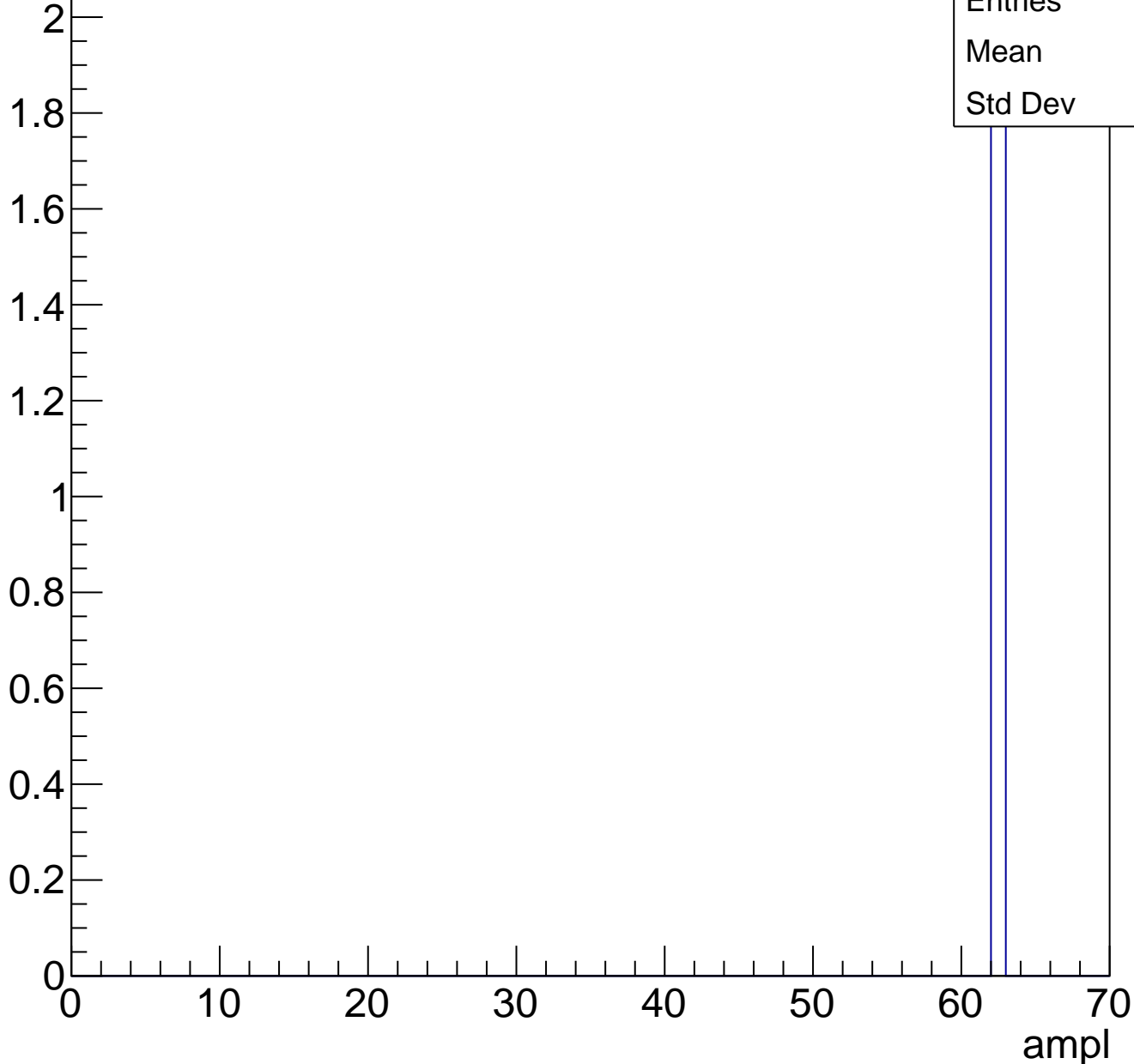
Entries	25
Mean	58.6
Std Dev	12.13



B0L002S, U4-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch44, adc0

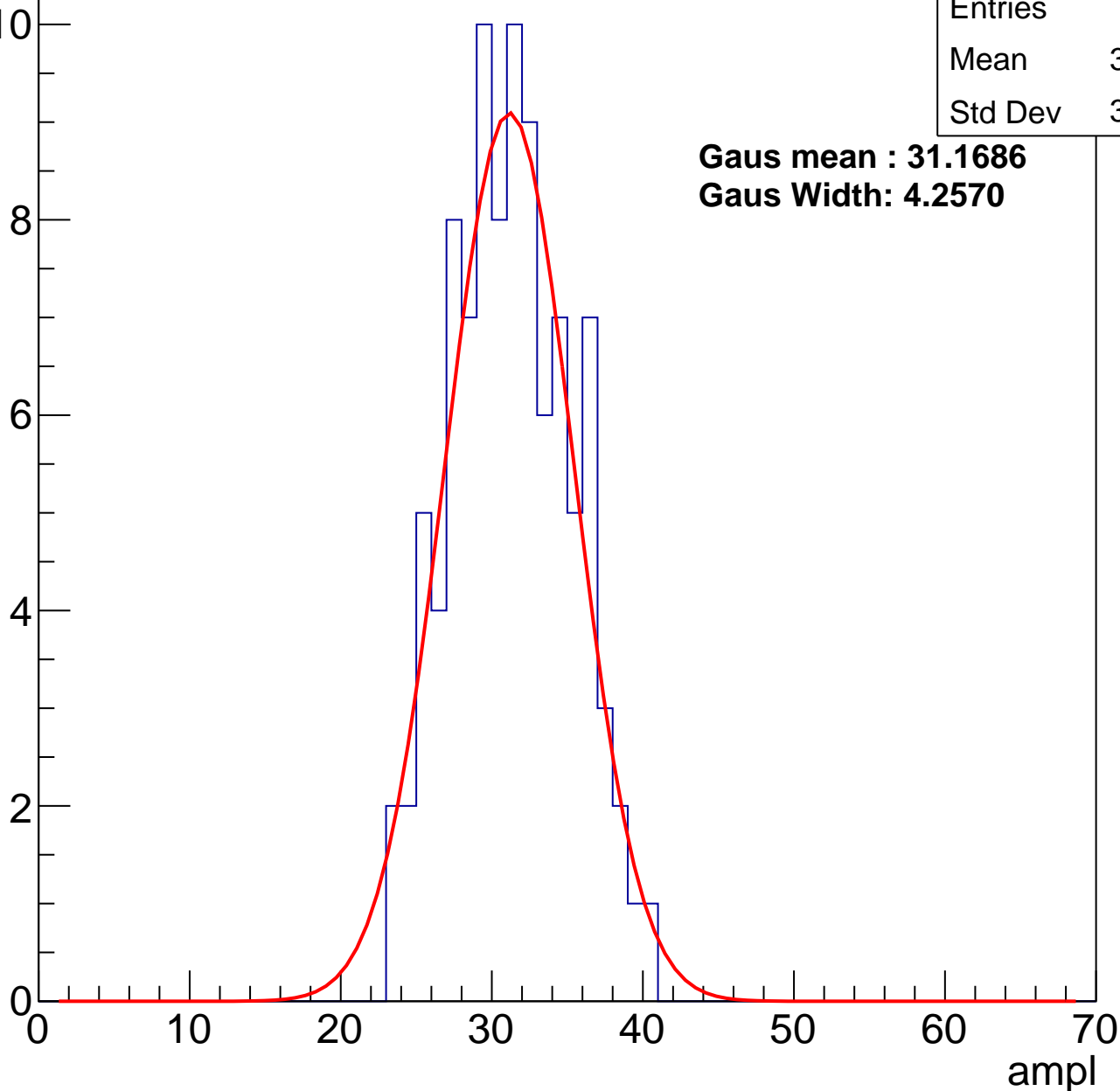
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	97
Mean	30.85
Std Dev	3.859

Gaus mean : 31.1686

Gaus Width: 4.2570



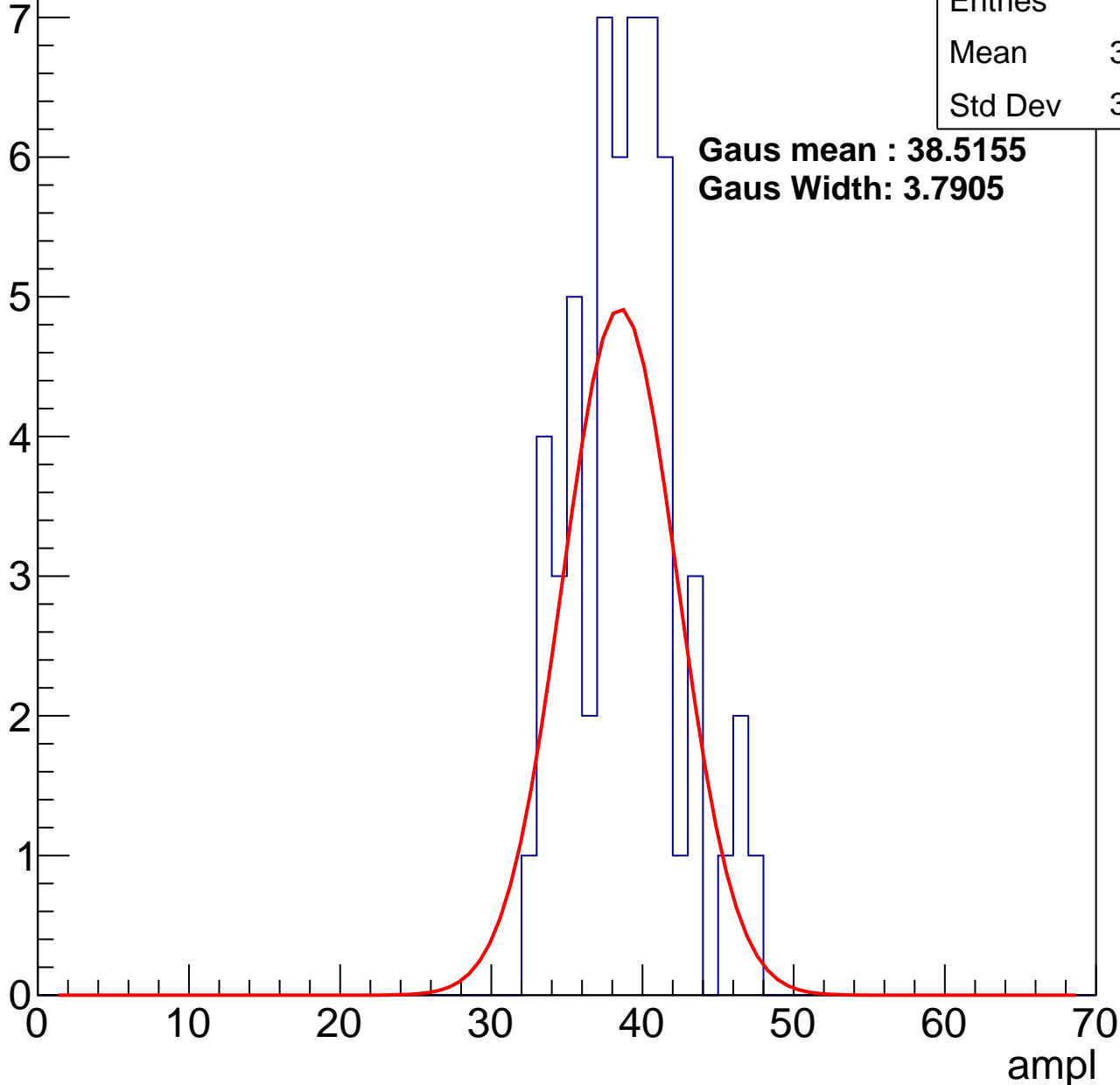
B0L002S, U4-ch44, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	38.46
Std Dev	3.459

Gaus mean : 38.5155
Gaus Width: 3.7905



B0L002S, U4-ch44, adc2

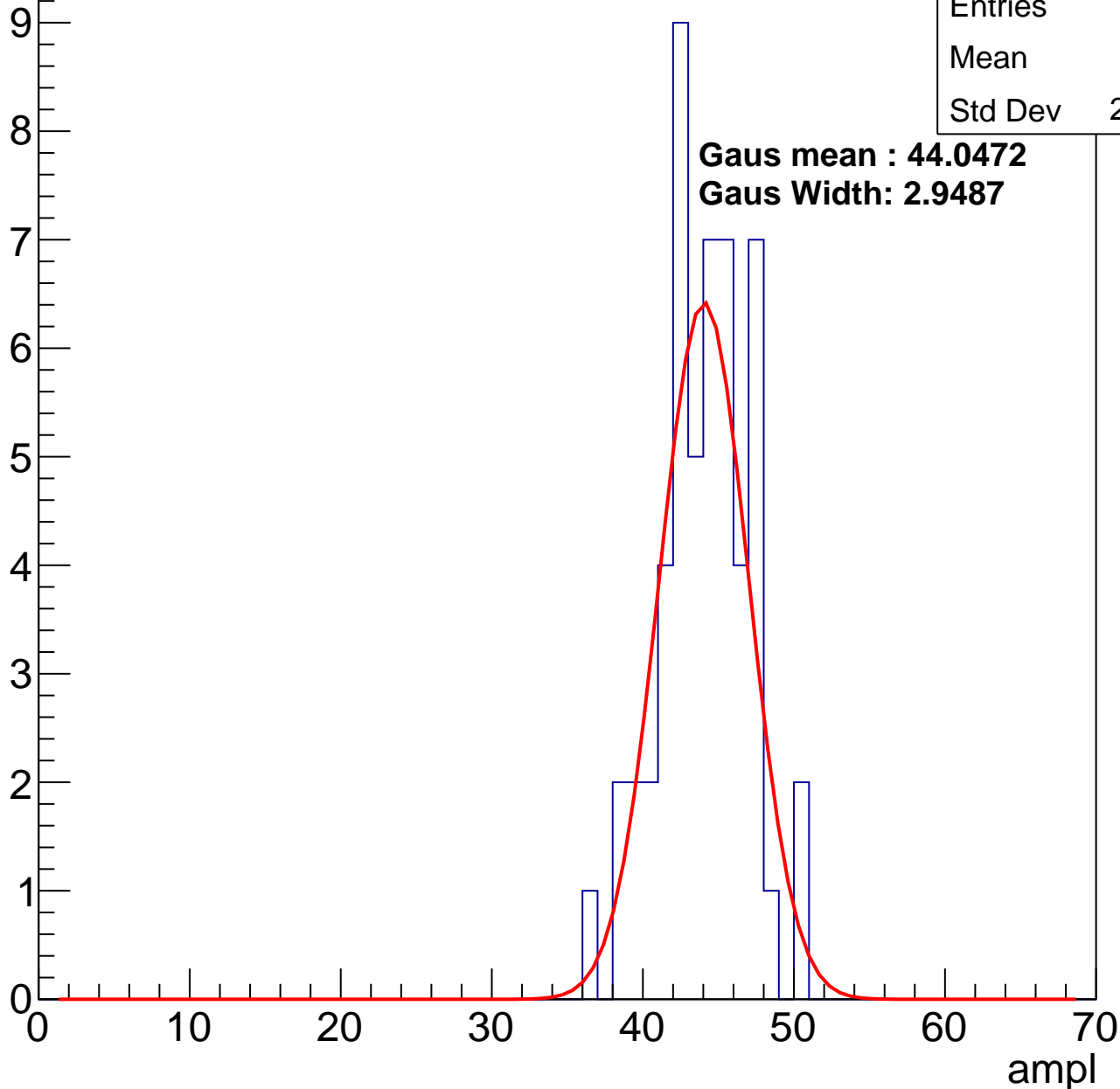
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	43.6
Std Dev	2.948

Gaus mean : 44.0472

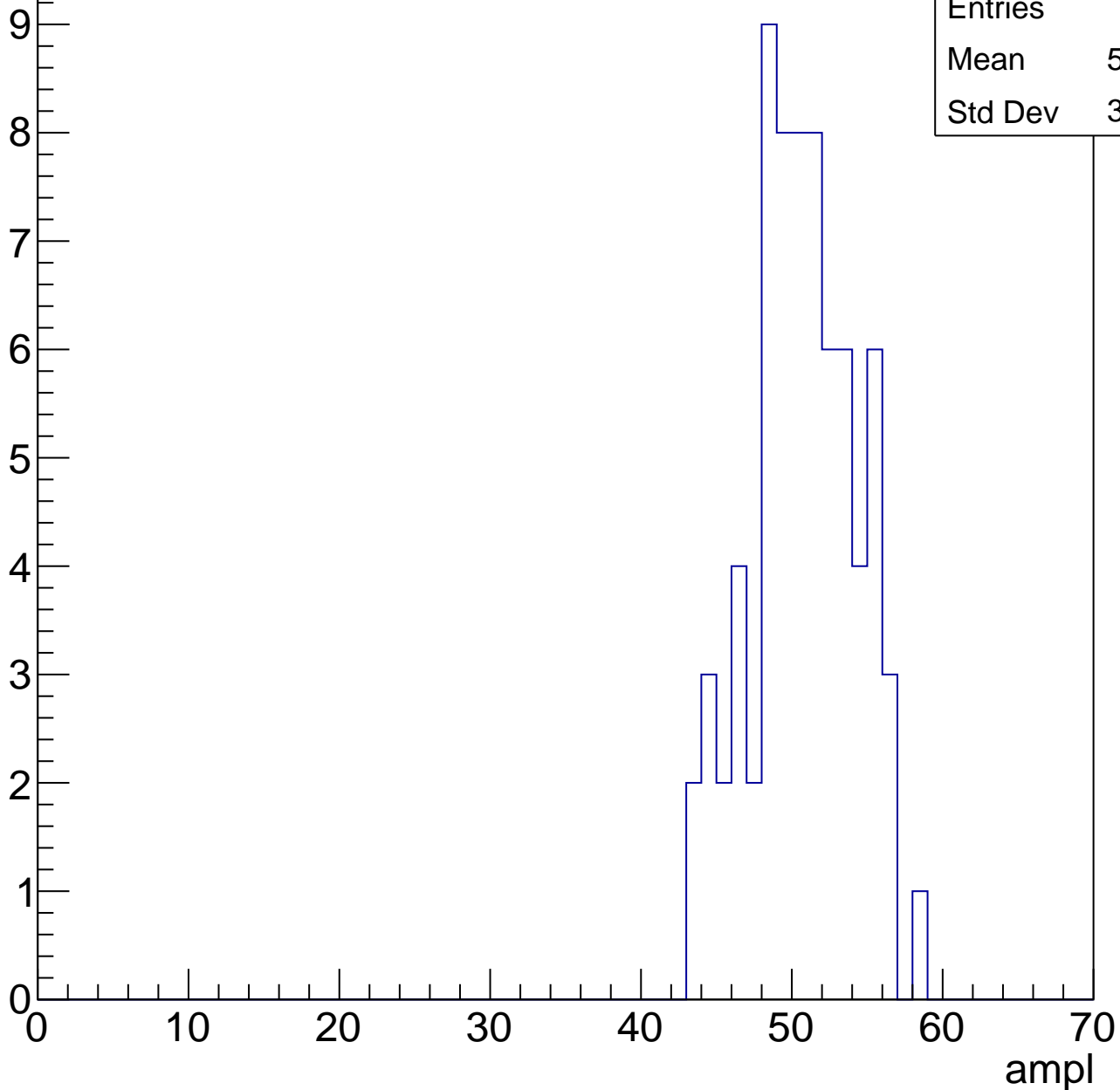
Gaus Width: 2.9487



B0L002S, U4-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

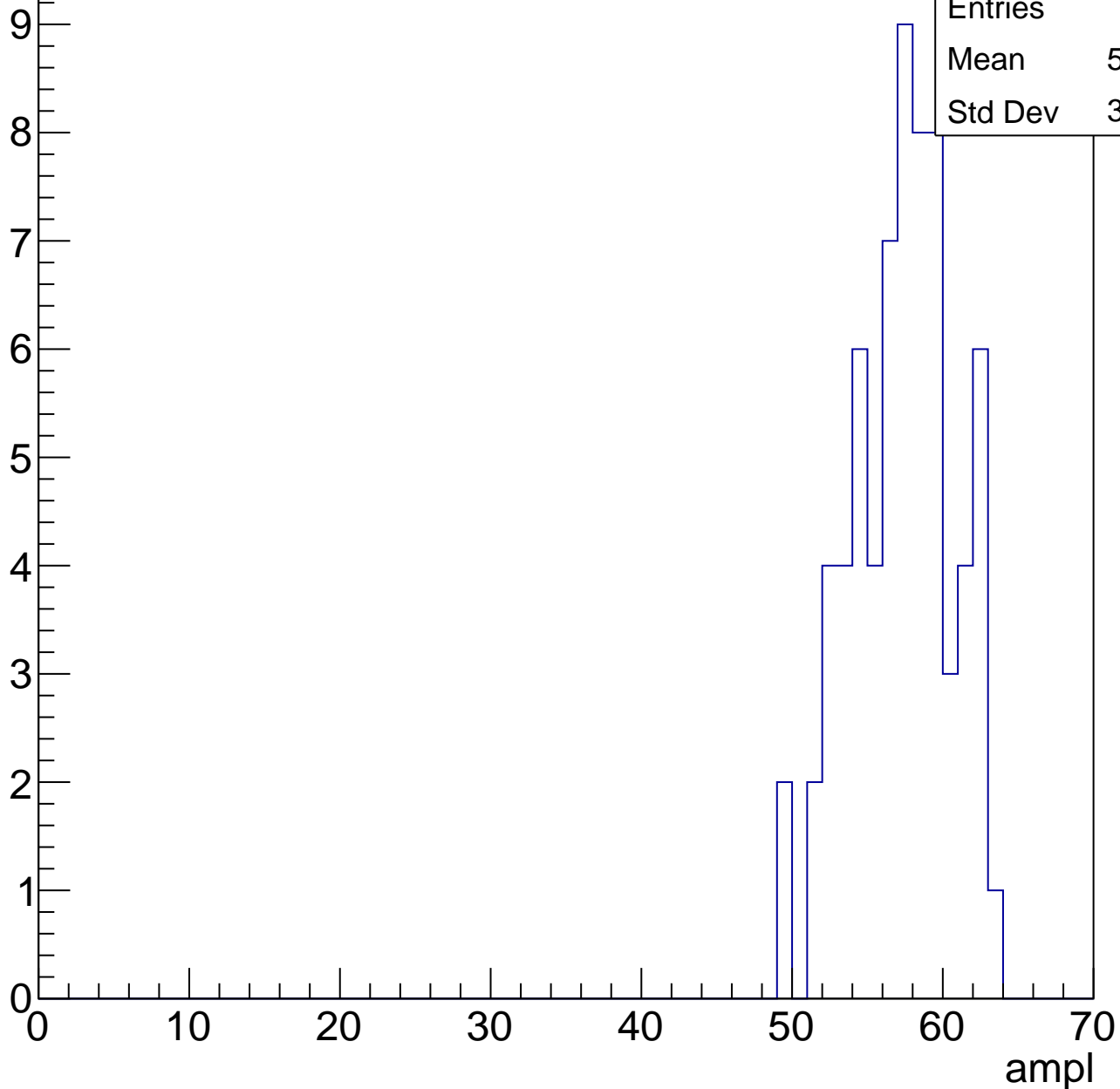
Entry



B0L002S, U4-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	29
Mean	61.07
Std Dev	1.617

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L002S, U4-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U4-ch45, adc0

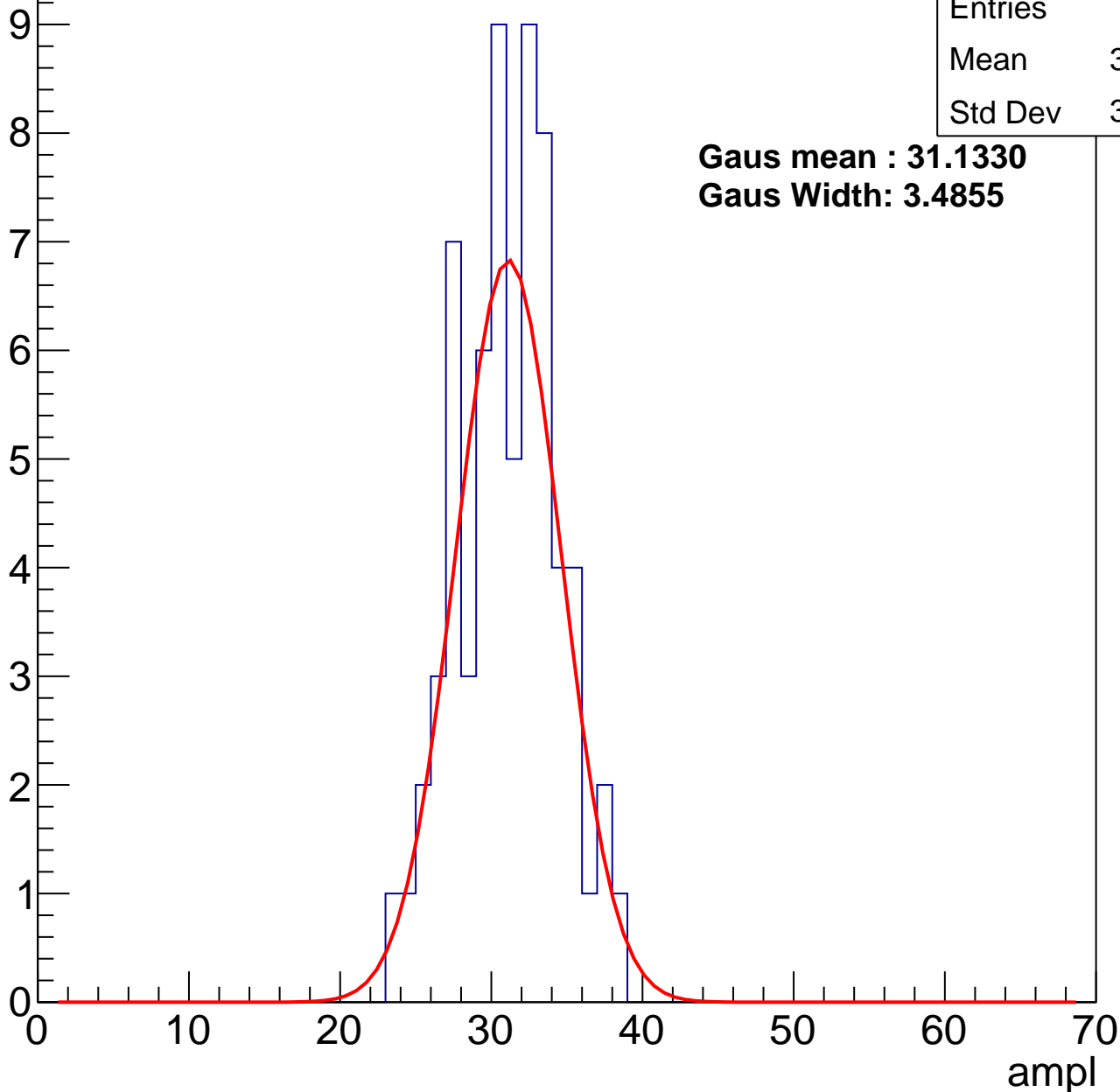
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.65
Std Dev	3.282

Gaus mean : 31.1330

Gaus Width: 3.4855



B0L002S, U4-ch45, adc1

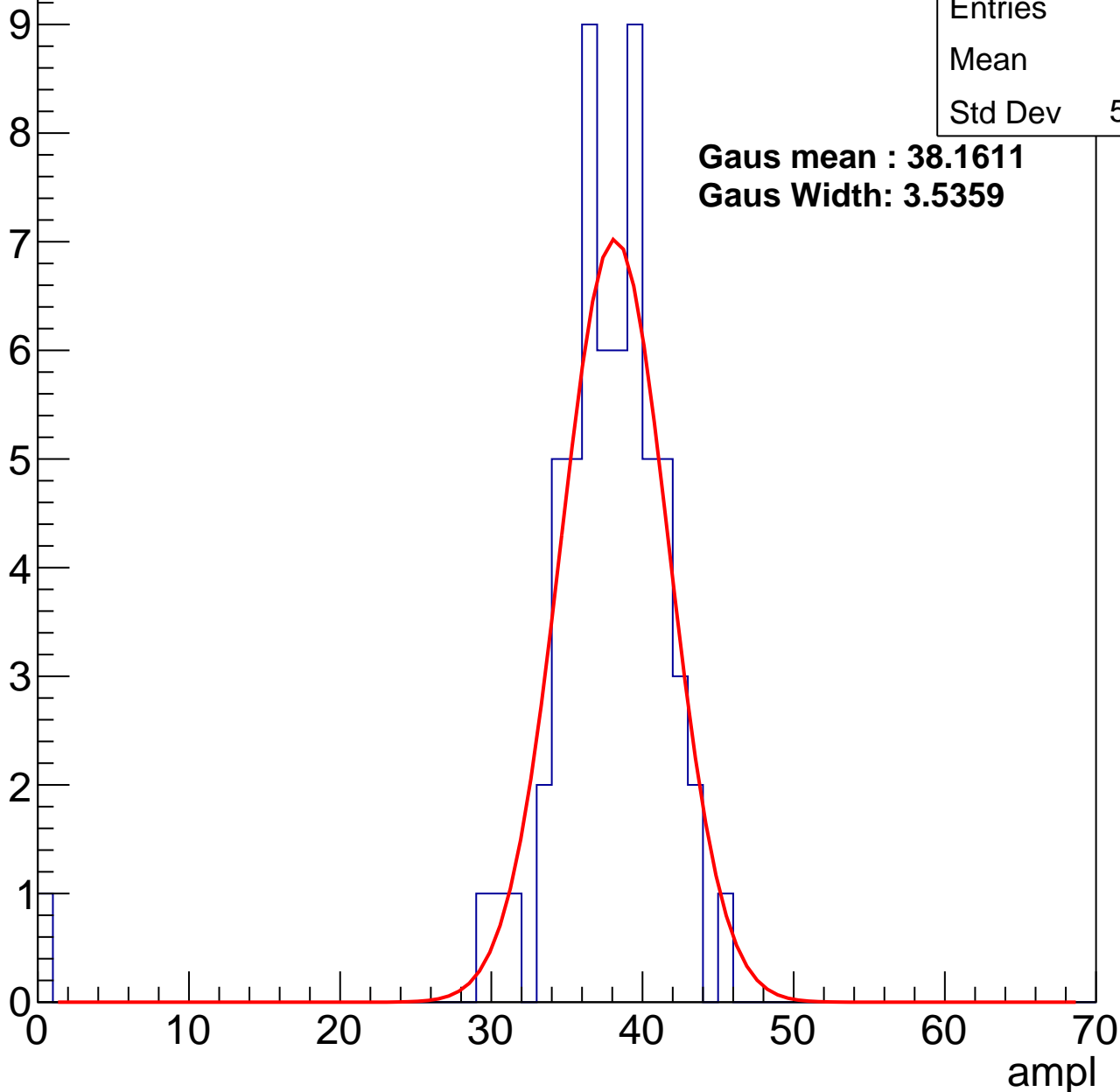
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.9
Std Dev	5.682

Gaus mean : 38.1611

Gaus Width: 3.5359



B0L002S, U4-ch45, adc2

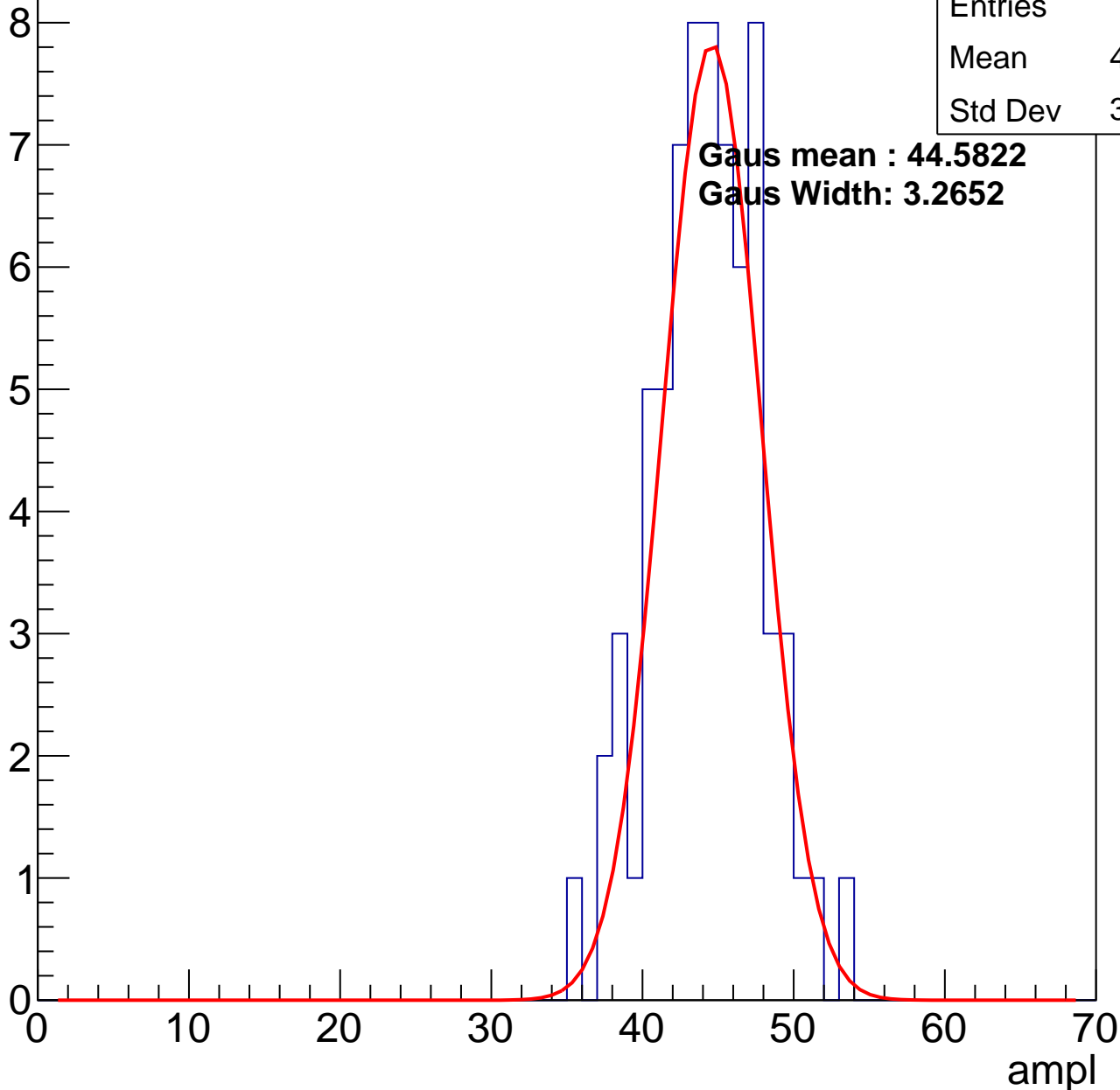
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.84
Std Dev	3.512

Gaus mean : 44.5822

Gaus Width: 3.2652

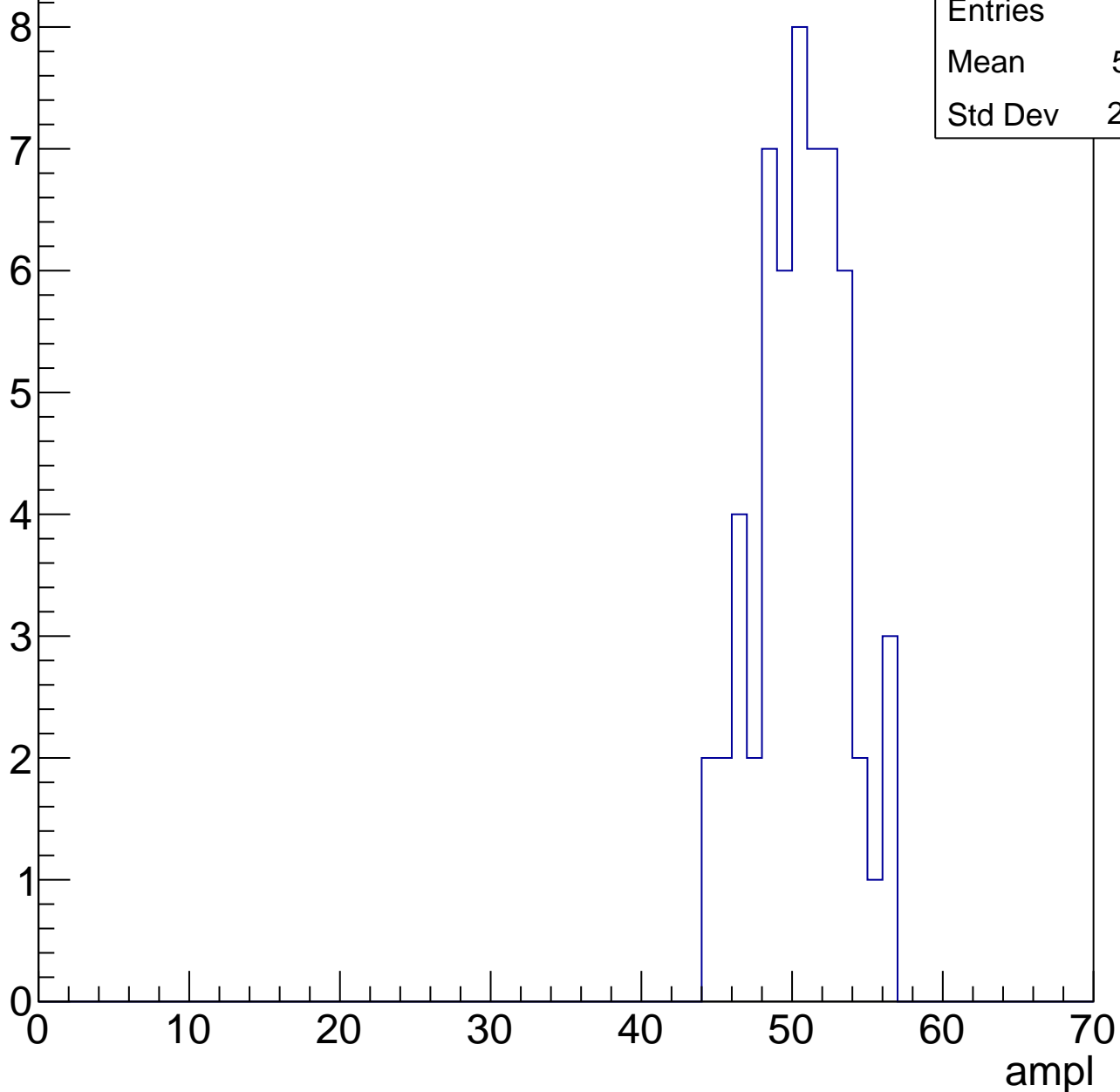


B0L002S, U4-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	50.11
Std Dev	2.936

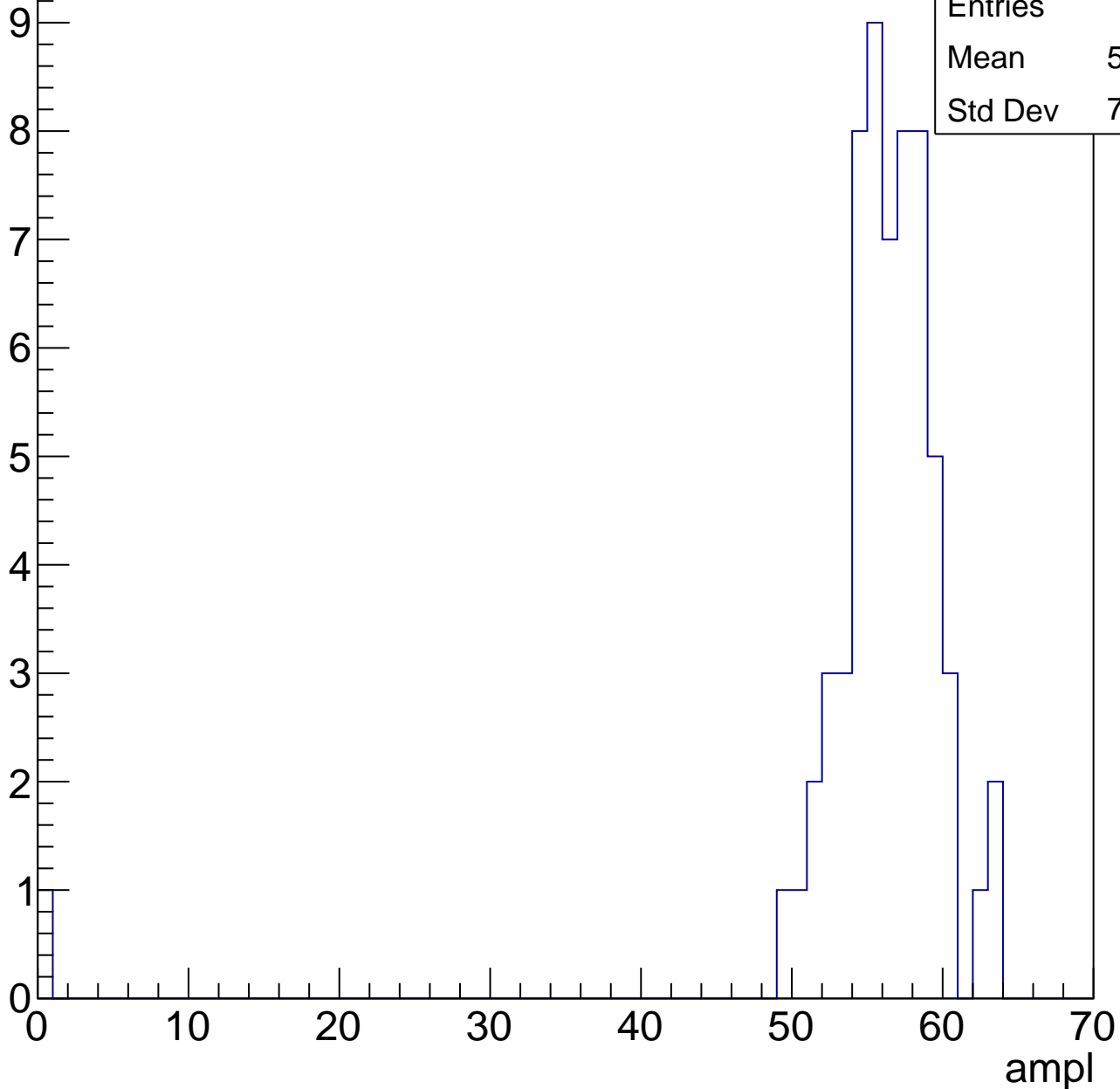


B0L002S, U4-ch45, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.13
Std Dev	7.627

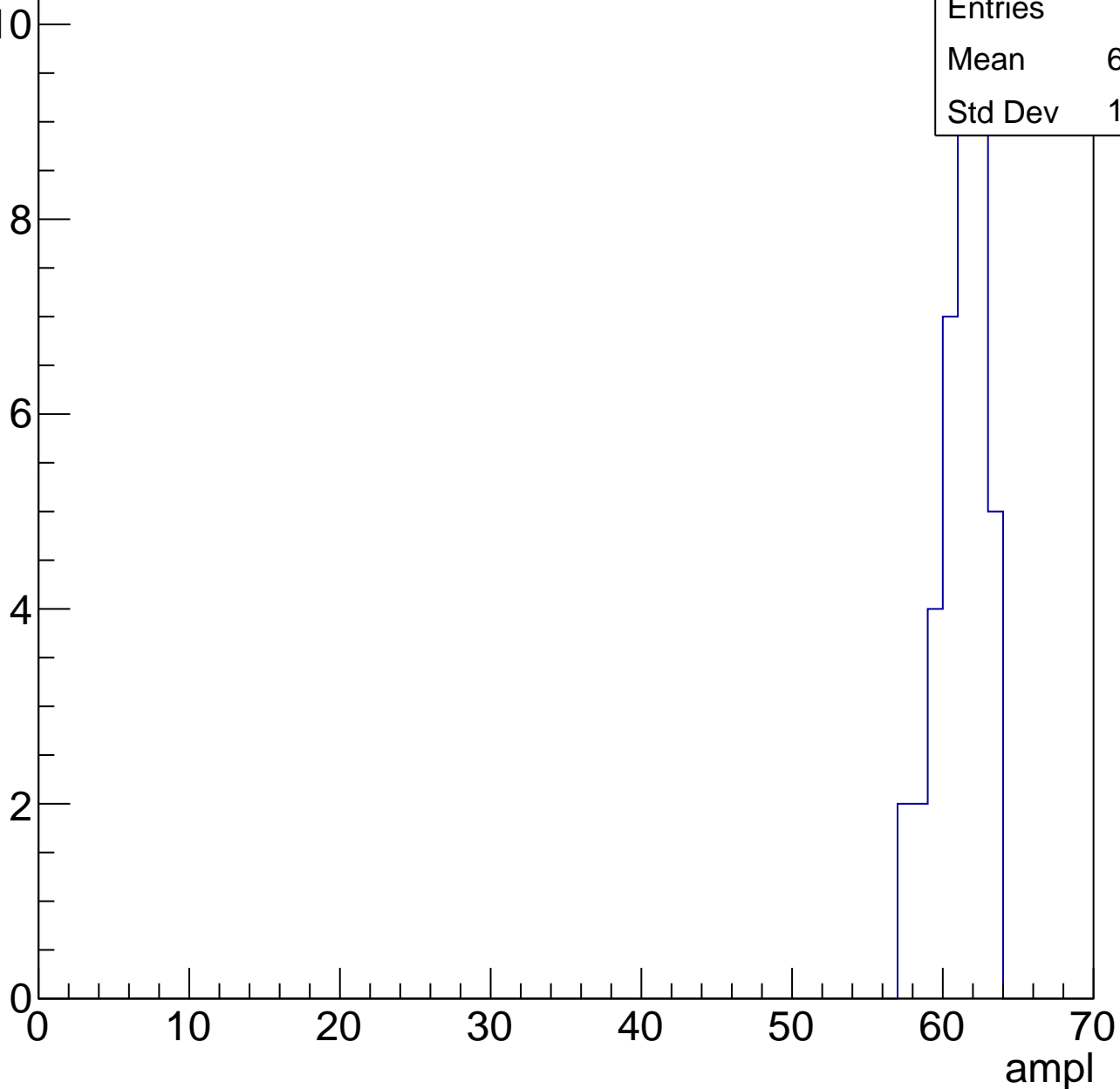


B0L002S, U4-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

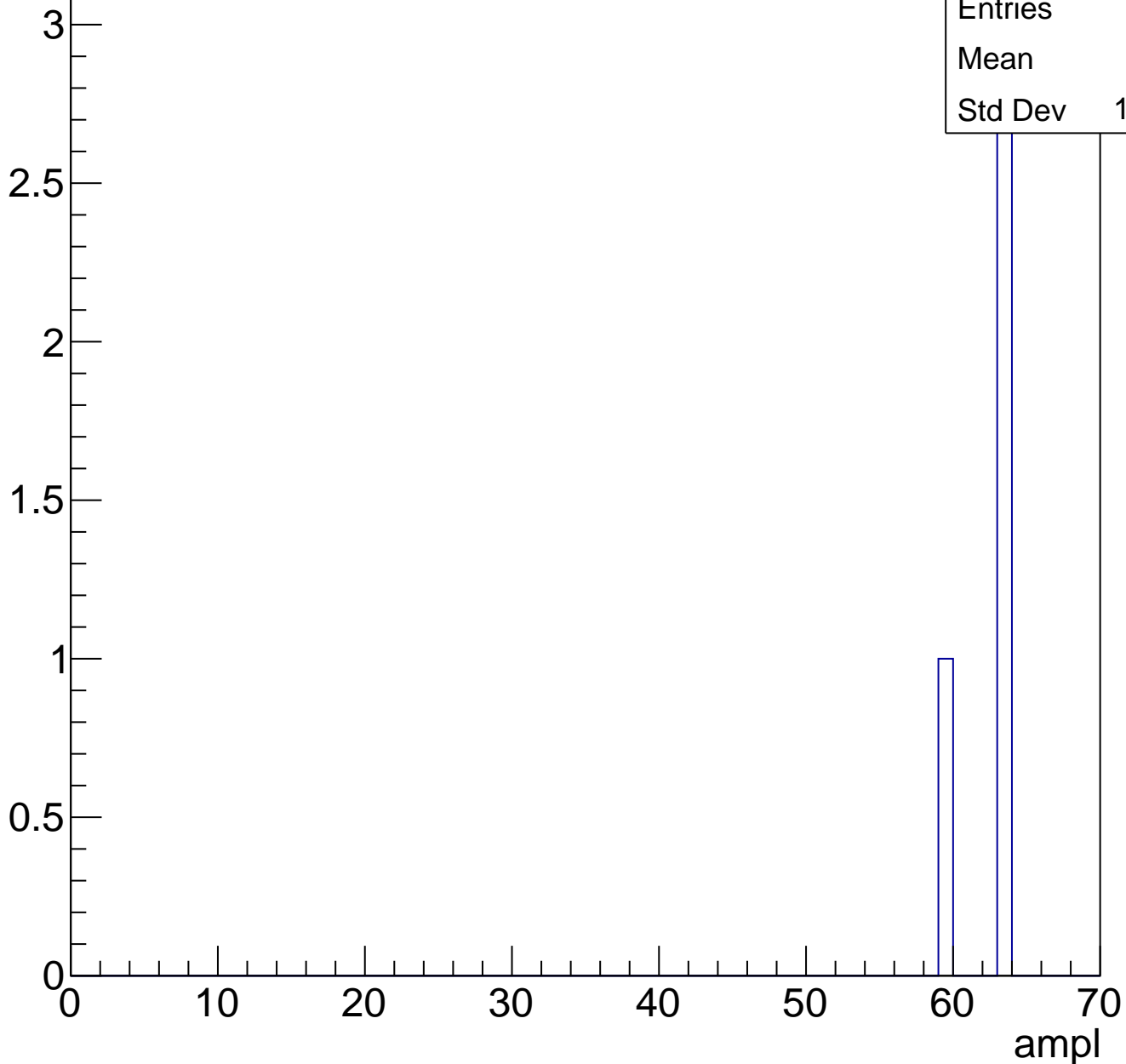
Entries	39
Mean	60.74
Std Dev	1.597



B0L002S, U4-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch46, adc0

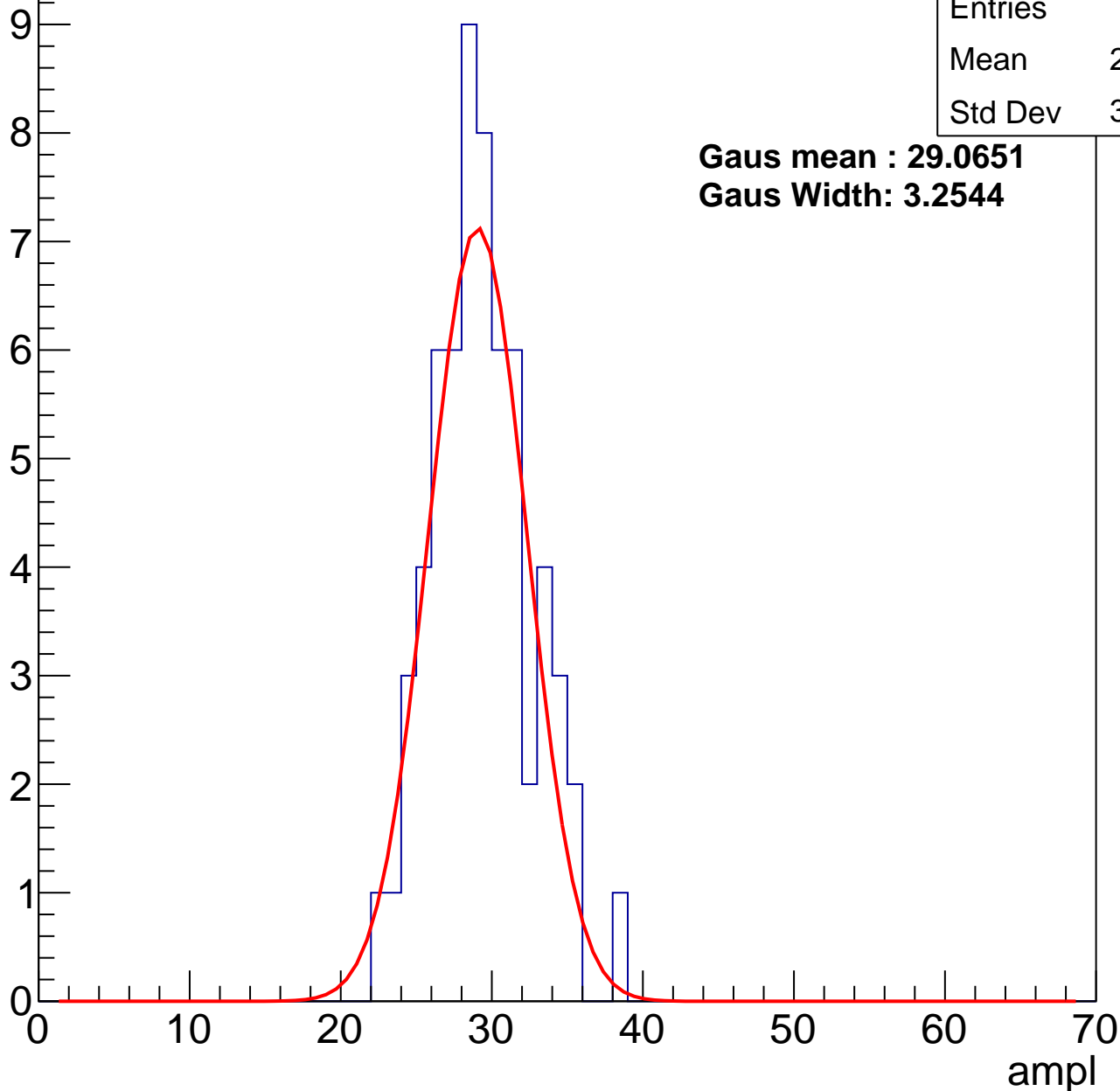
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	28.89
Std Dev	3.238

Gaus mean : 29.0651

Gaus Width: 3.2544



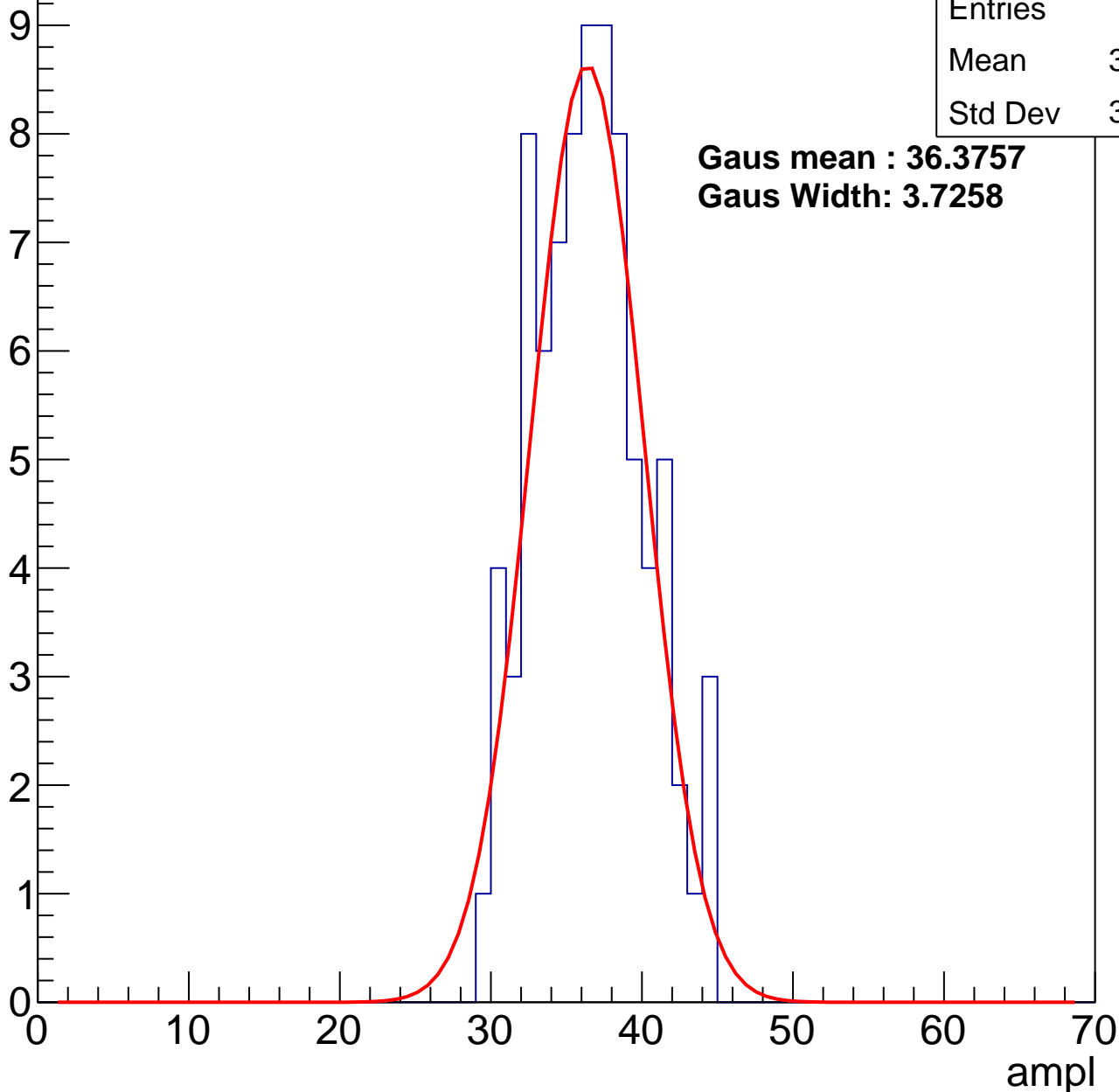
B0L002S, U4-ch46, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	36.07
Std Dev	3.596

Gaus mean : 36.3757
Gaus Width: 3.7258



B0L002S, U4-ch46, adc2

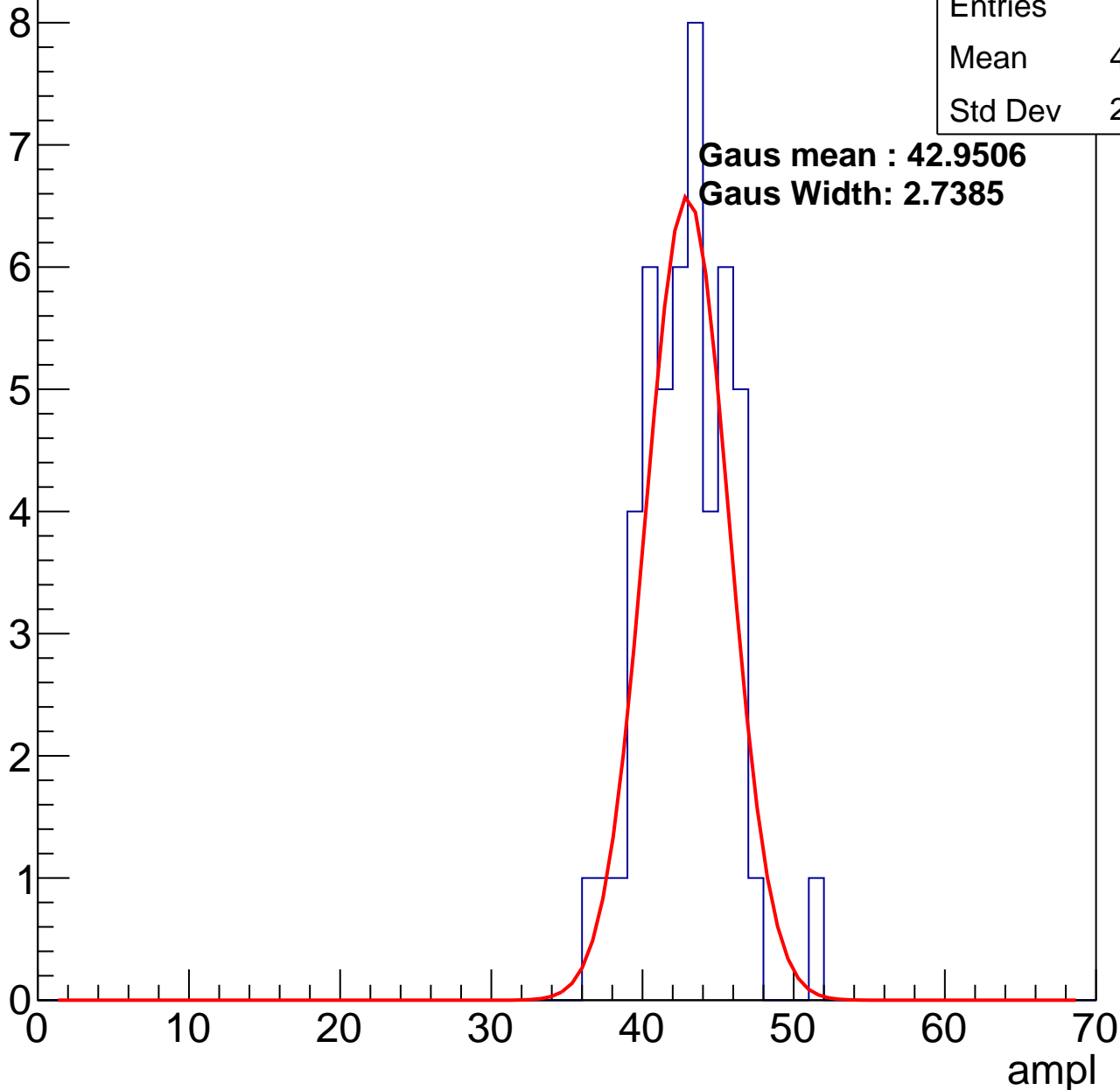
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	42.49
Std Dev	2.837

Gaus mean : 42.9506

Gaus Width: 2.7385

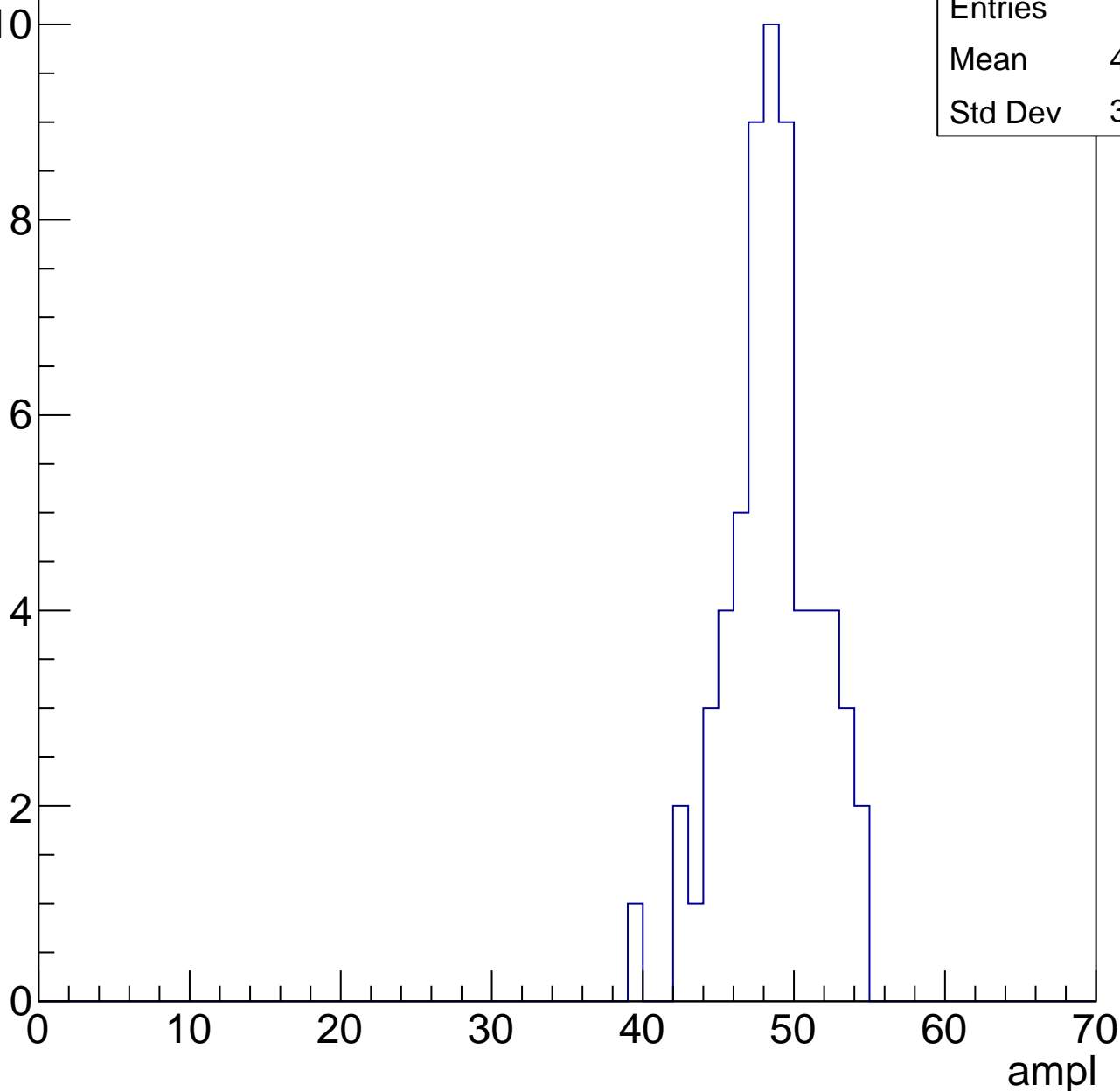


B0L002S, U4-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	48.05
Std Dev	3.038



B0L002S, U4-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 63

Mean 54.75

Std Dev 3.076

0

10

20

30

40

50

60

ampl

0

10

20

30

40

50

60

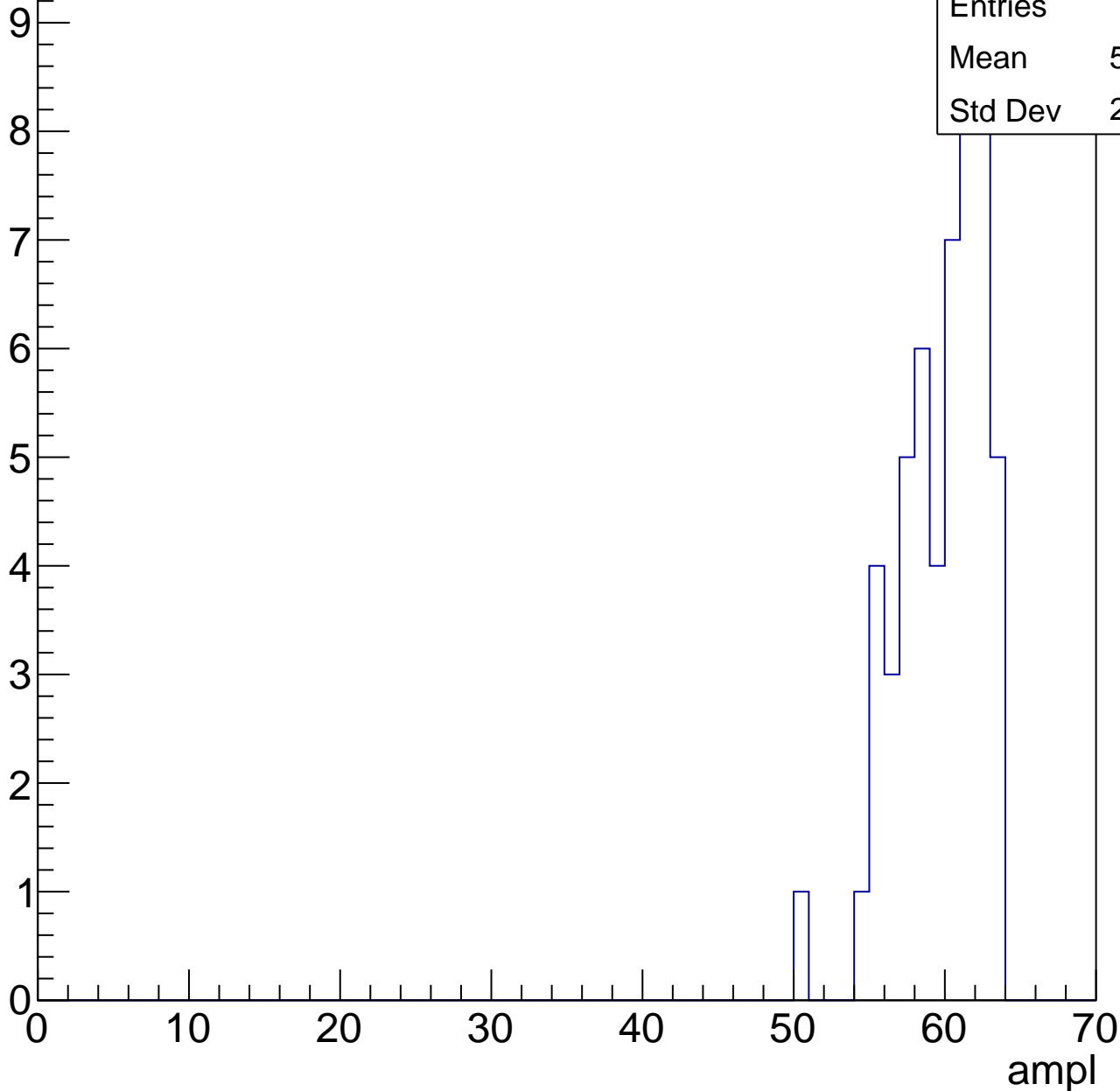
70

B0L002S, U4-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	59.26
Std Dev	2.796

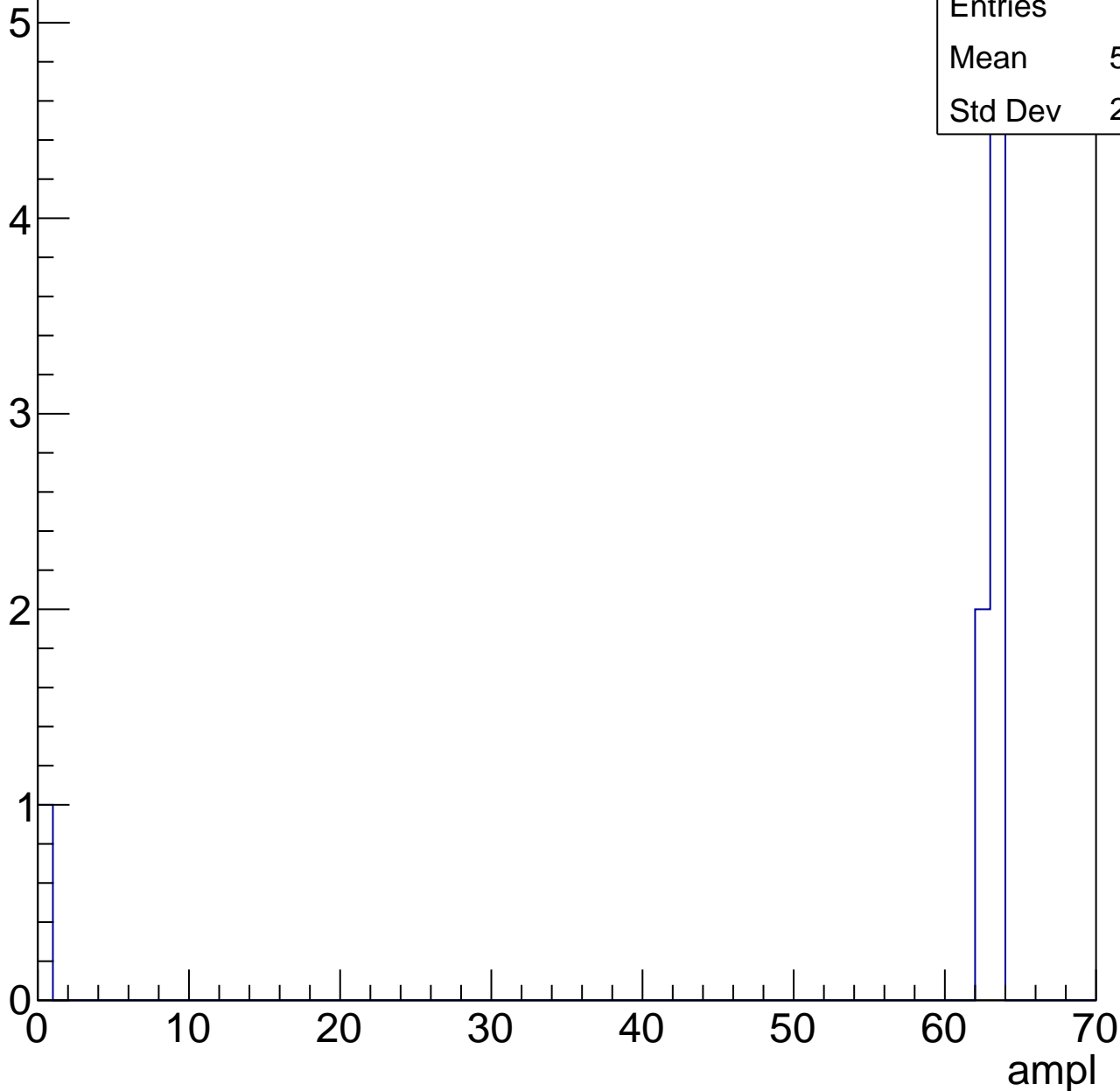


B0L002S, U4-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	54.88
Std Dev	20.75



B0L002S, U4-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch47, adc0

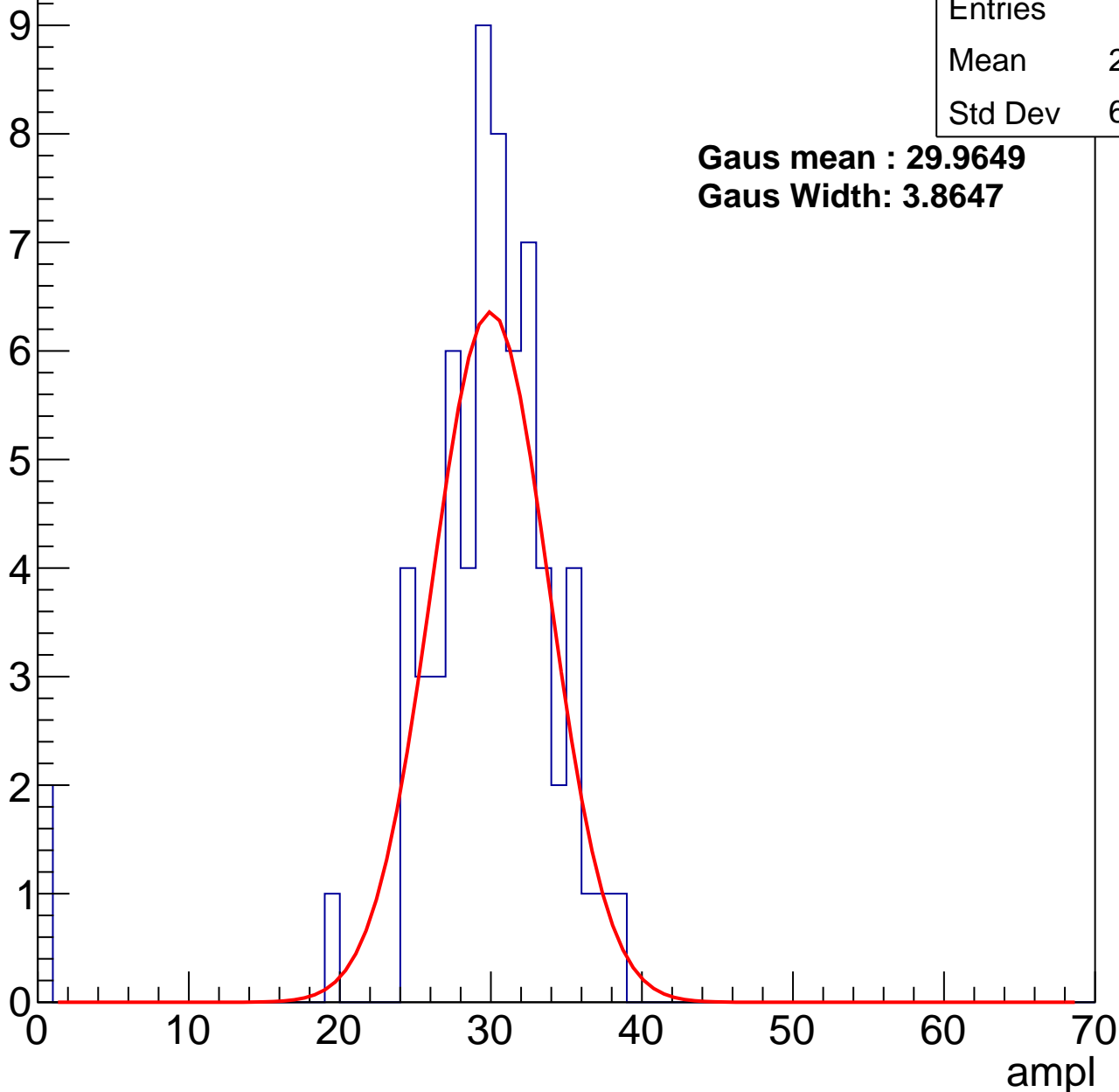
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.85
Std Dev	6.192

Gaus mean : 29.9649

Gaus Width: 3.8647



B0L002S, U4-ch47, adc1

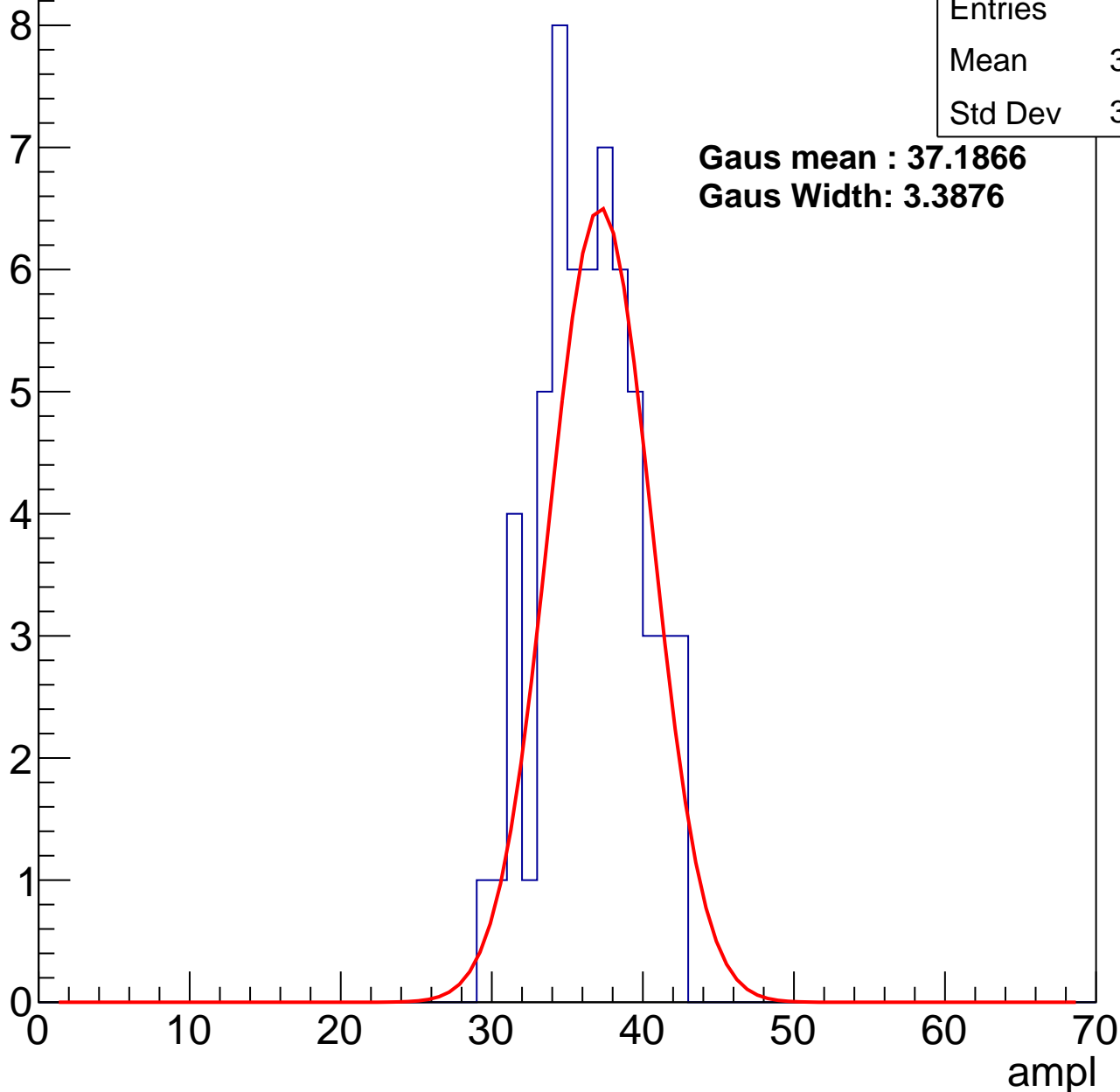
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.08
Std Dev	3.164

Gaus mean : 37.1866

Gaus Width: 3.3876



B0L002S, U4-ch47, adc2

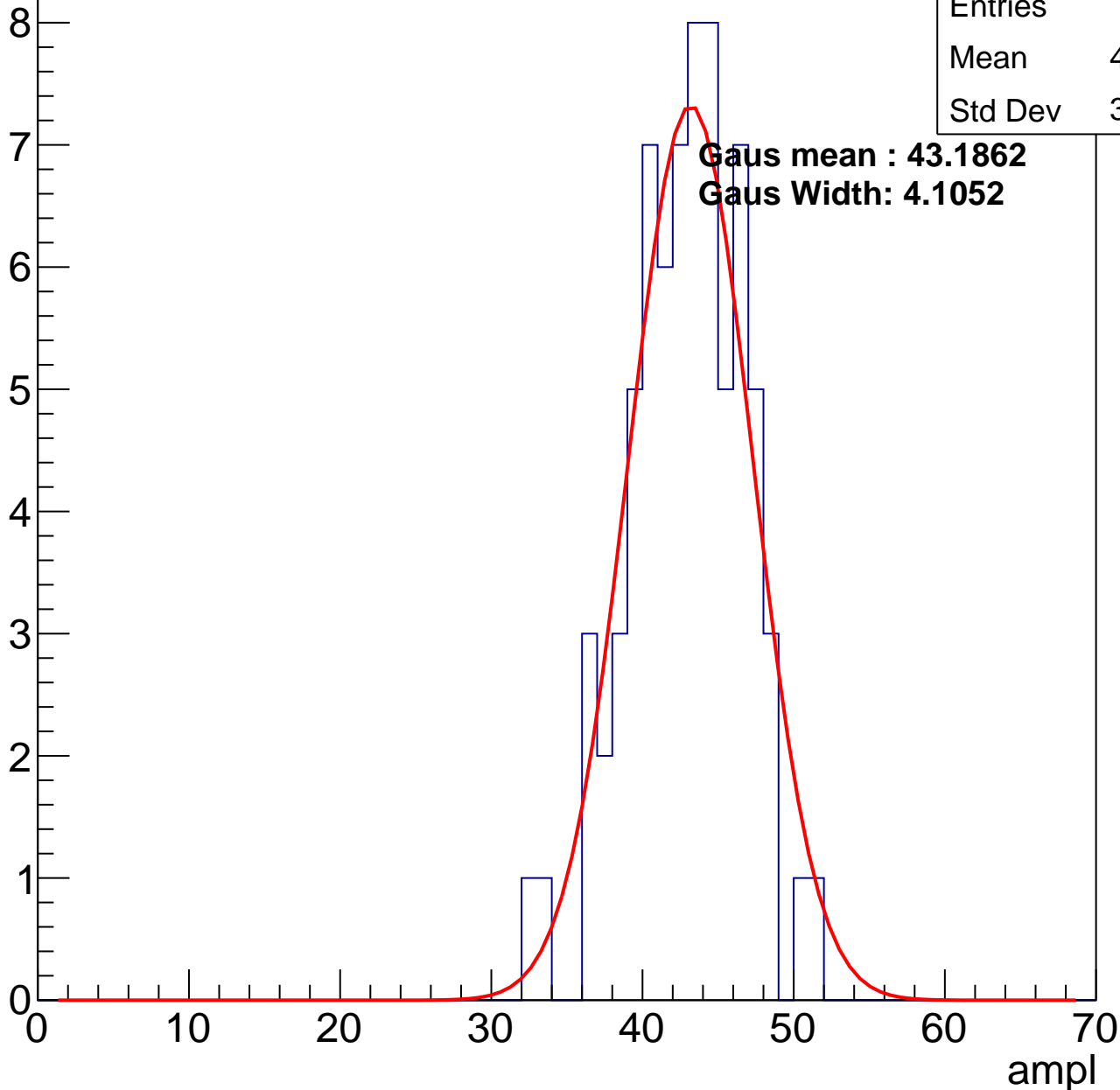
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	42.45
Std Dev	3.749

Gaus mean : 43.1862

Gaus Width: 4.1052

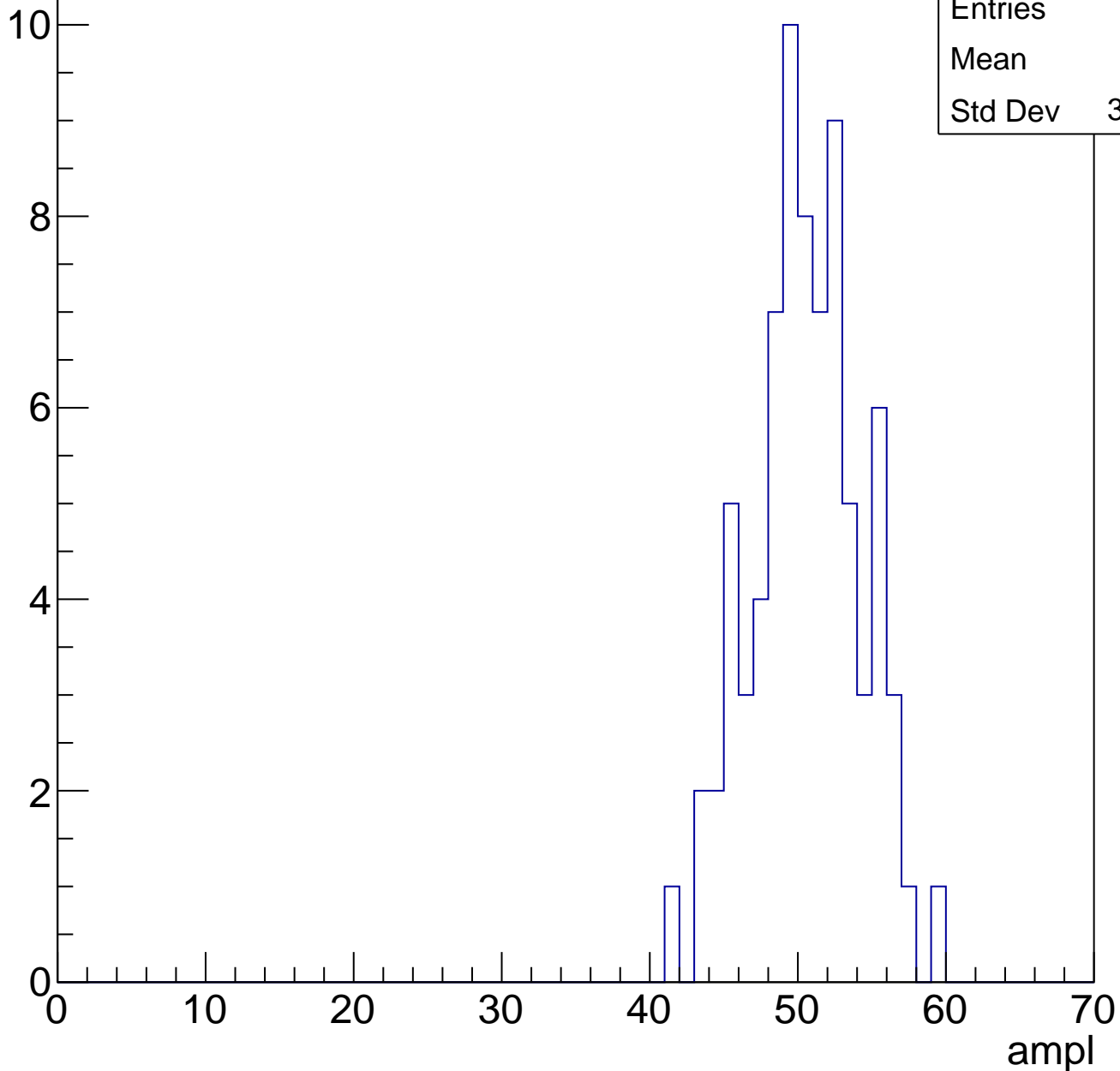


B0L002S, U4-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

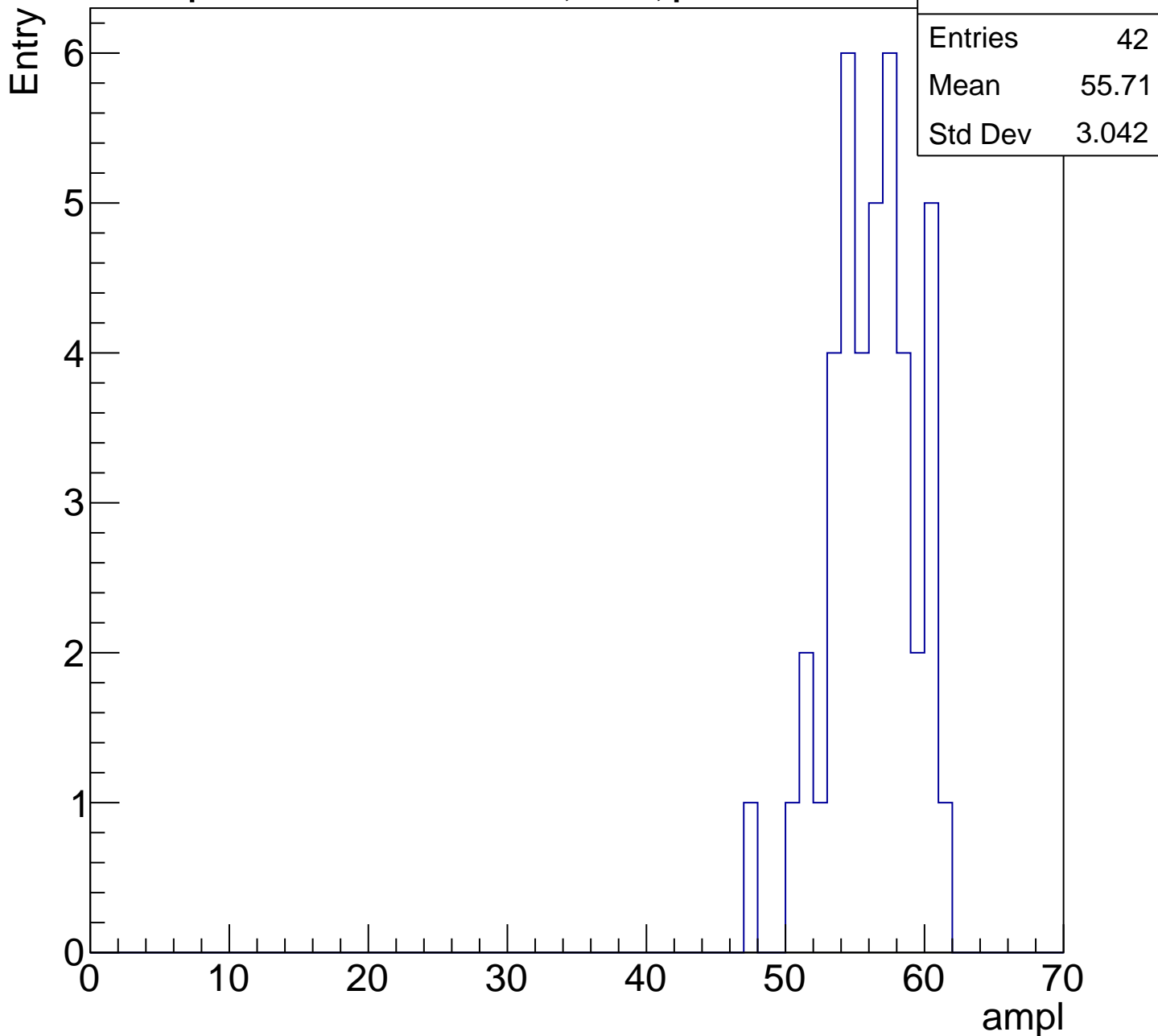
Entries	77
Mean	50.1
Std Dev	3.642

Entry



B0L002S, U4-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch47, adc5

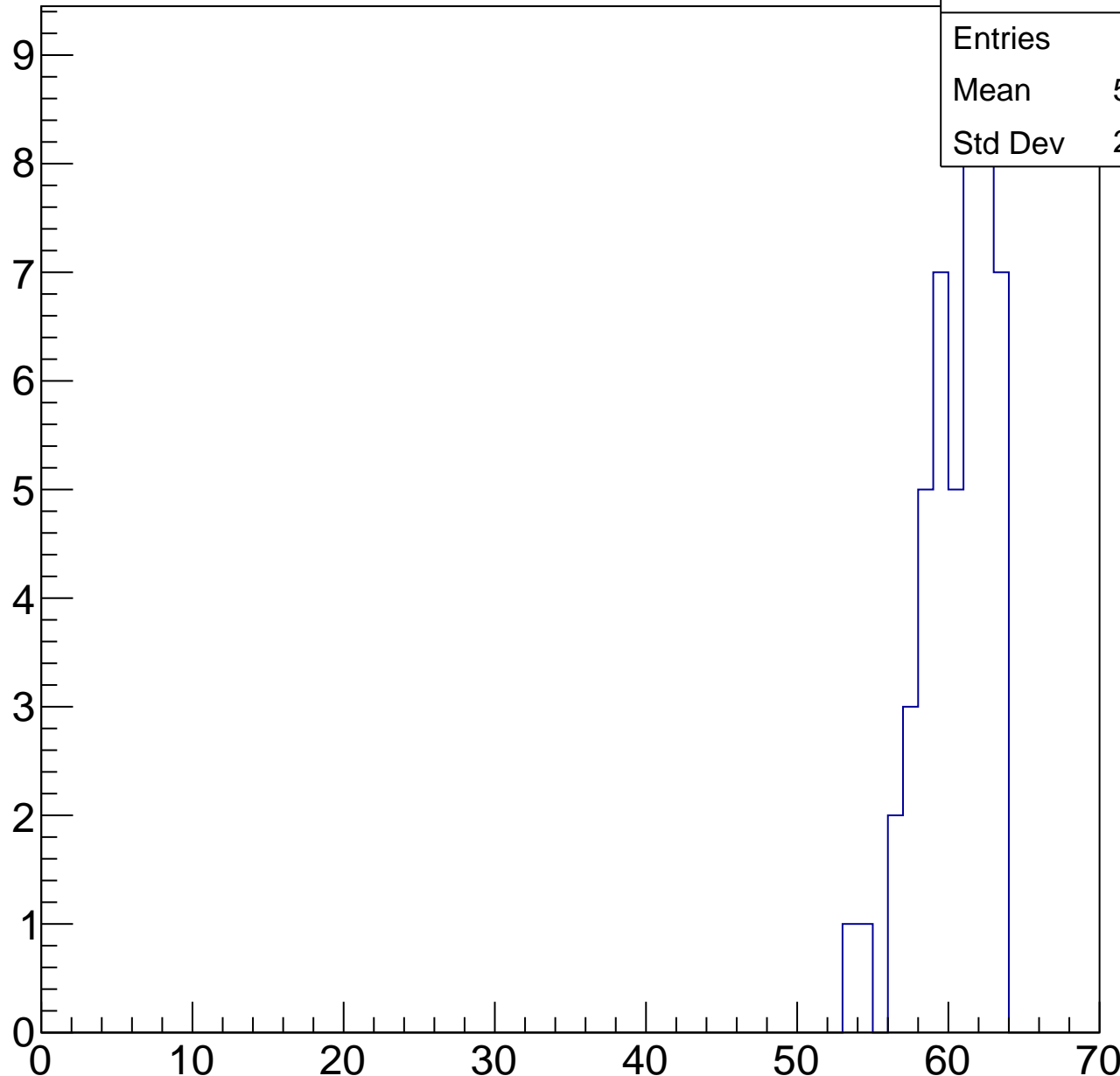
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	59.98
Std Dev	2.393

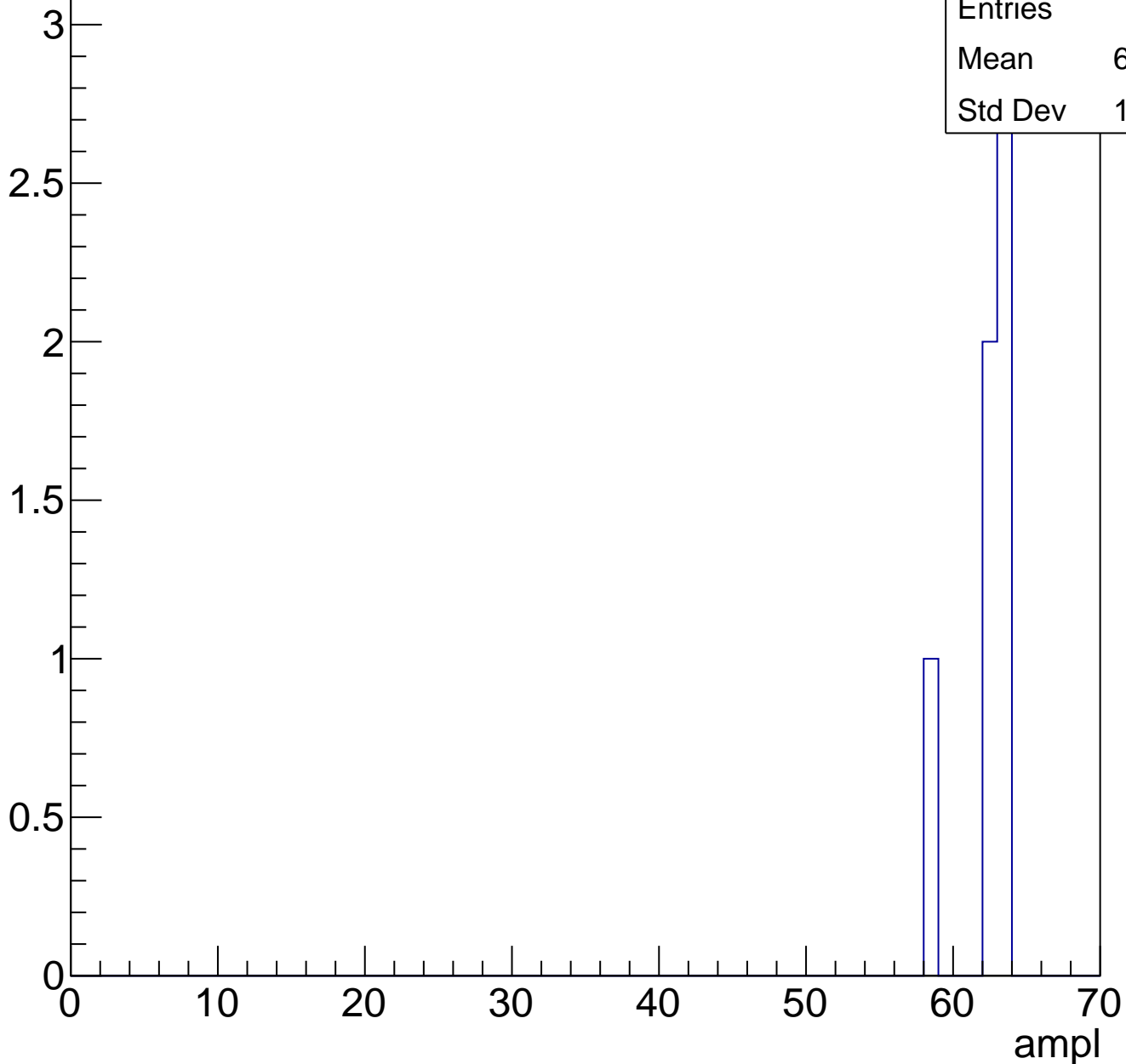
ampl



B0L002S, U4-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch48, adc0

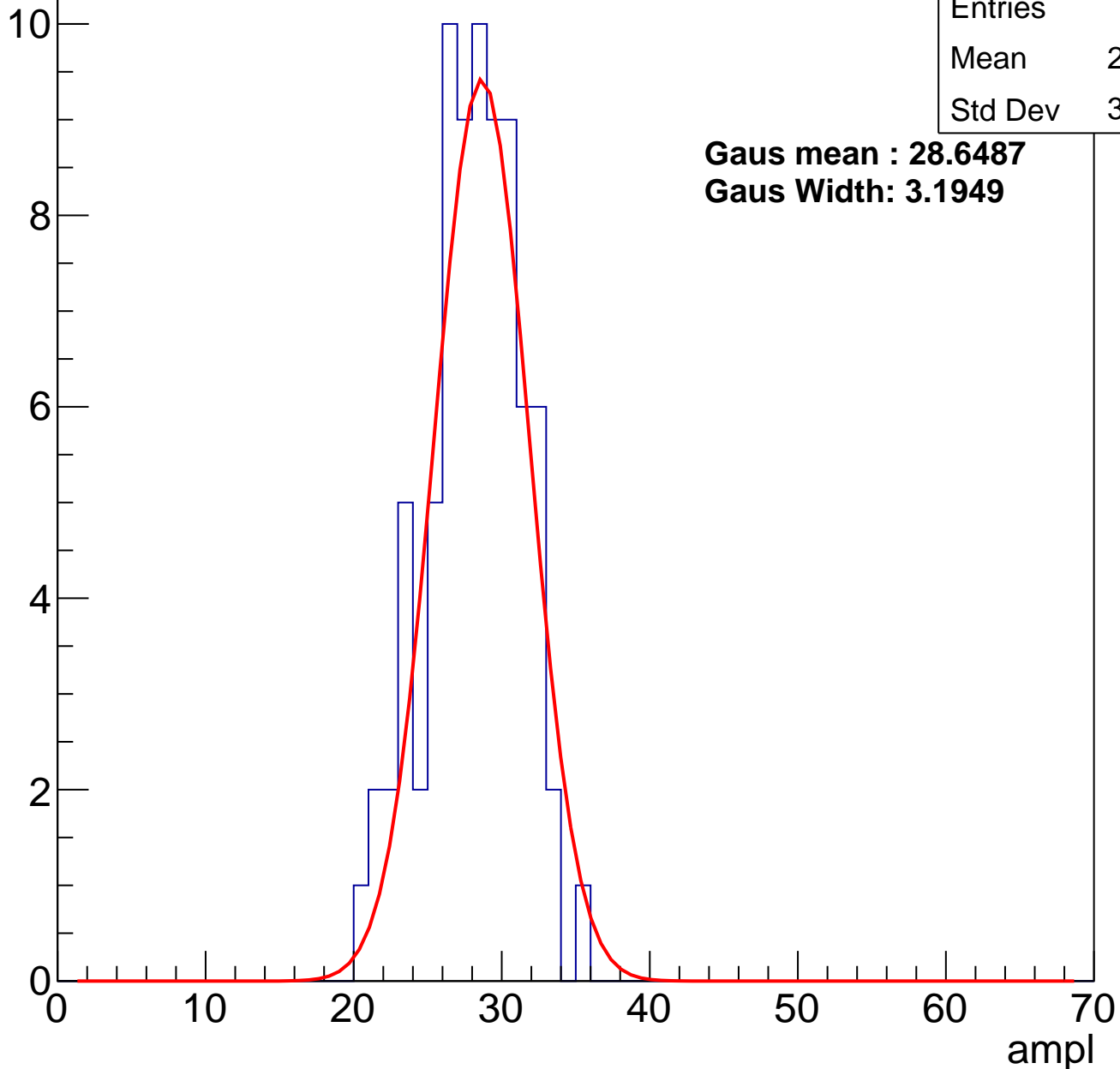
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	27.68
Std Dev	3.124

Gaus mean : 28.6487

Gaus Width: 3.1949

Entry



B0L002S, U4-ch48, adc1

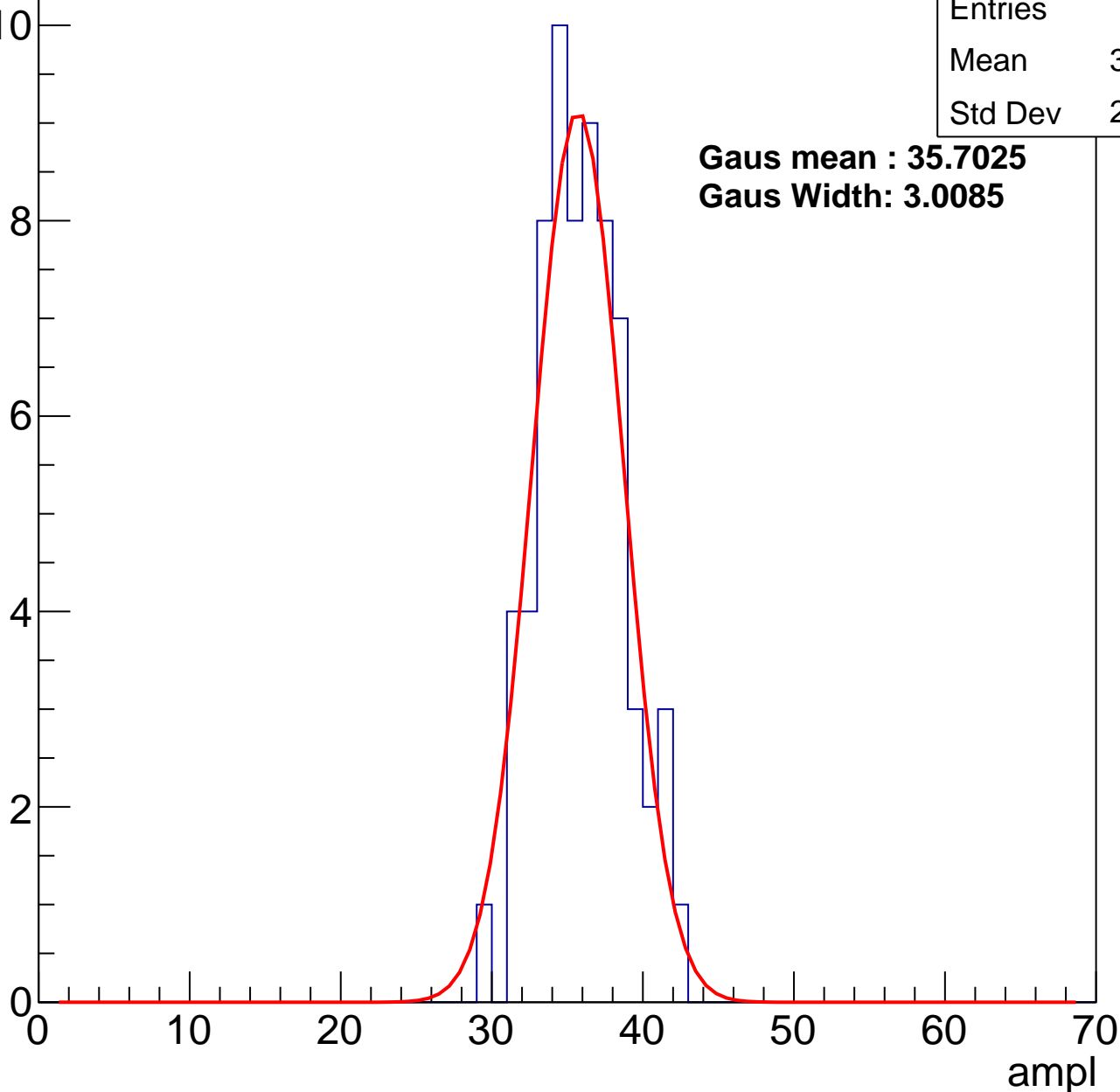
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.49
Std Dev	2.768

Gaus mean : 35.7025

Gaus Width: 3.0085



B0L002S, U4-ch48, adc2

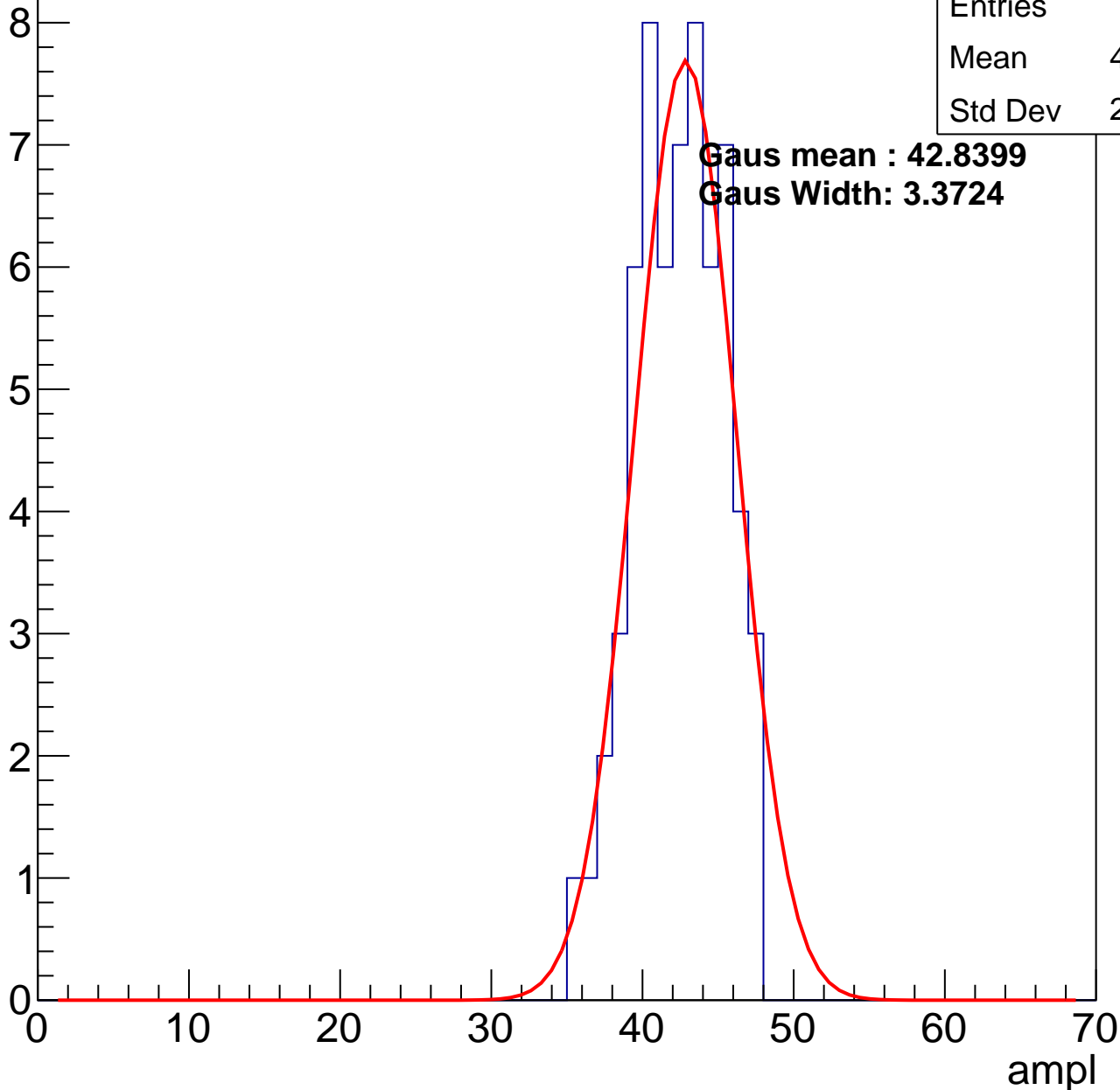
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	41.95
Std Dev	2.865

Gaus mean : 42.8399

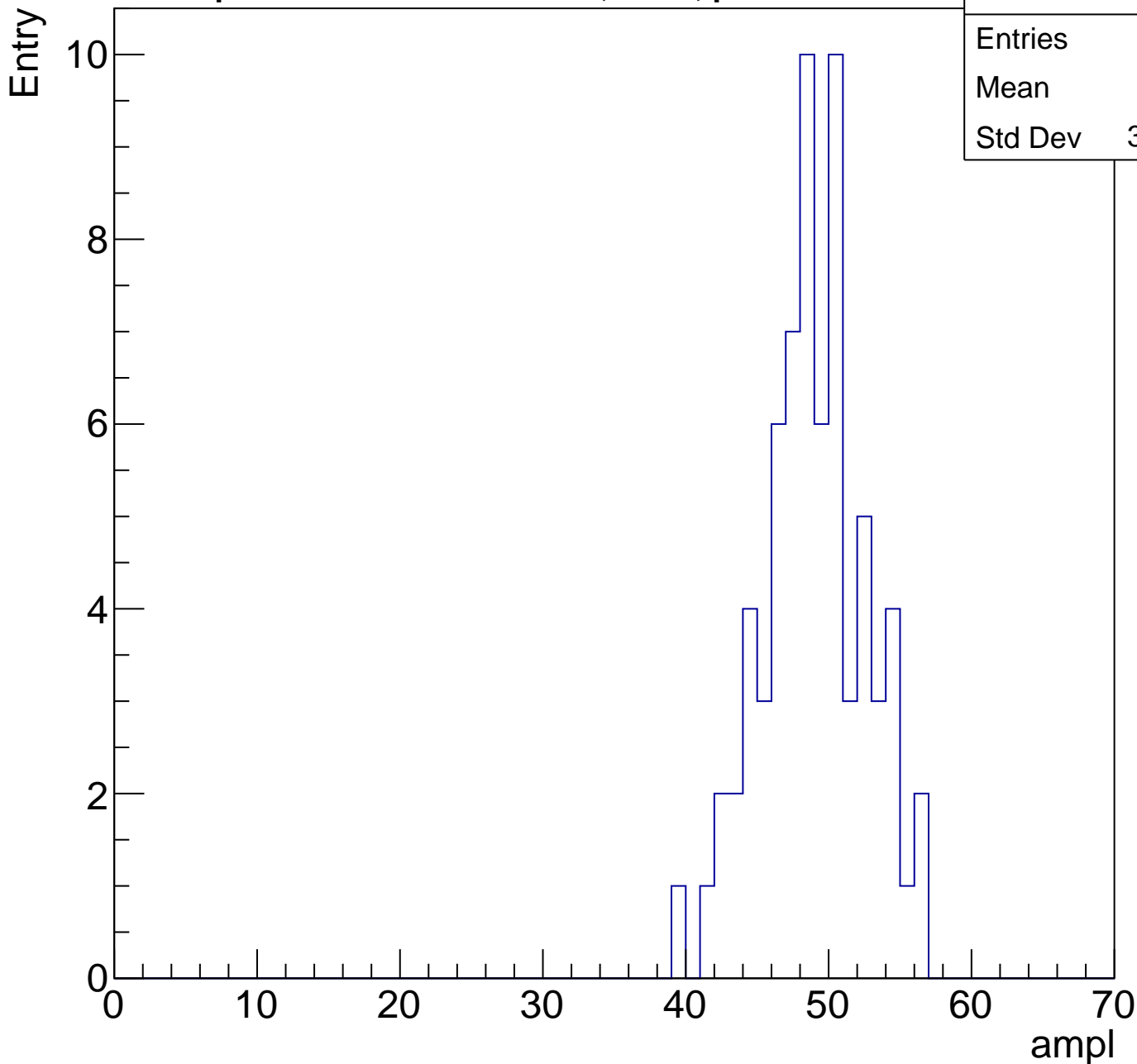
Gaus Width: 3.3724



B0L002S, U4-ch48, adc3

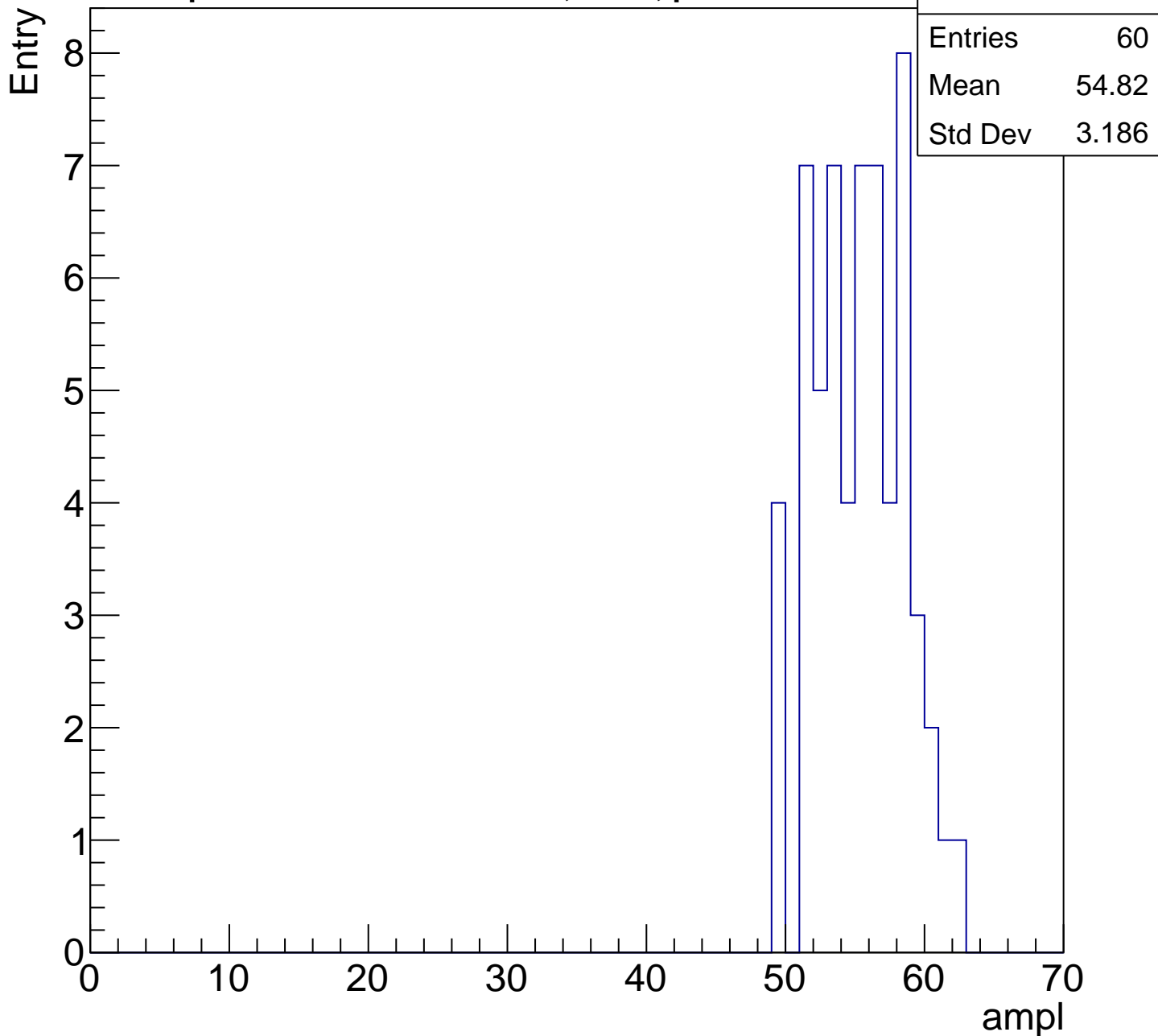
calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	48.5
Std Dev	3.597



B0L002S, U4-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

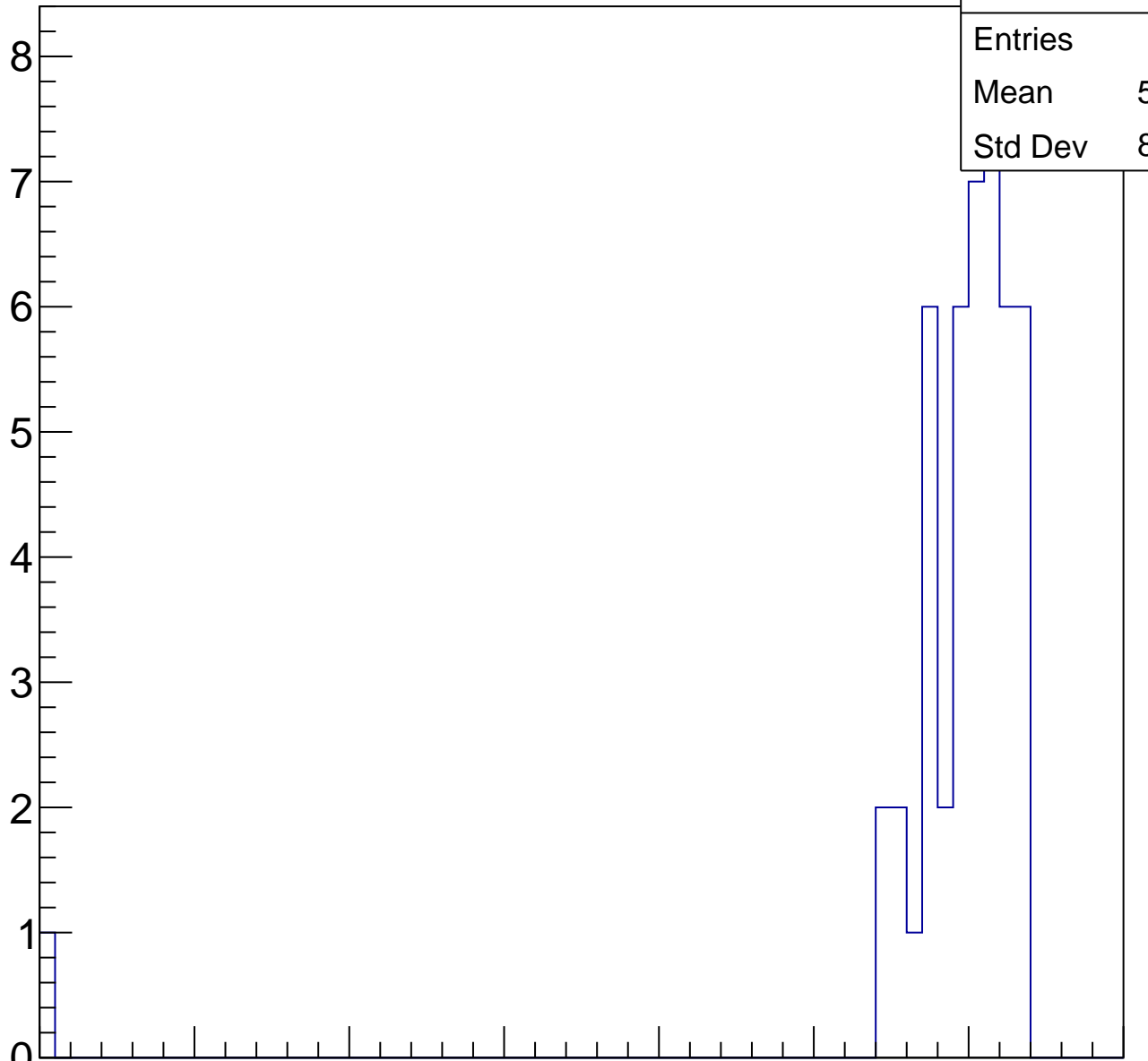
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.38
Std Dev	8.955

ampl

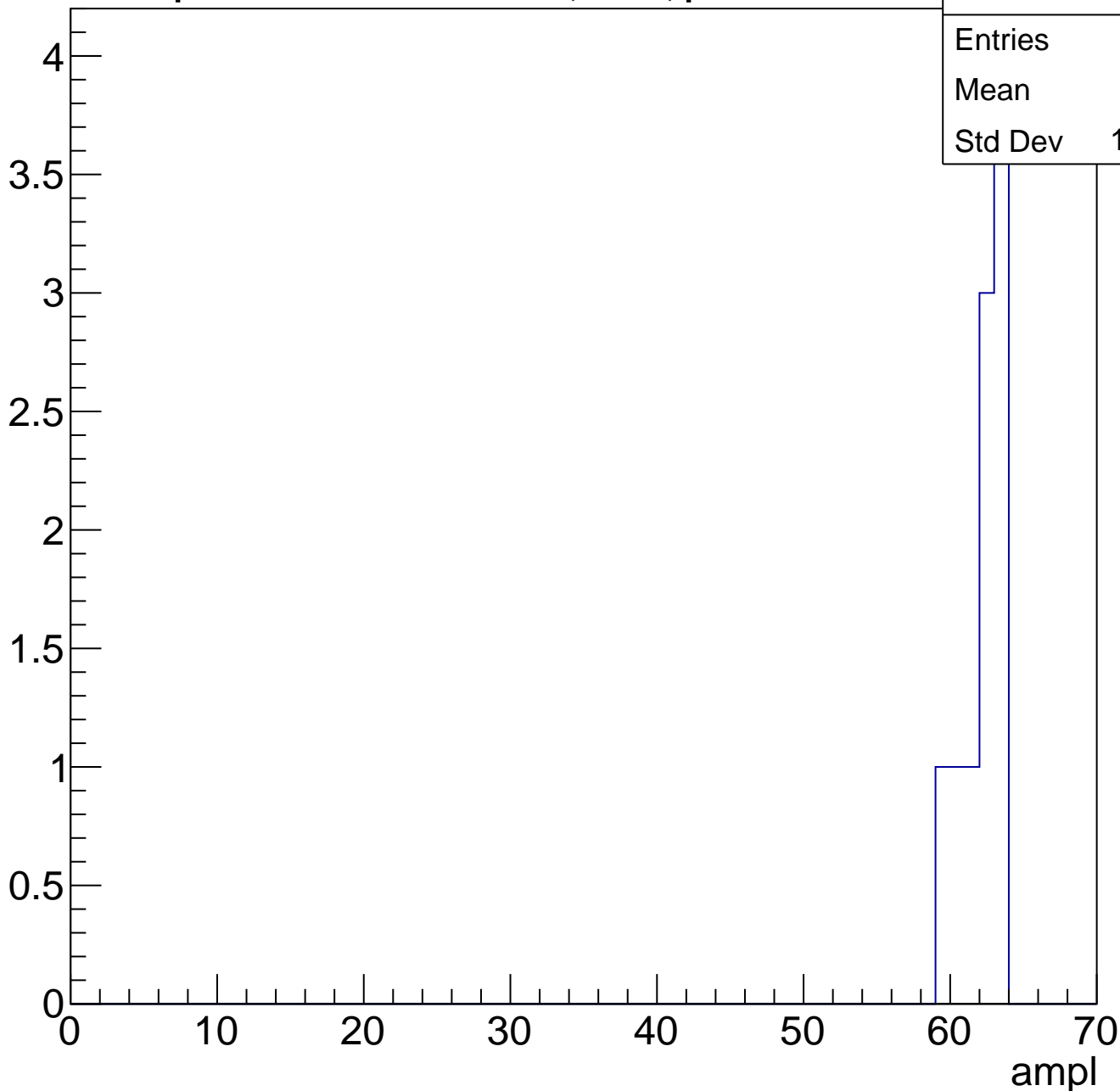
0 10 20 30 40 50 60 70



B0L002S, U4-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch49, adc0

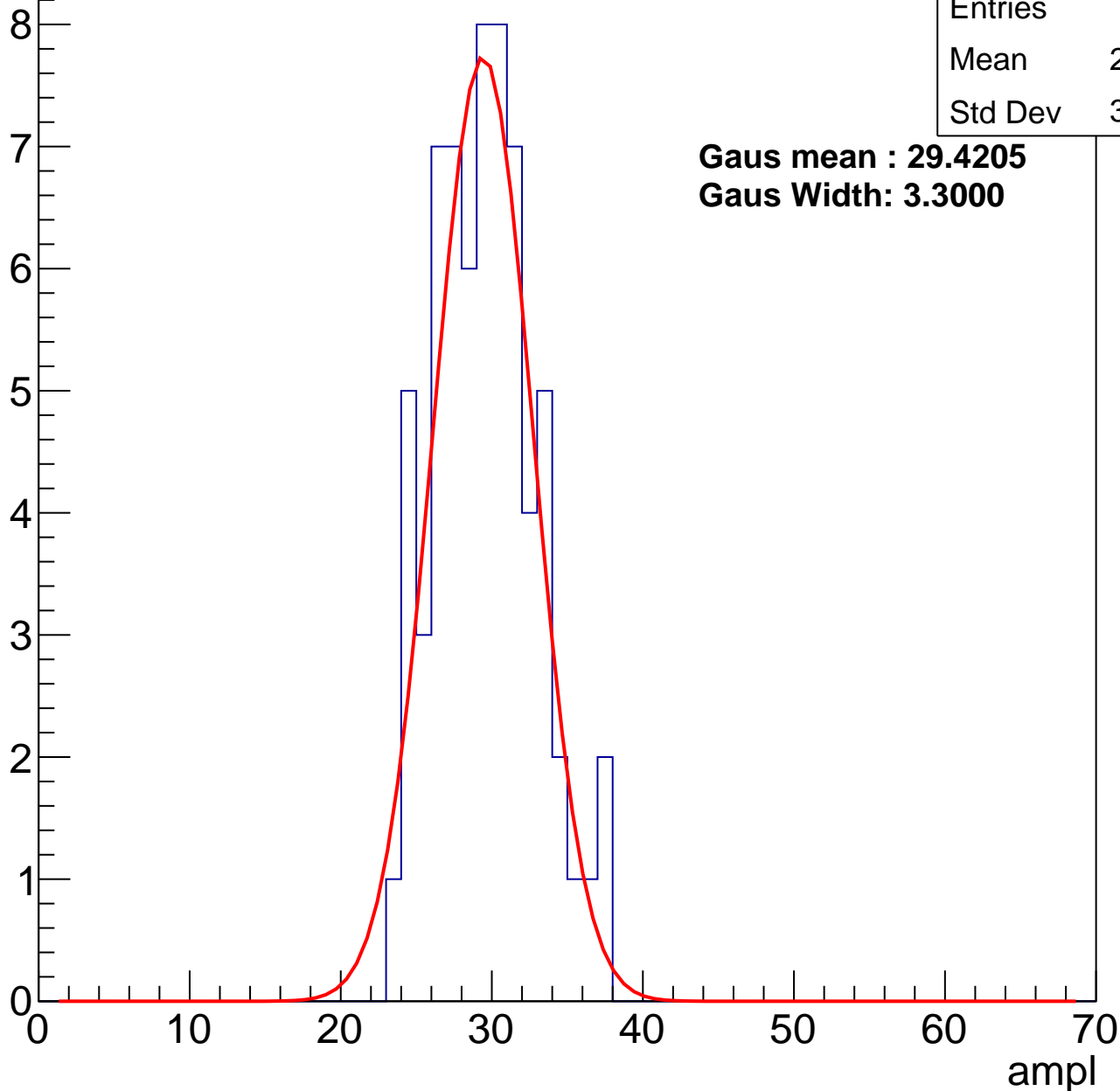
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.13
Std Dev	3.278

Gaus mean : 29.4205

Gaus Width: 3.3000



B0L002S, U4-ch49, adc1

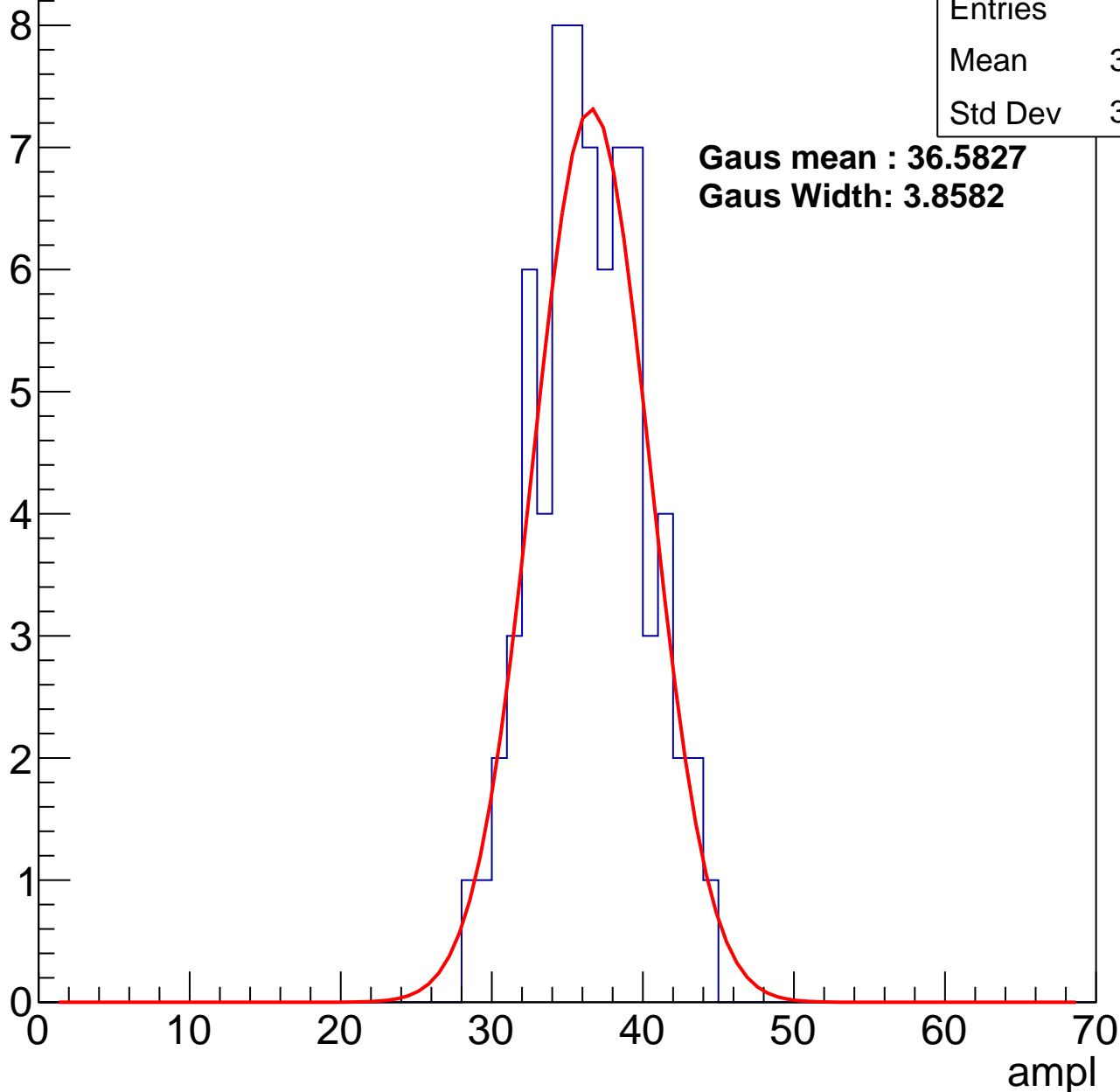
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.07
Std Dev	3.556

Gaus mean : 36.5827

Gaus Width: 3.8582



B0L002S, U4-ch49, adc2

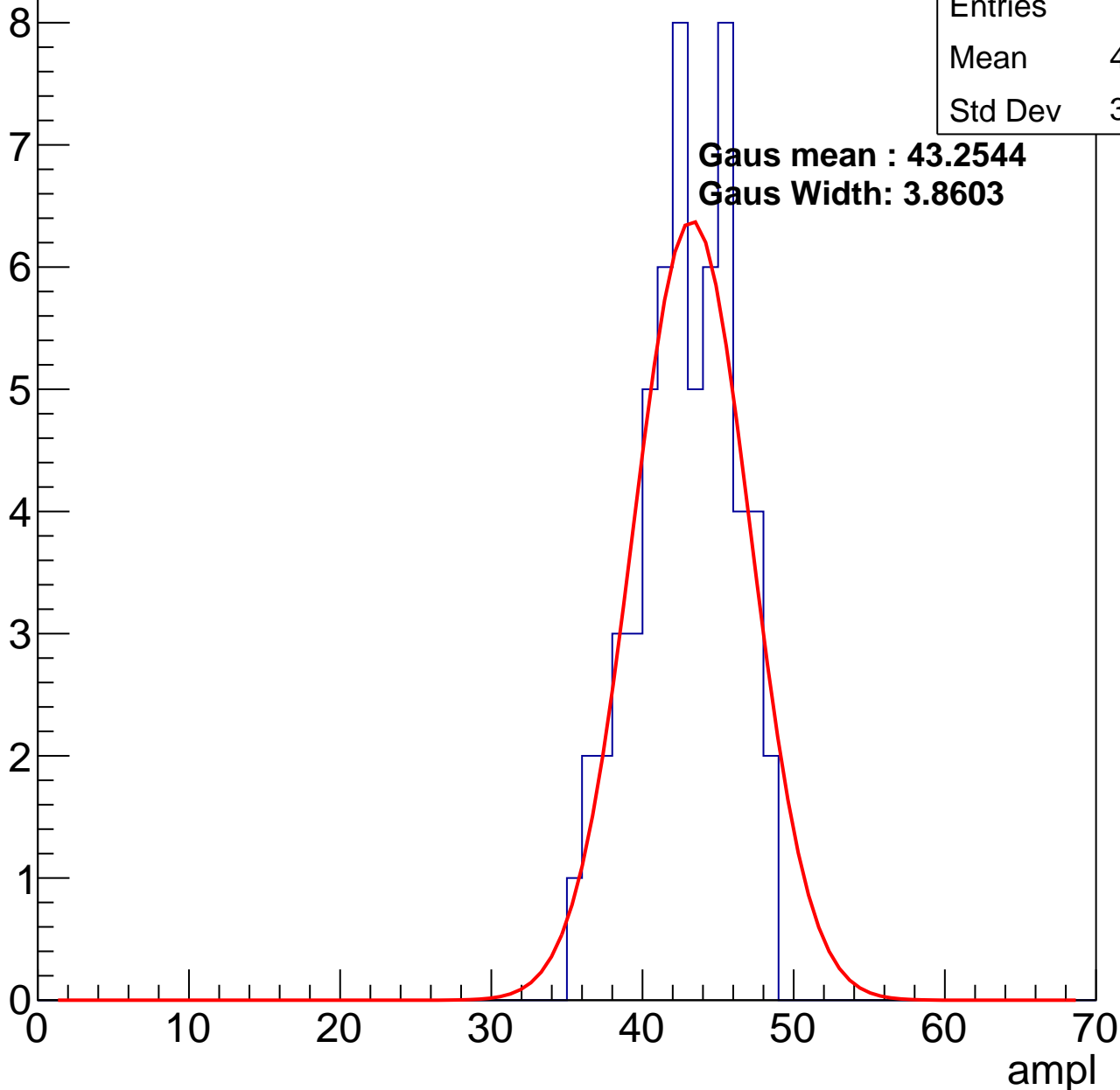
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	42.39
Std Dev	3.189

Gaus mean : 43.2544

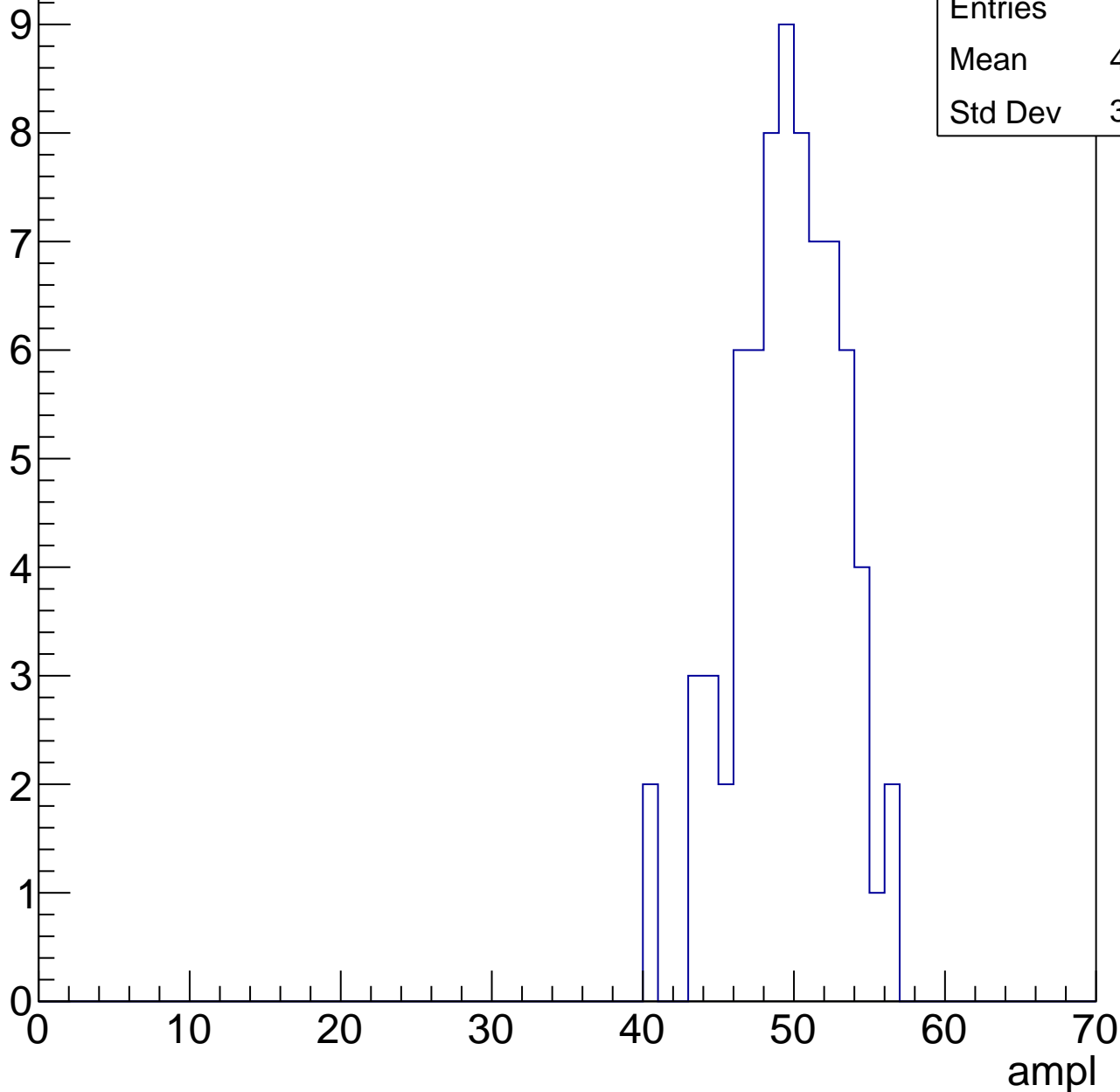
Gaus Width: 3.8603



B0L002S, U4-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

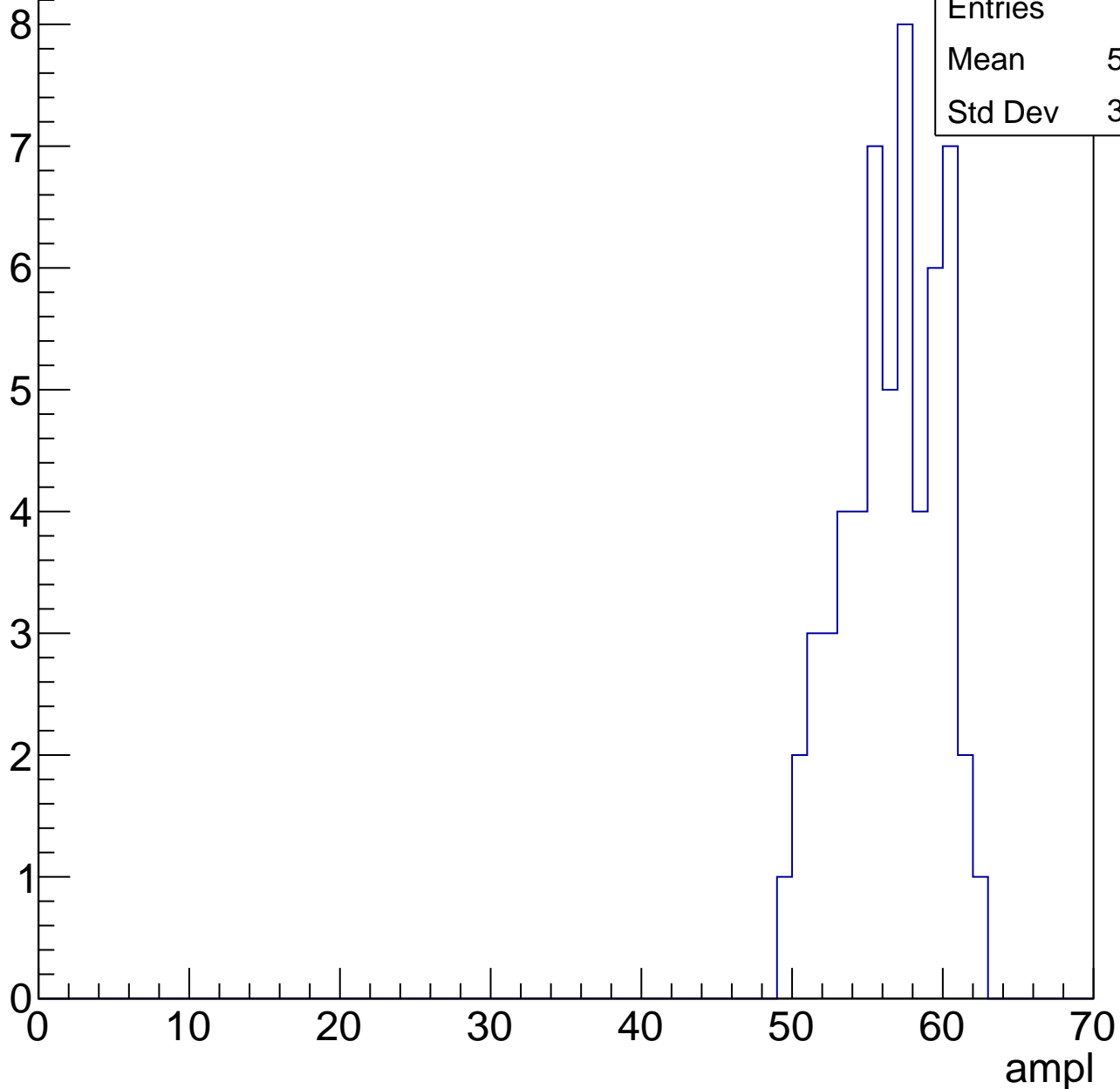
Entry



B0L002S, U4-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

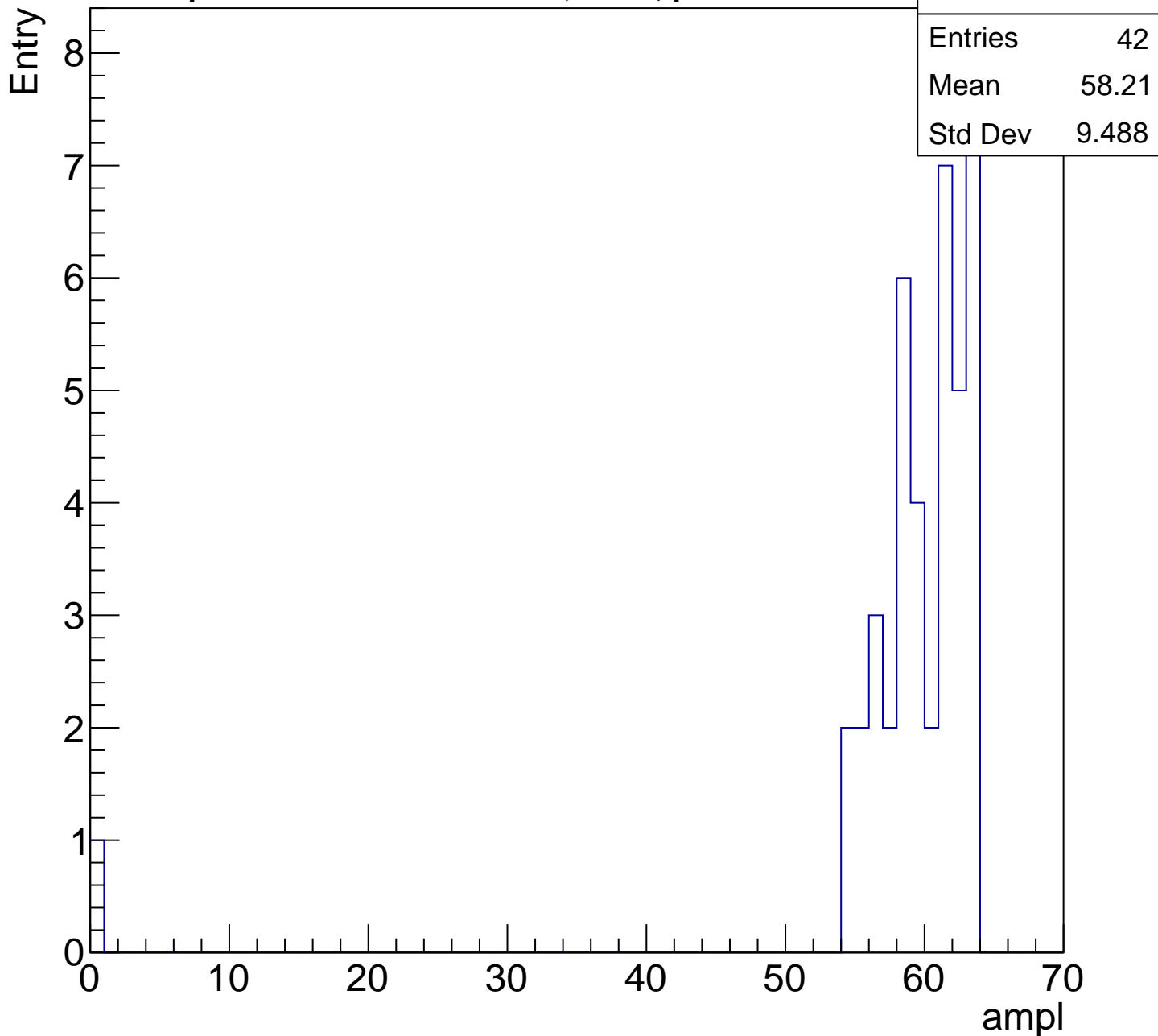
Entry



Entries	57
Mean	56.09
Std Dev	3.186

B0L002S, U4-ch49, adc5

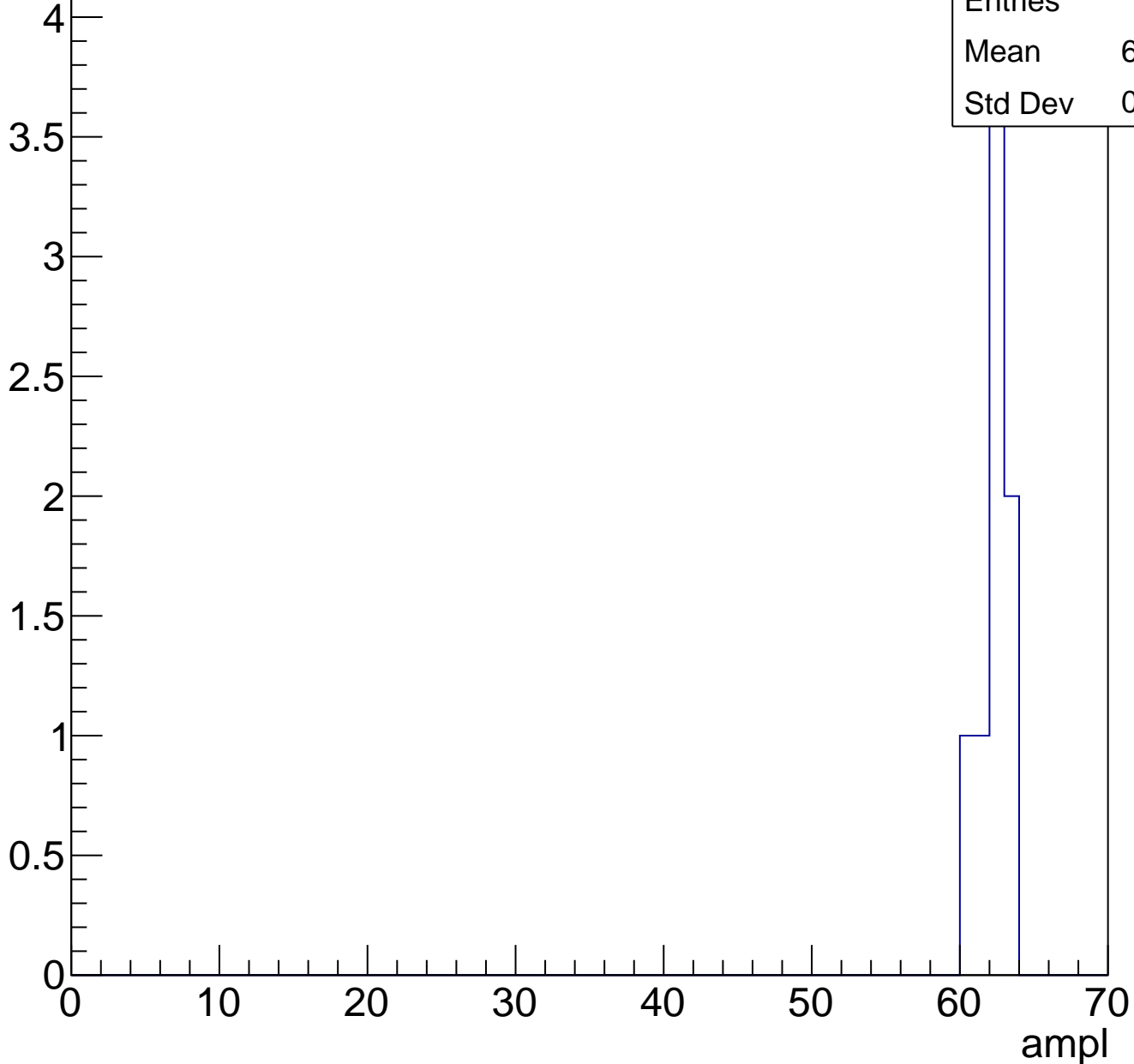
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U4-ch50, adc0

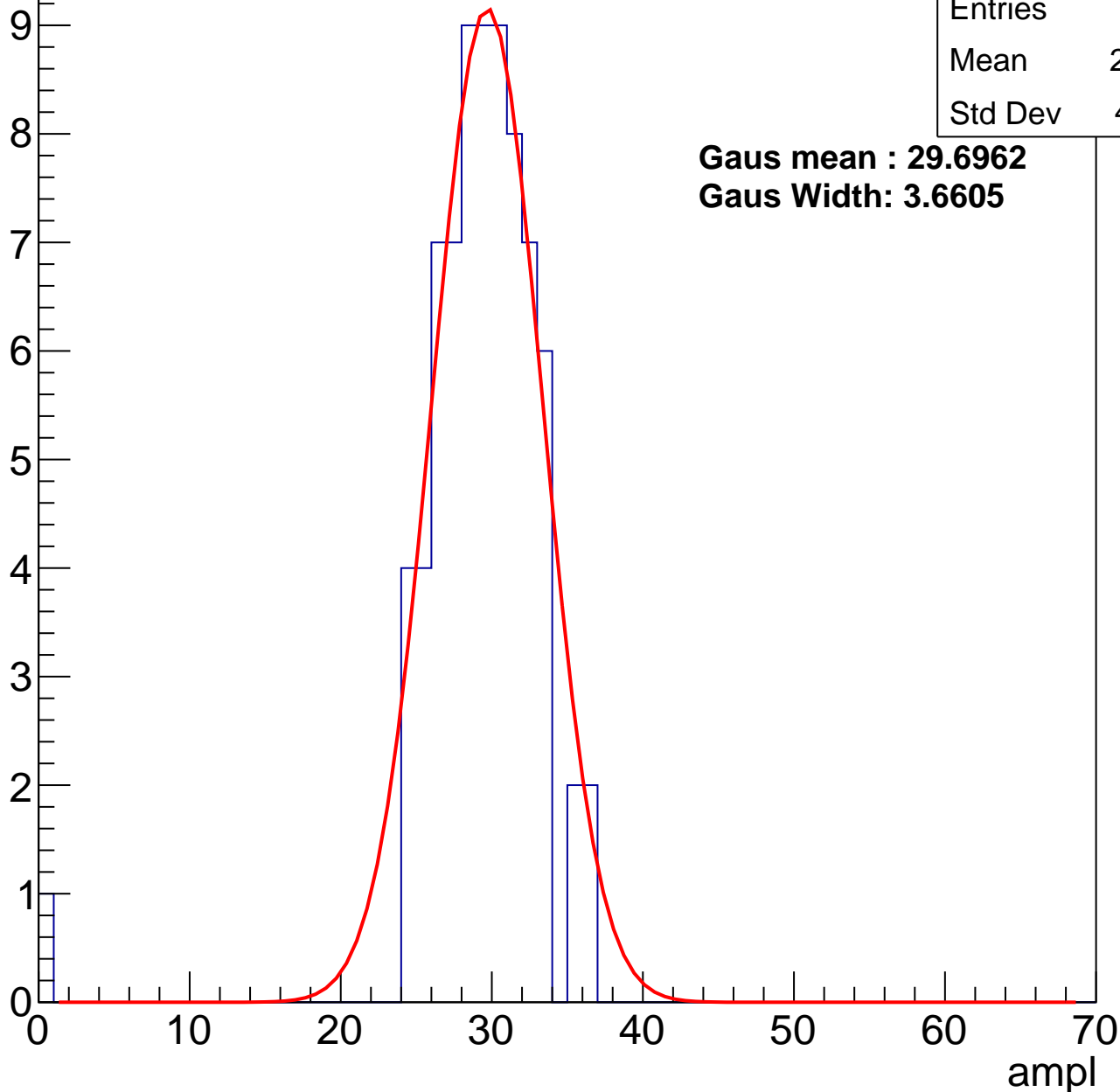
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	28.83
Std Dev	4.431

Gaus mean : 29.6962

Gaus Width: 3.6605



B0L002S, U4-ch50, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	36.85
Std Dev	3.412

Gaus mean : 36.7079

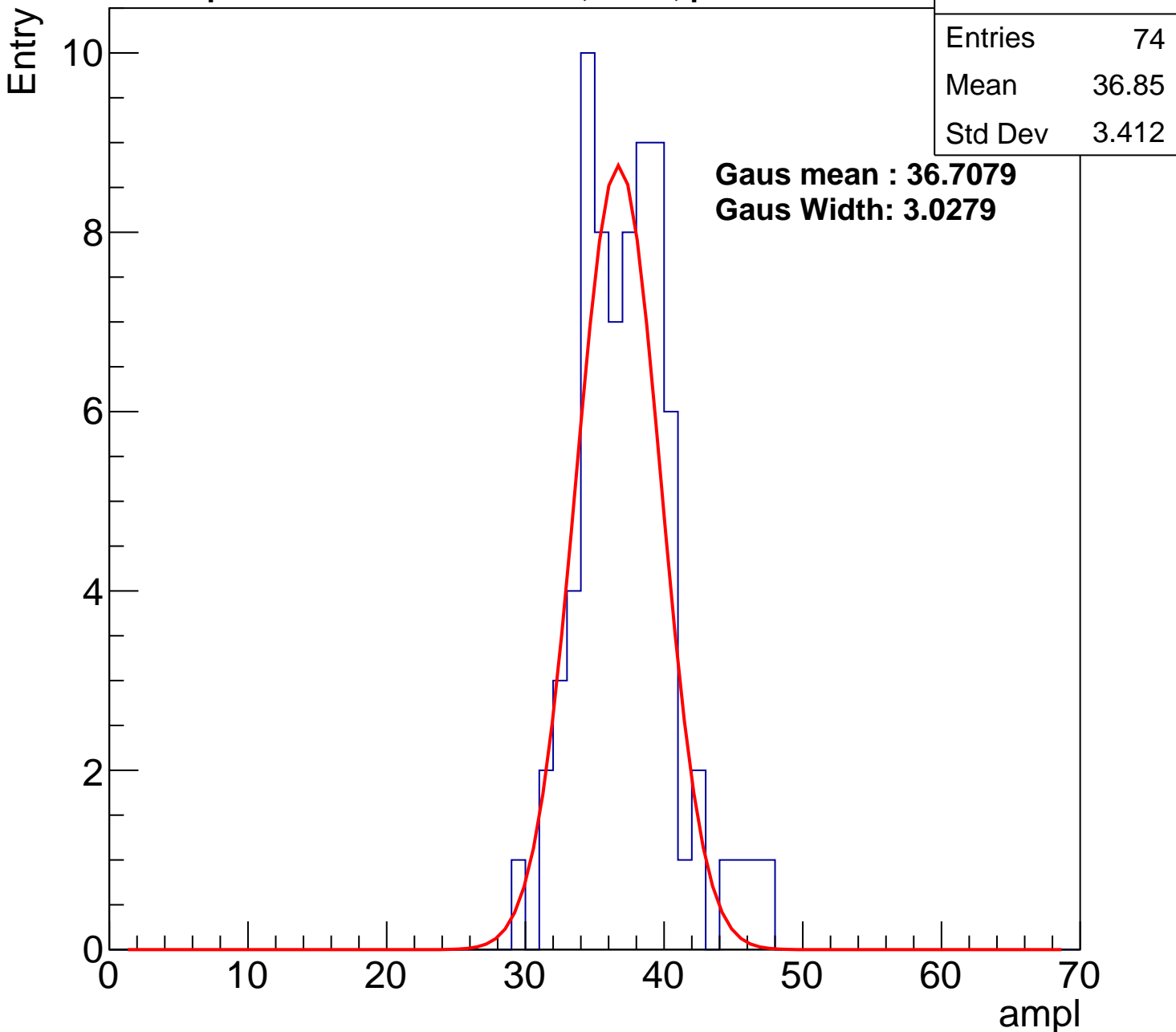
Gaus Width: 3.0279

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch50, adc2

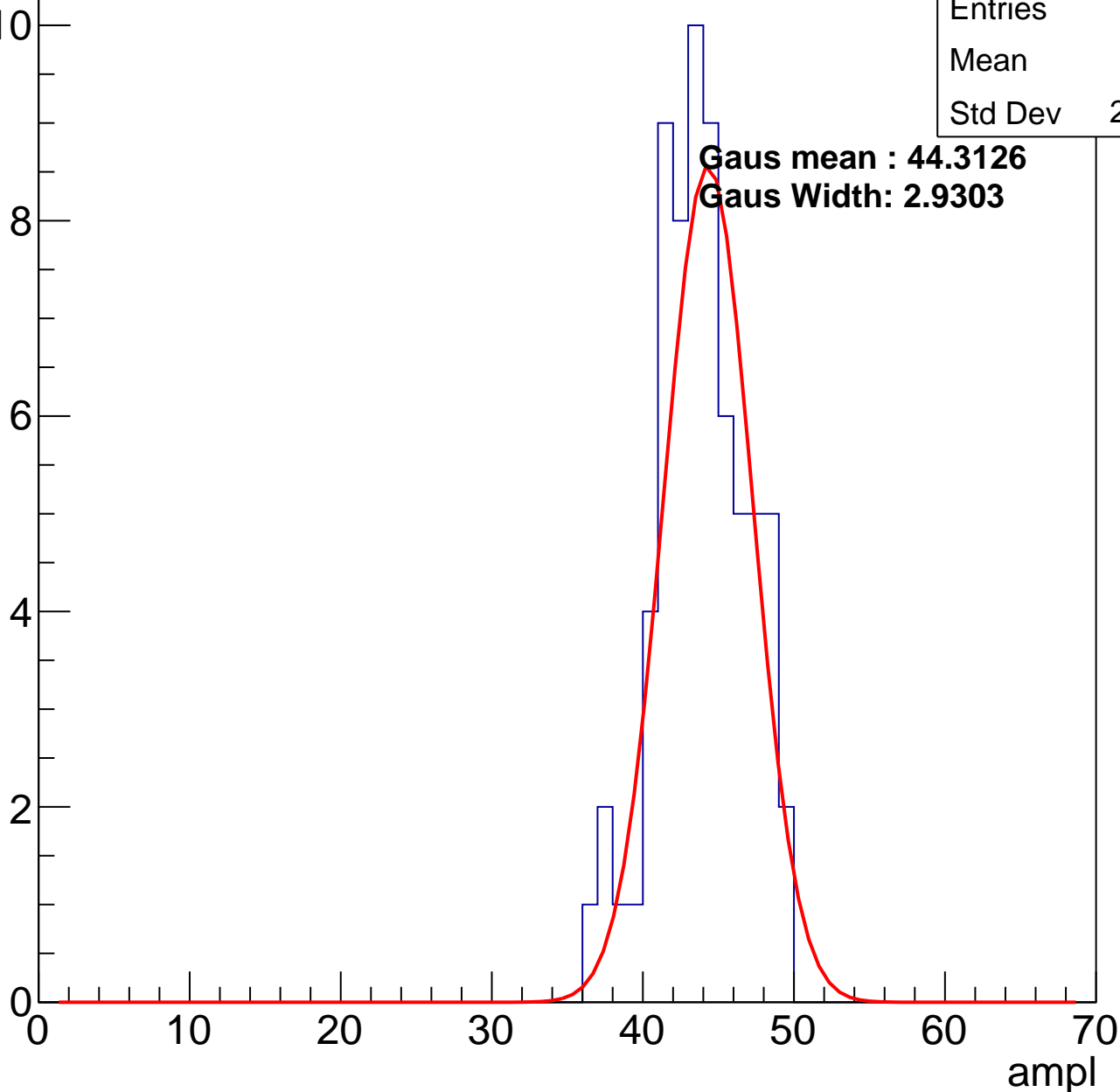
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	43.4
Std Dev	2.936

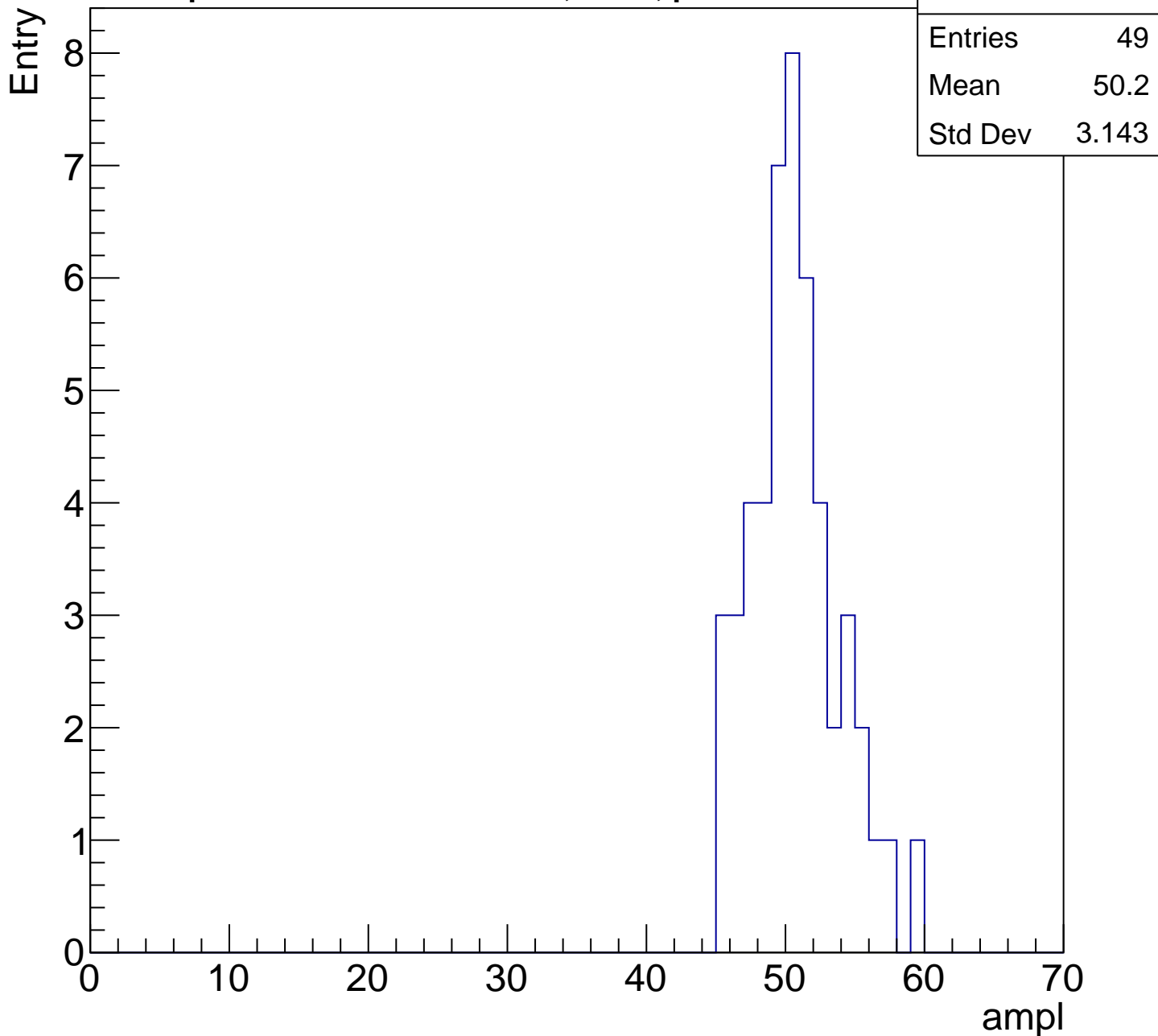
Gaus mean : 44.3126

Gaus Width: 2.9303



B0L002S, U4-ch50, adc3

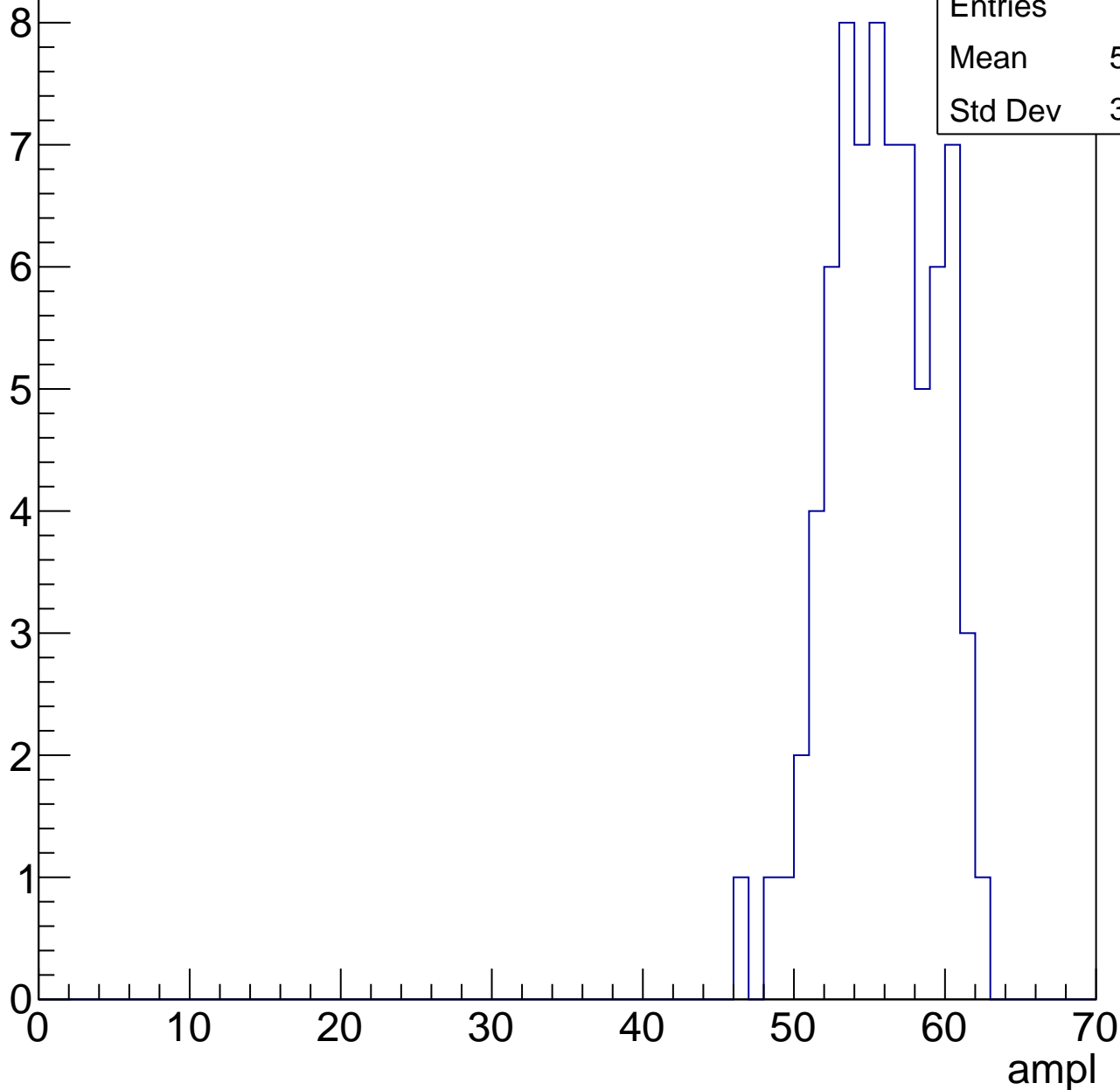
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



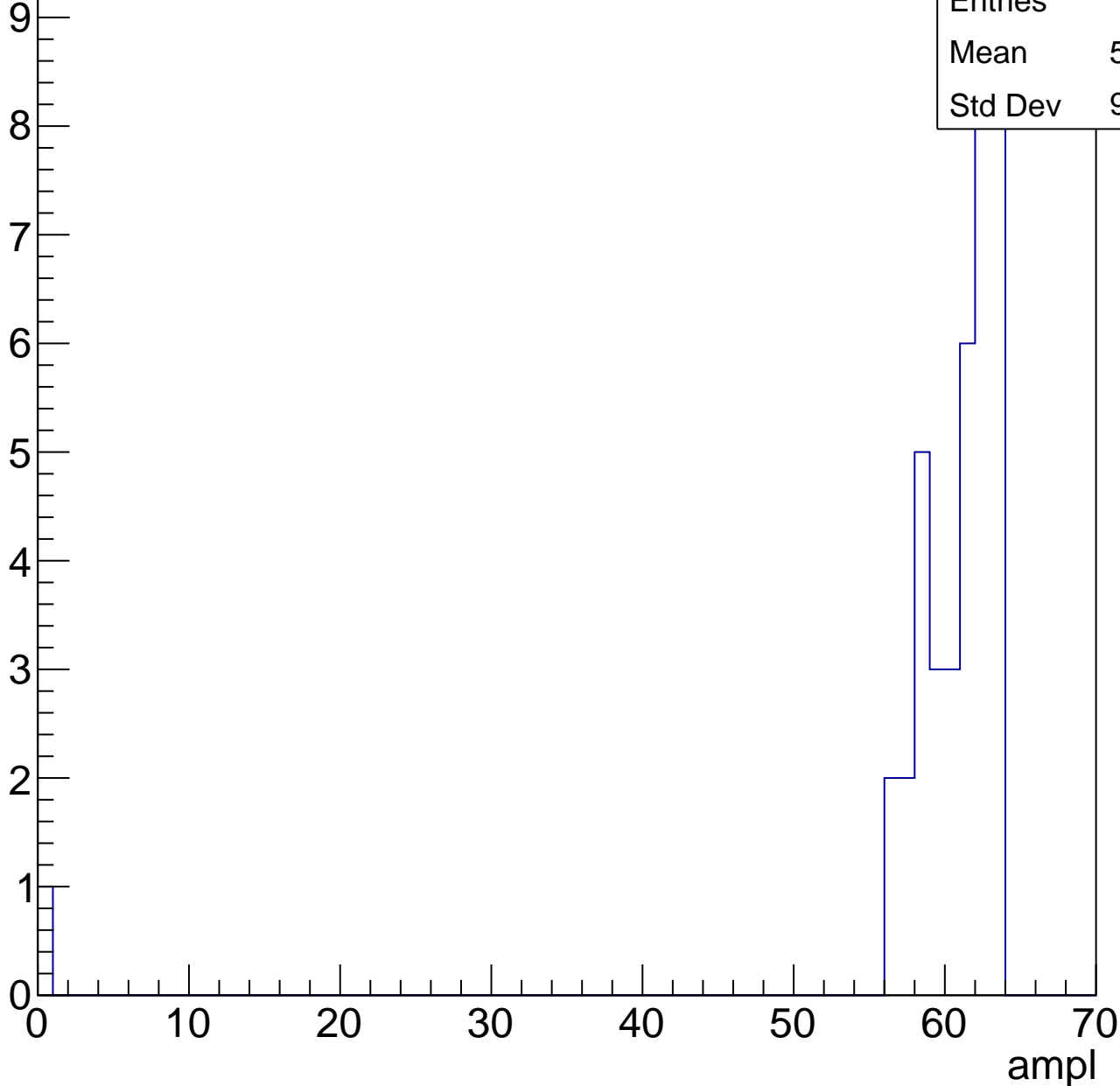
Entries	74
Mean	55.42
Std Dev	3.425

B0L002S, U4-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.03
Std Dev	9.812



B0L002S, U4-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch51, adc0

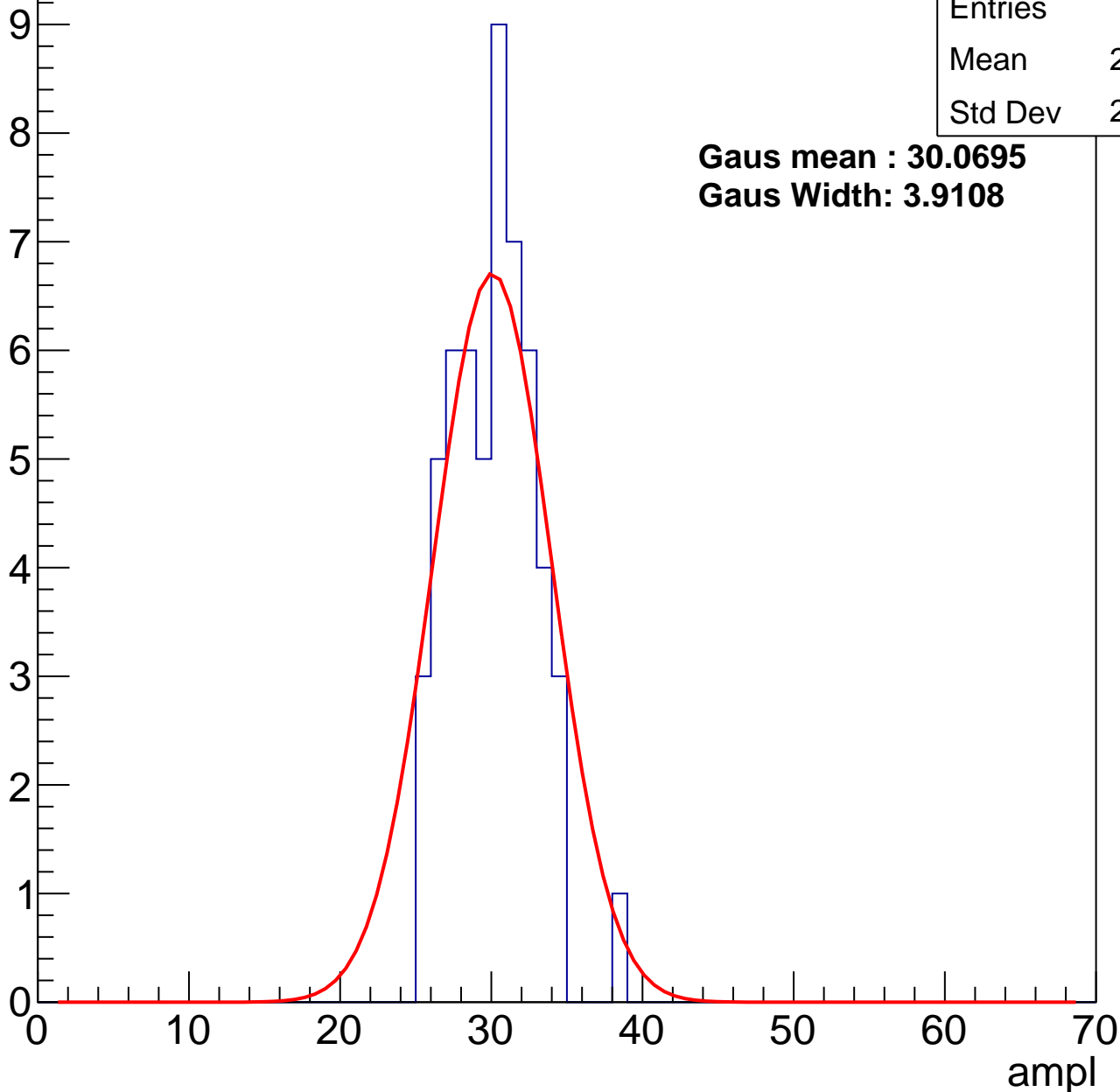
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	29.65
Std Dev	2.732

Gaus mean : 30.0695

Gaus Width: 3.9108



B0L002S, U4-ch51, adc1

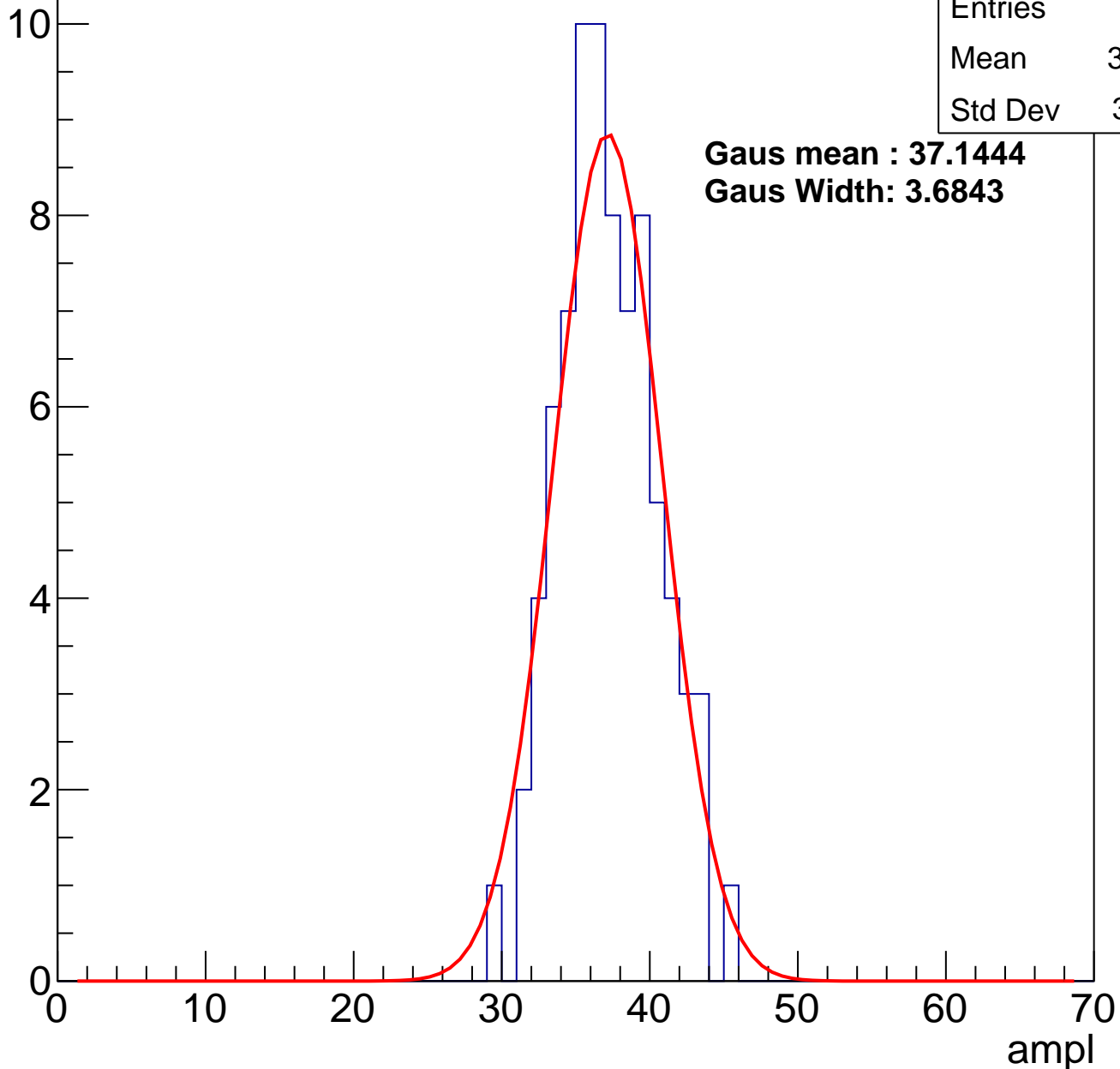
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	36.75
Std Dev	3.251

Gaus mean : 37.1444

Gaus Width: 3.6843

Entry



B0L002S, U4-ch51, adc2

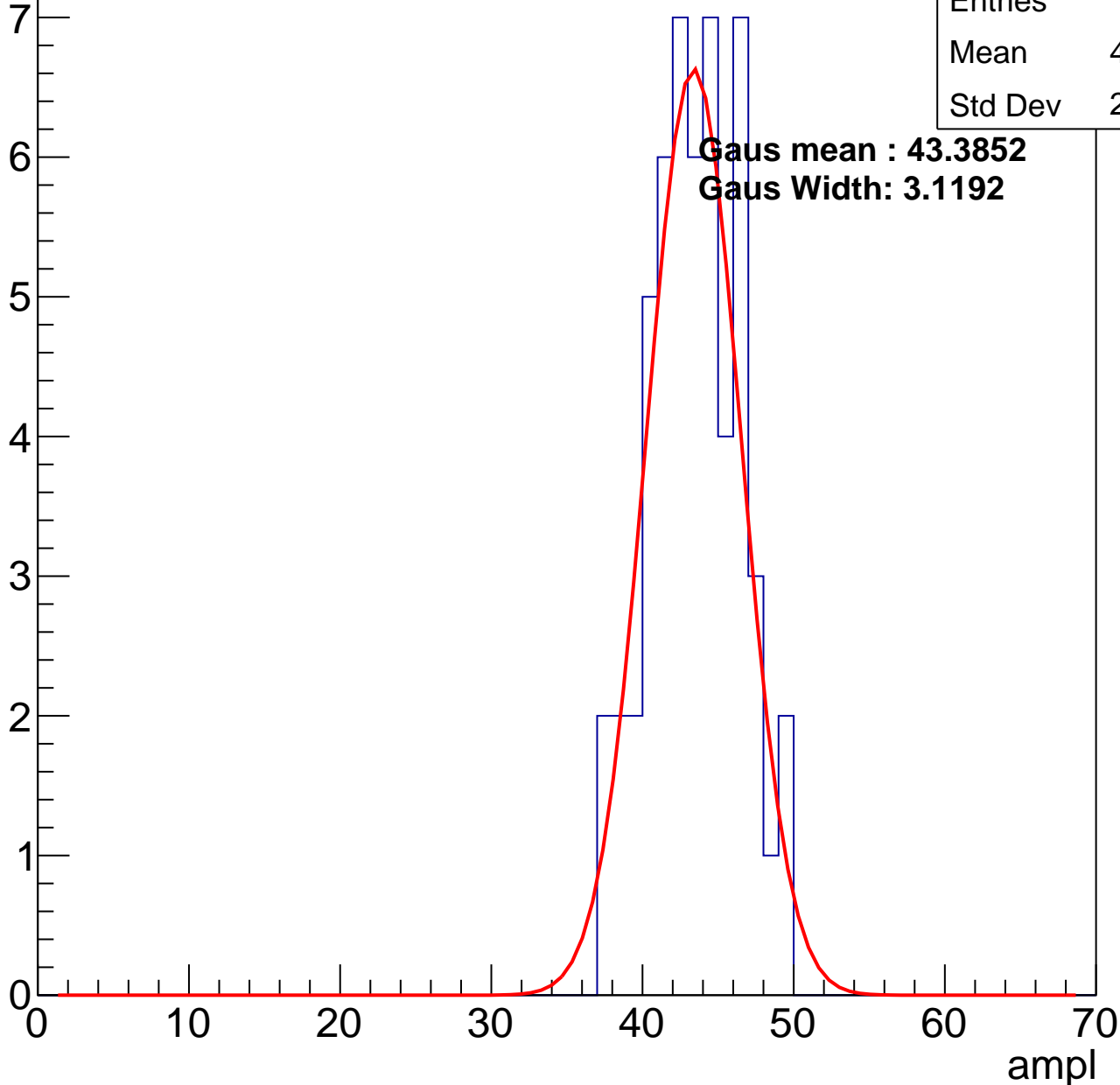
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.02
Std Dev	2.922

Gaus mean : 43.3852

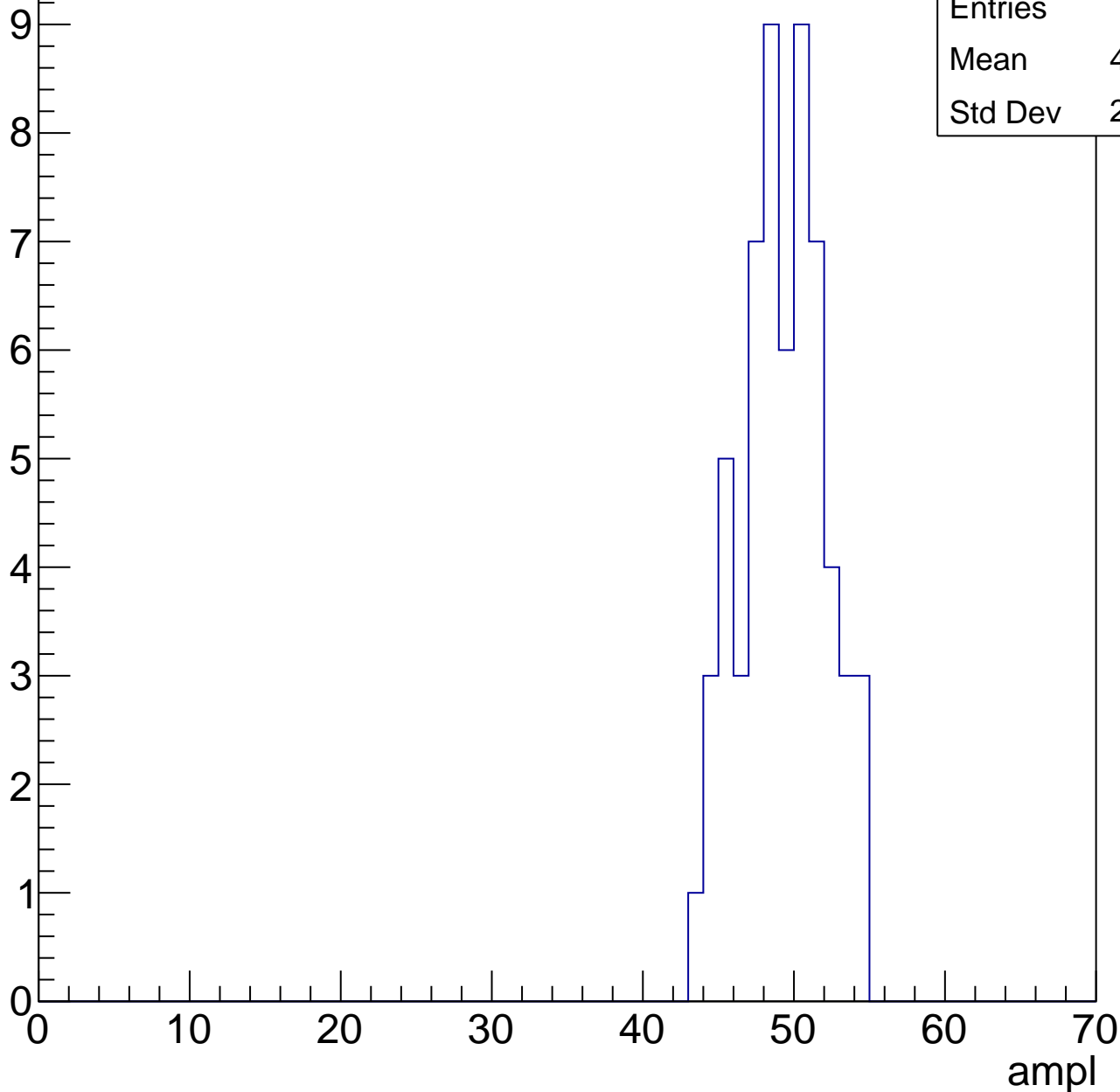
Gaus Width: 3.1192



B0L002S, U4-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	60
Mean	48.82
Std Dev	2.736

B0L002S, U4-ch51, adc4

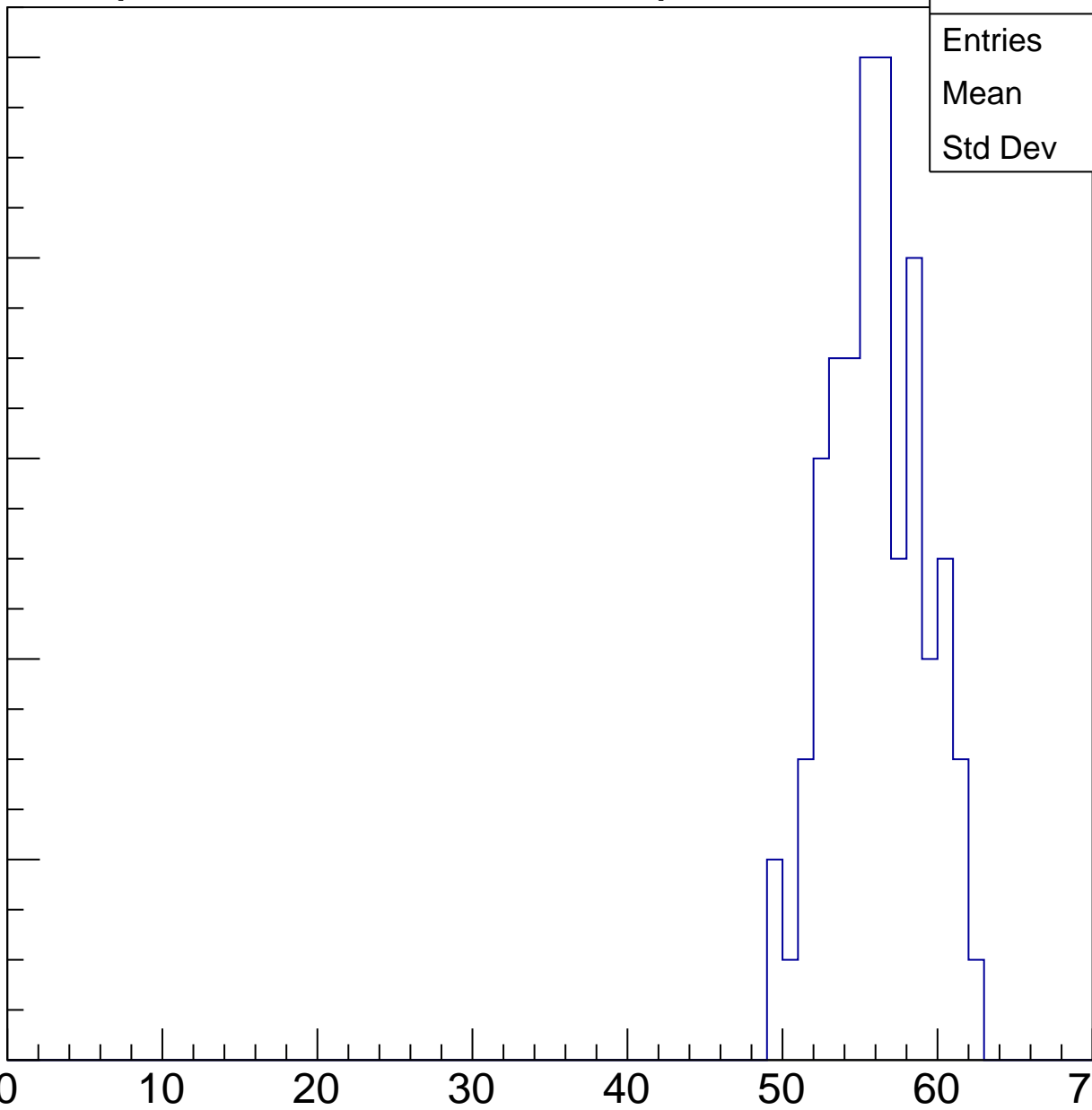
calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	55.58
Std Dev	3.022

Entry

10
8
6
4
2
0

ampl

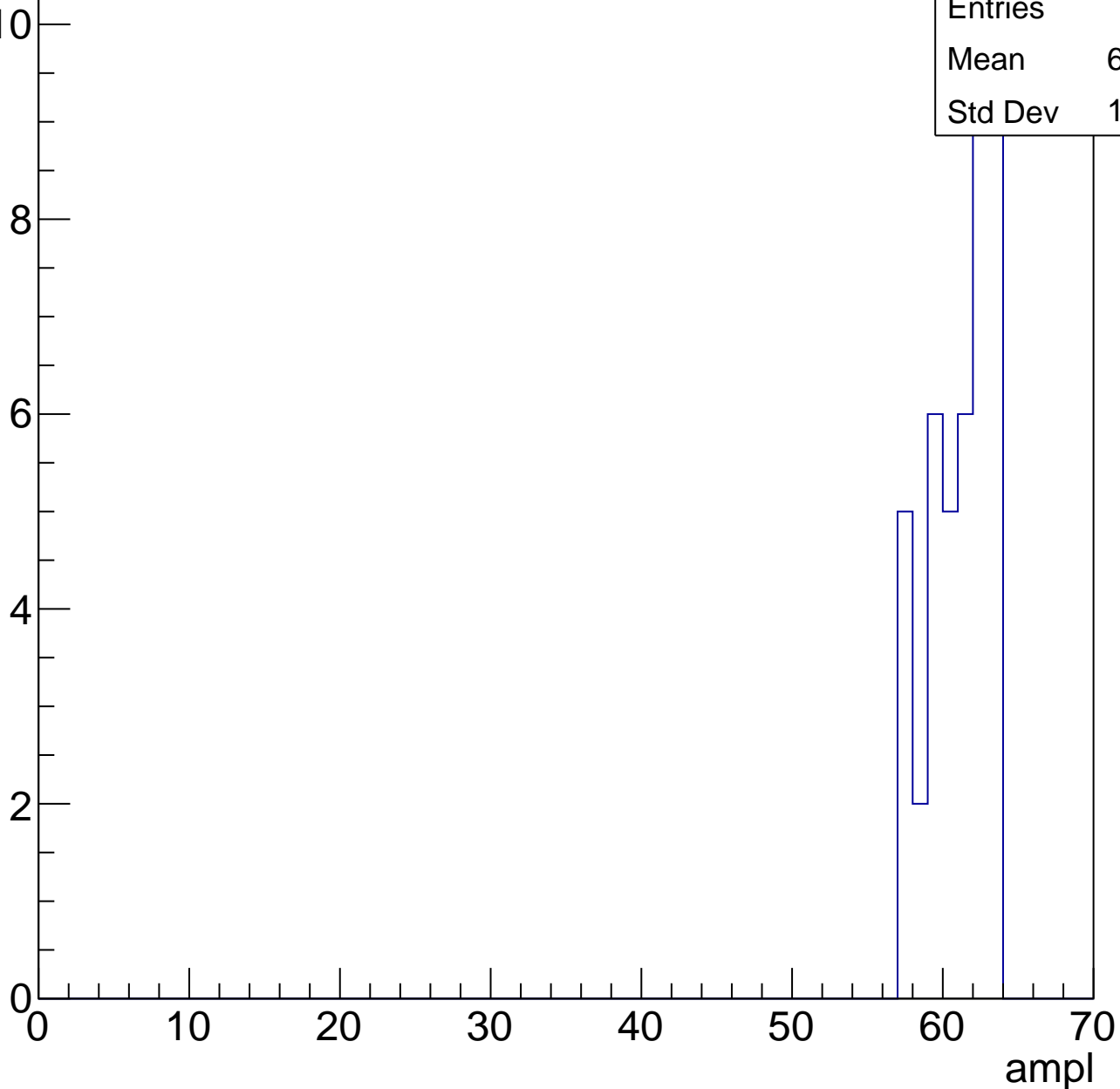


B0L002S, U4-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

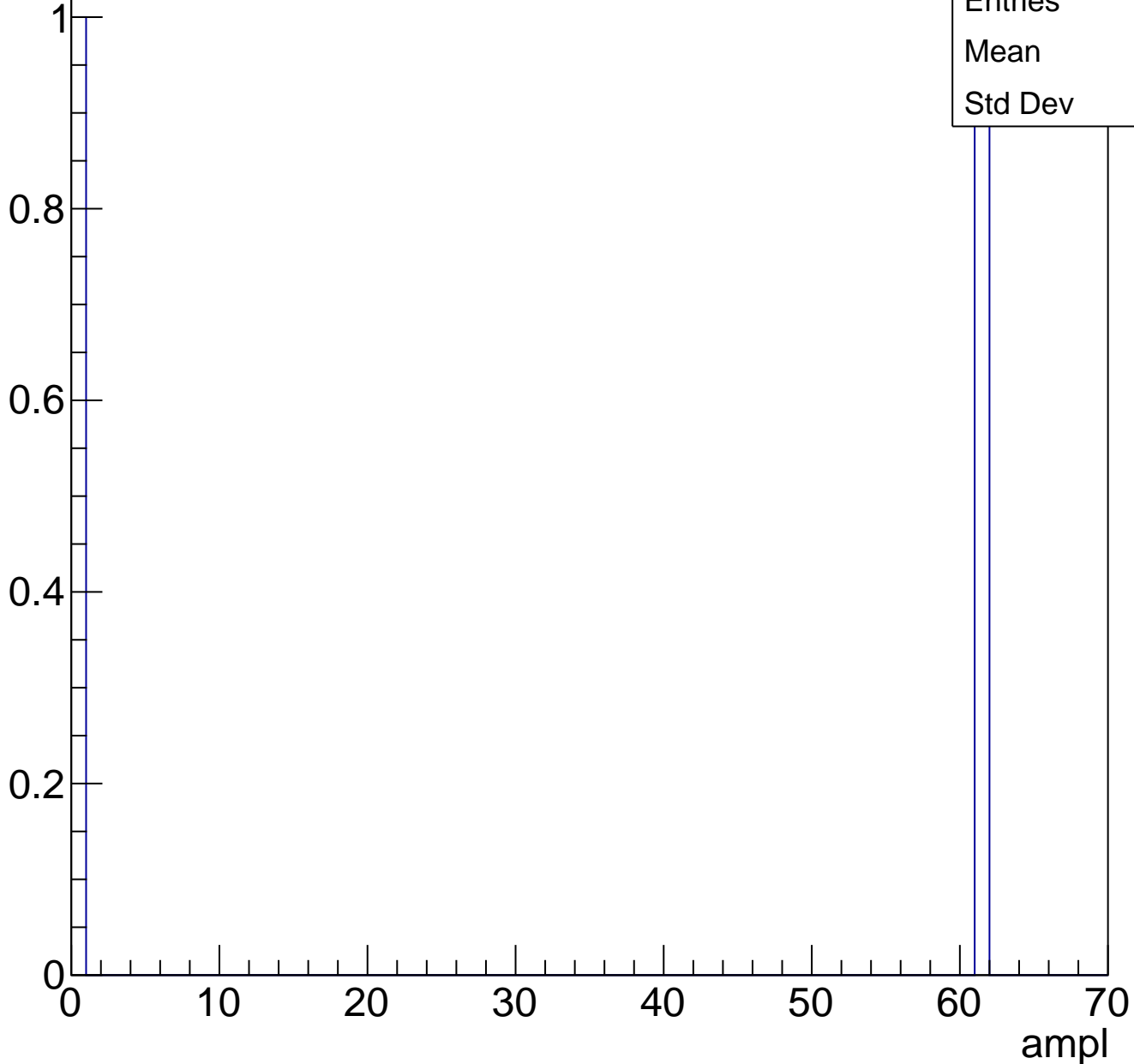
Entries	43
Mean	60.67
Std Dev	1.997



B0L002S, U4-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch52, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	29.15
Std Dev	4.862

Gaus mean : 29.6087

Gaus Width: 3.1097

Entry

10
8
6
4
2
0

ampl

0

10

20

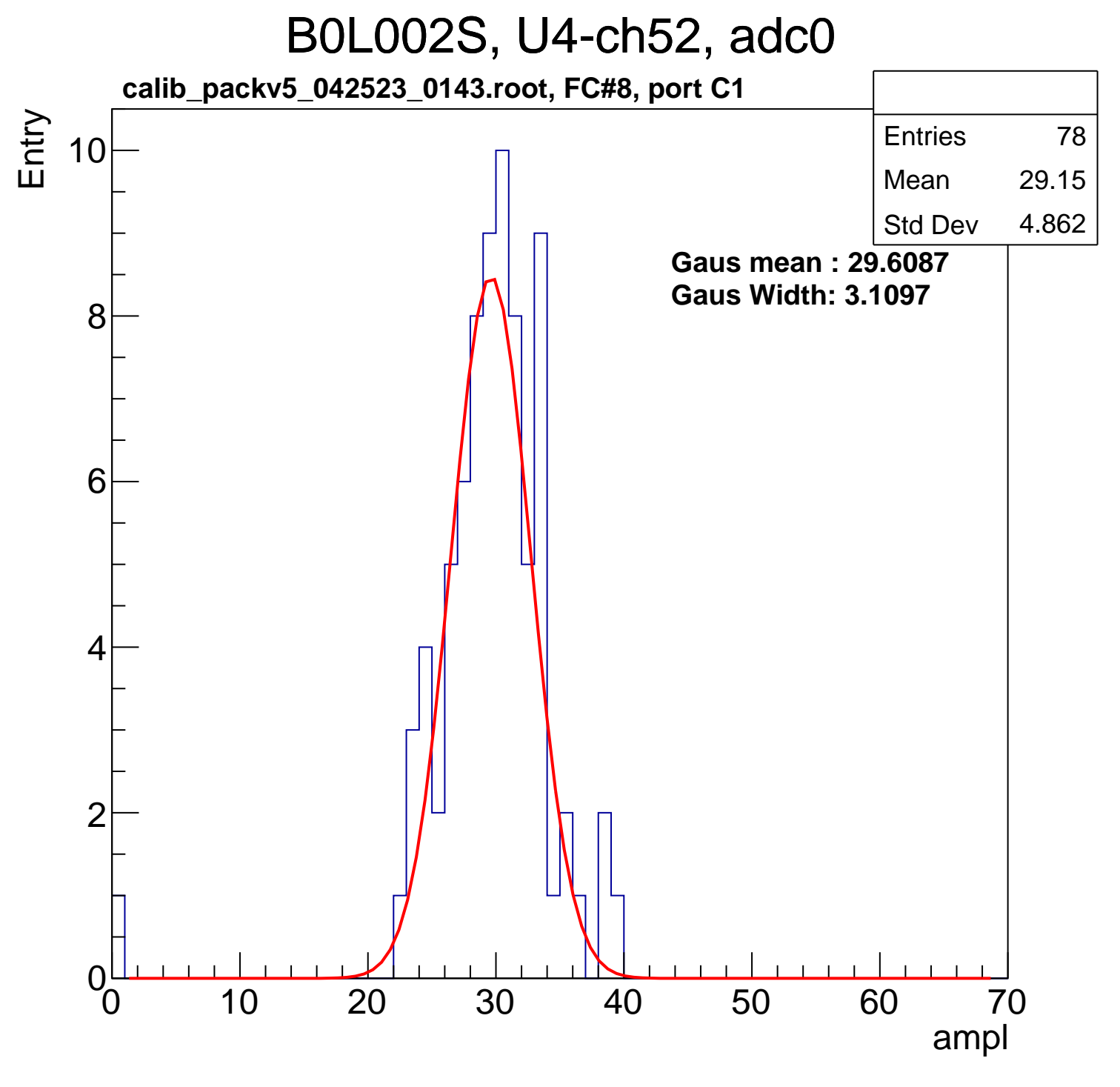
30

40

50

60

70



B0L002S, U4-ch52, adc1

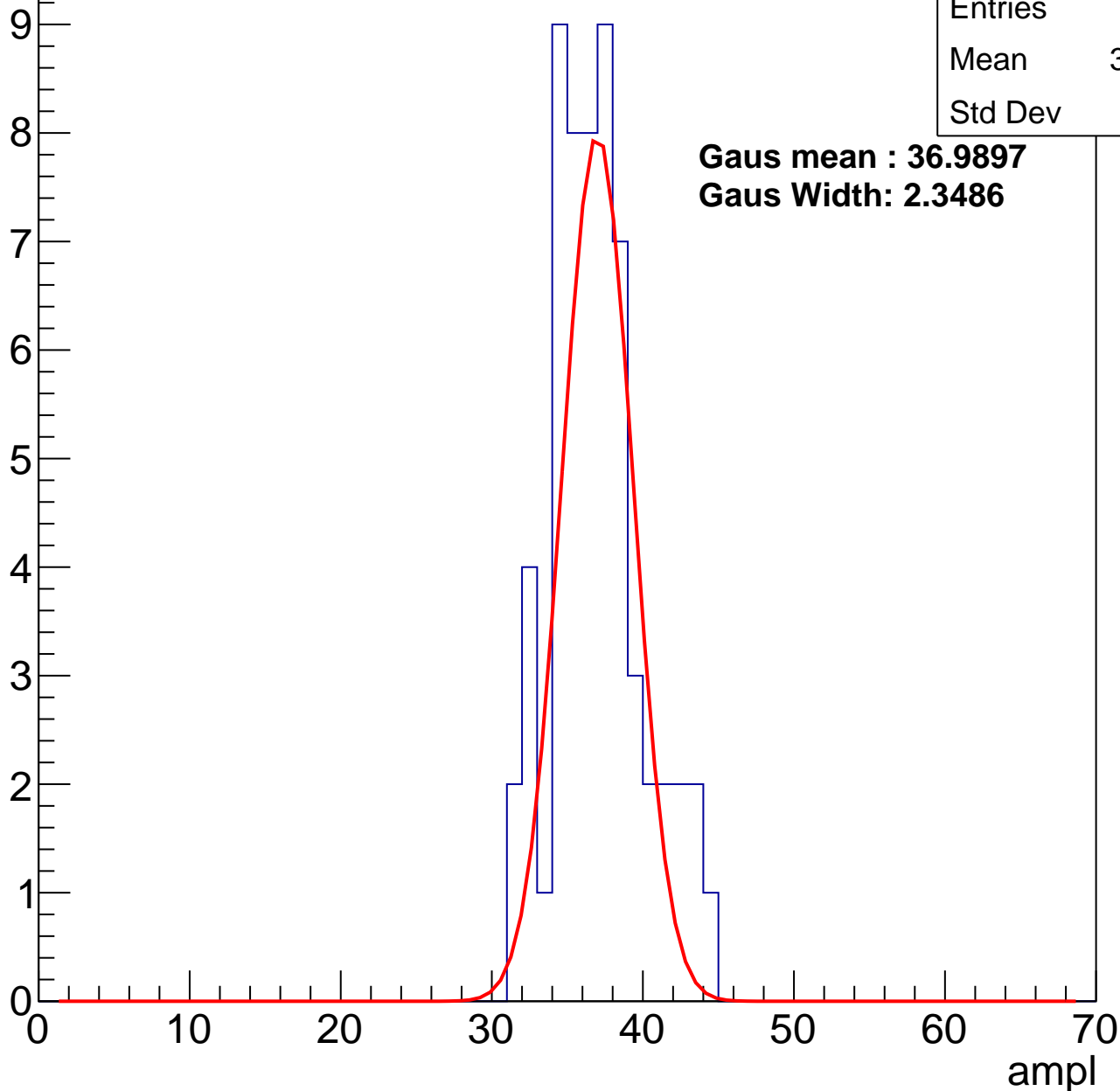
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.48
Std Dev	2.98

Gaus mean : 36.9897

Gaus Width: 2.3486



B0L002S, U4-ch52, adc2

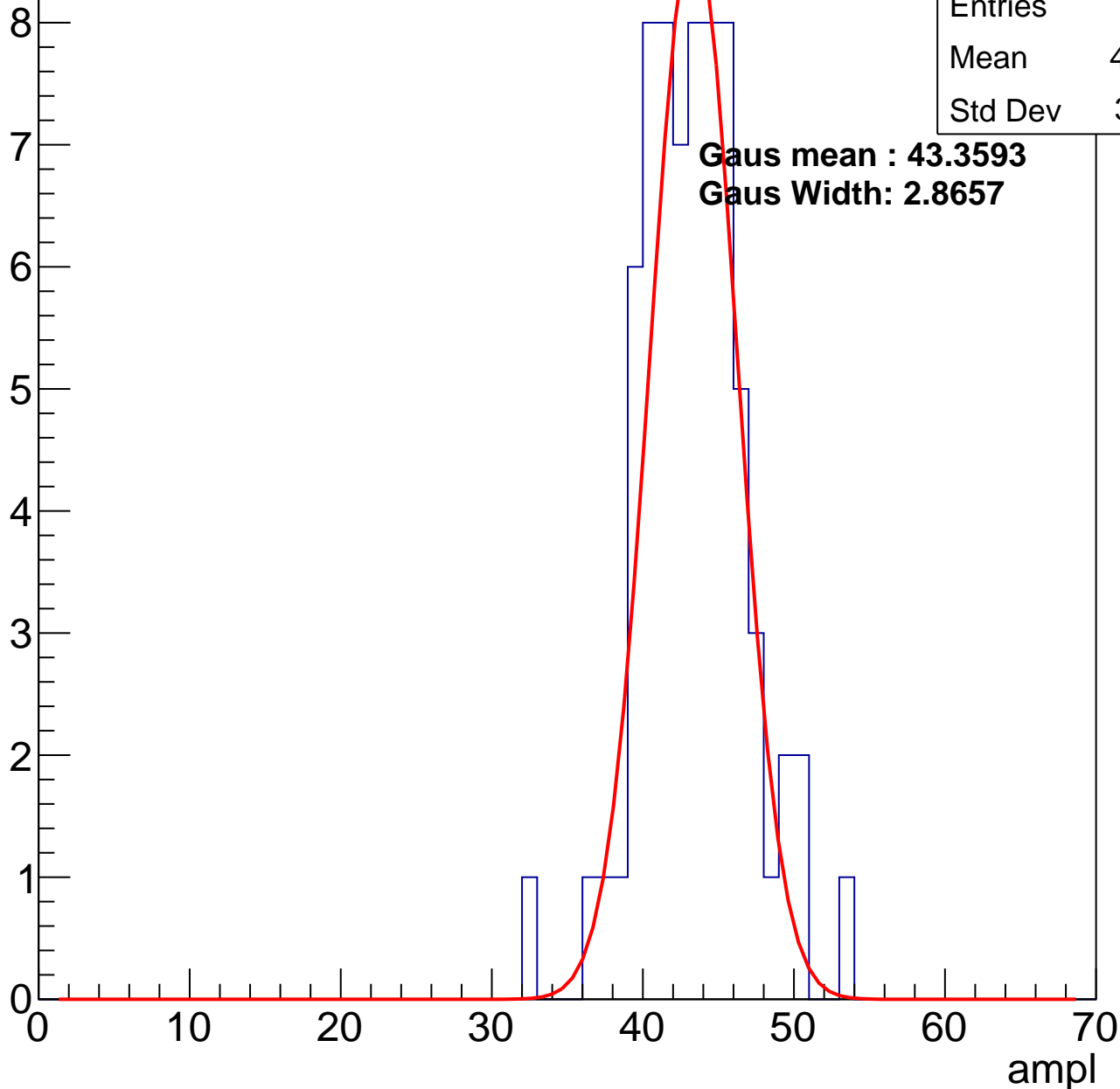
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.89
Std Dev	3.491

Gaus mean : 43.3593

Gaus Width: 2.8657

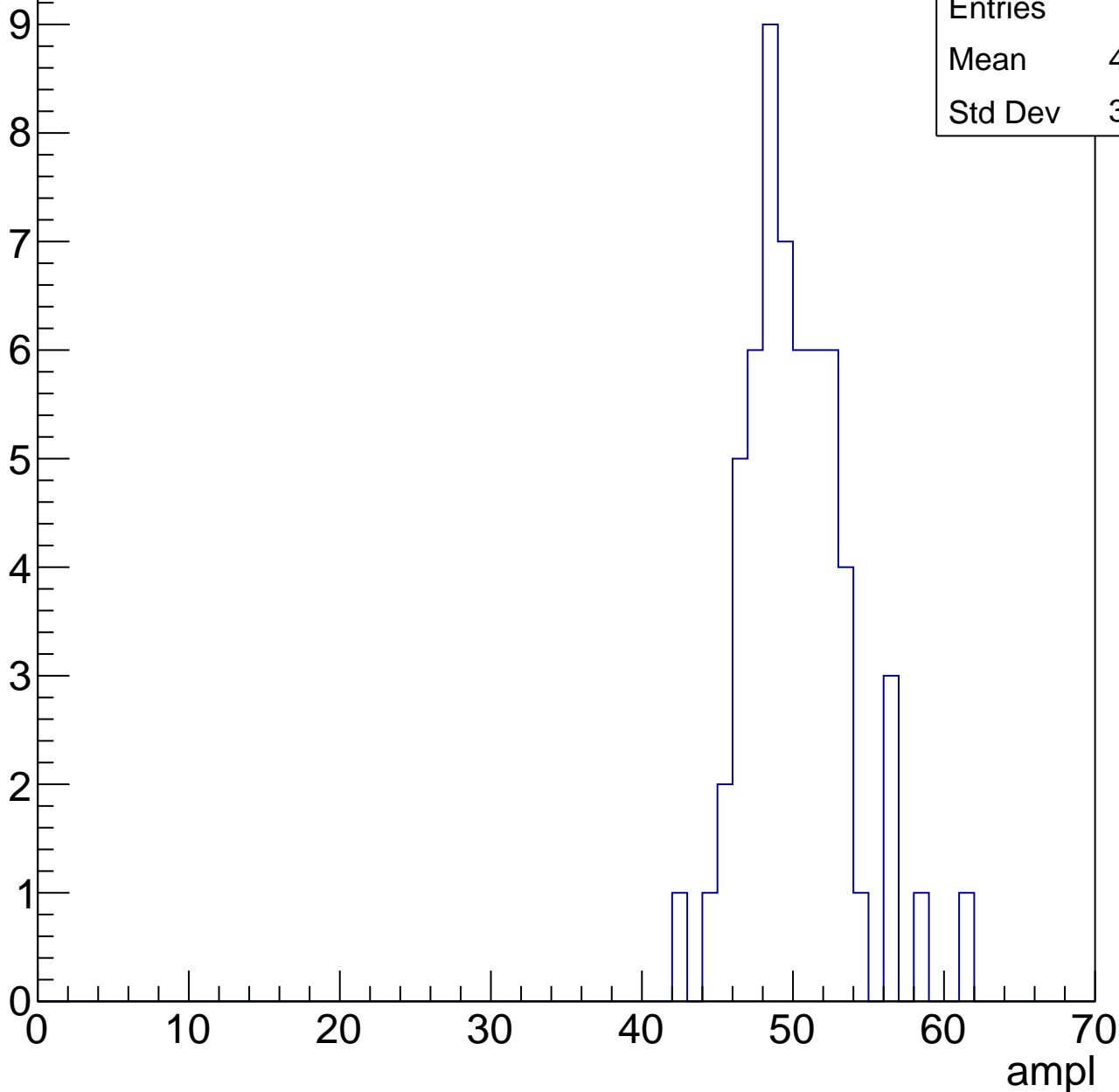


B0L002S, U4-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	49.73
Std Dev	3.439

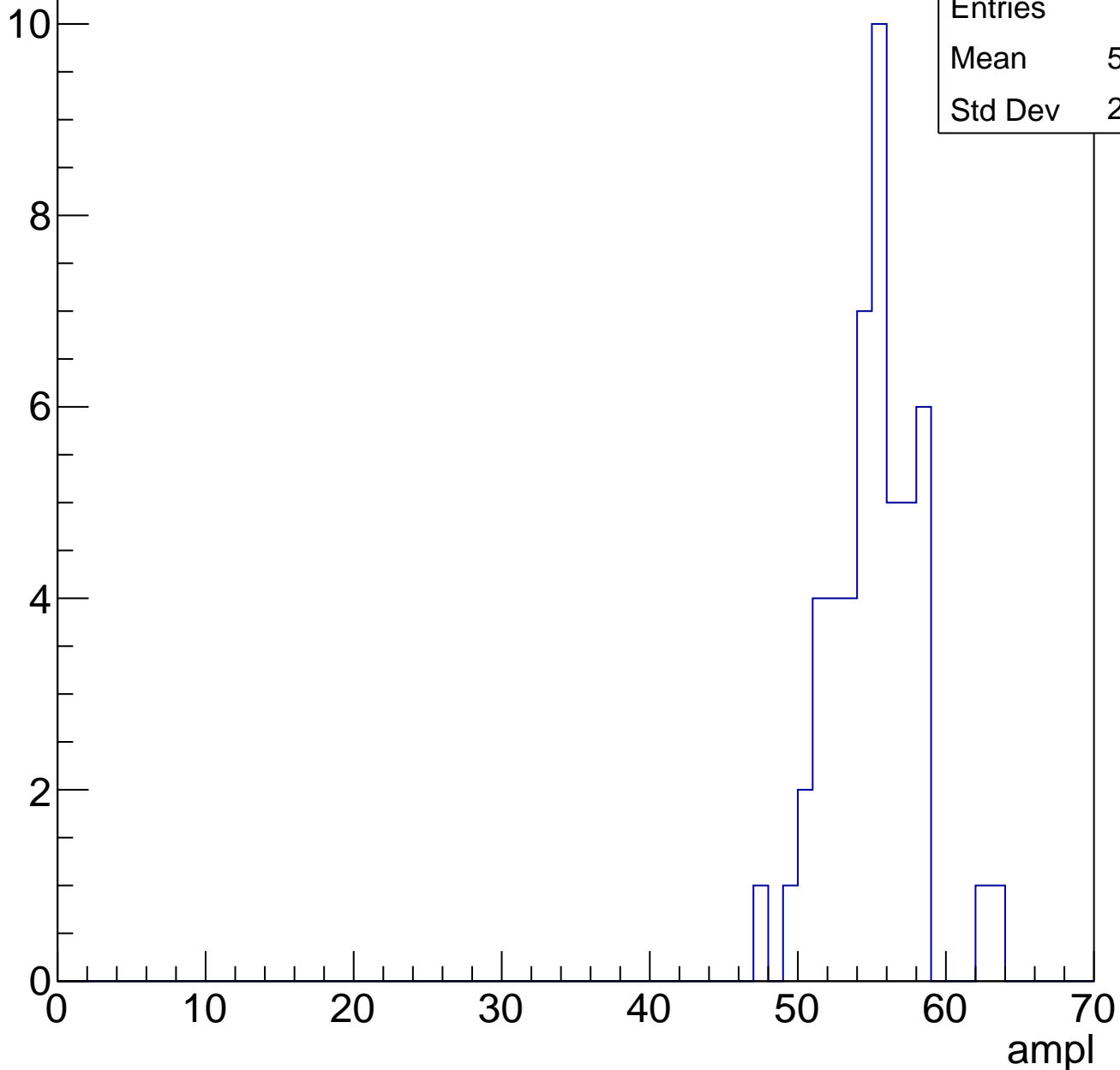


B0L002S, U4-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	51
Mean	54.63
Std Dev	2.996

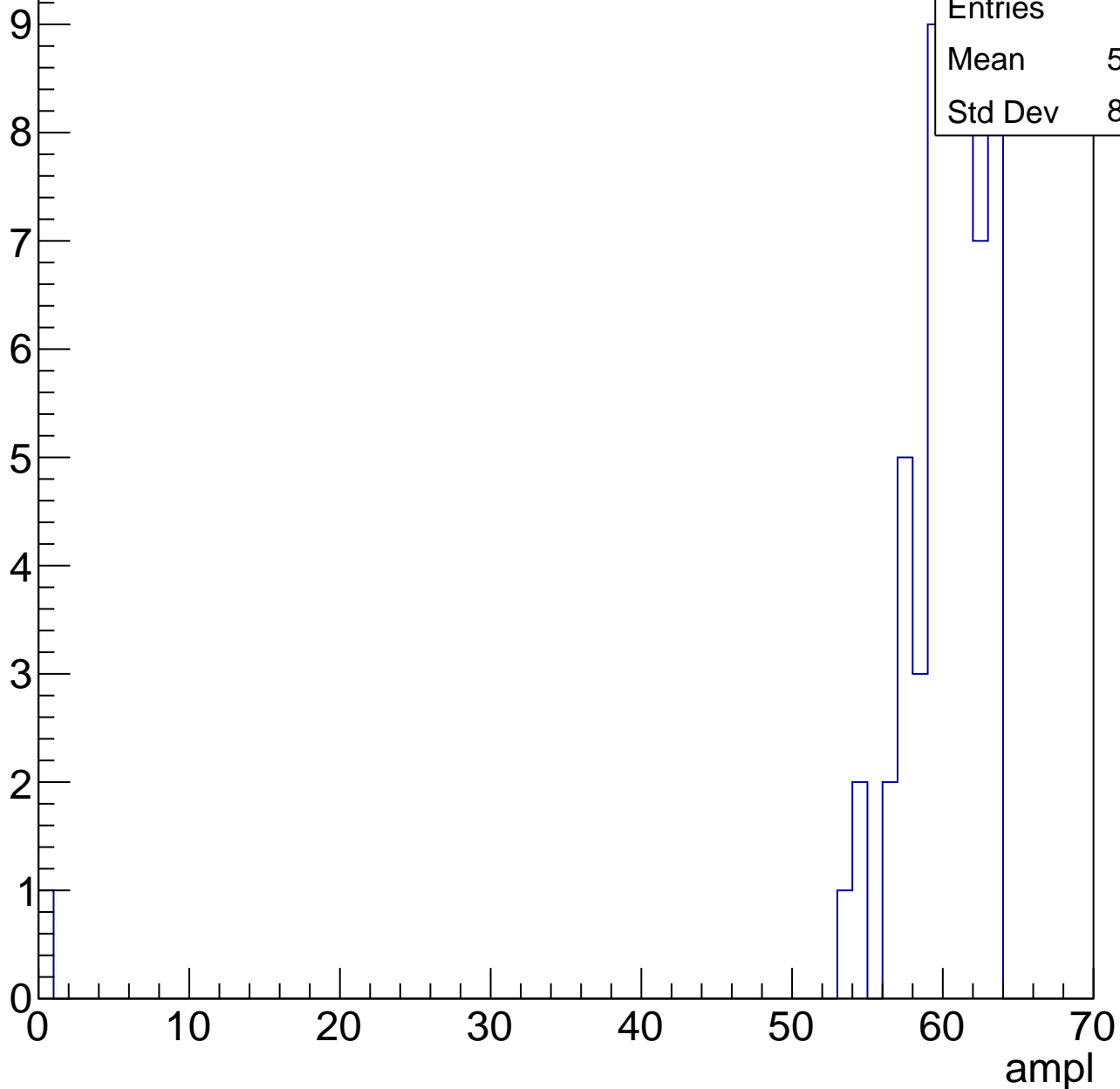
Entry



B0L002S, U4-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

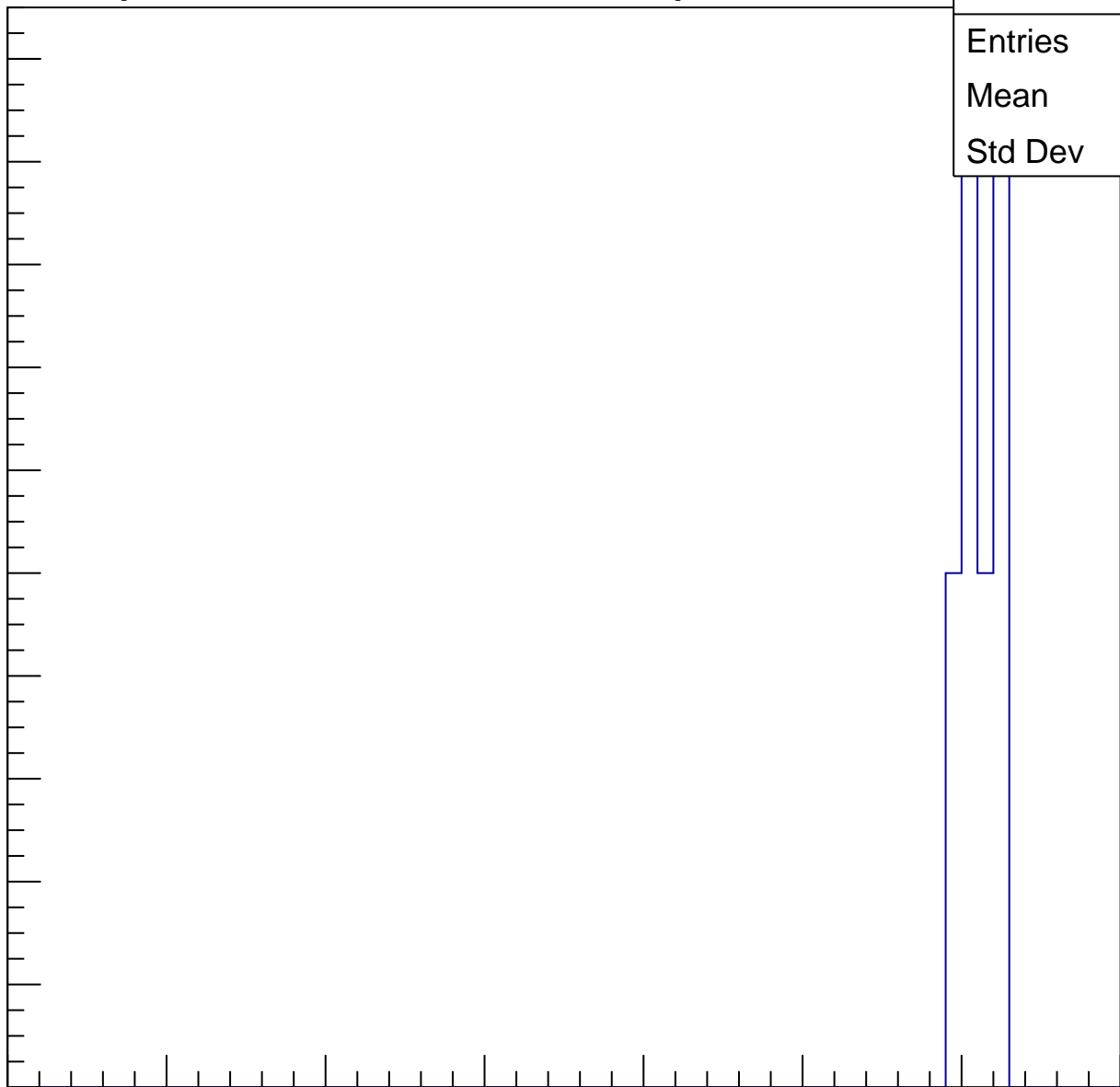
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.67
Std Dev	1.106

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch53, adc0

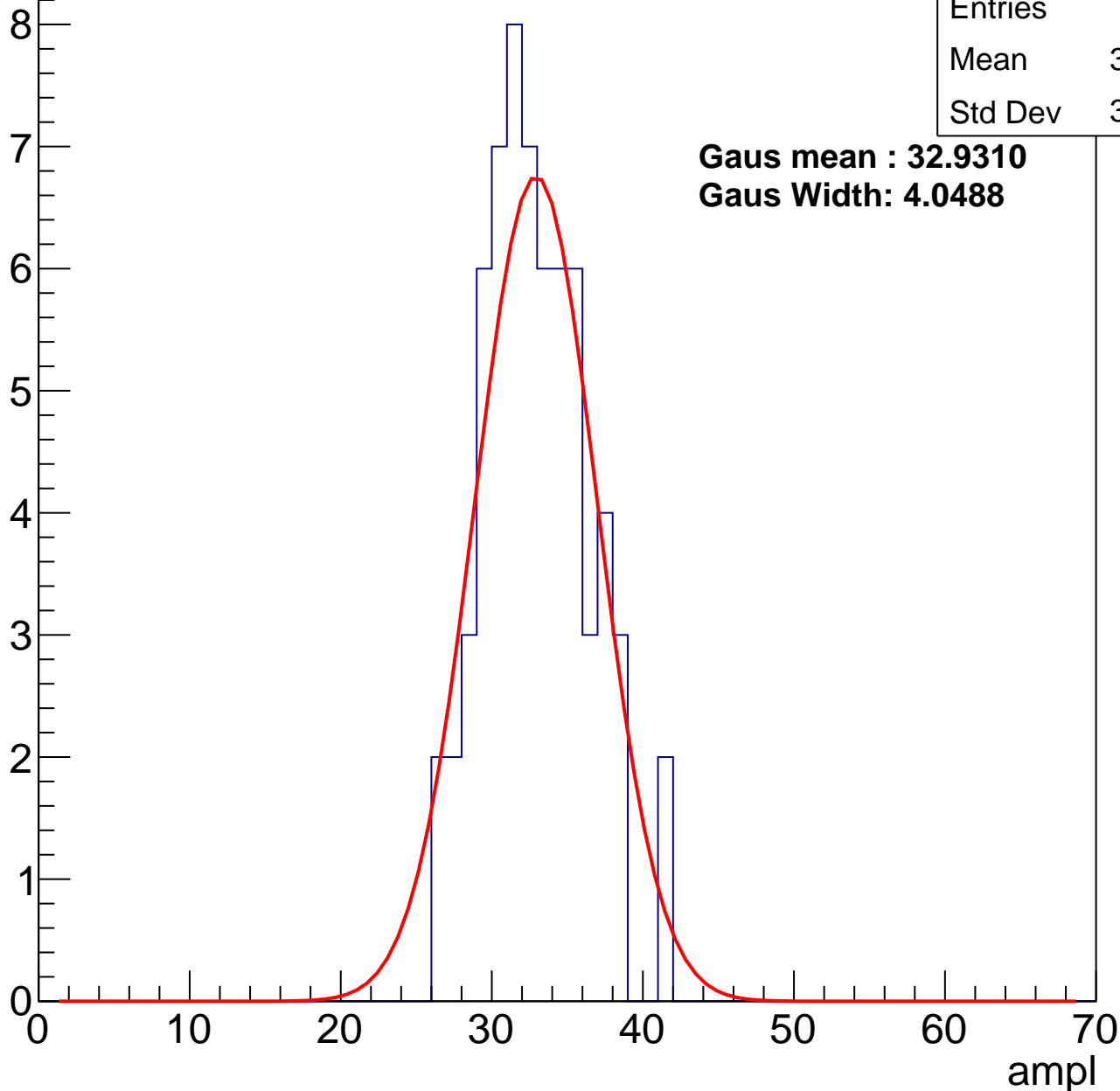
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	32.46
Std Dev	3.393

Gaus mean : 32.9310

Gaus Width: 4.0488



B0L002S, U4-ch53, adc1

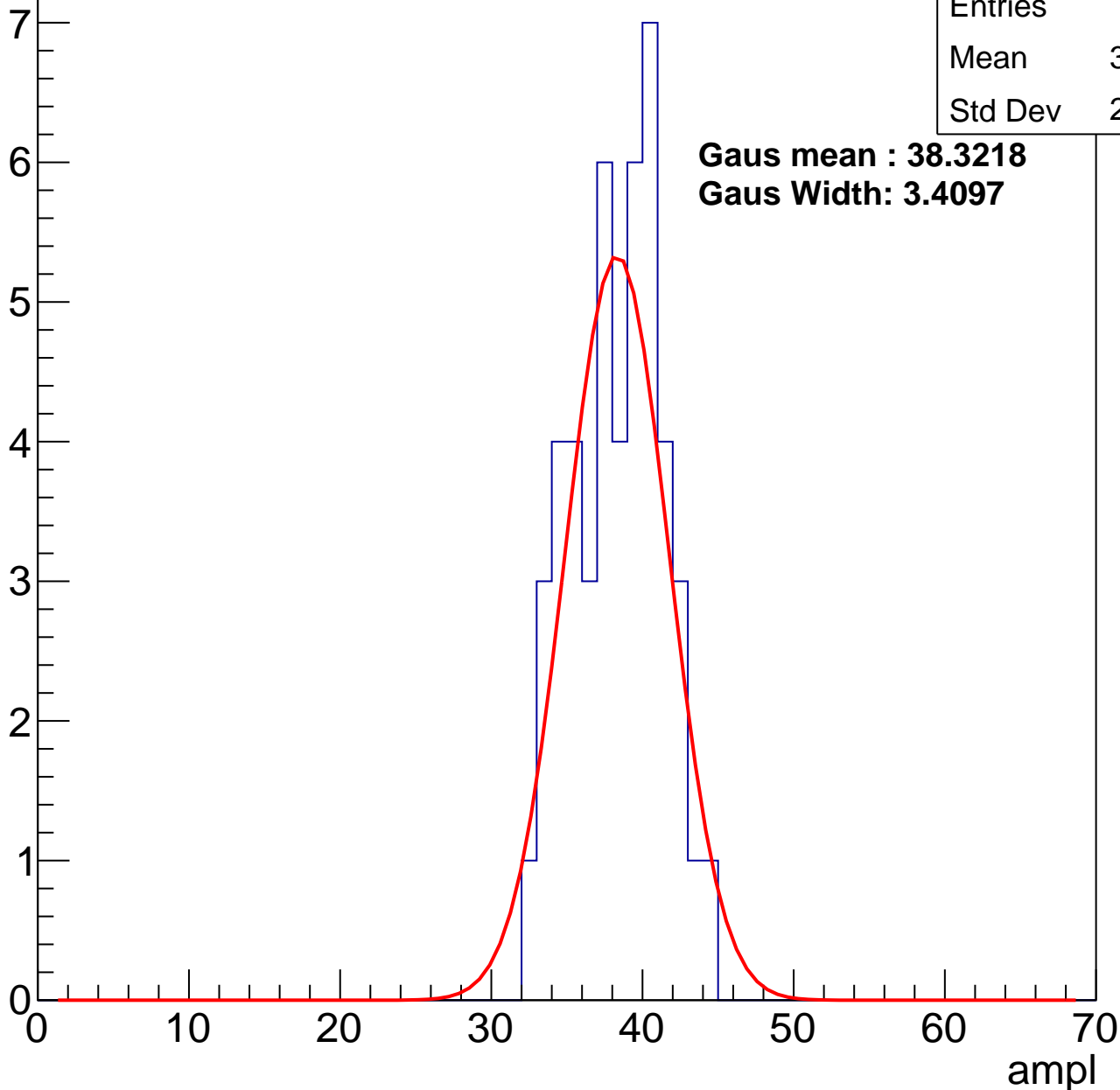
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	37.87
Std Dev	2.944

Gaus mean : 38.3218

Gaus Width: 3.4097



B0L002S, U4-ch53, adc2

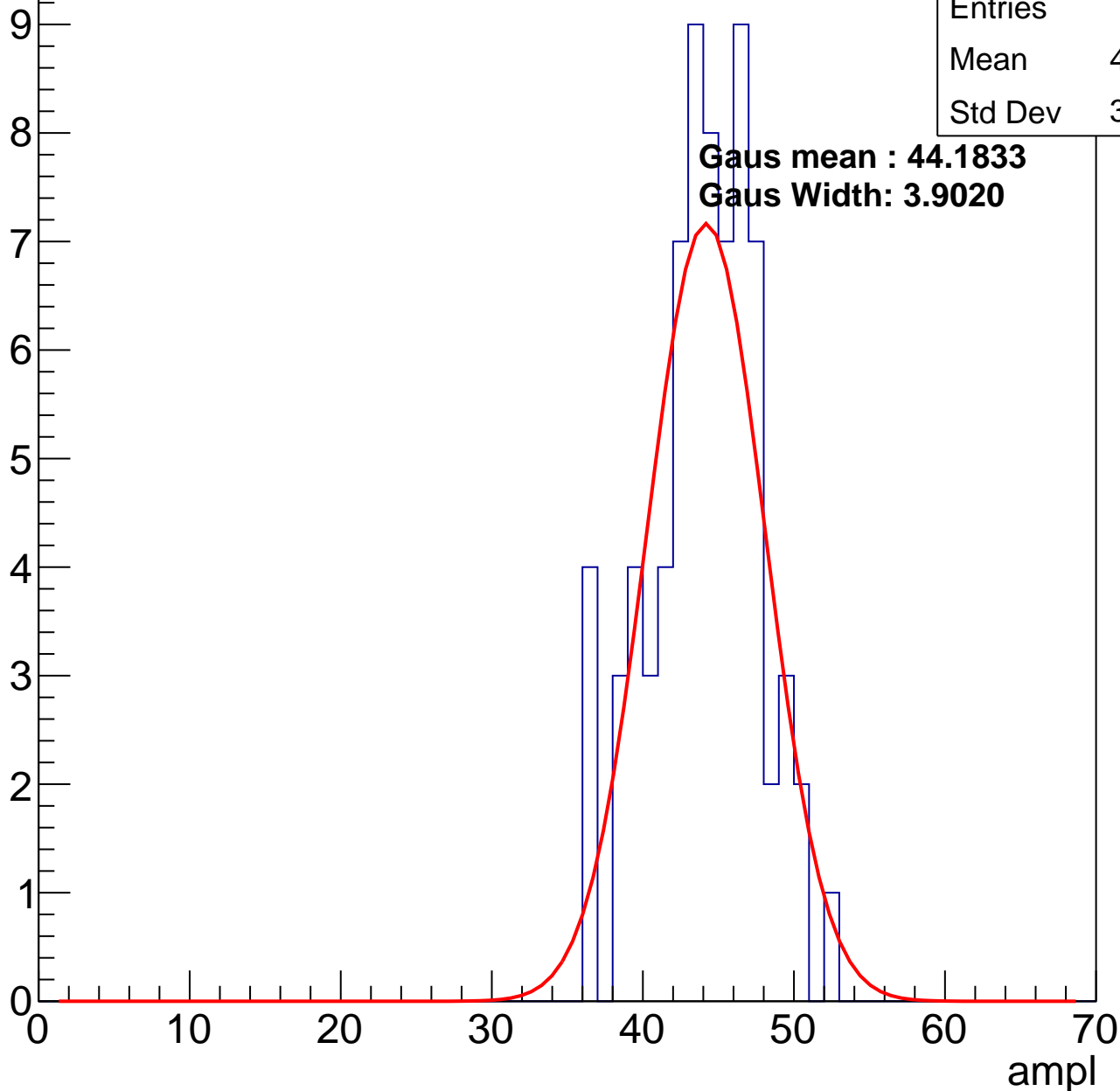
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	43.62
Std Dev	3.556

Gaus mean : 44.1833

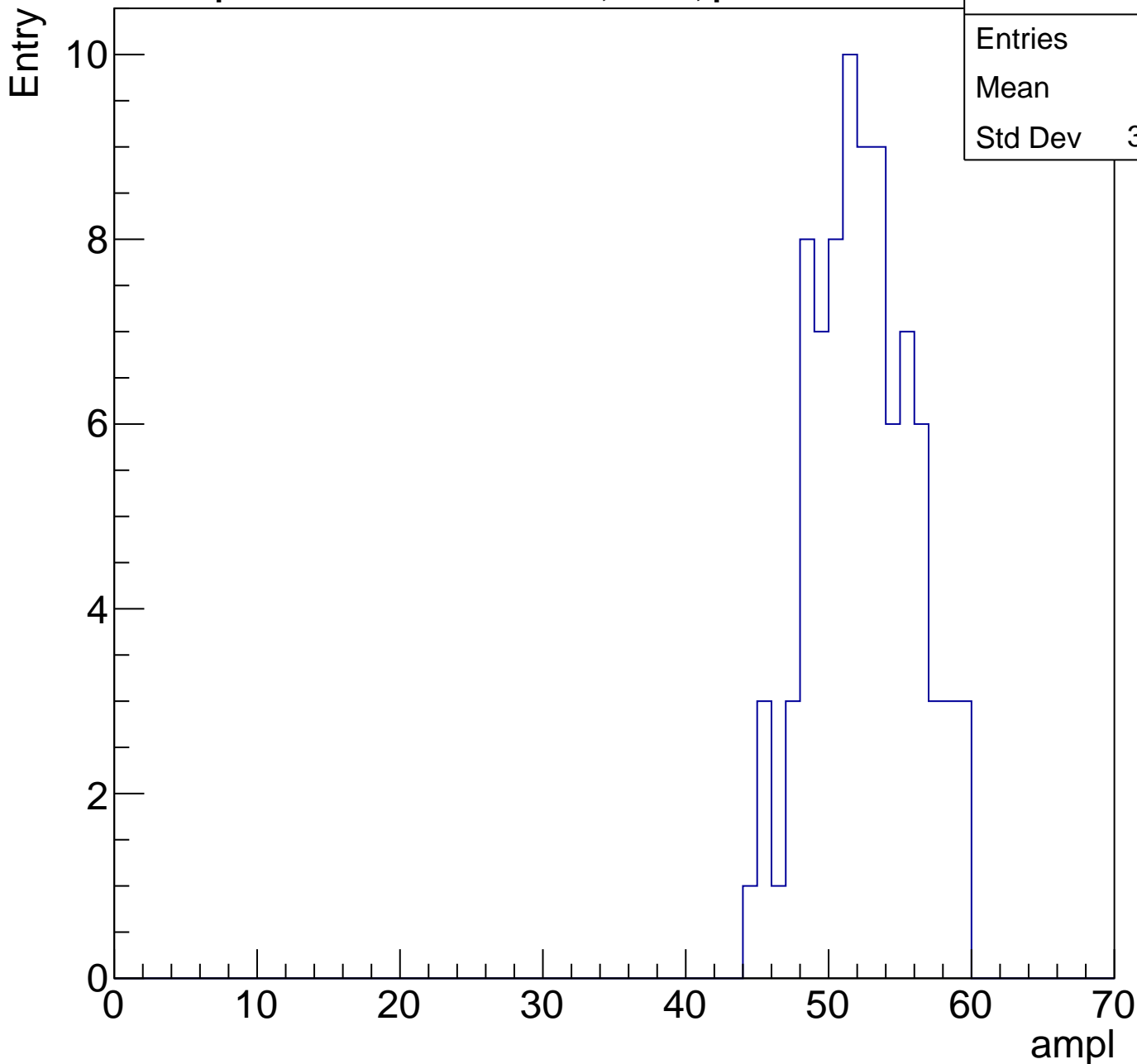
Gaus Width: 3.9020



B0L002S, U4-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

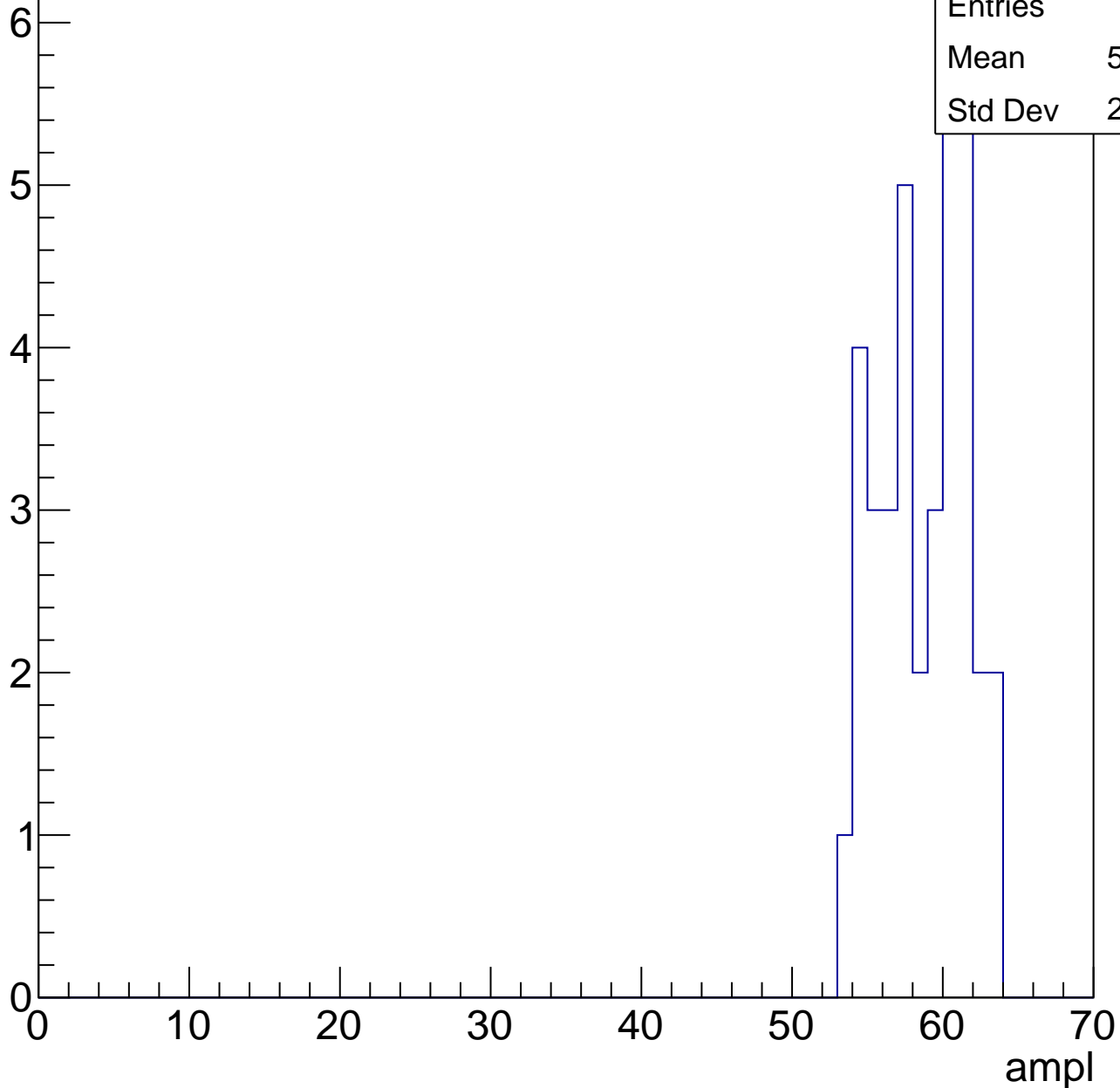
Entries	87
Mean	51.9
Std Dev	3.517



B0L002S, U4-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

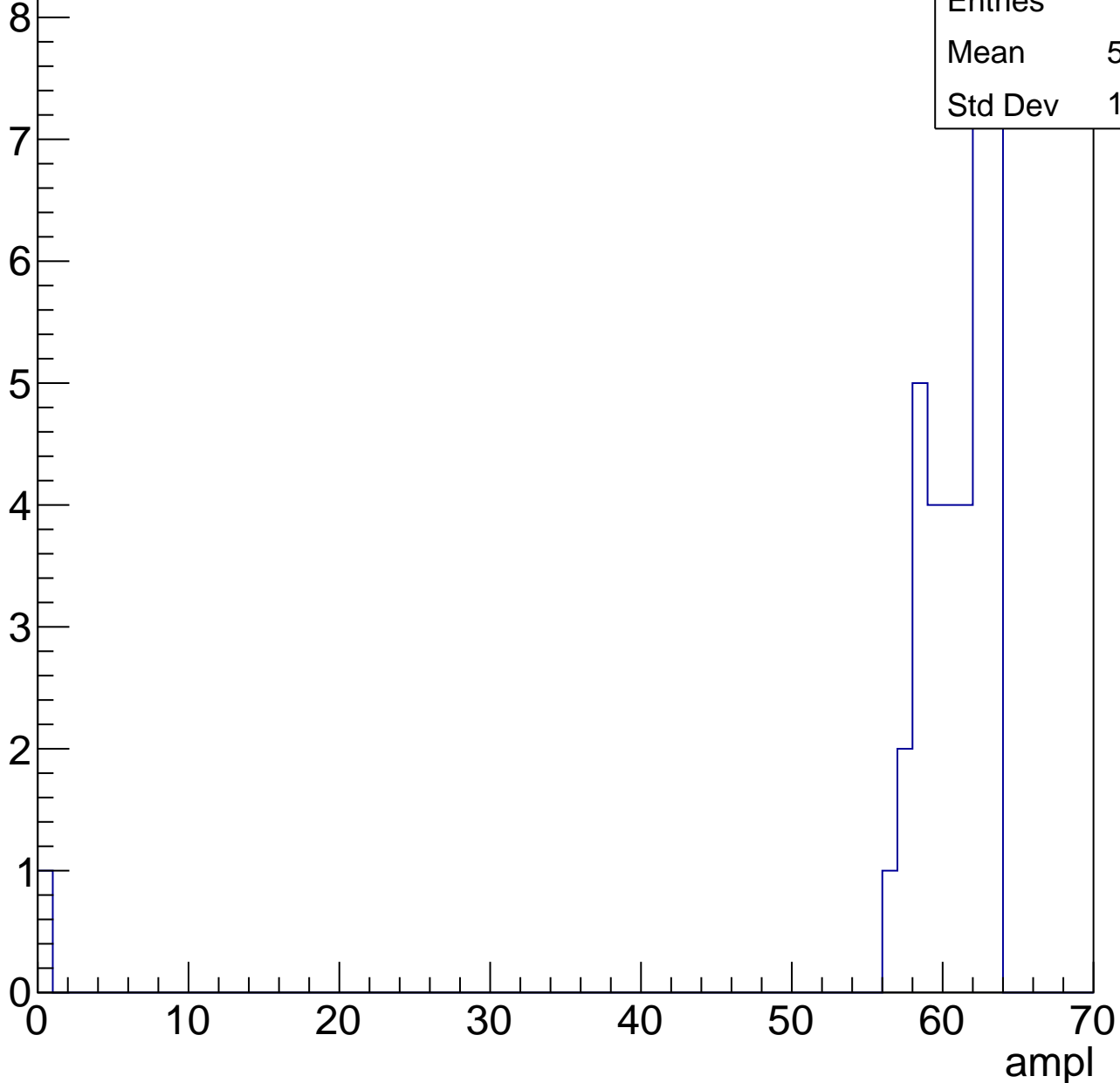


B0L002S, U4-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.92
Std Dev	10.03



B0L002S, U4-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch54, adc0

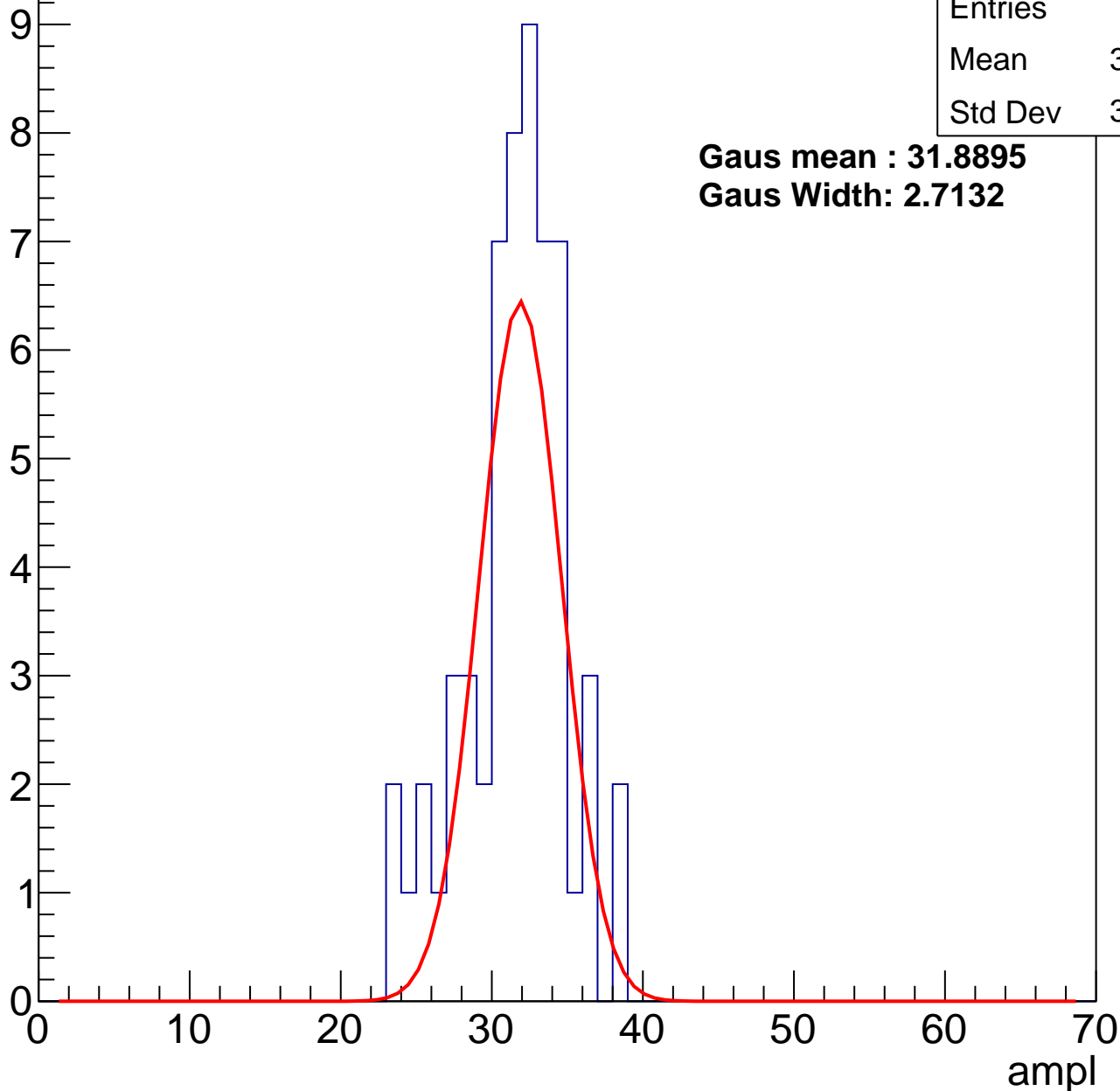
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.09
Std Dev	3.354

Gaus mean : 31.8895

Gaus Width: 2.7132



B0L002S, U4-ch54, adc1

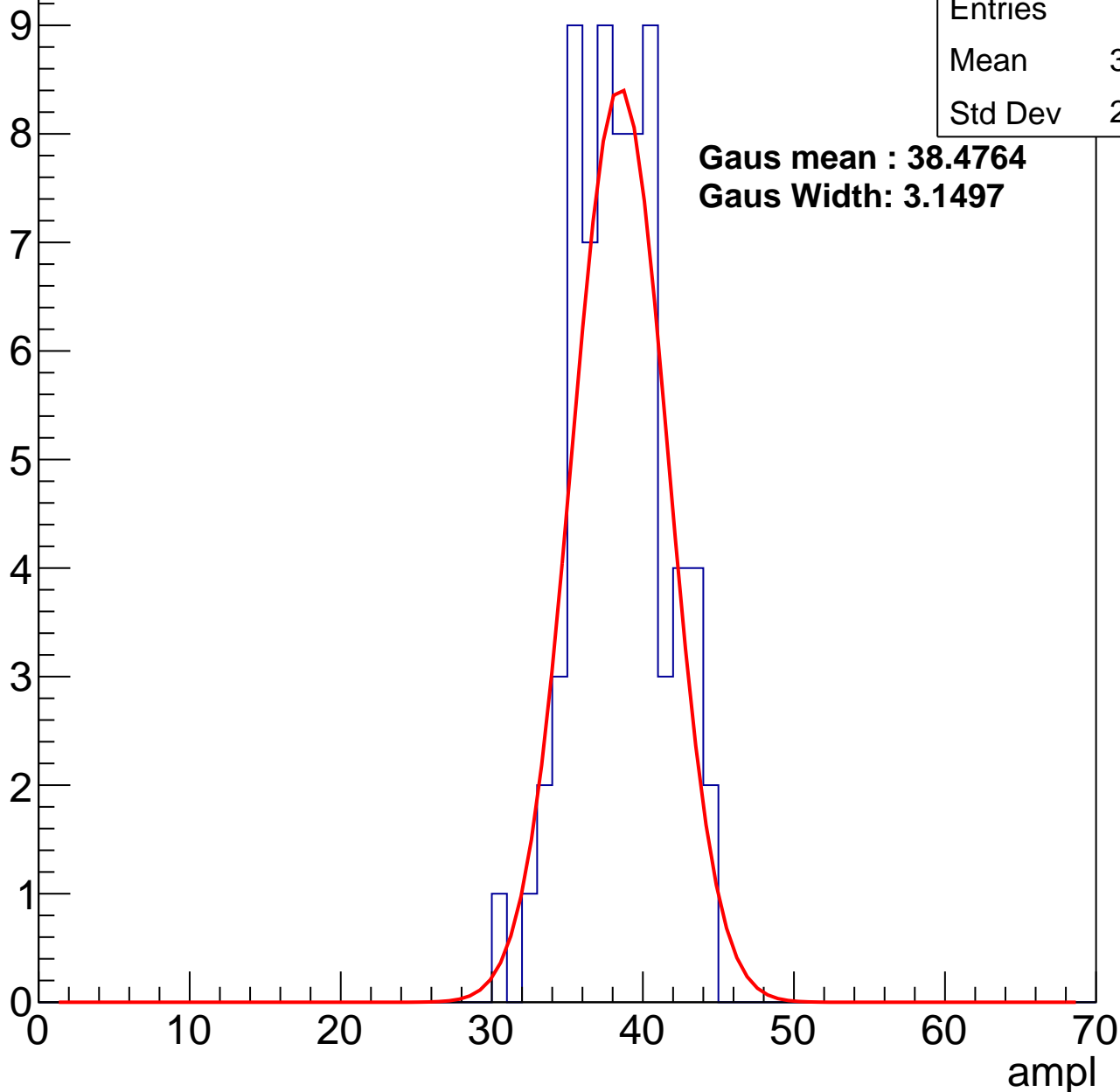
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	37.96
Std Dev	2.983

Gaus mean : 38.4764

Gaus Width: 3.1497



B0L002S, U4-ch54, adc2

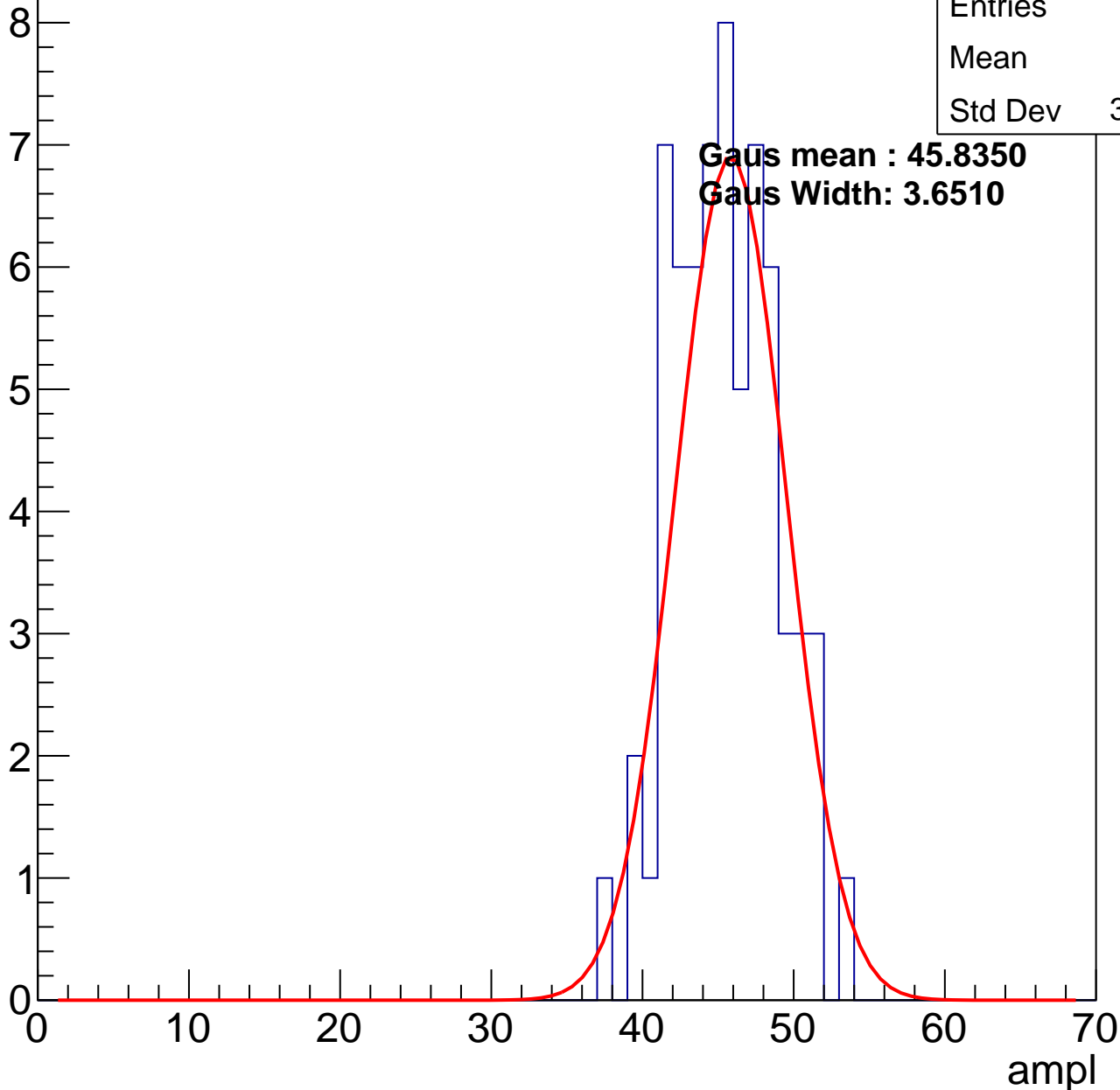
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45
Std Dev	3.348

Gaus mean : 45.8350

Gaus Width: 3.6510



B0L002S, U4-ch54, adc3

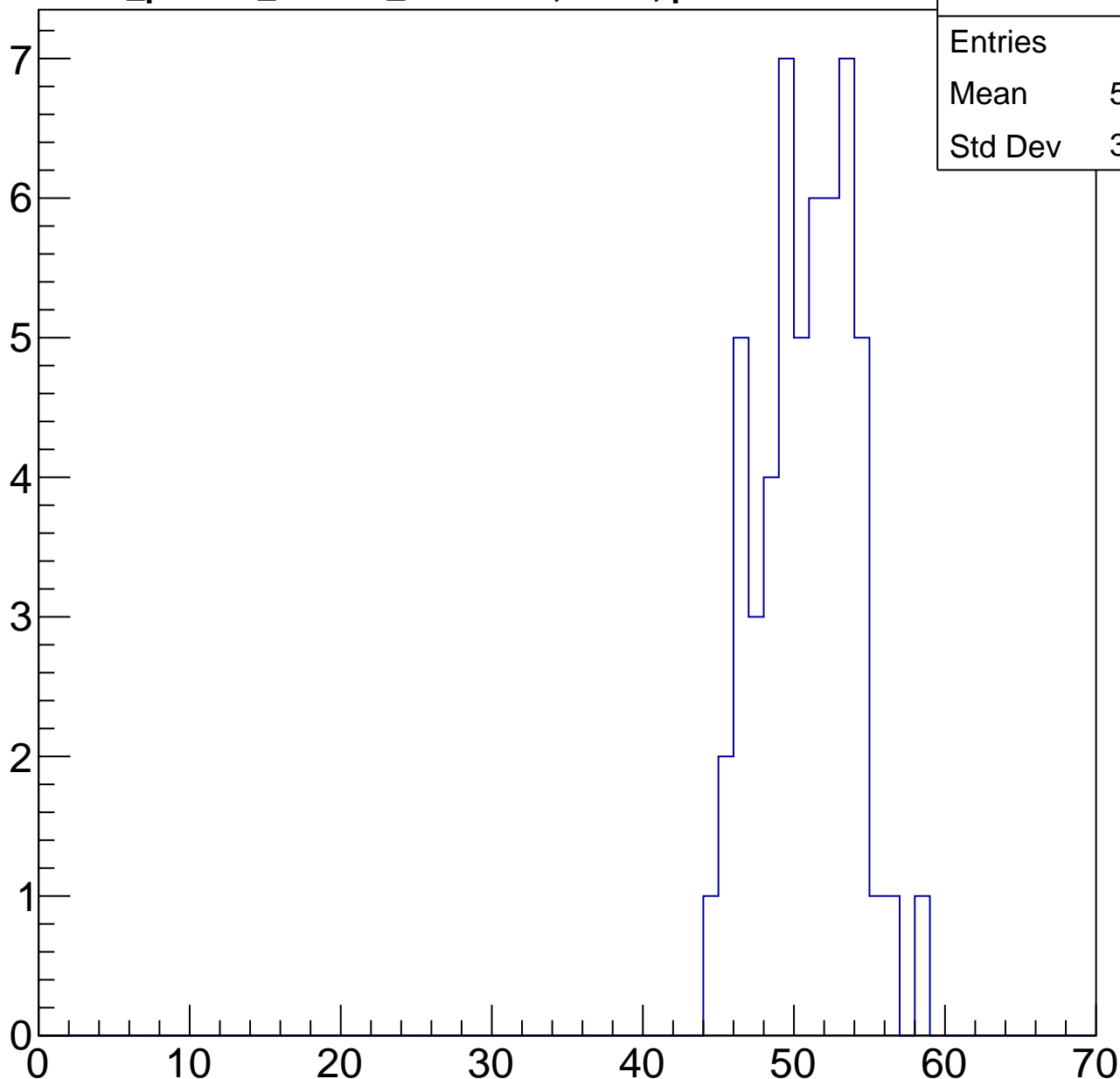
calib_packv5_042523_0143.root, FC#8, port C1

Entry

7
6
5
4
3
2
1
0

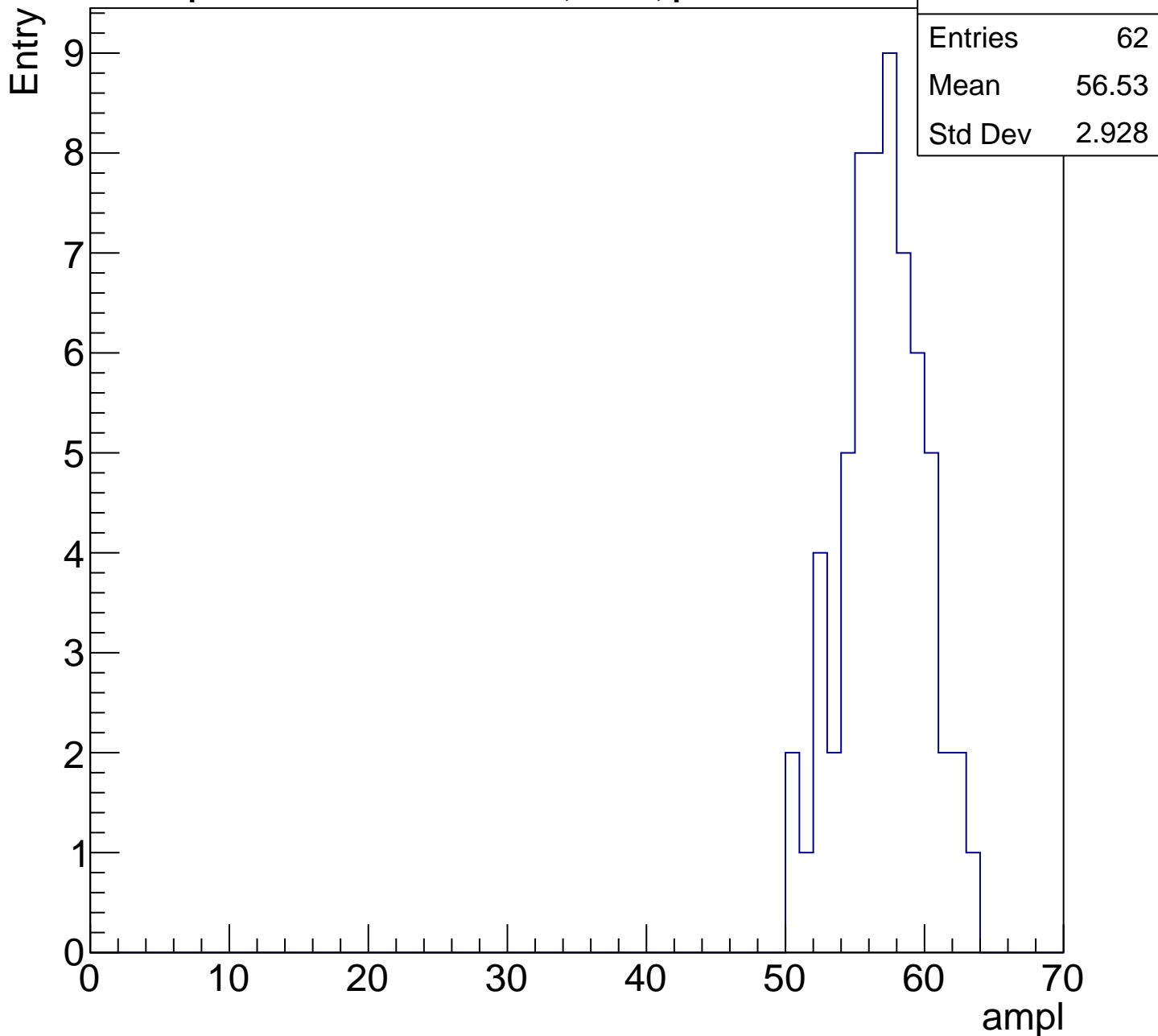
Entries	54
Mean	50.33
Std Dev	3.067

ampl



B0L002S, U4-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

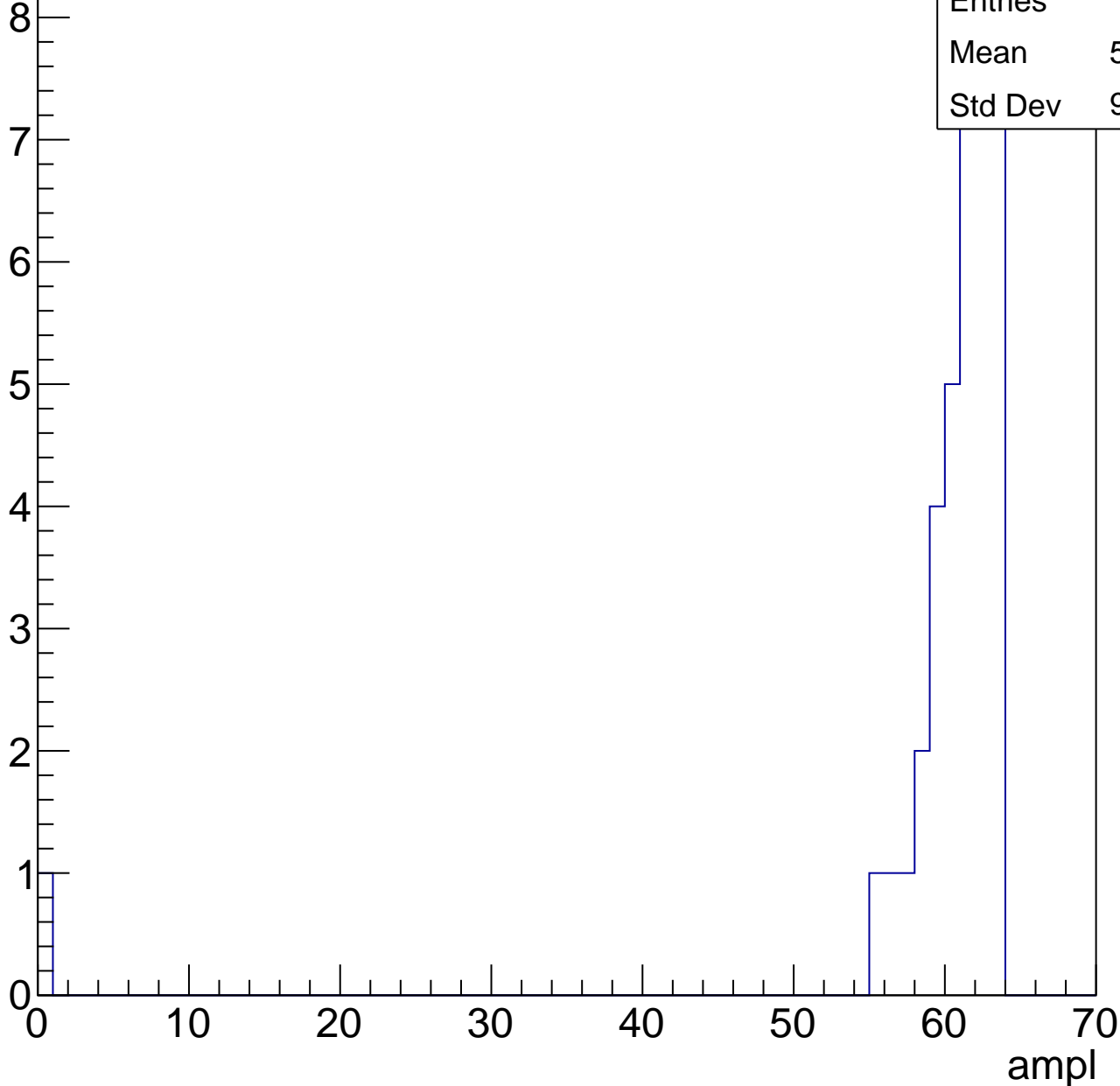


B0L002S, U4-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	59.18
Std Dev	9.803



B0L002S, U4-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U4-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch55, adc0

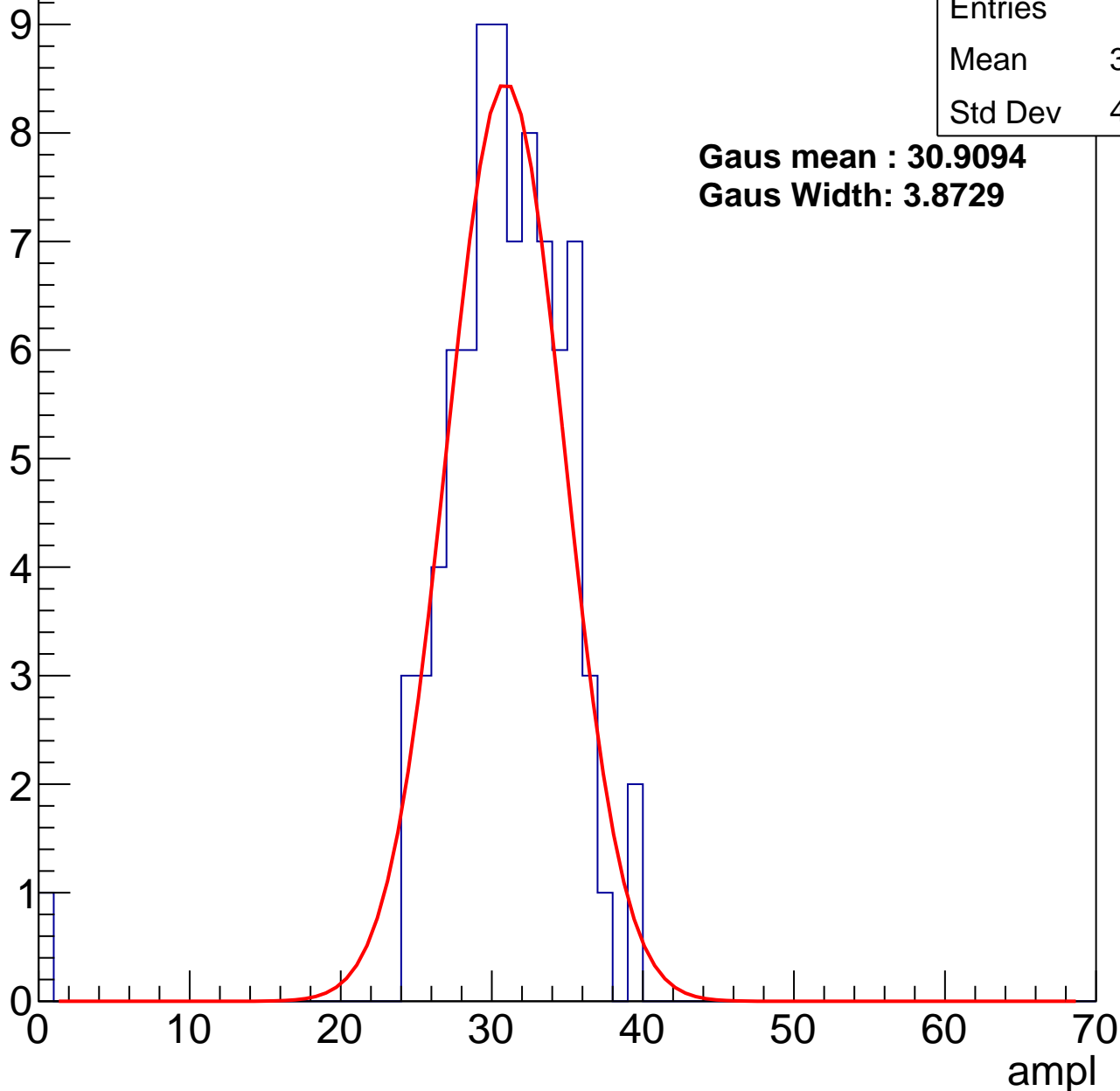
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	30.34
Std Dev	4.834

Gaus mean : 30.9094

Gaus Width: 3.8729



B0L002S, U4-ch55, adc1

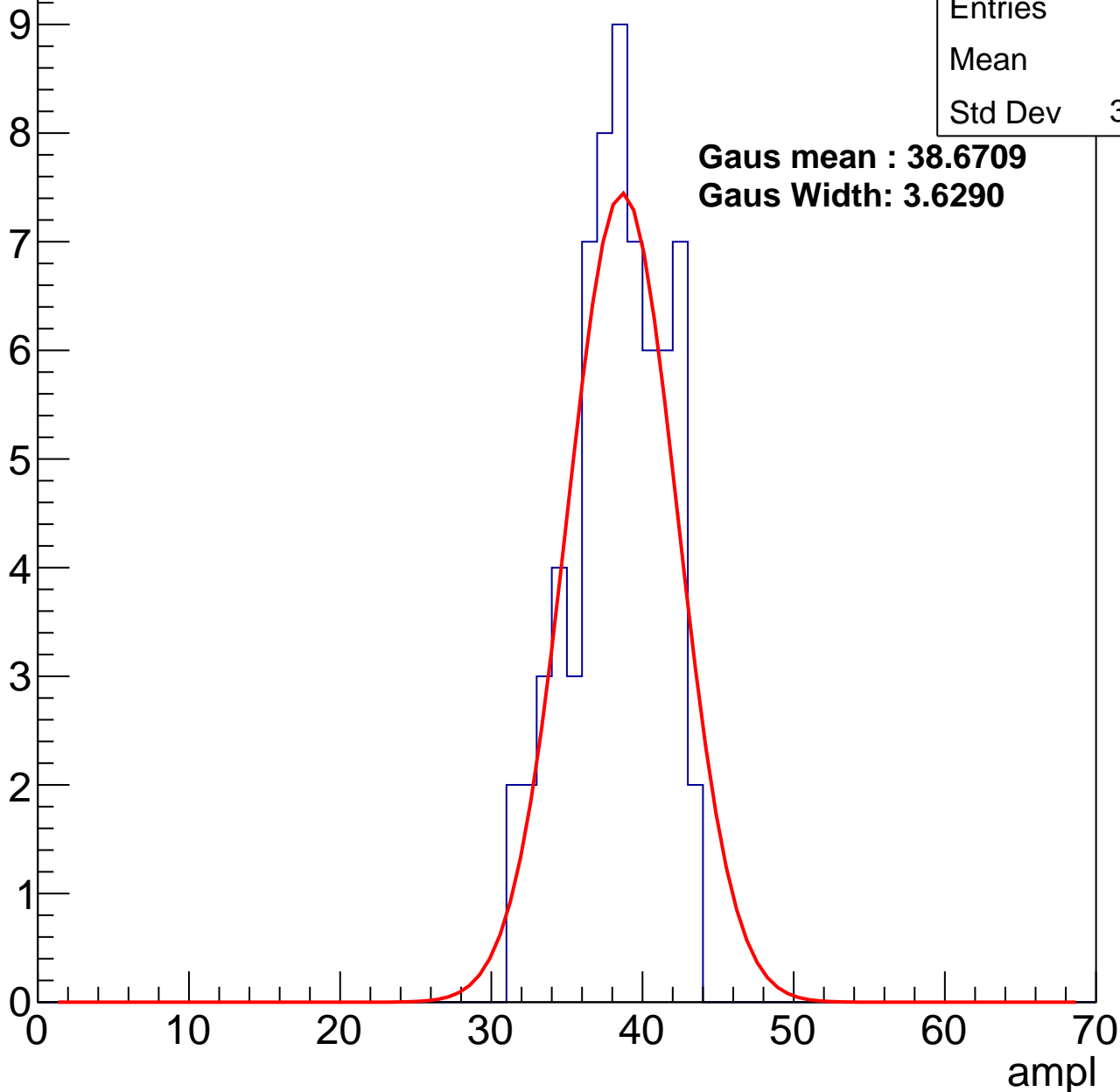
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.8
Std Dev	3.056

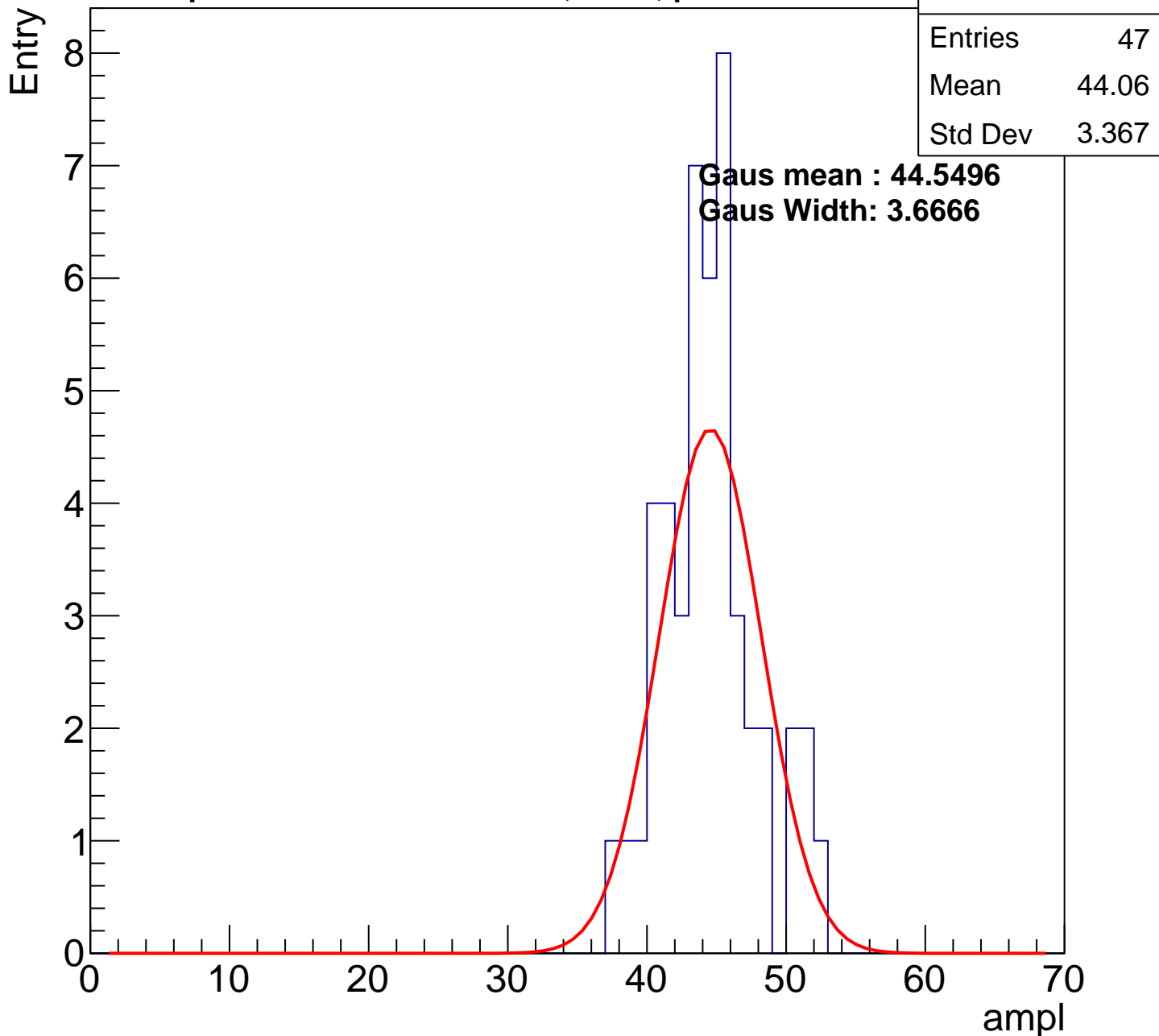
Gaus mean : 38.6709

Gaus Width: 3.6290



B0L002S, U4-ch55, adc2

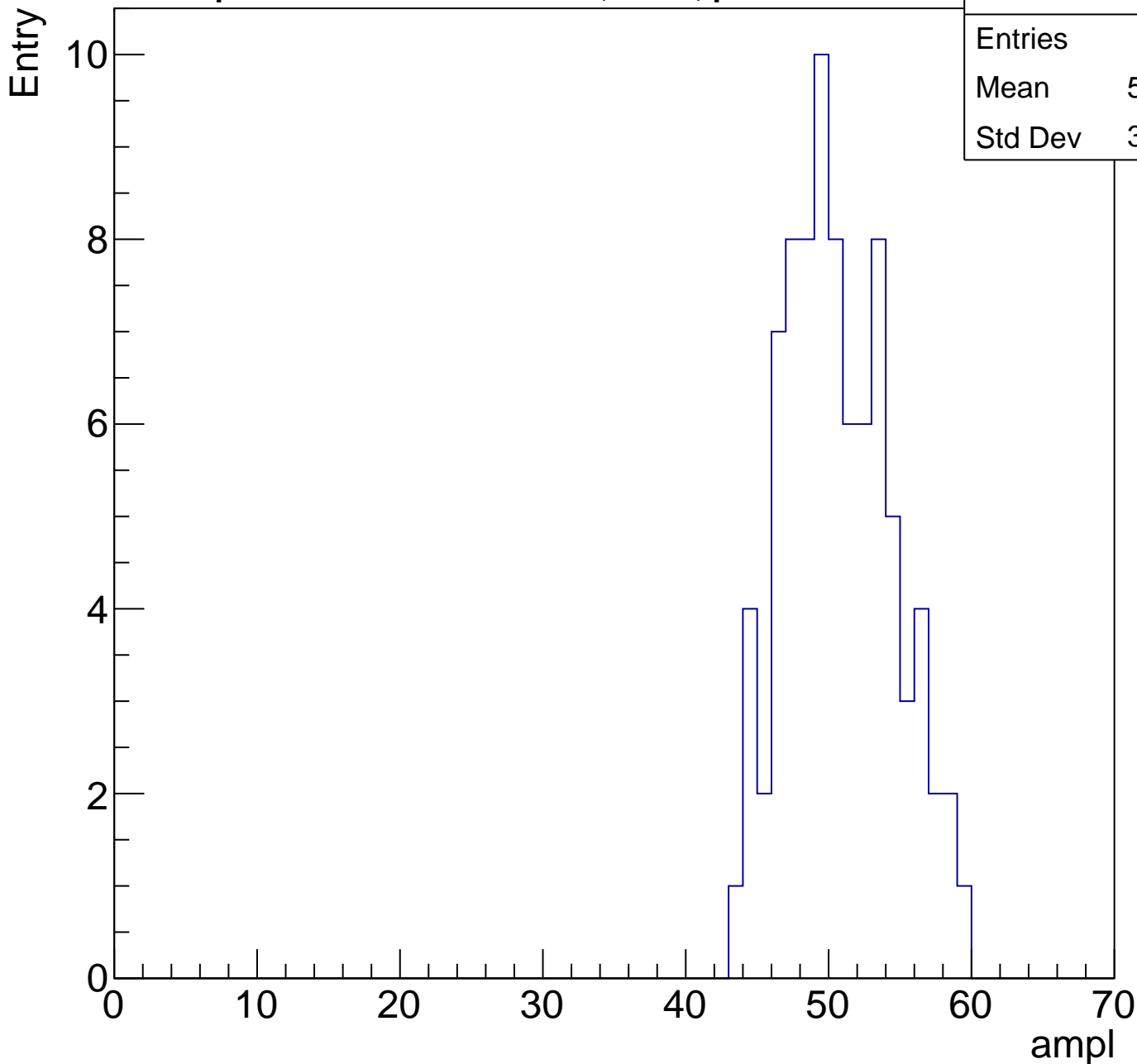
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

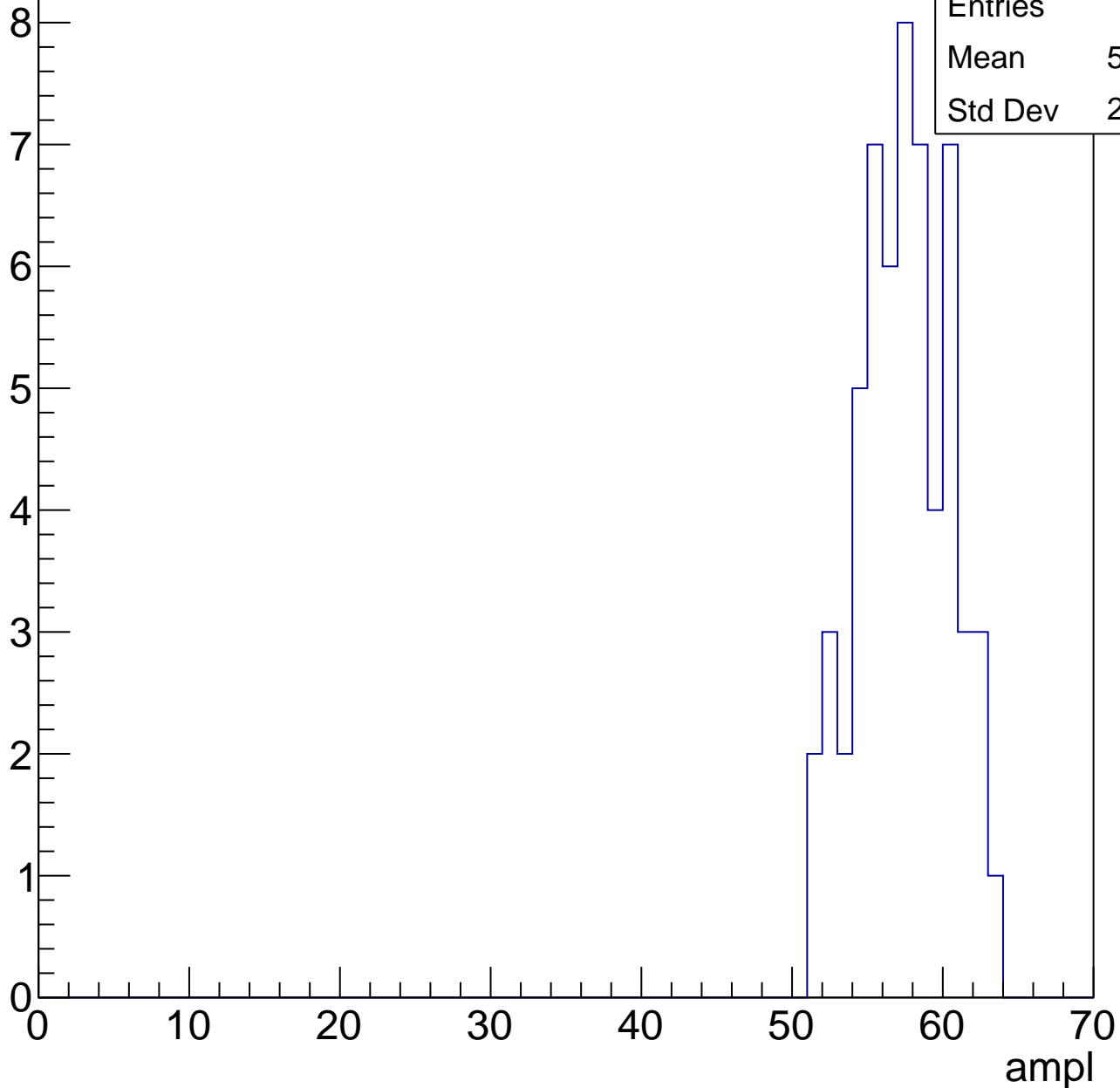
Entries	85
Mean	50.25
Std Dev	3.713



B0L002S, U4-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

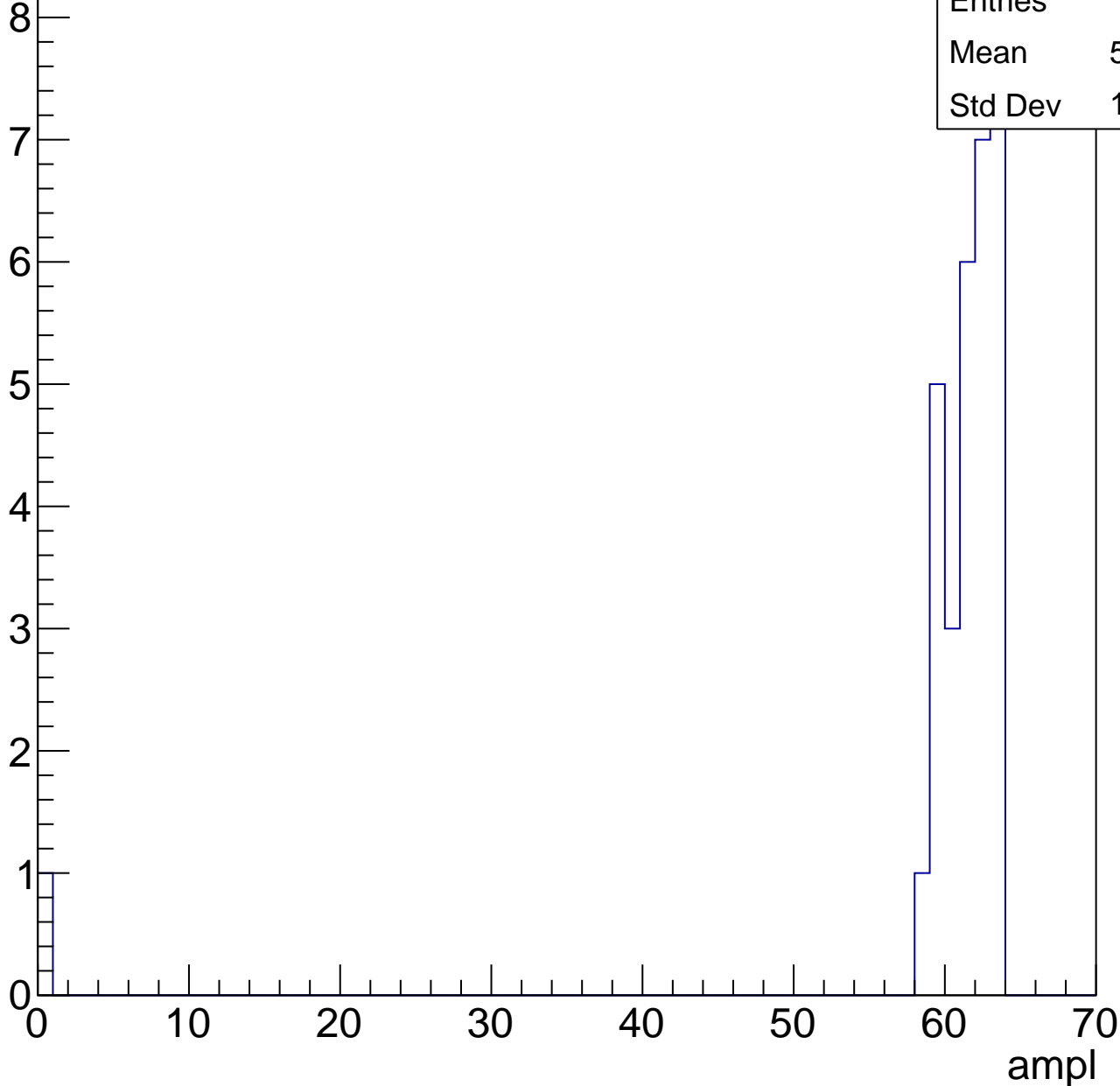


B0L002S, U4-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.26
Std Dev	10.92



B0L002S, U4-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch56, adc0

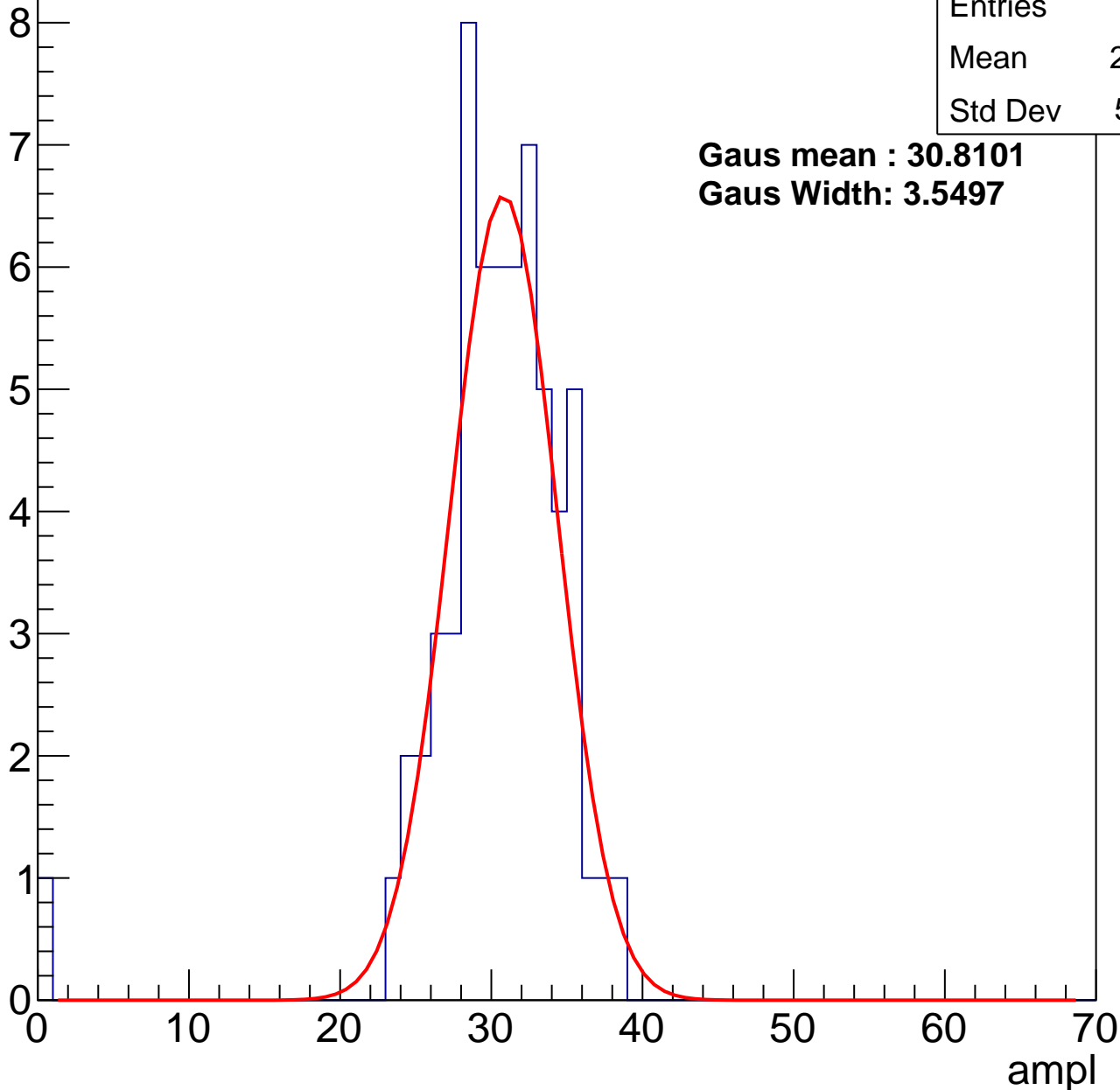
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.92
Std Dev	5.081

Gaus mean : 30.8101

Gaus Width: 3.5497



B0L002S, U4-ch56, adc1

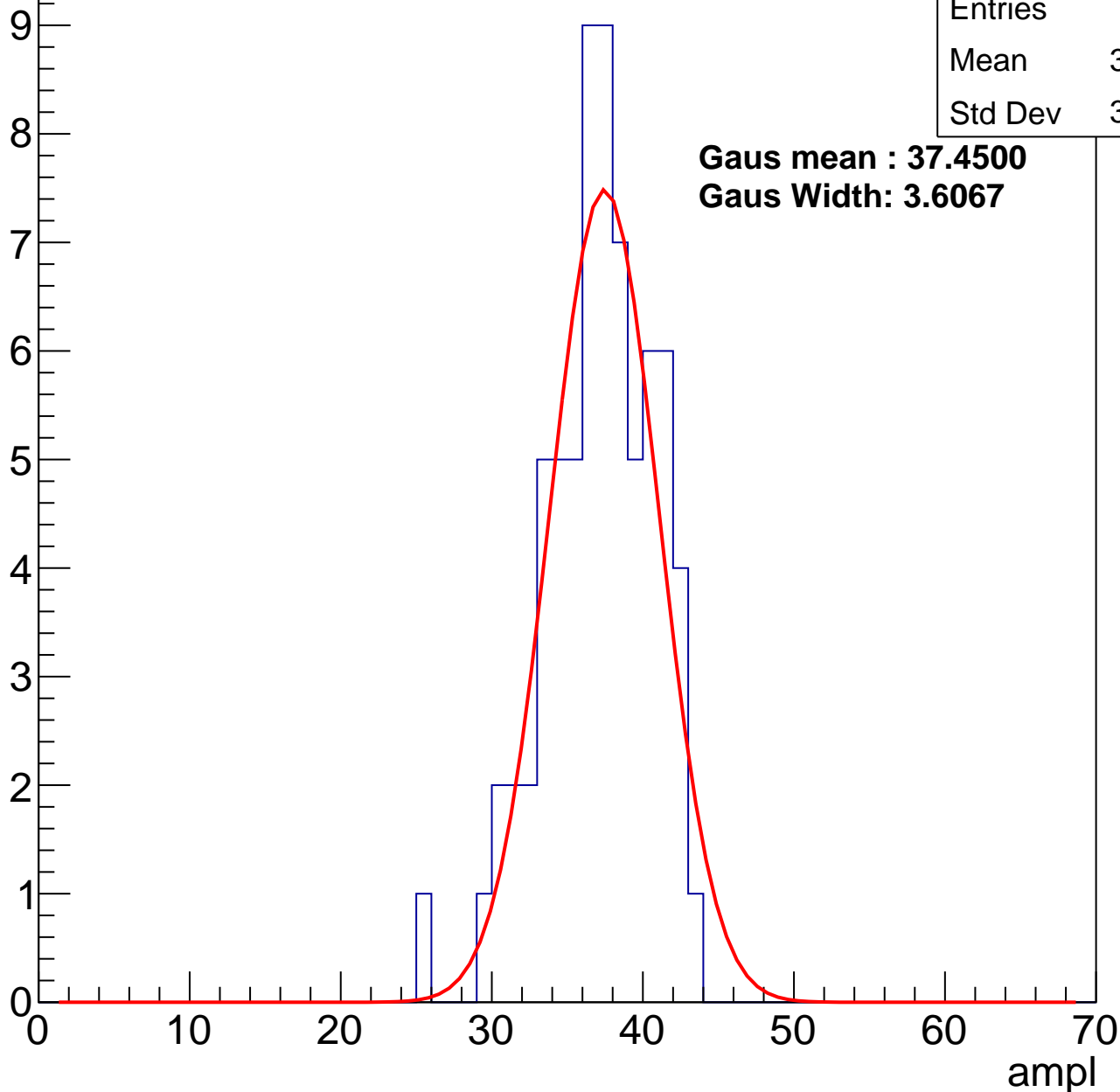
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36.64
Std Dev	3.558

Gaus mean : 37.4500

Gaus Width: 3.6067



B0L002S, U4-ch56, adc2

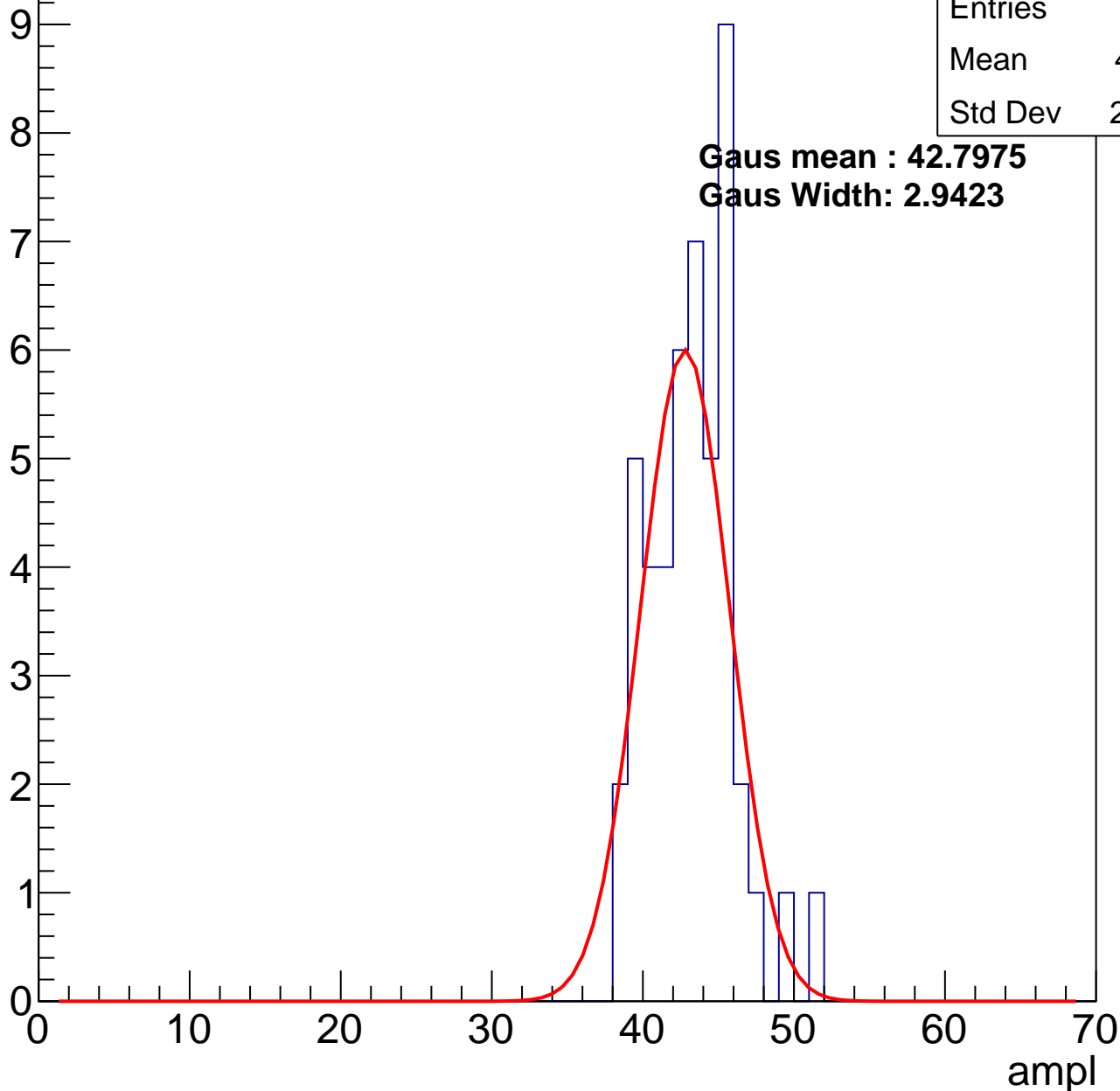
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	42.81
Std Dev	2.772

Gaus mean : 42.7975

Gaus Width: 2.9423



B0L002S, U4-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	49.6
Std Dev	3.413

Entry

10

8

6

4

2

0

0

10

20

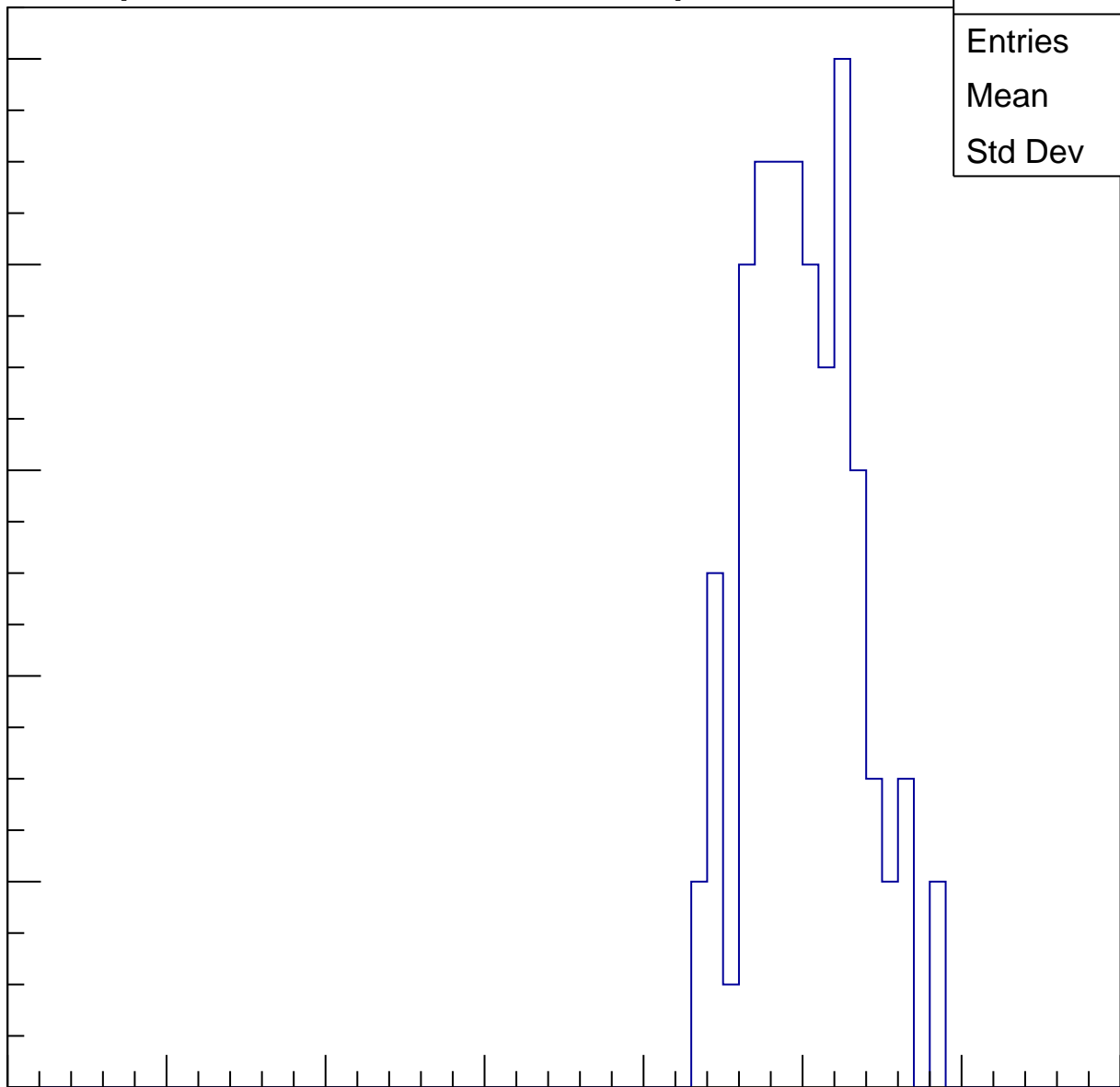
30

40

50

60

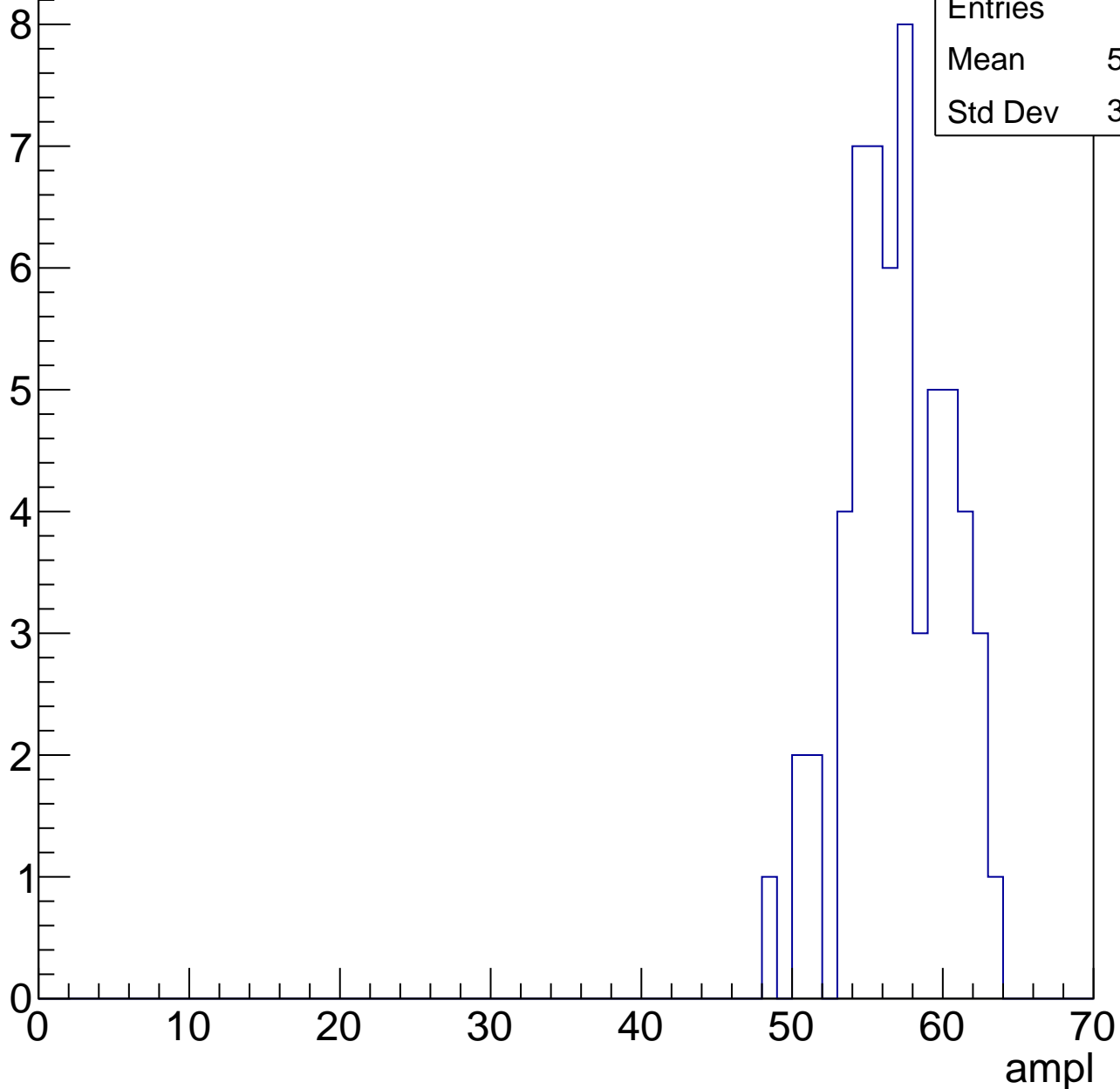
ampl



B0L002S, U4-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



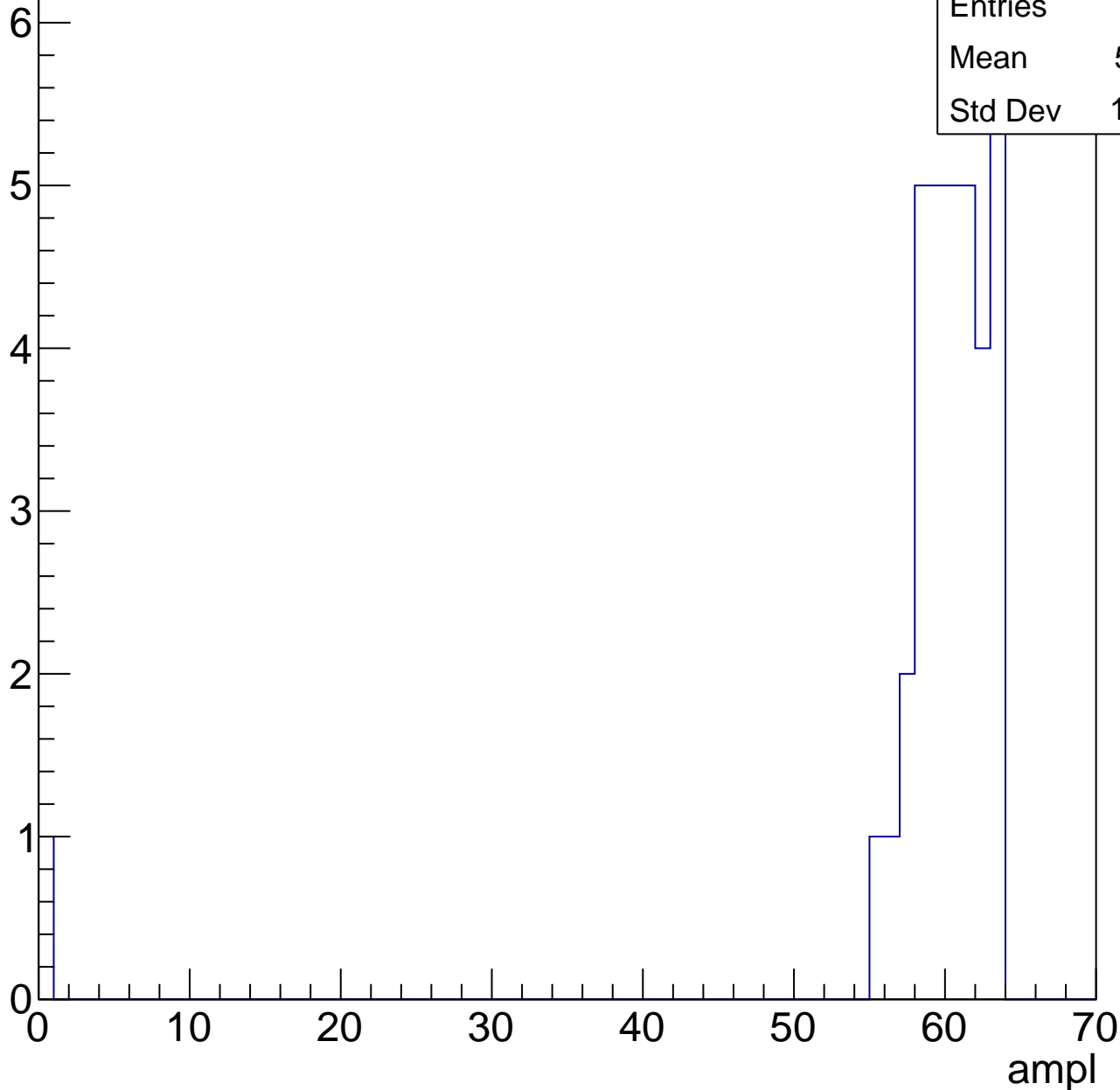
Entries	58
Mean	56.53
Std Dev	3.328

B0L002S, U4-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

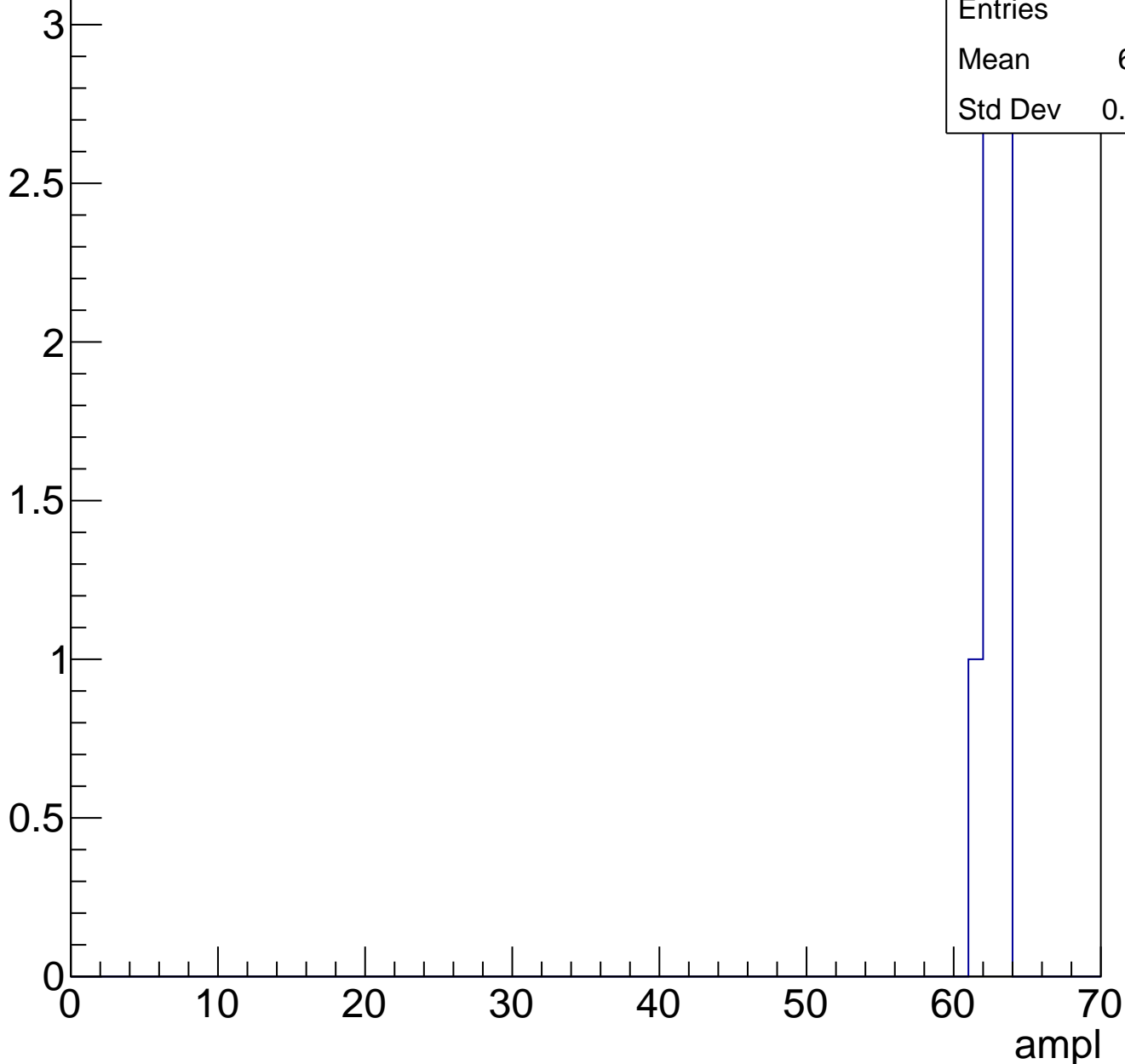
Entries	35
Mean	58.31
Std Dev	10.23



B0L002S, U4-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U4-ch57, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	29.29
Std Dev	3.704

Gaus mean : 29.6894

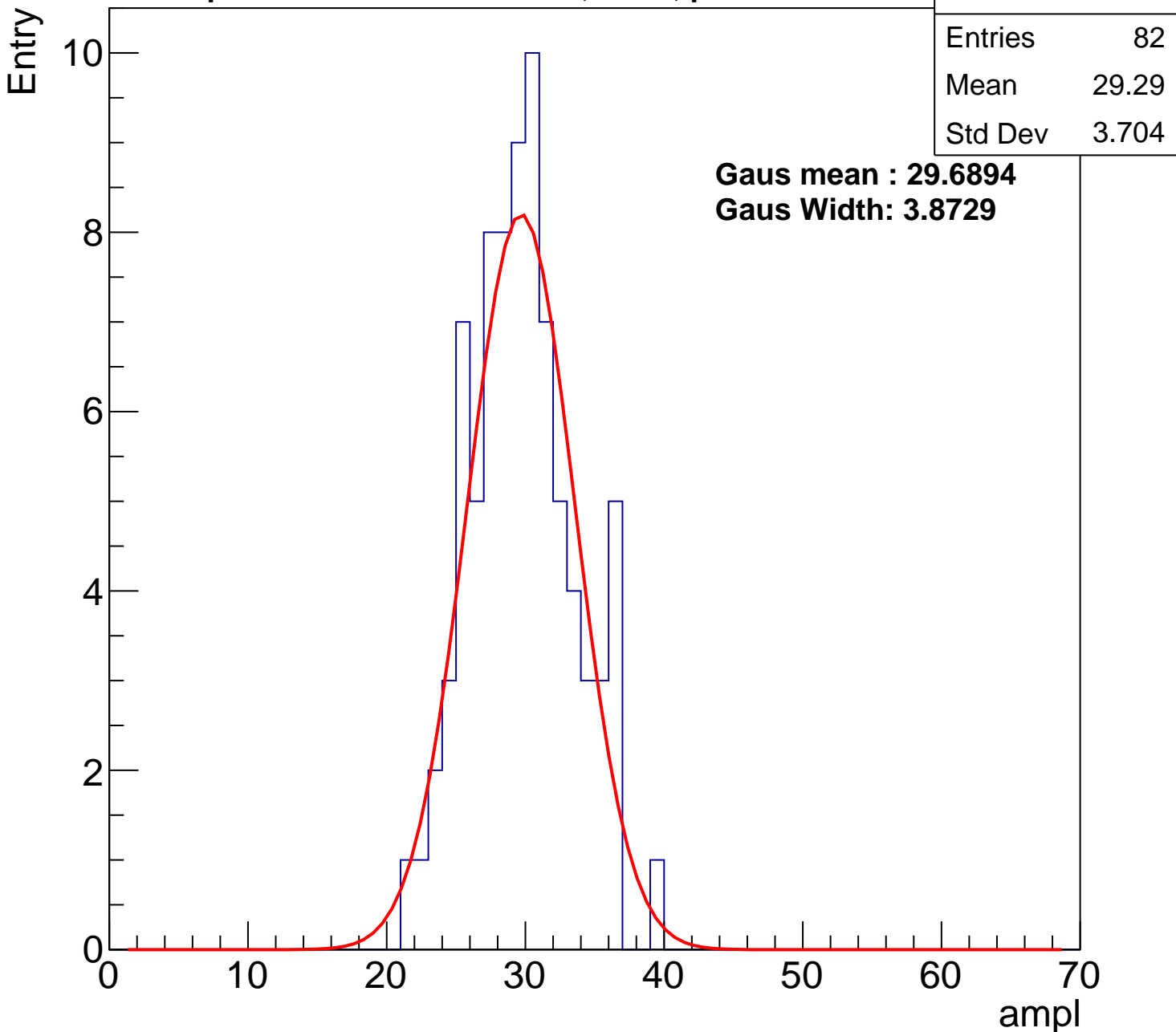
Gaus Width: 3.8729

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch57, adc1

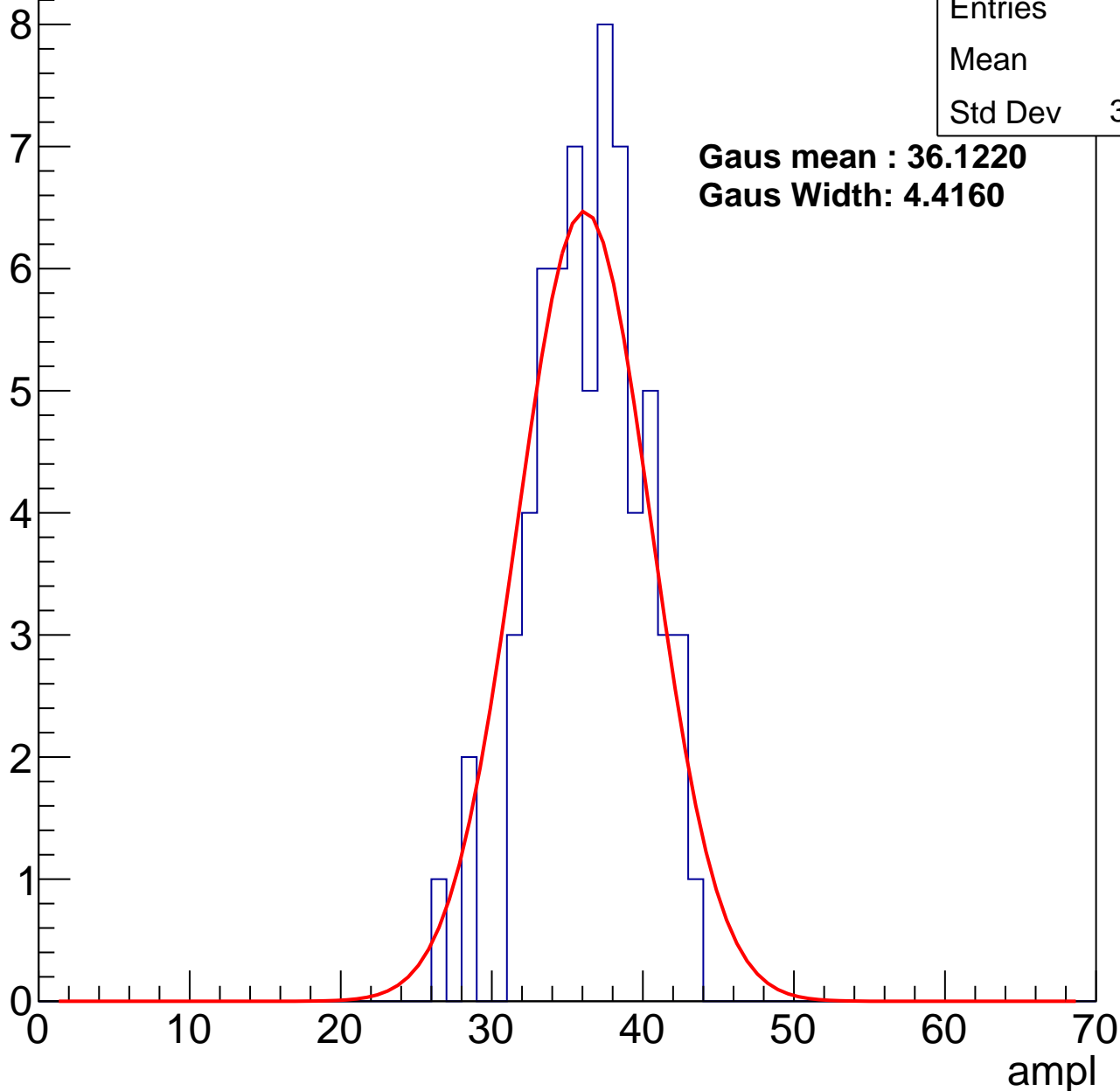
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36
Std Dev	3.586

Gaus mean : 36.1220

Gaus Width: 4.4160



B0L002S, U4-ch57, adc2

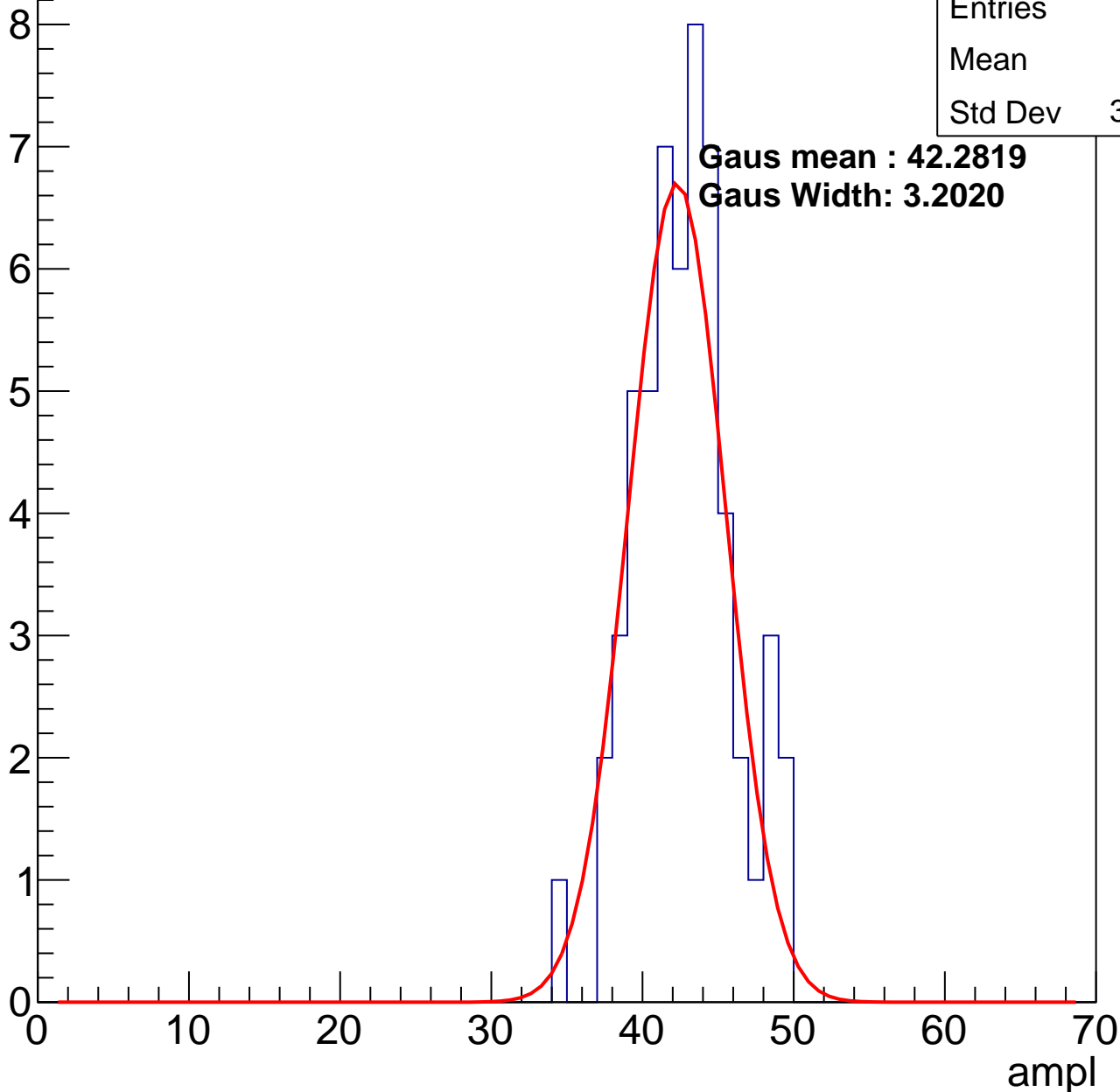
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	42.3
Std Dev	3.173

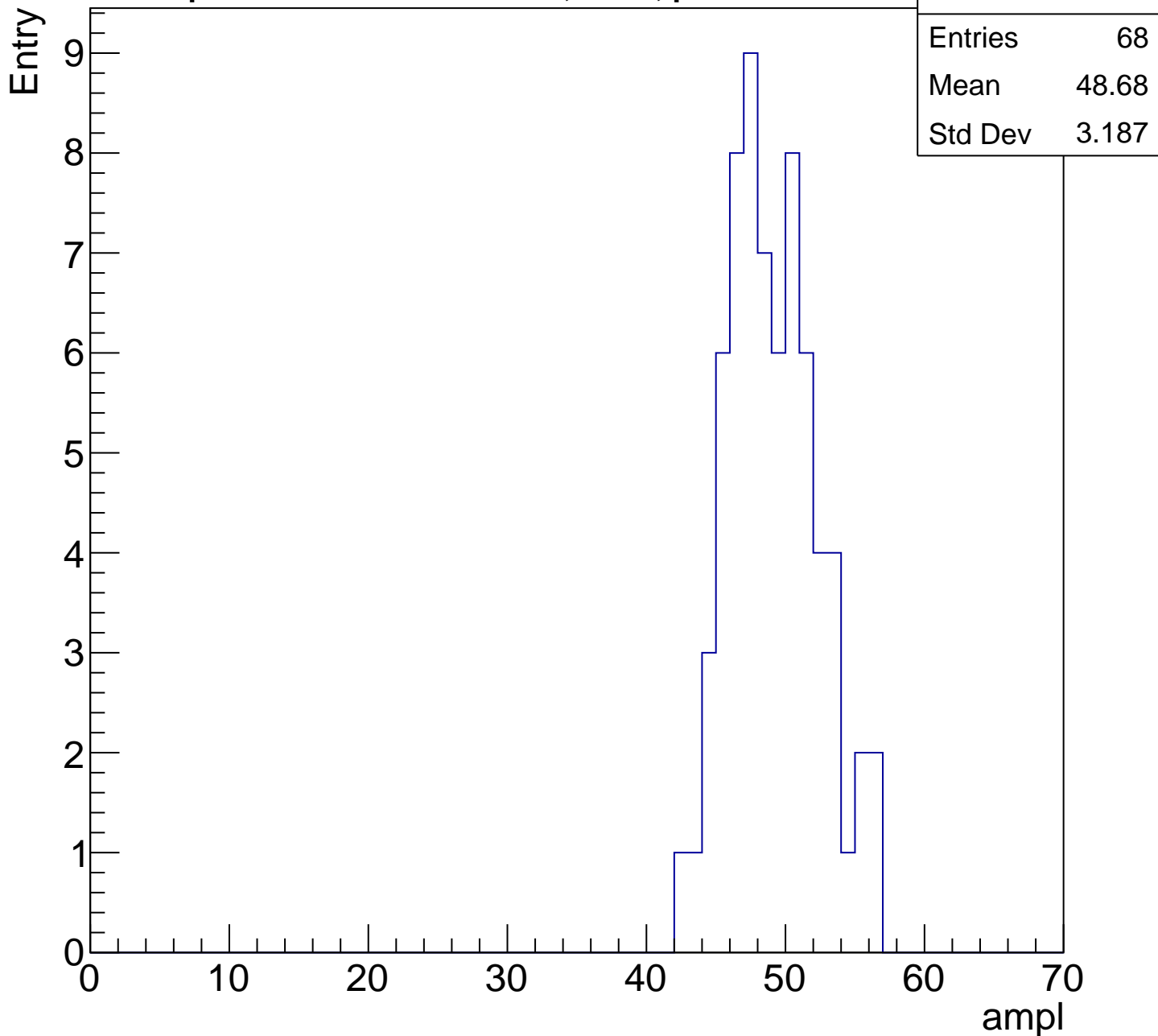
Gaus mean : 42.2819

Gaus Width: 3.2020



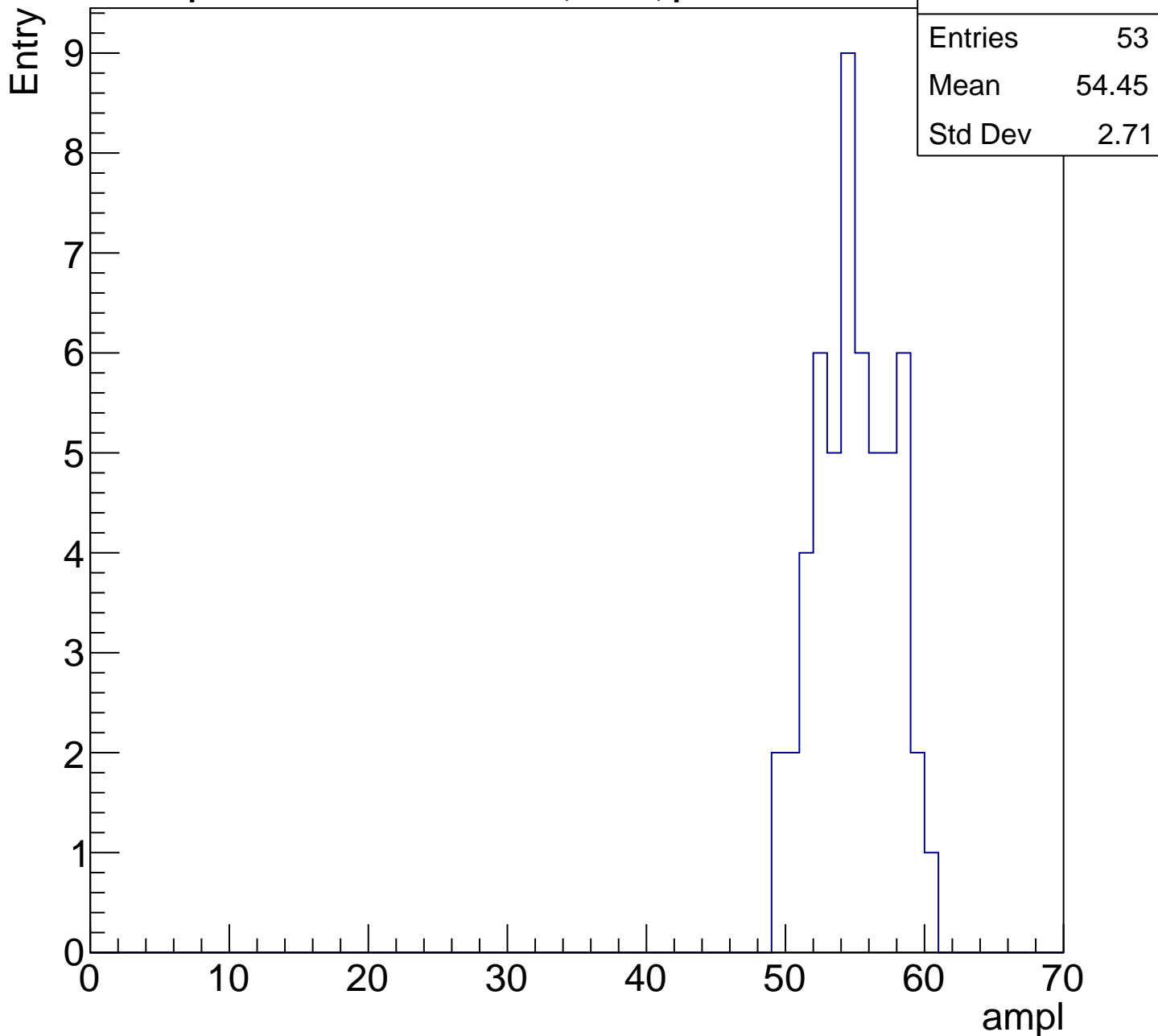
B0L002S, U4-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

10

8

6

4

2

0

0

10

20

30

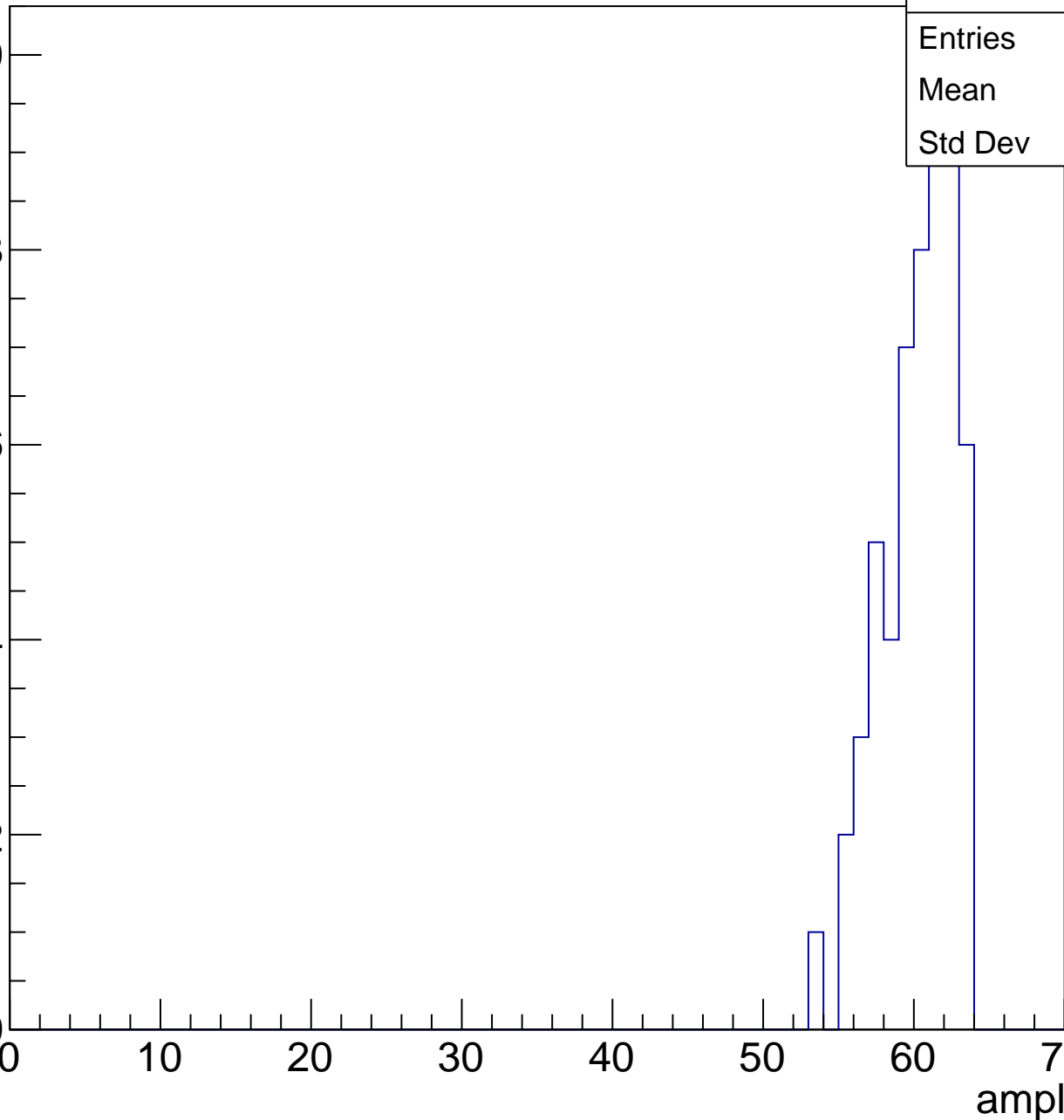
40

50

60

ampl

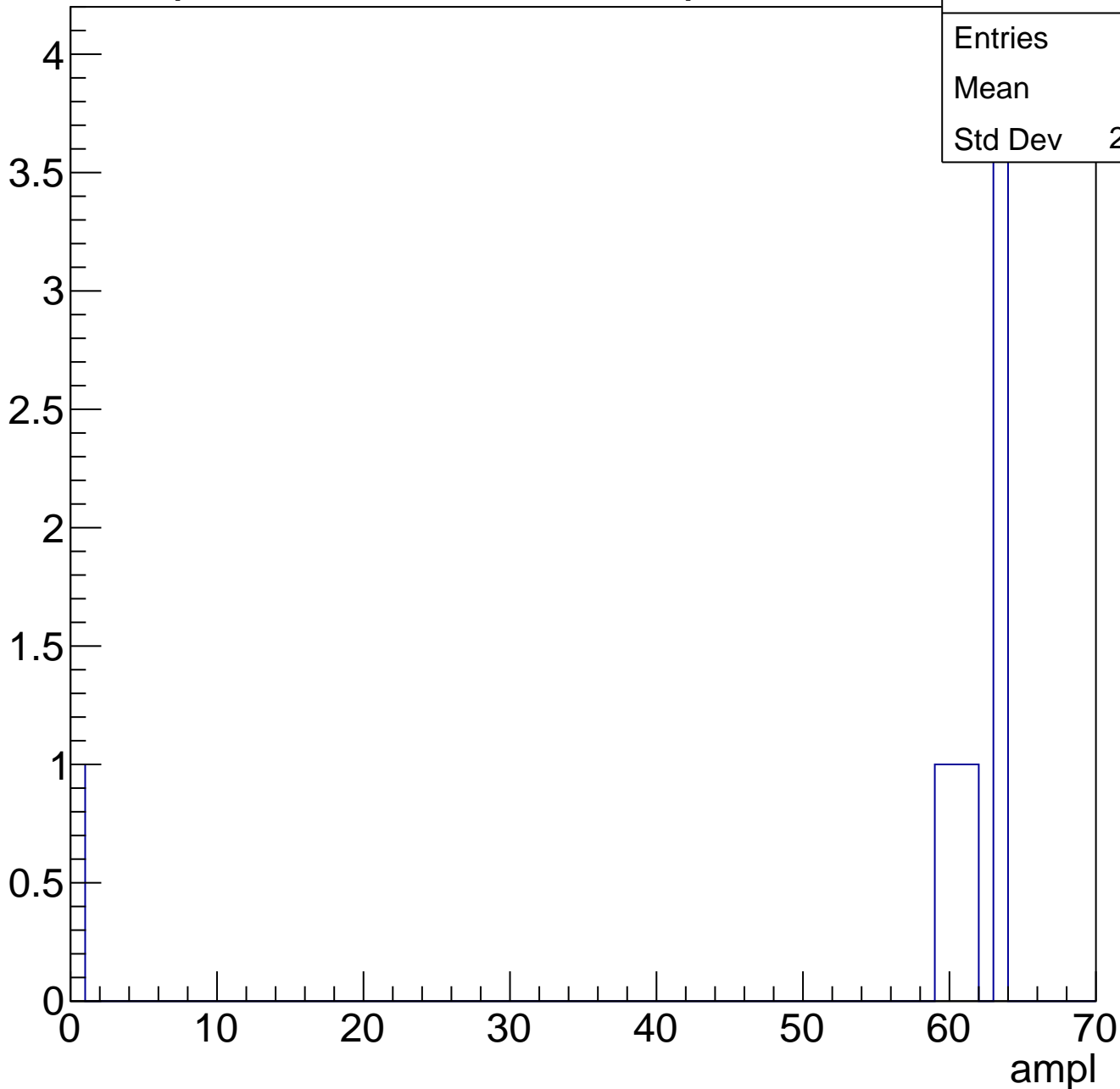
Entries	55
Mean	59.78
Std Dev	2.395



B0L002S, U4-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch58, adc0

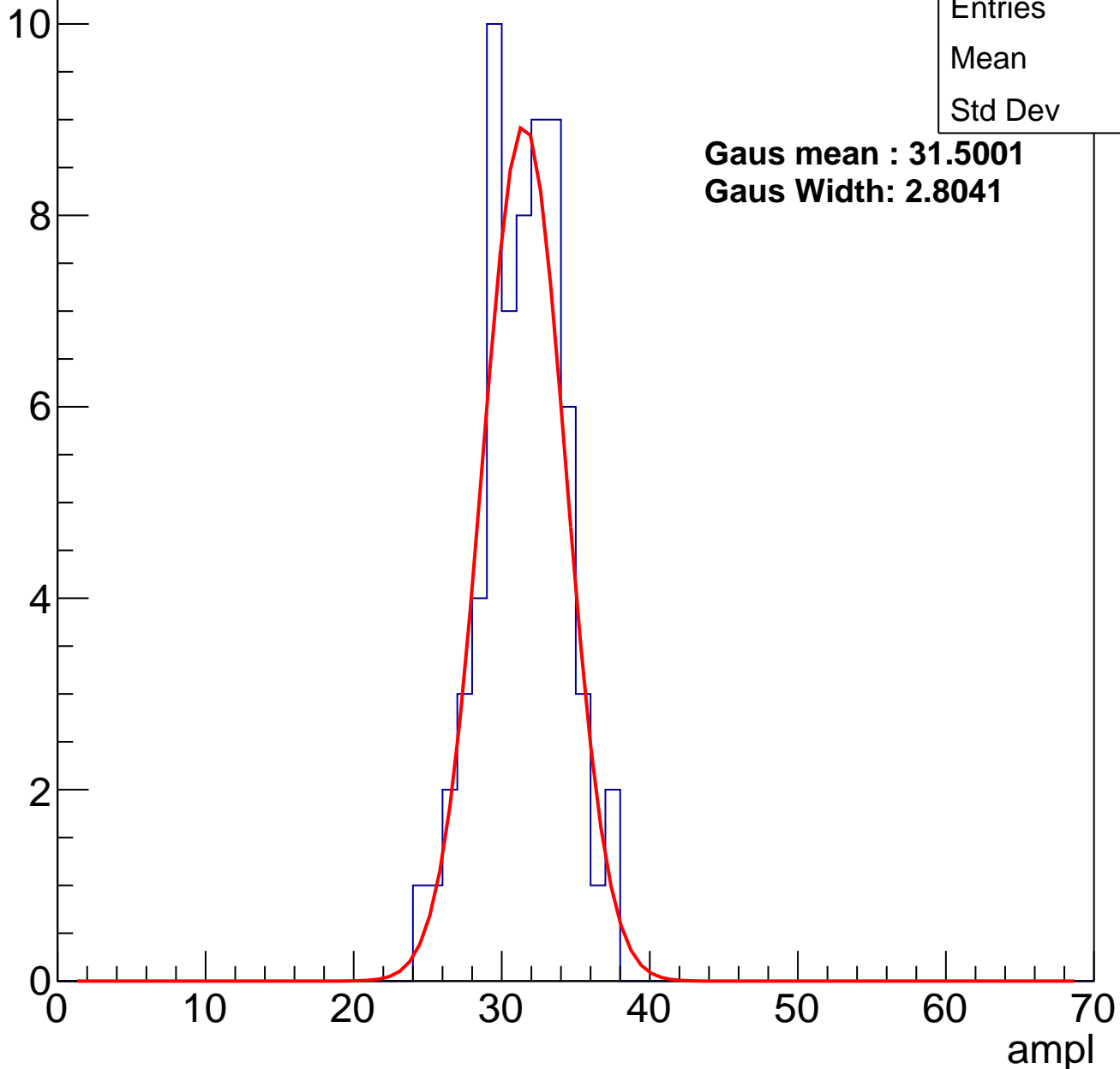
calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	31
Std Dev	2.78

Gaus mean : 31.5001

Gaus Width: 2.8041

Entry



B0L002S, U4-ch58, adc1

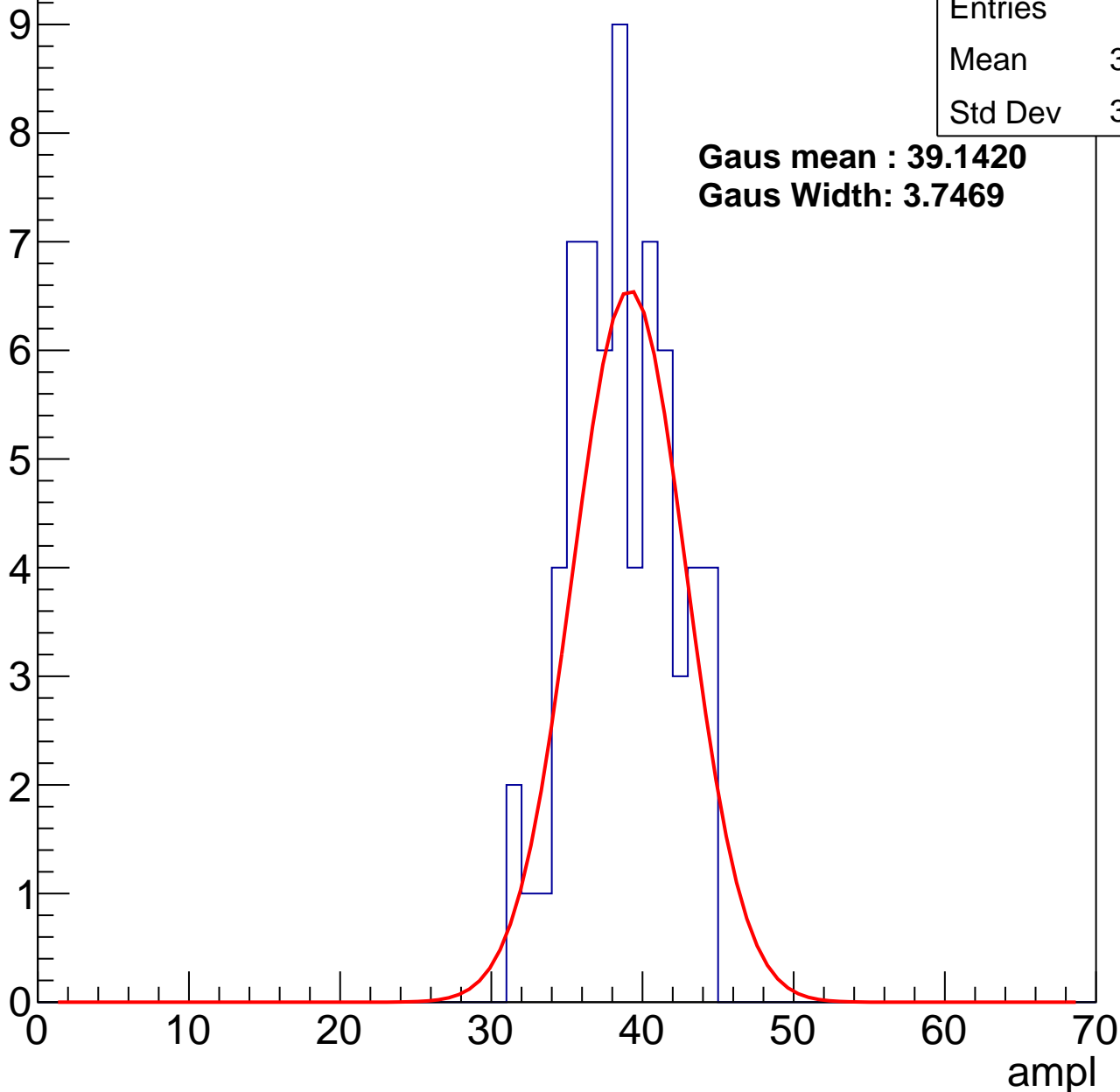
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.15
Std Dev	3.273

Gaus mean : 39.1420

Gaus Width: 3.7469



B0L002S, U4-ch58, adc2

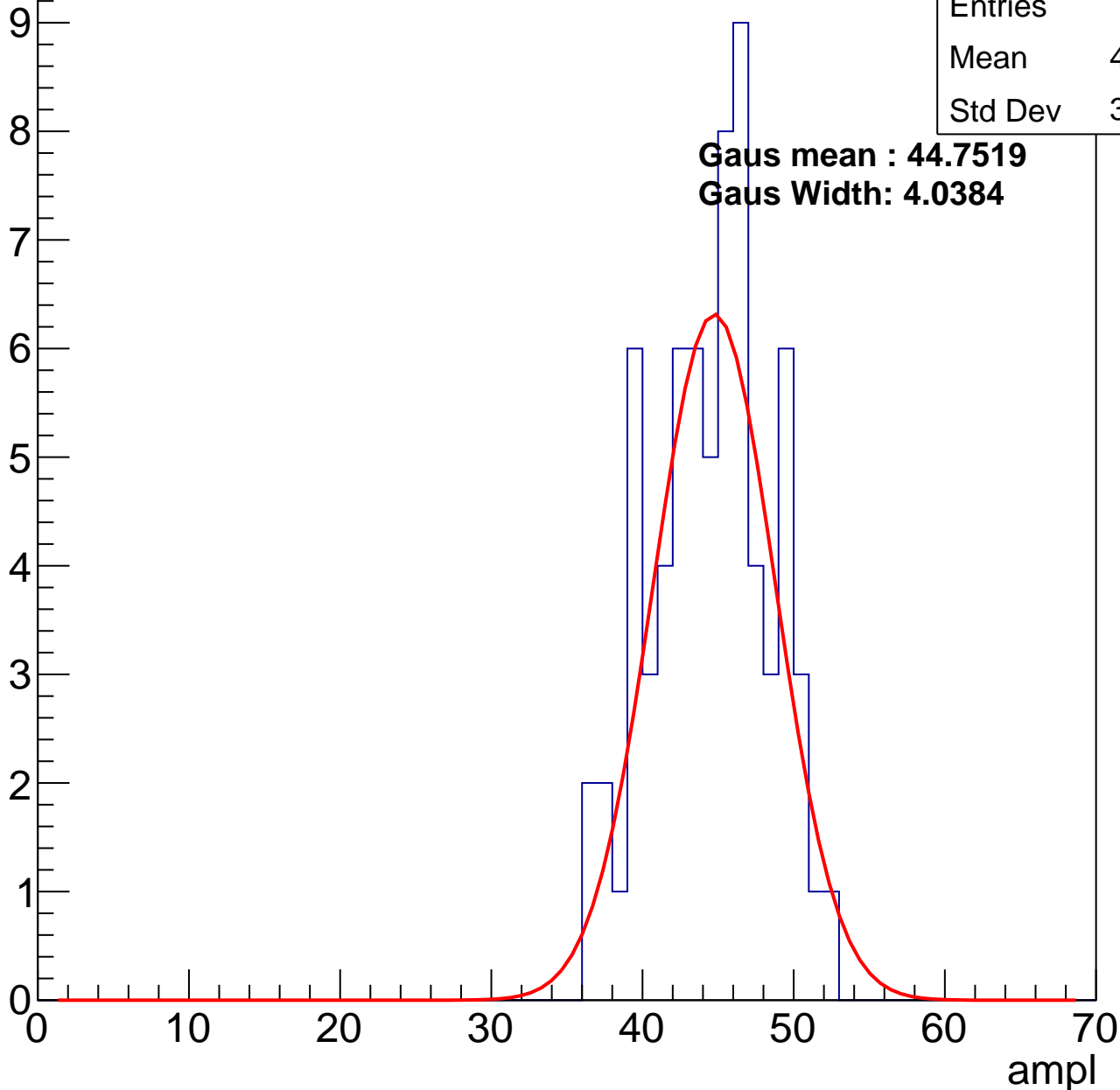
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.07
Std Dev	3.826

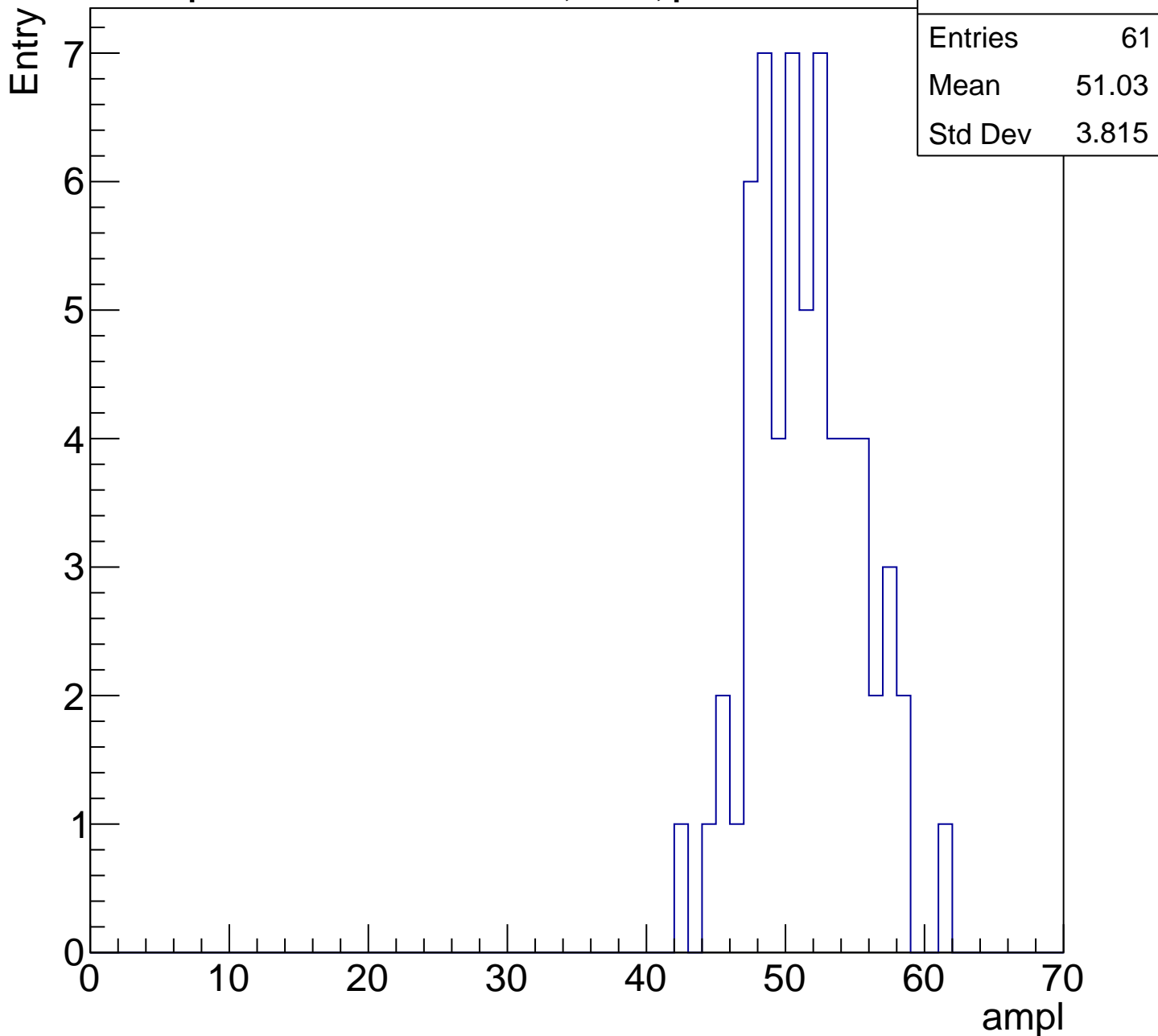
Gaus mean : 44.7519

Gaus Width: 4.0384



B0L002S, U4-ch58, adc3

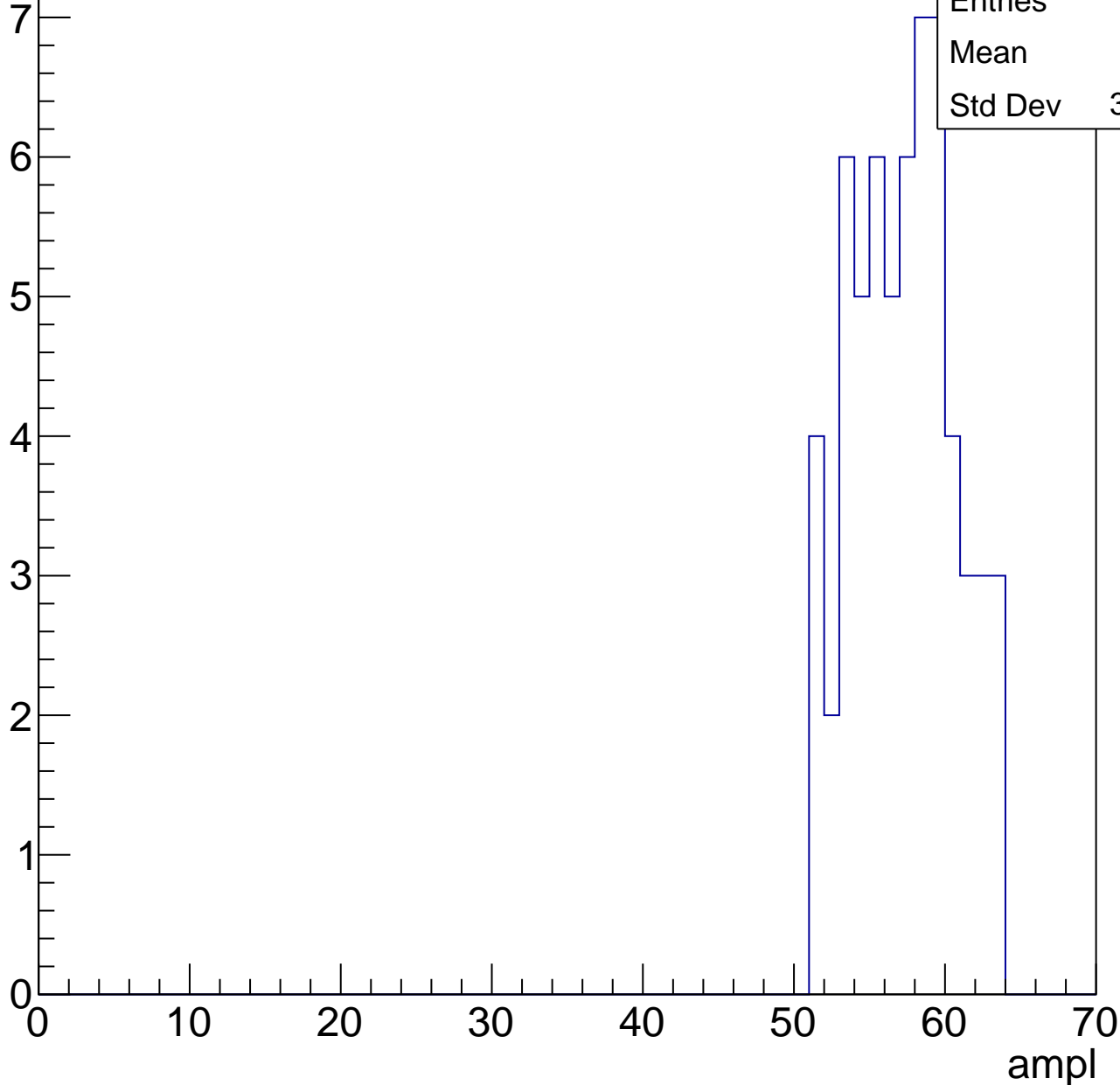
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

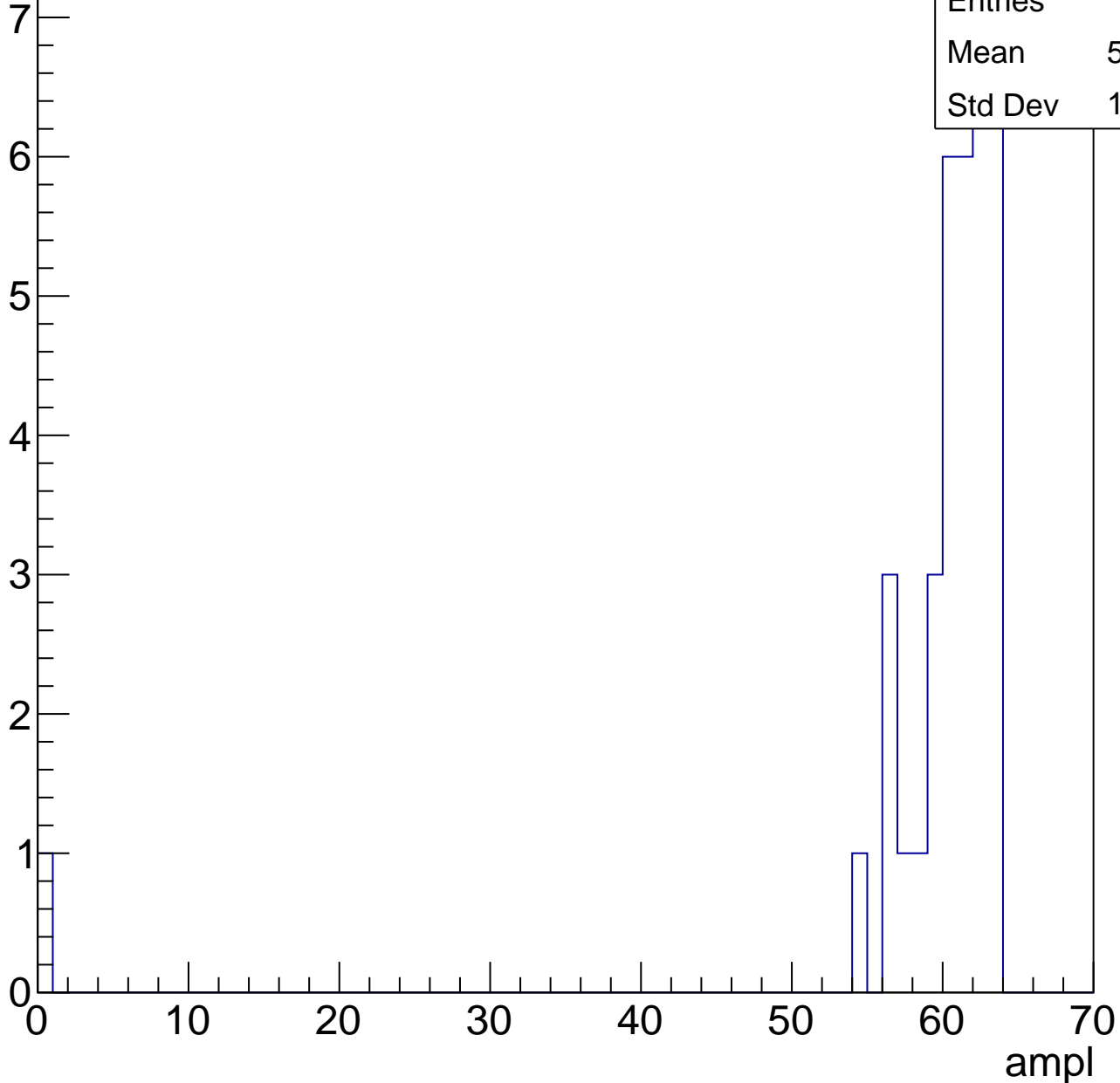


B0L002S, U4-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	58.75
Std Dev	10.19



B0L002S, U4-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B0L002S, U4-ch59, adc0

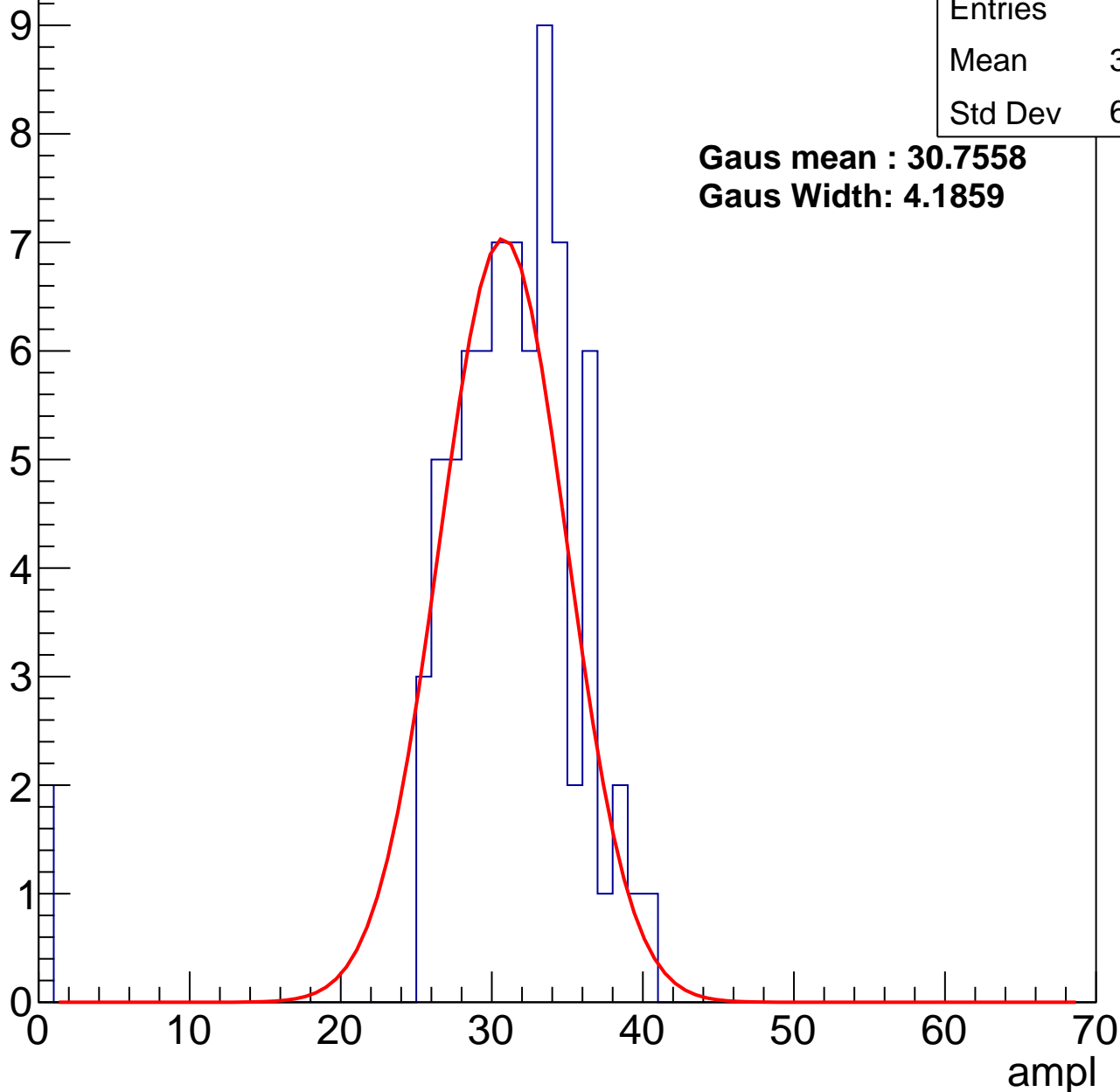
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.45
Std Dev	6.137

Gaus mean : 30.7558

Gaus Width: 4.1859



B0L002S, U4-ch59, adc1

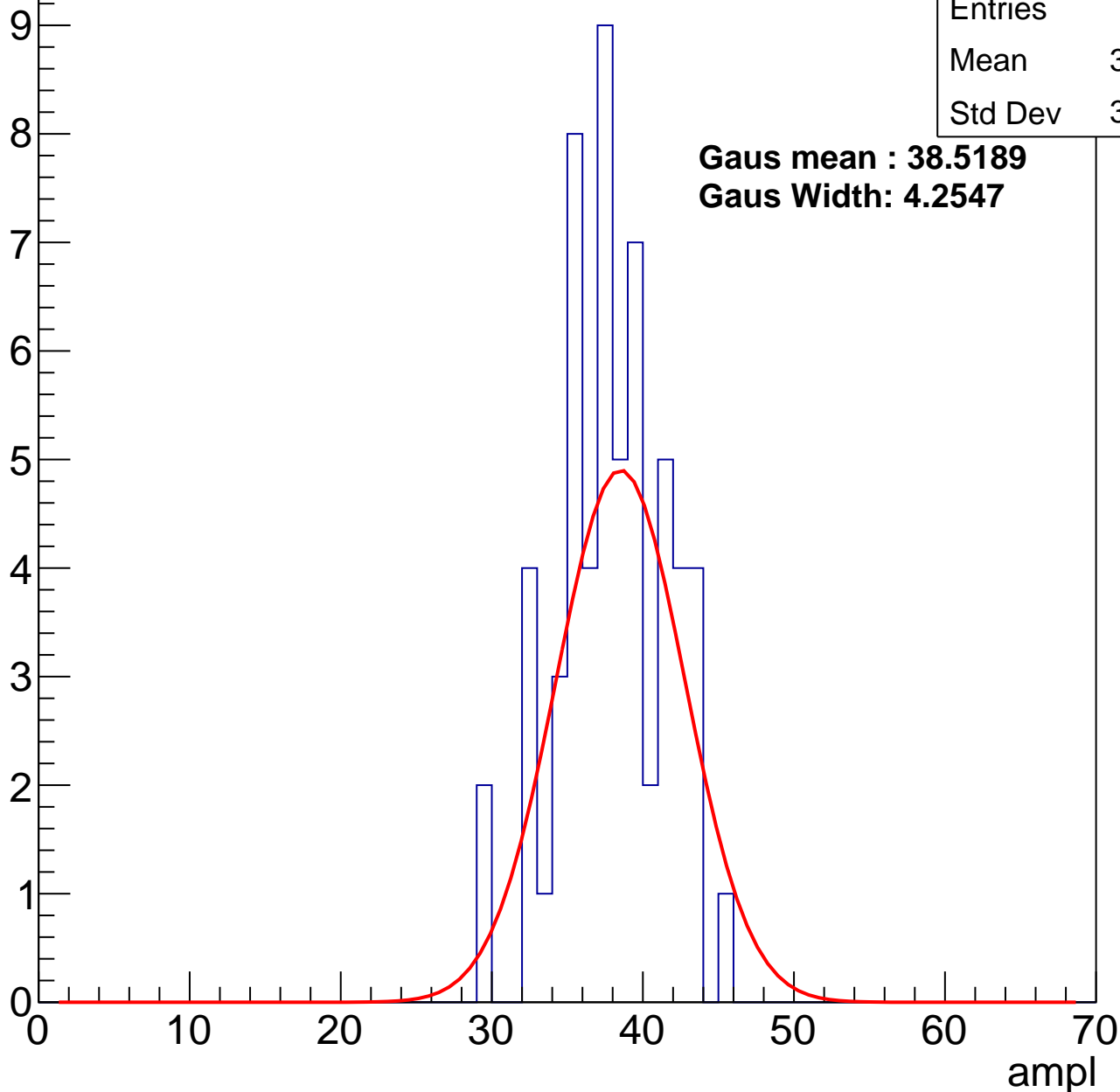
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	37.47
Std Dev	3.524

Gaus mean : 38.5189

Gaus Width: 4.2547



B0L002S, U4-ch59, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	44.49
Std Dev	3.587

Gaus mean : 44.7278

Gaus Width: 4.0706

10

8

6

4

2

0

0

10

20

30

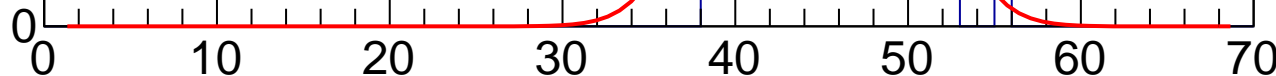
40

50

60

70

ampl

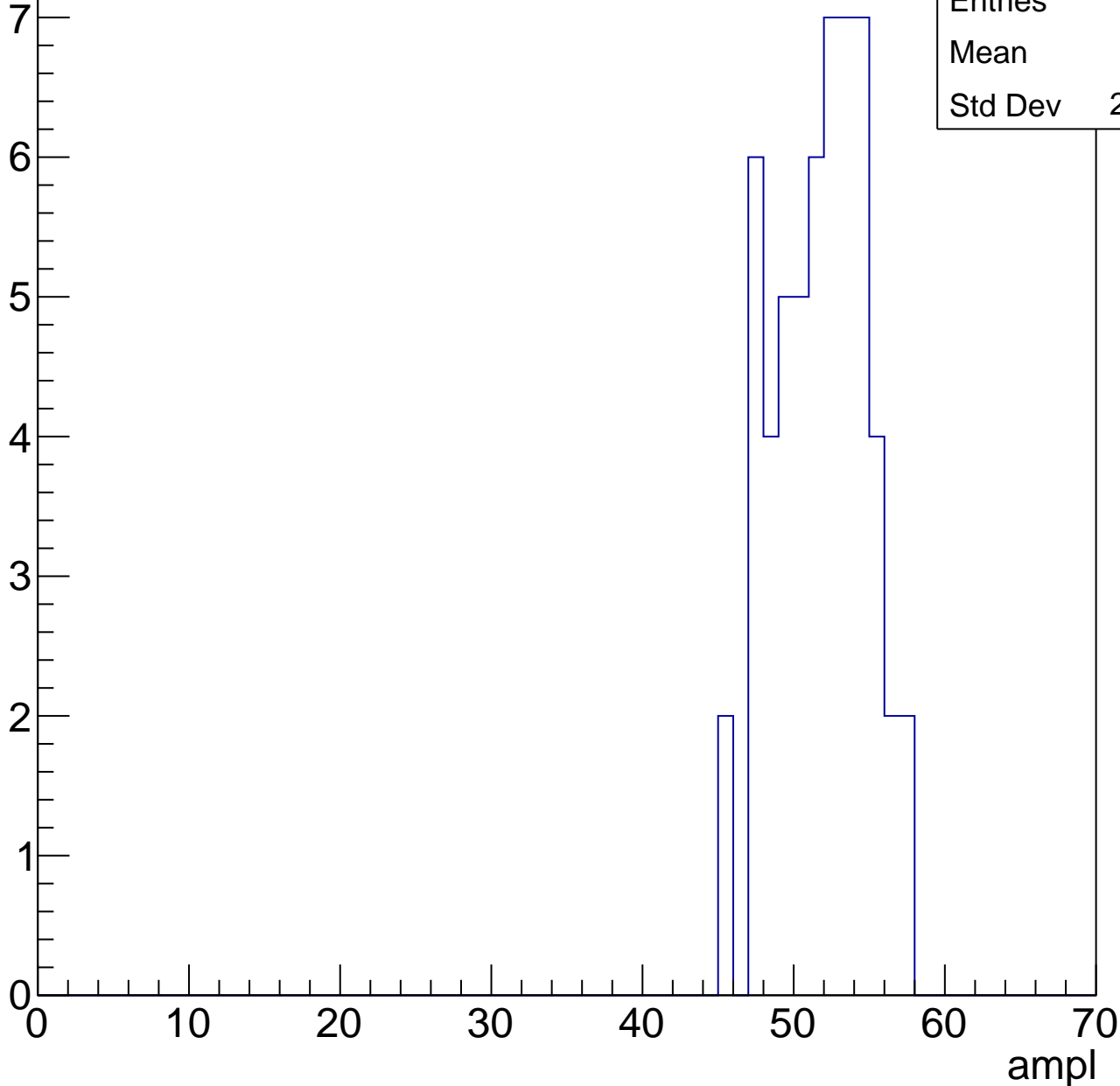


B0L002S, U4-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	51.3
Std Dev	2.985

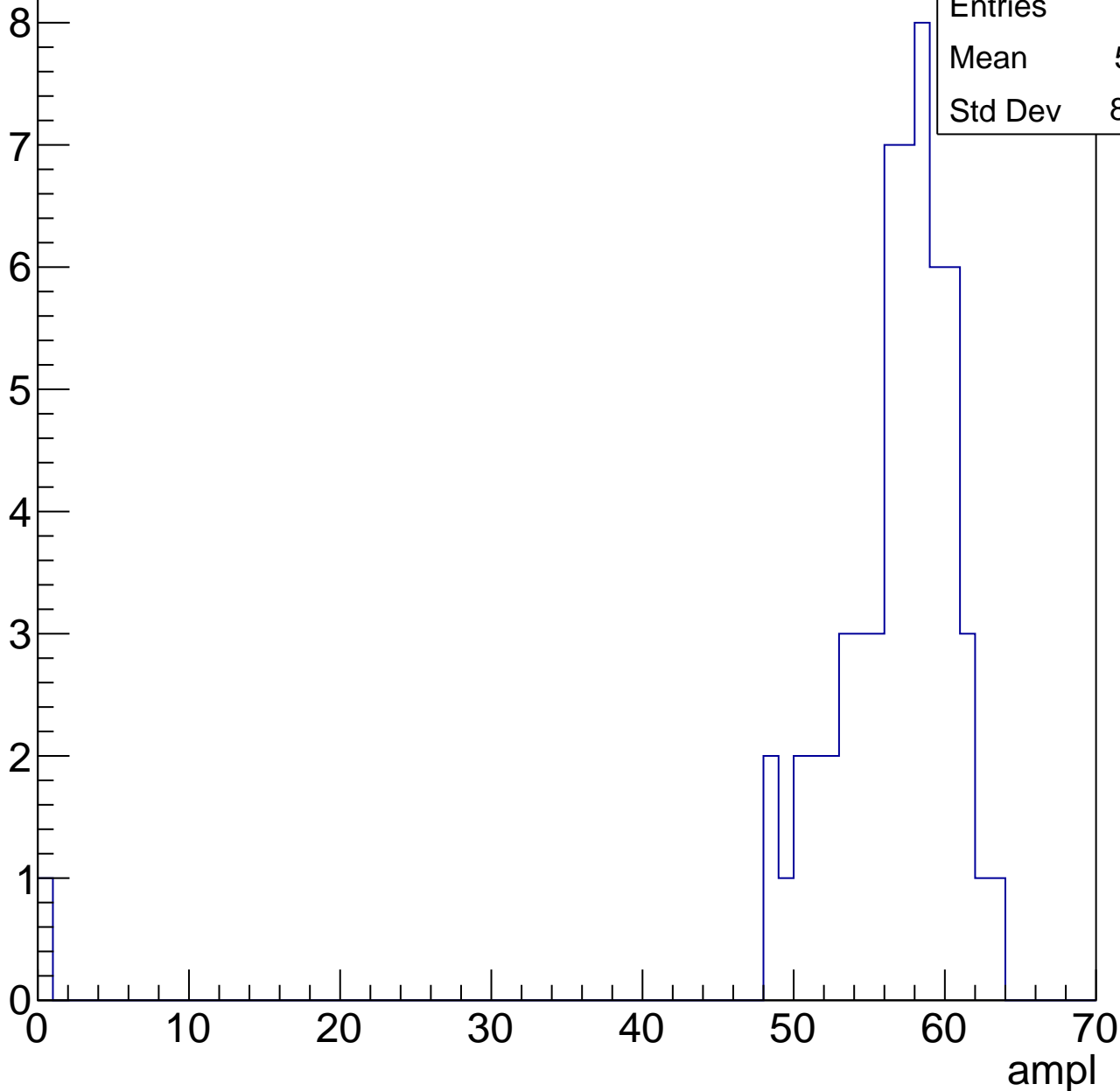


B0L002S, U4-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	55.41
Std Dev	8.135

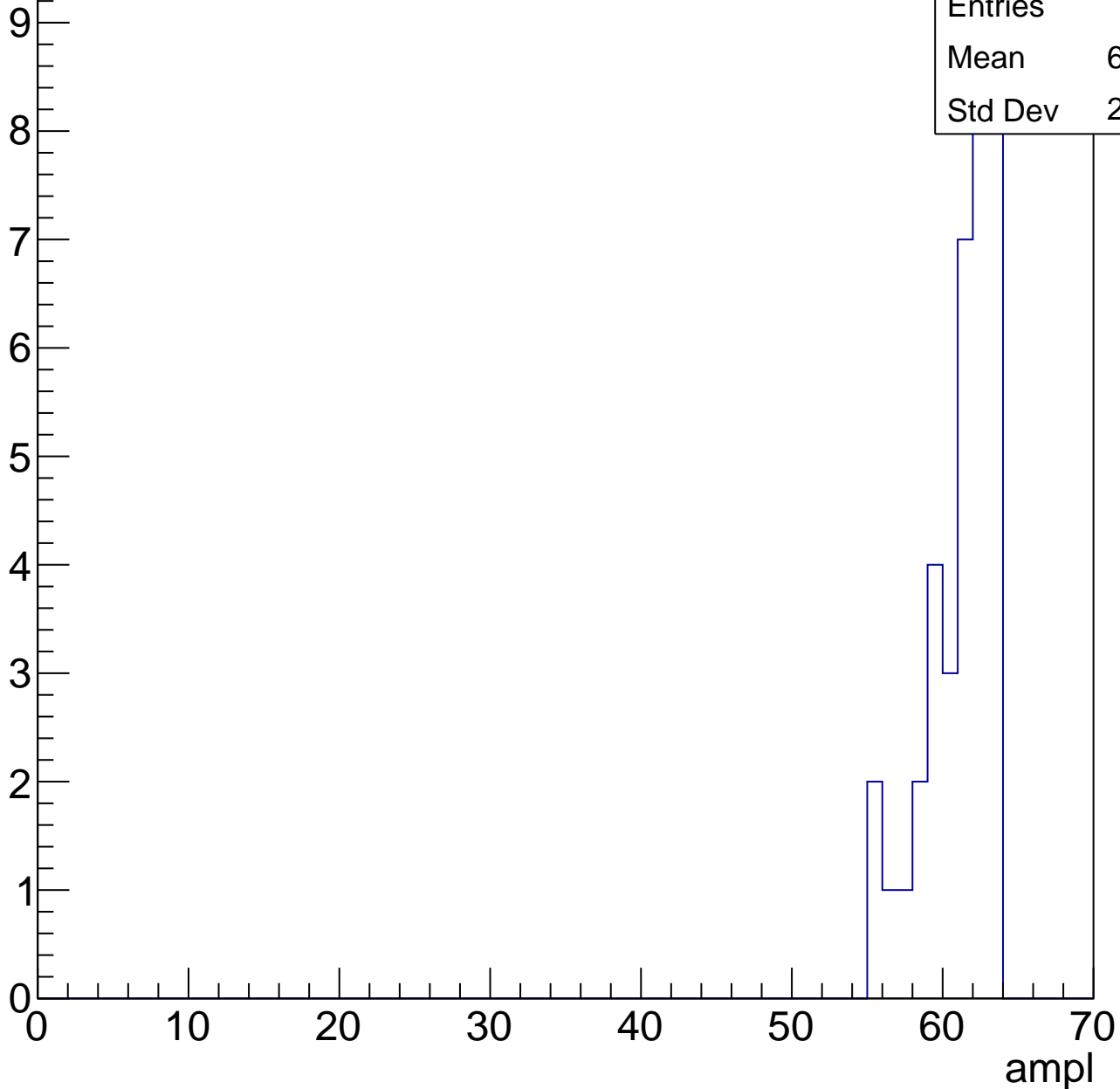


B0L002S, U4-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.65
Std Dev	2.245



B0L002S, U4-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch60, adc0

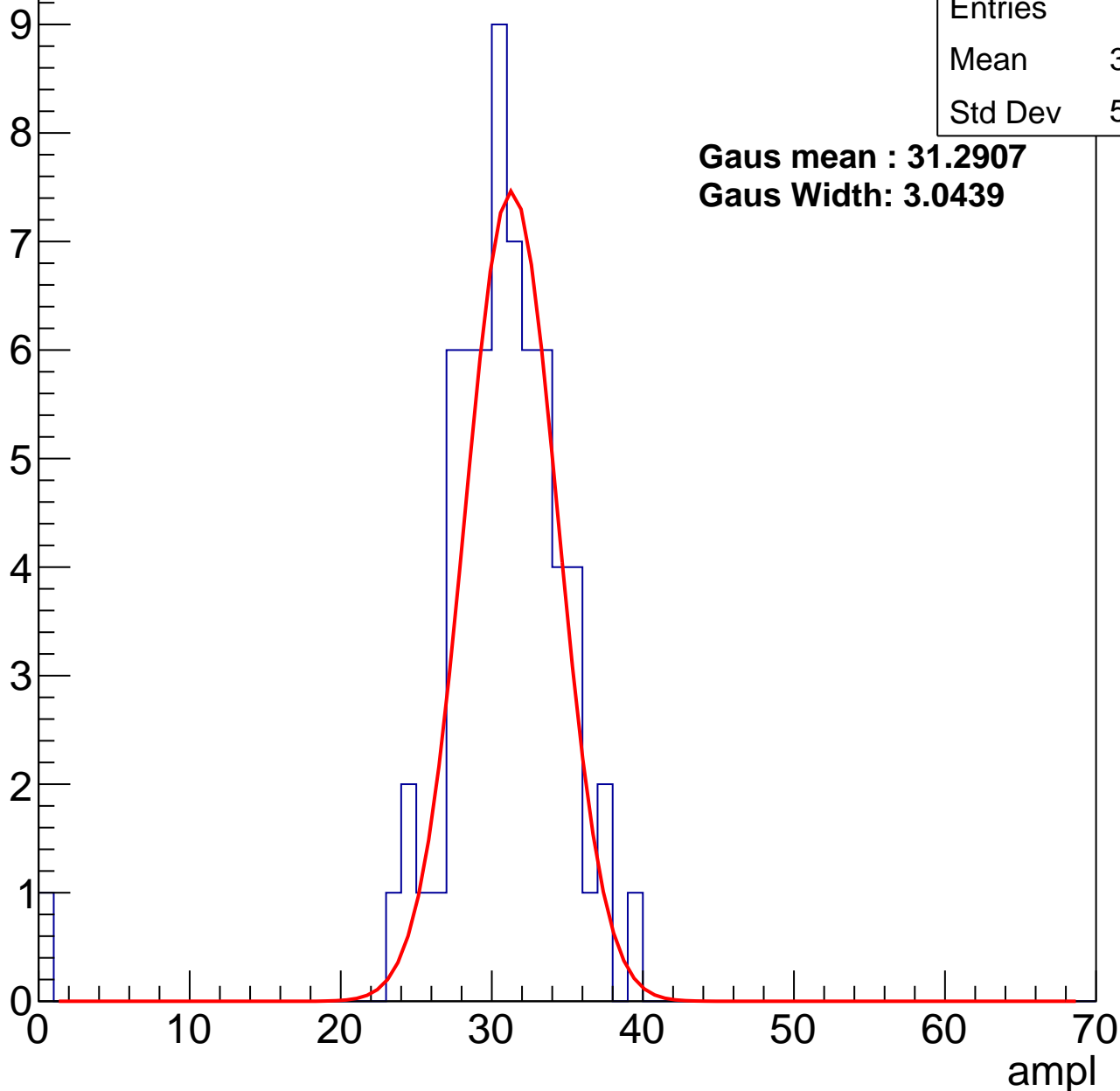
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.12
Std Dev	5.014

Gaus mean : 31.2907

Gaus Width: 3.0439



B0L002S, U4-ch60, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	37.12
Std Dev	3.201

Gaus mean : 37.2249

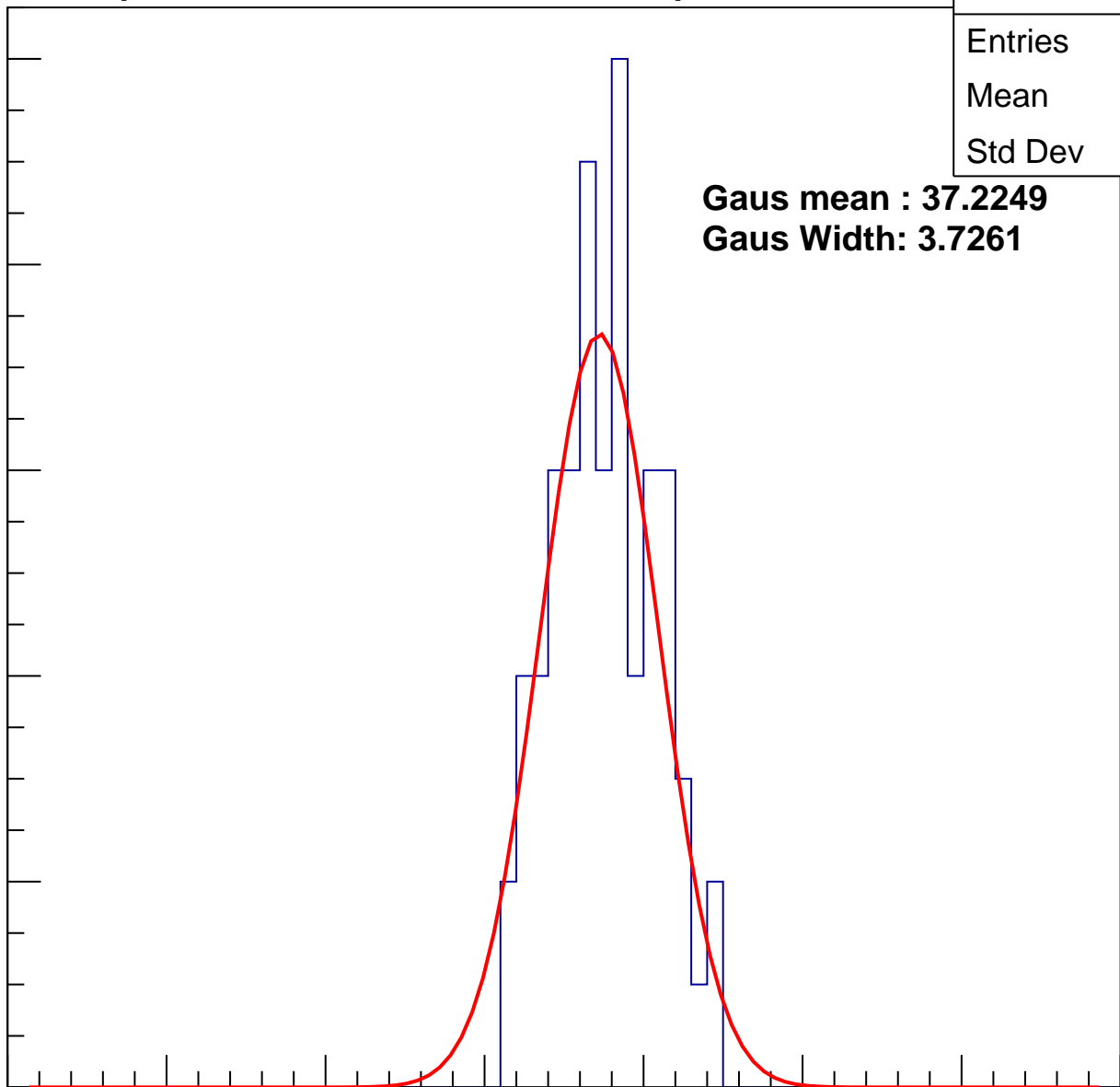
Gaus Width: 3.7261

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch60, adc2

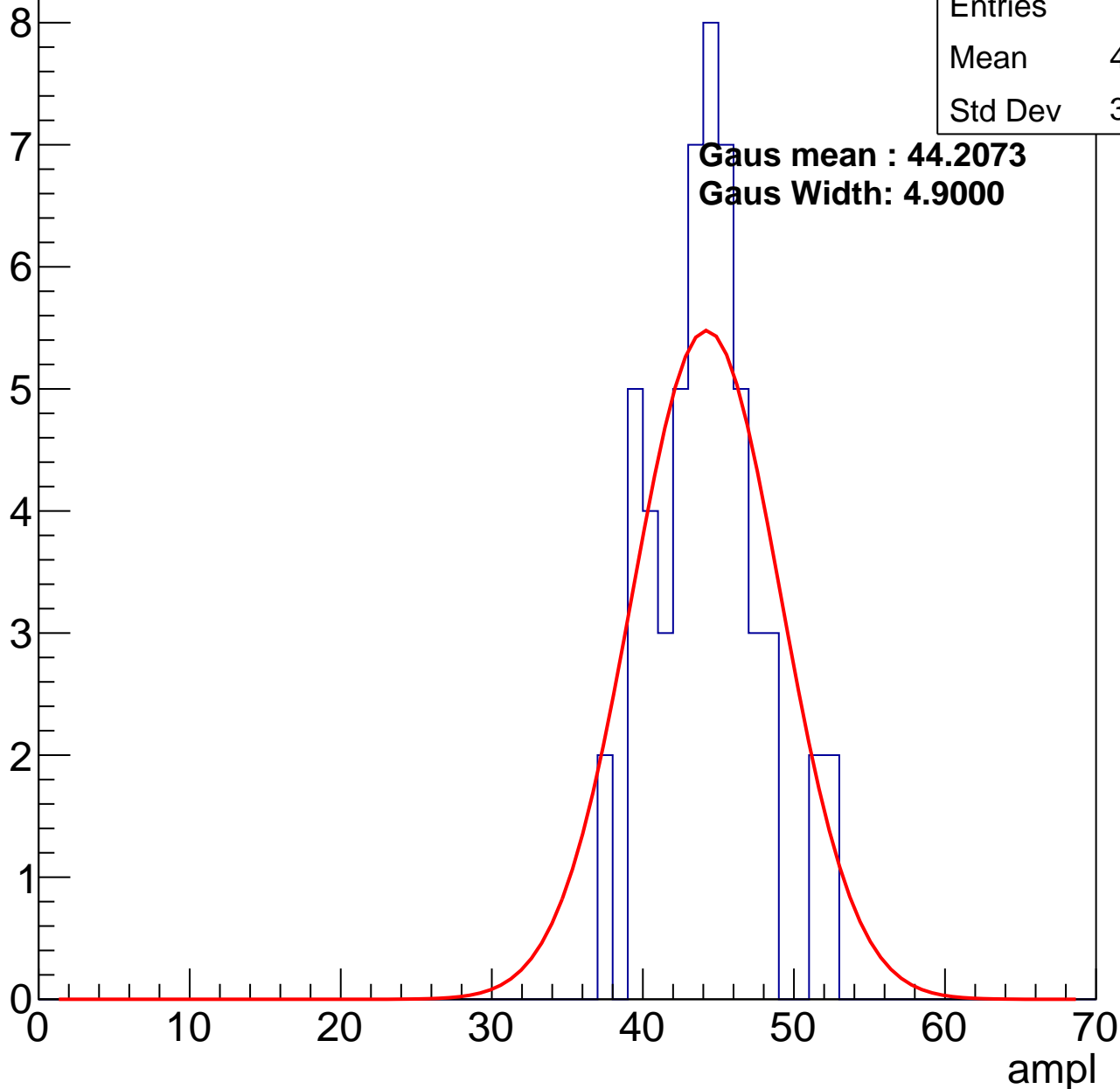
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.77
Std Dev	3.449

Gaus mean : 44.2073

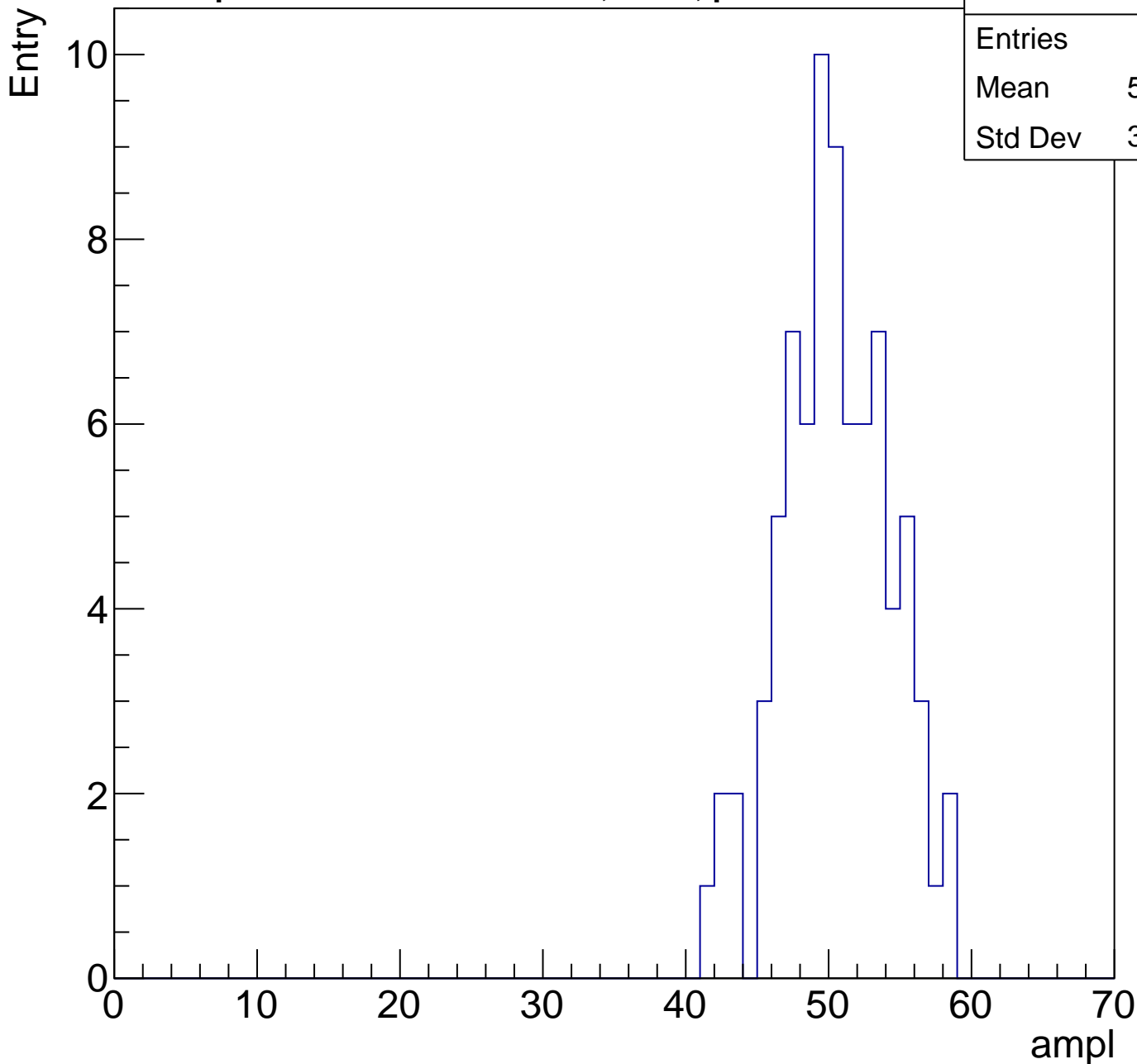
Gaus Width: 4.9000



B0L002S, U4-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	50.05
Std Dev	3.775

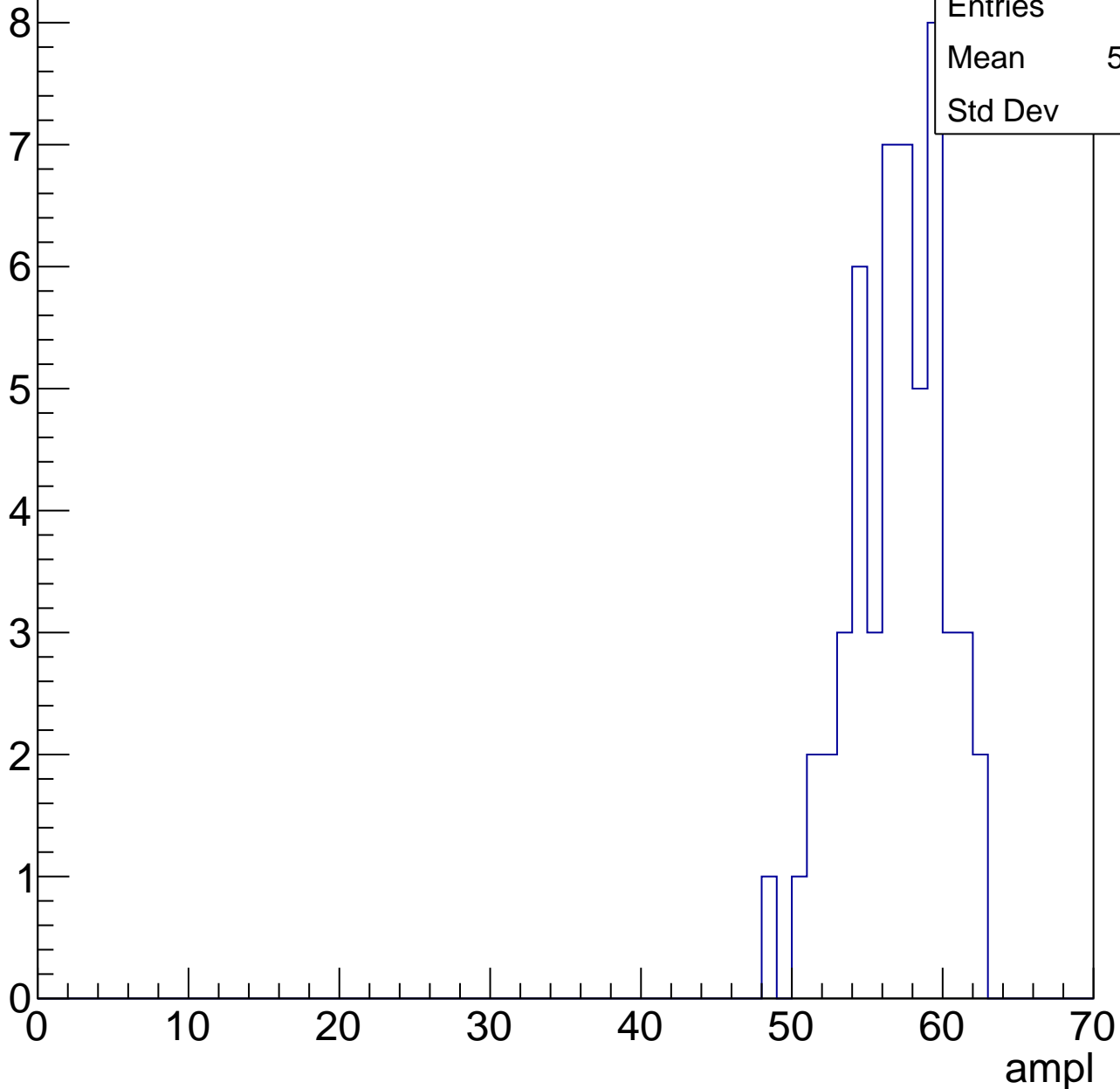


B0L002S, U4-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

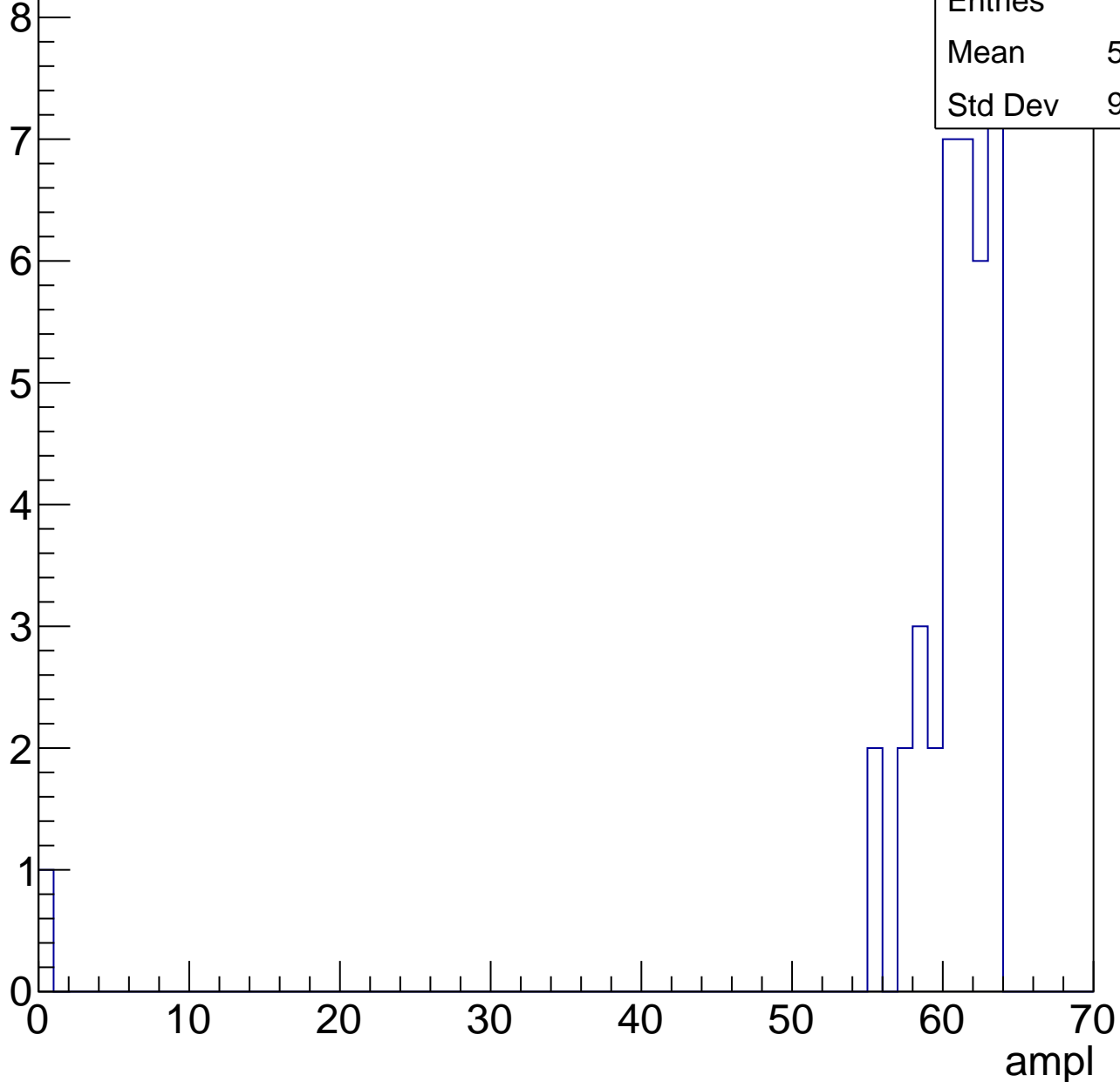
Entries	53
Mean	56.45
Std Dev	3.13



B0L002S, U4-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch61, adc0

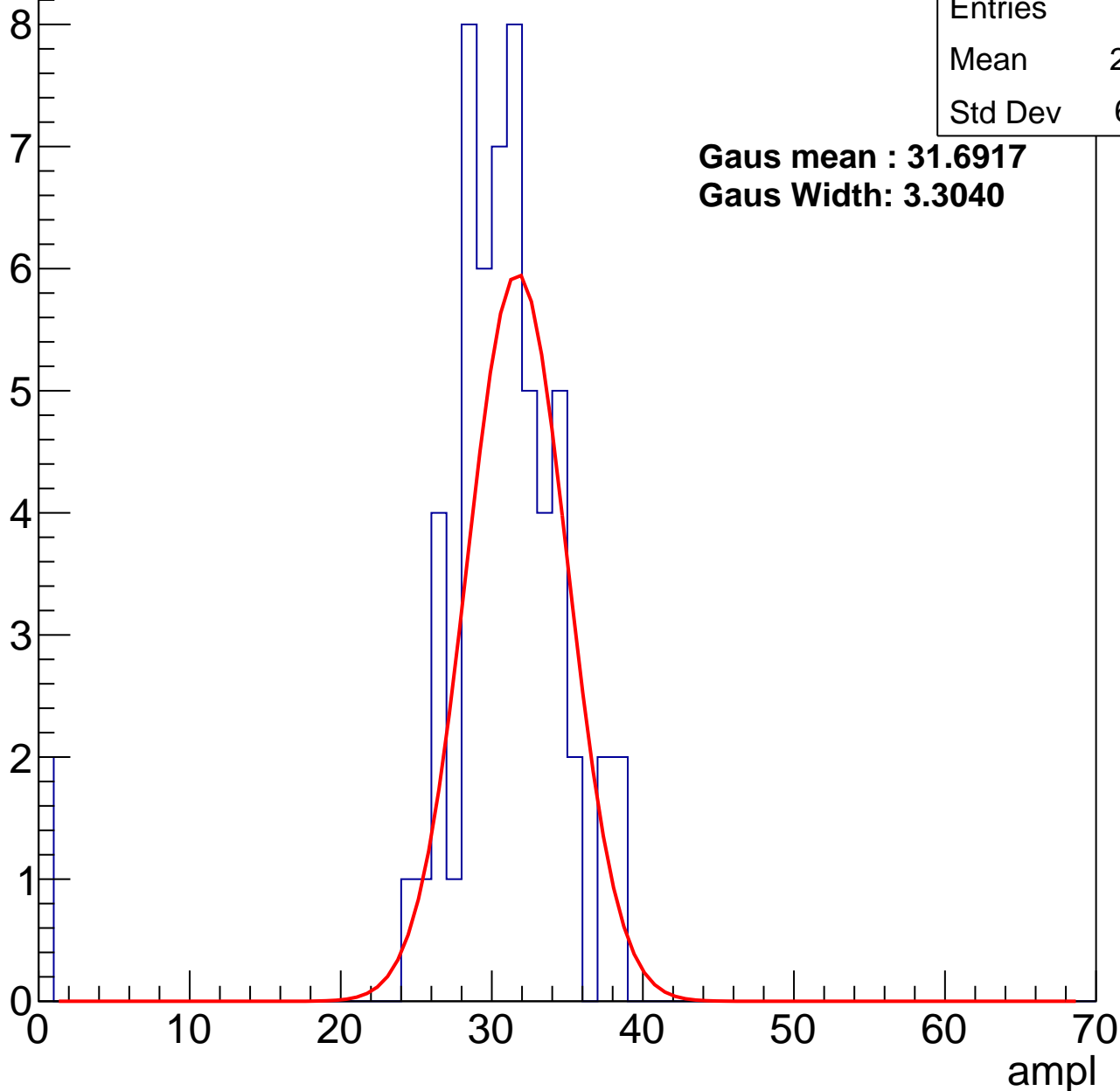
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.62
Std Dev	6.411

Gaus mean : 31.6917

Gaus Width: 3.3040



B0L002S, U4-ch61, adc1

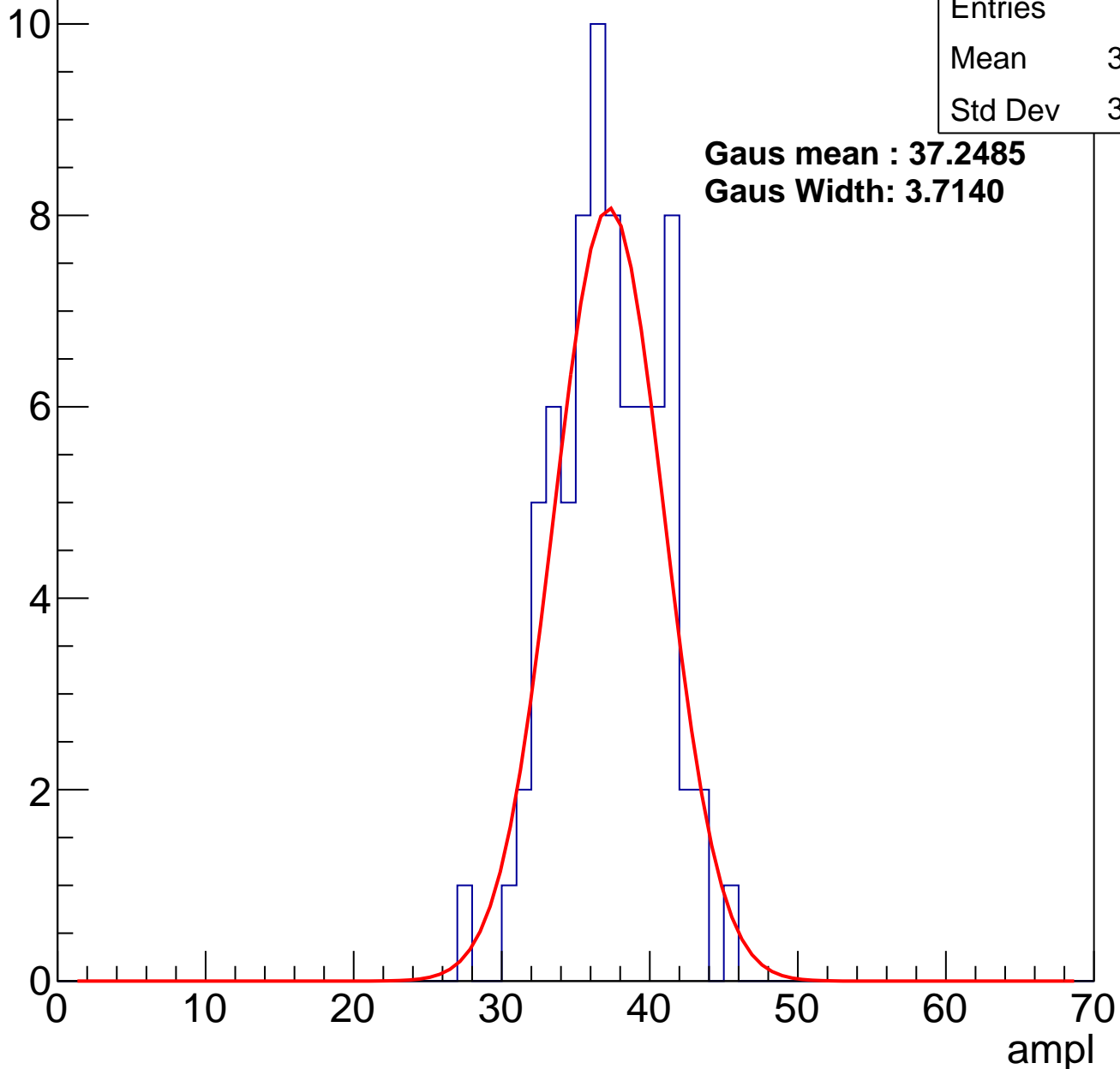
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	36.73
Std Dev	3.459

Gaus mean : 37.2485

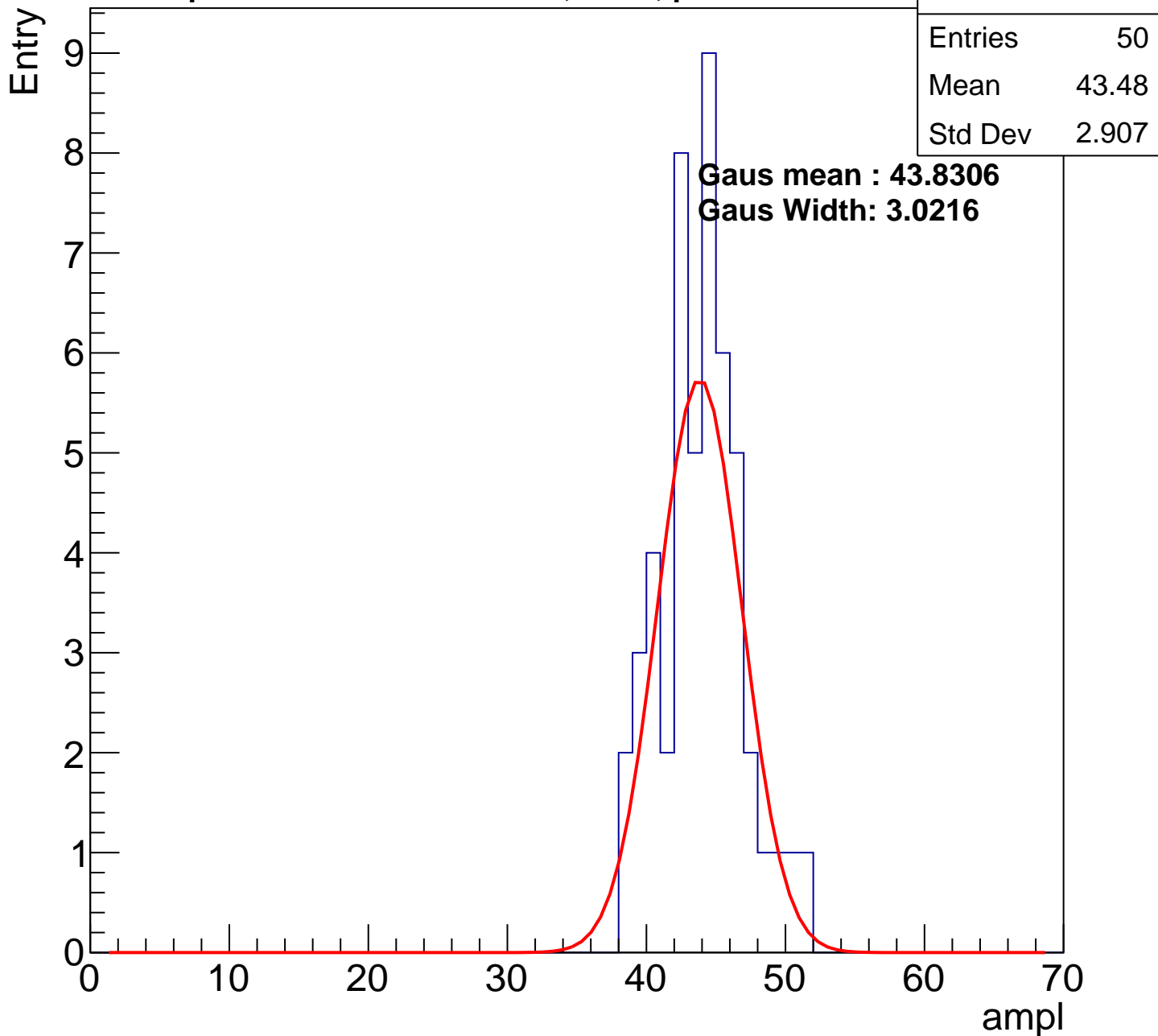
Gaus Width: 3.7140

Entry



B0L002S, U4-ch61, adc2

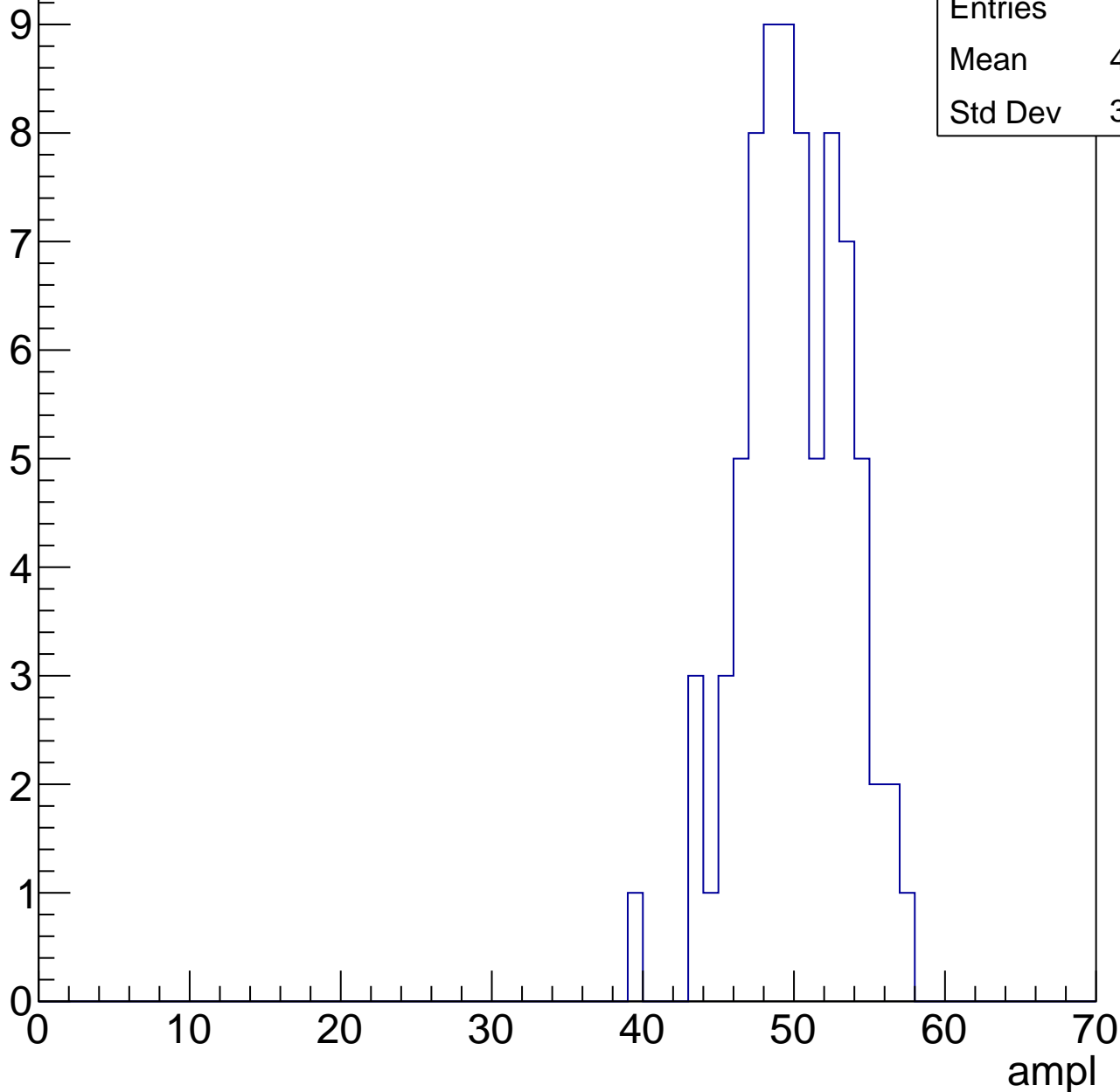
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch61, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



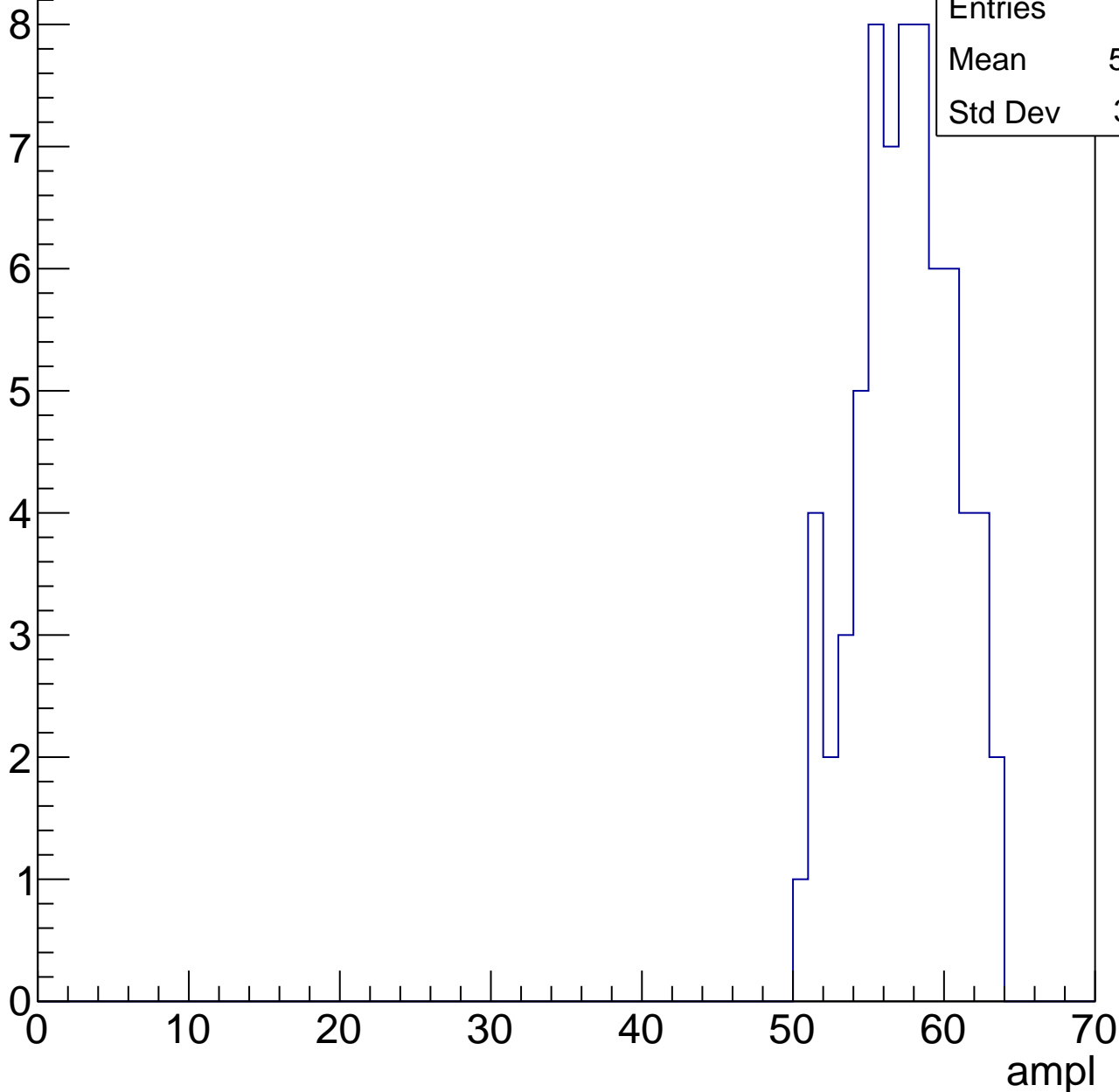
Entries	77
Mean	49.57
Std Dev	3.443

B0L002S, U4-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	56.93
Std Dev	3.201

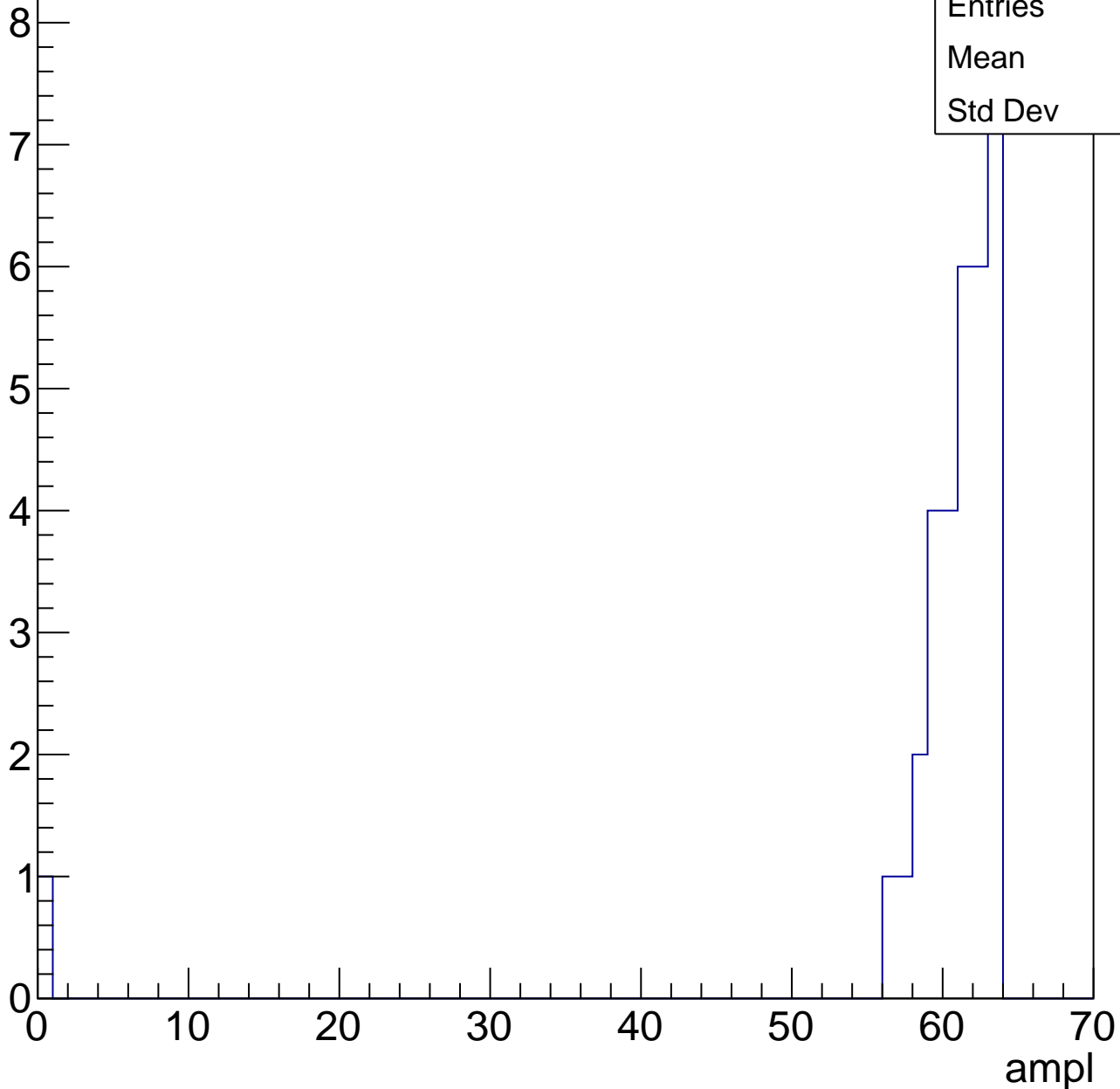


B0L002S, U4-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	59
Std Dev	10.6



B0L002S, U4-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch62, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	30.17
Std Dev	4.888

Gaus mean : 30.6976

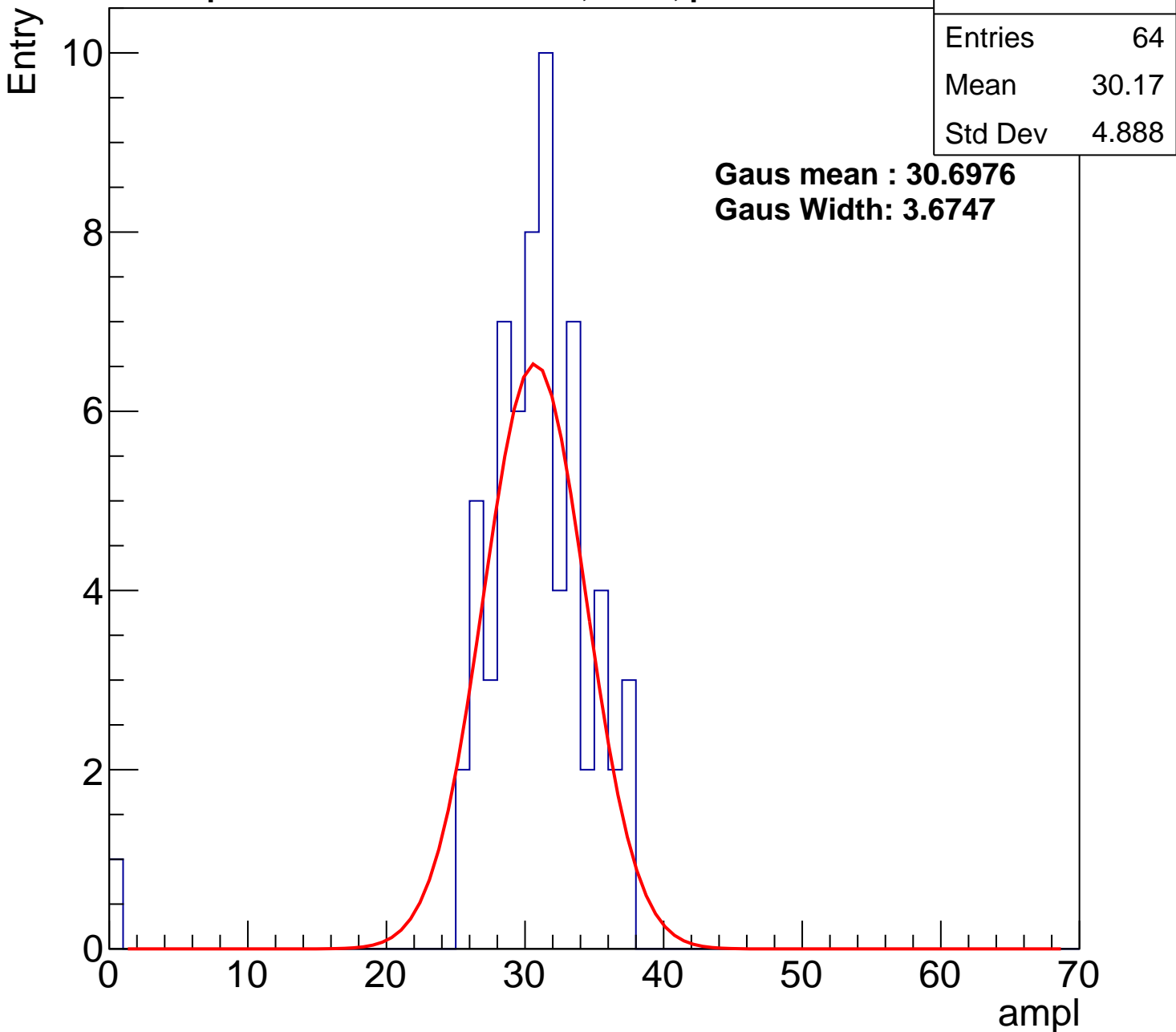
Gaus Width: 3.6747

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch62, adc1

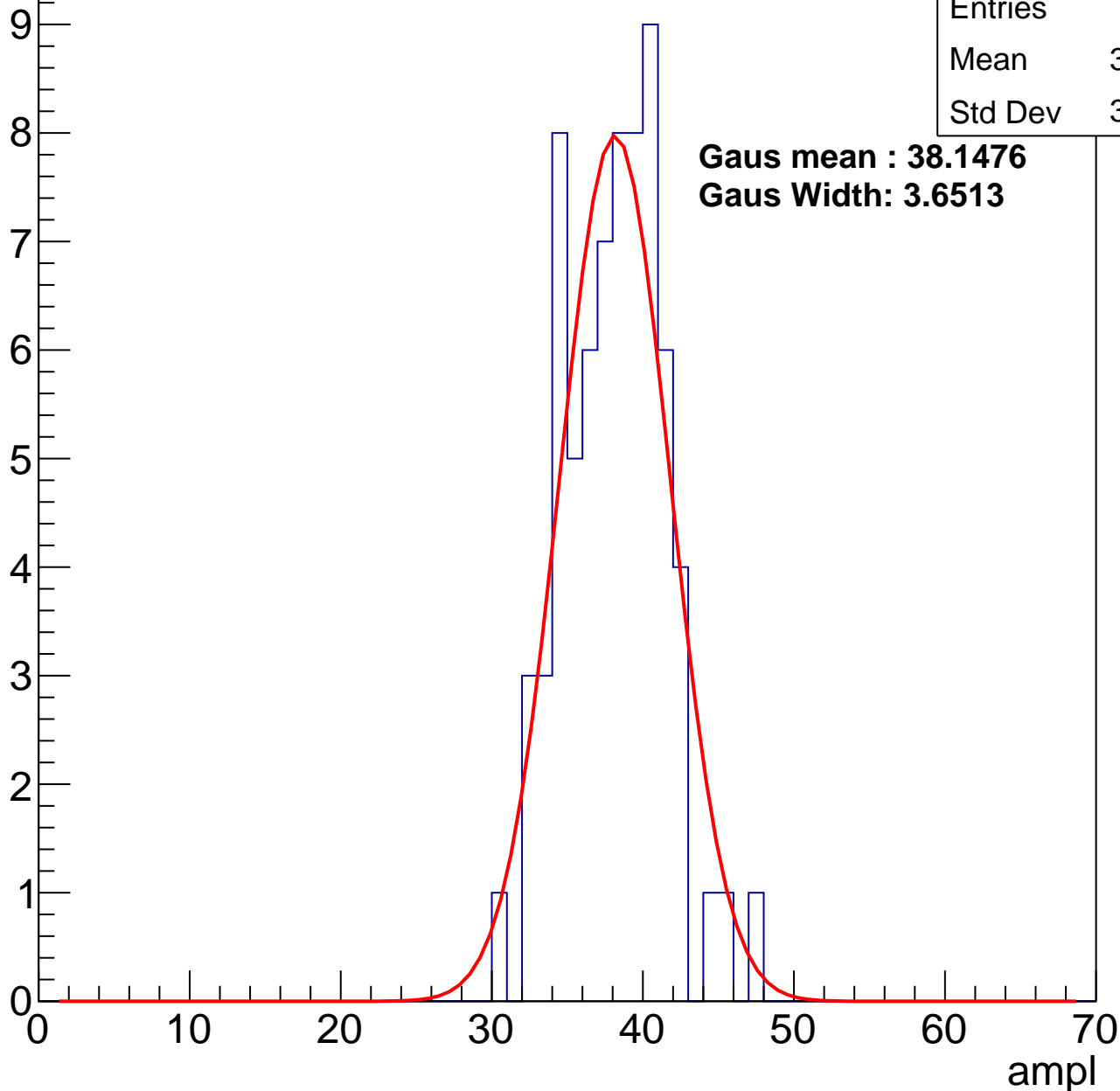
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.65
Std Dev	3.294

Gaus mean : 38.1476

Gaus Width: 3.6513



B0L002S, U4-ch62, adc2

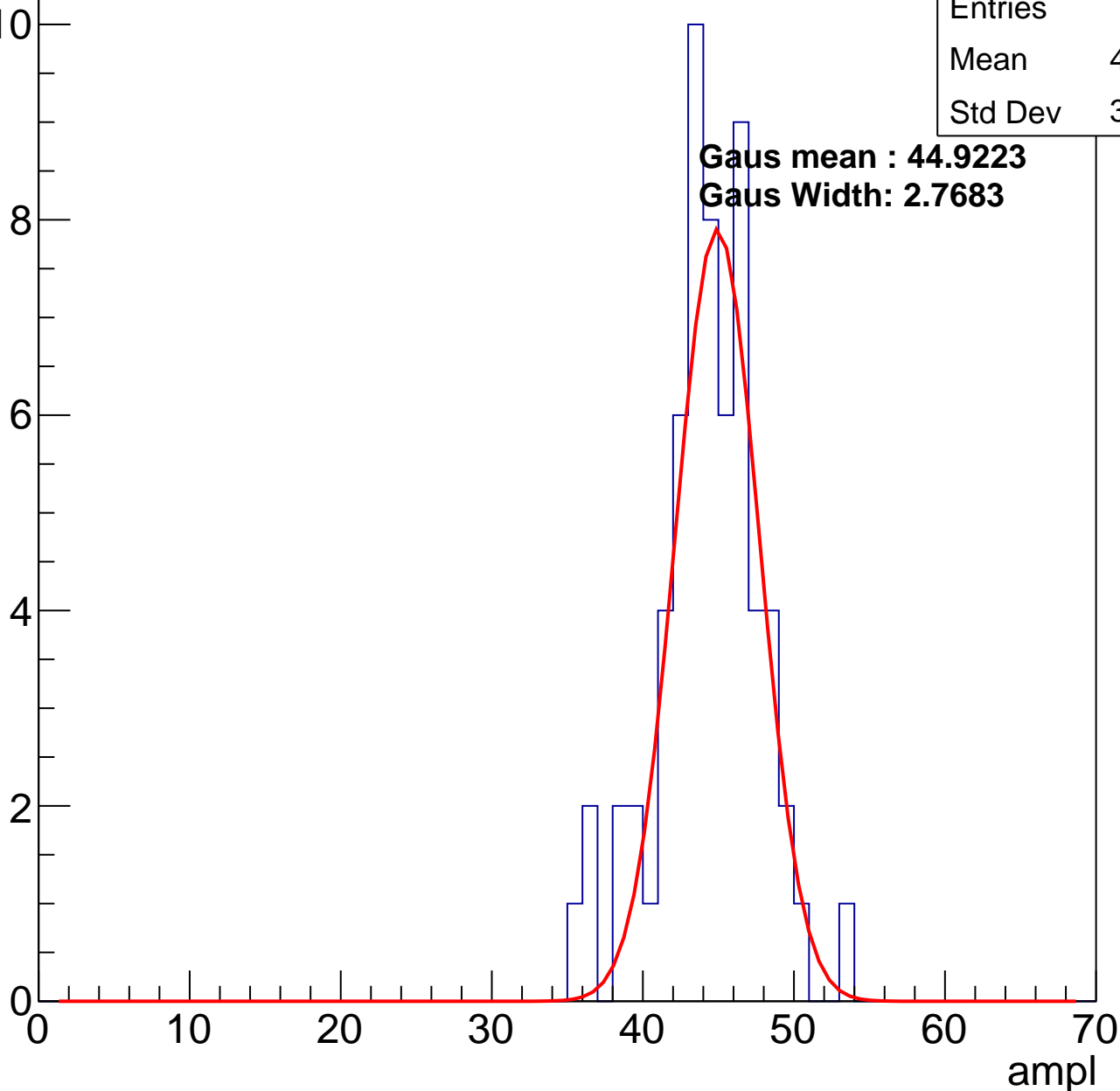
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.87
Std Dev	3.392

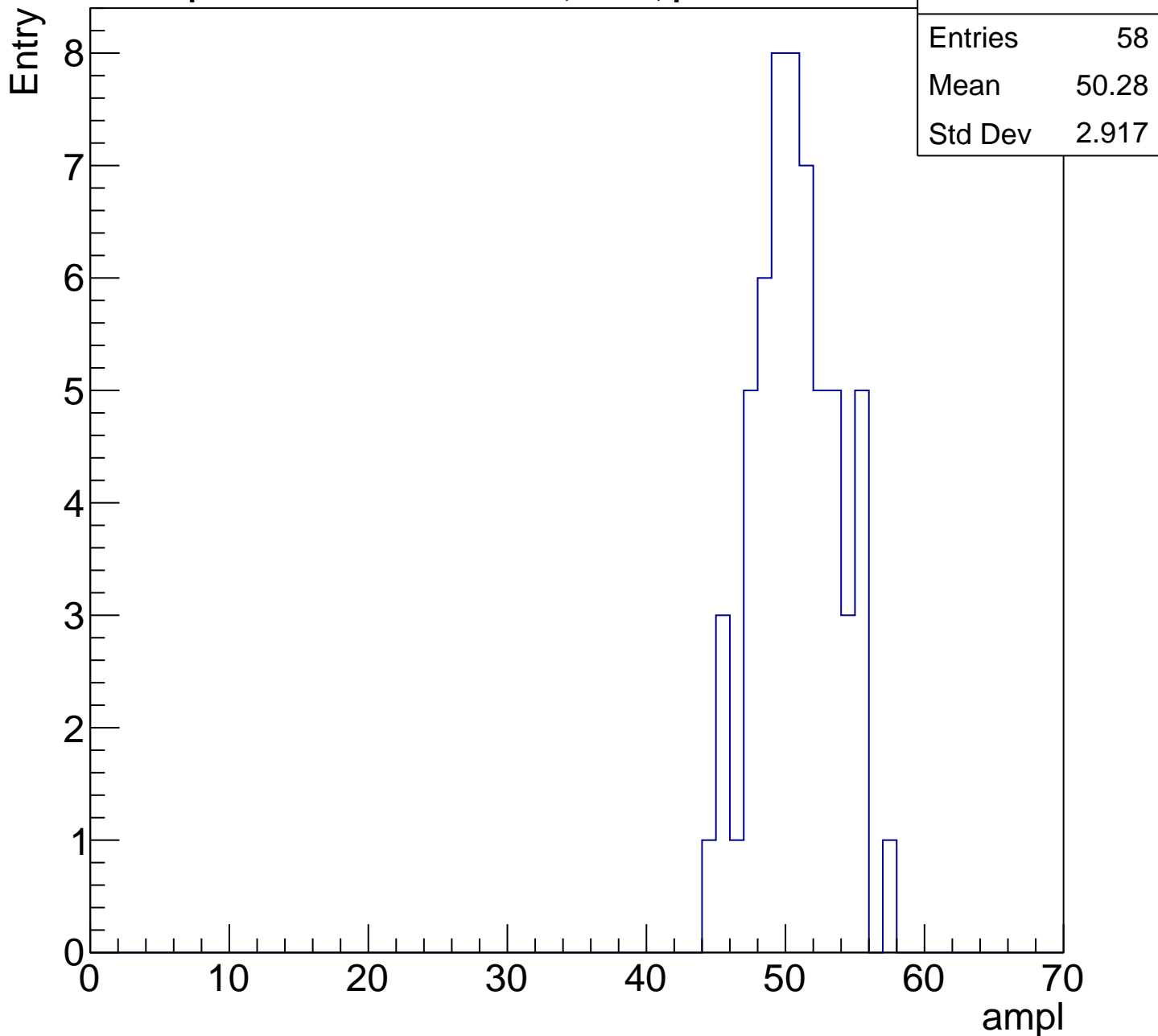
Gaus mean : 44.9223

Gaus Width: 2.7683



B0L002S, U4-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

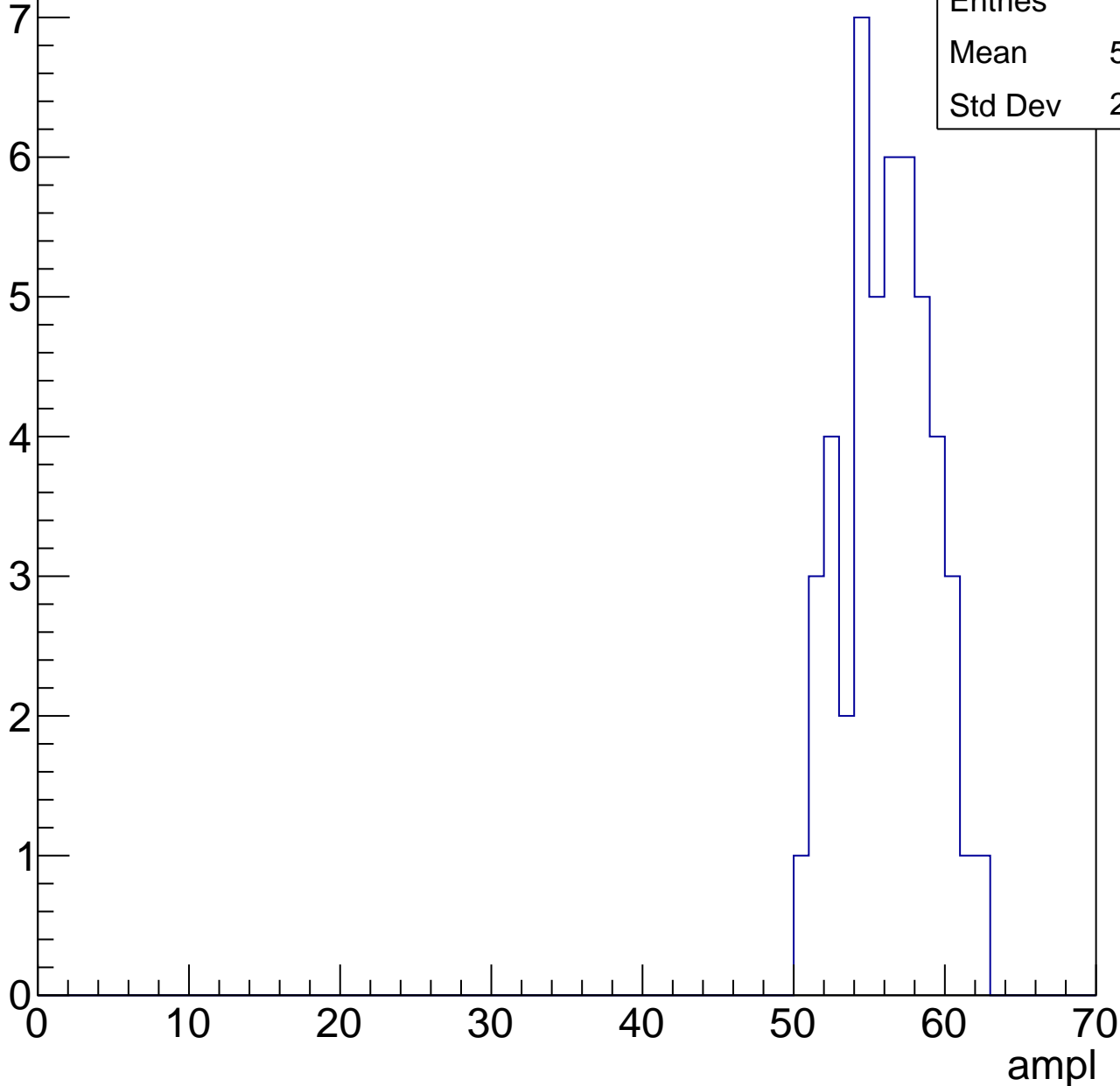


B0L002S, U4-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	55.77
Std Dev	2.867

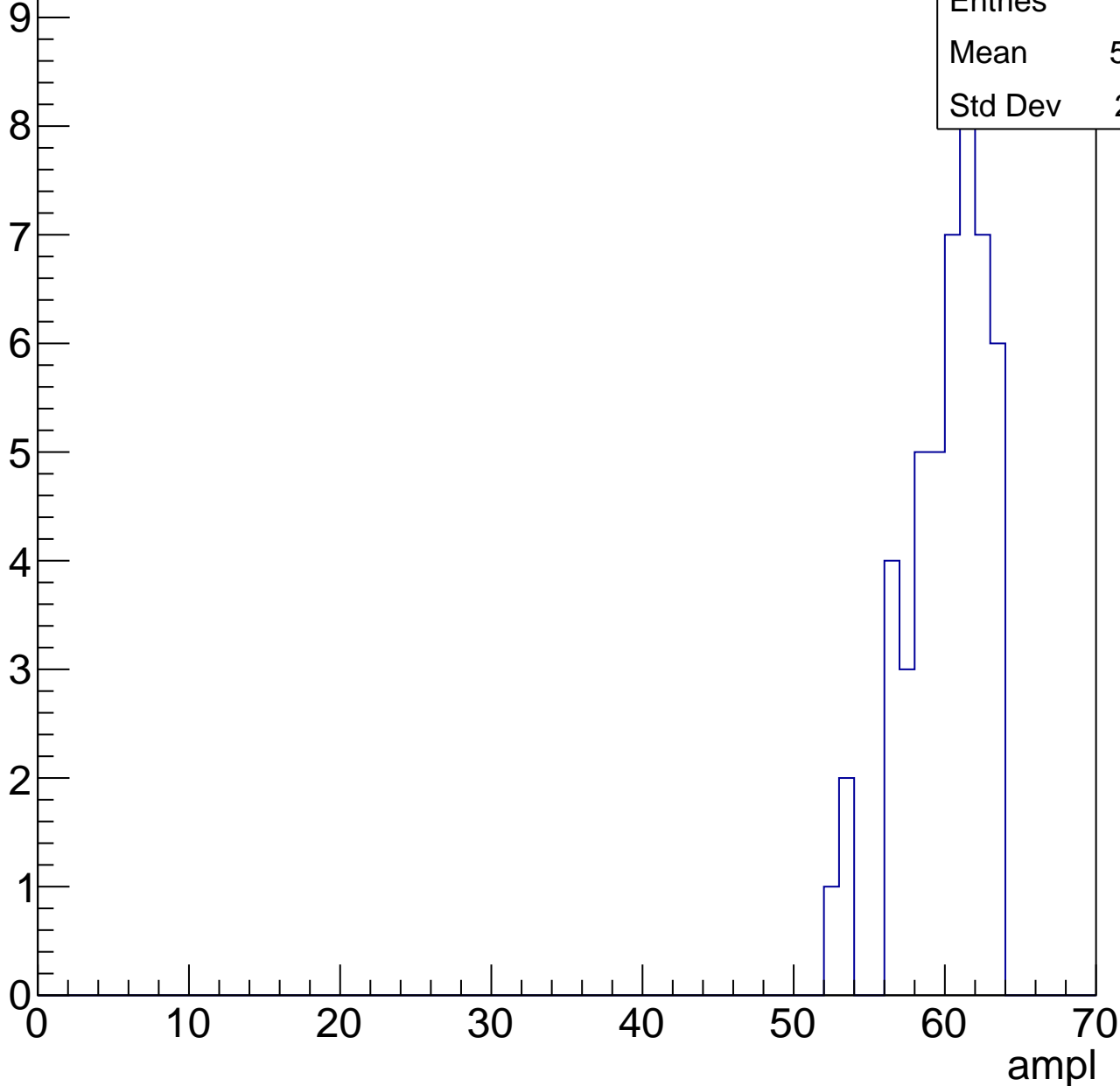


B0L002S, U4-ch62, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

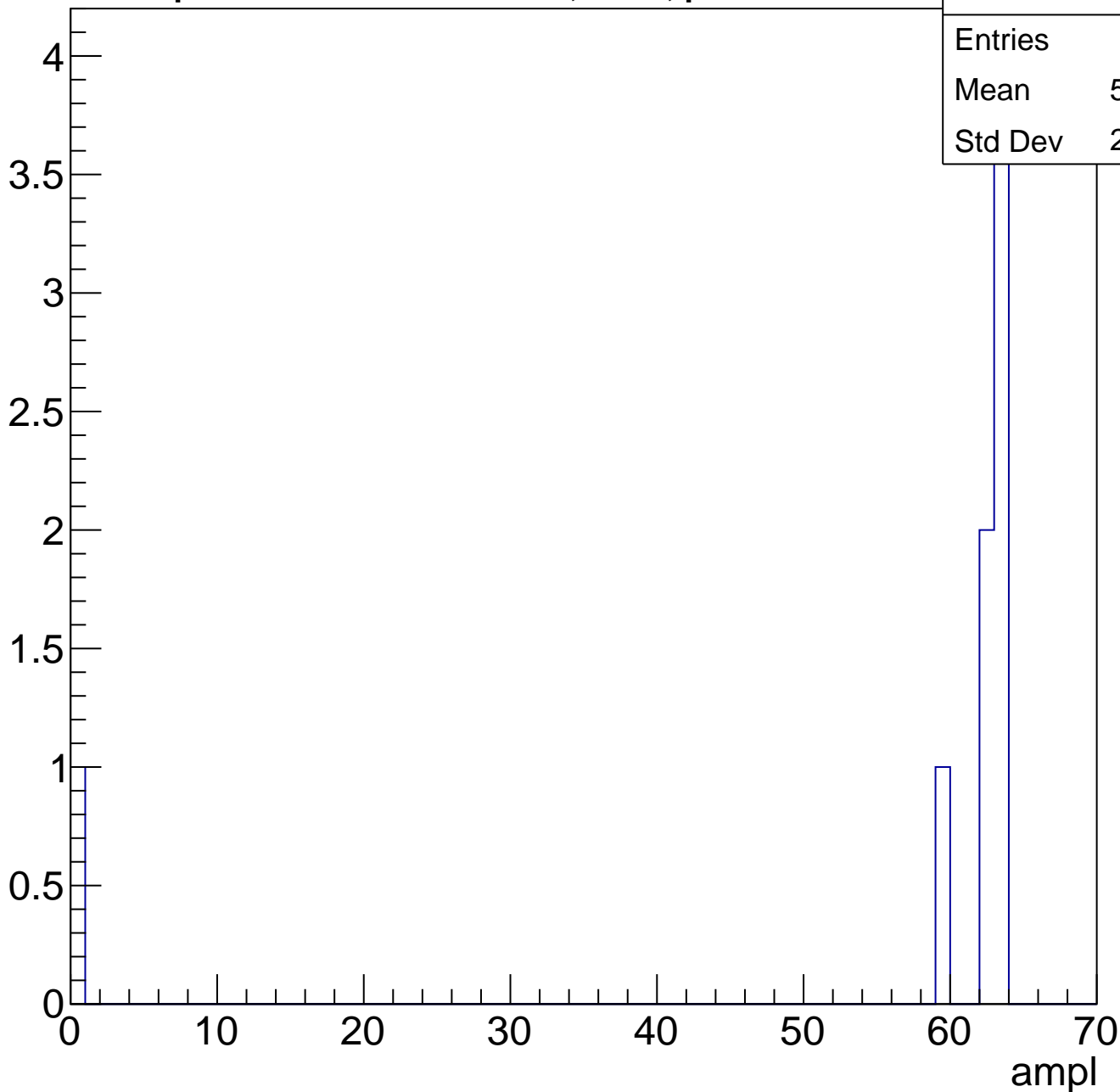
Entries	49
Mean	59.57
Std Dev	2.711



B0L002S, U4-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch63, adc0

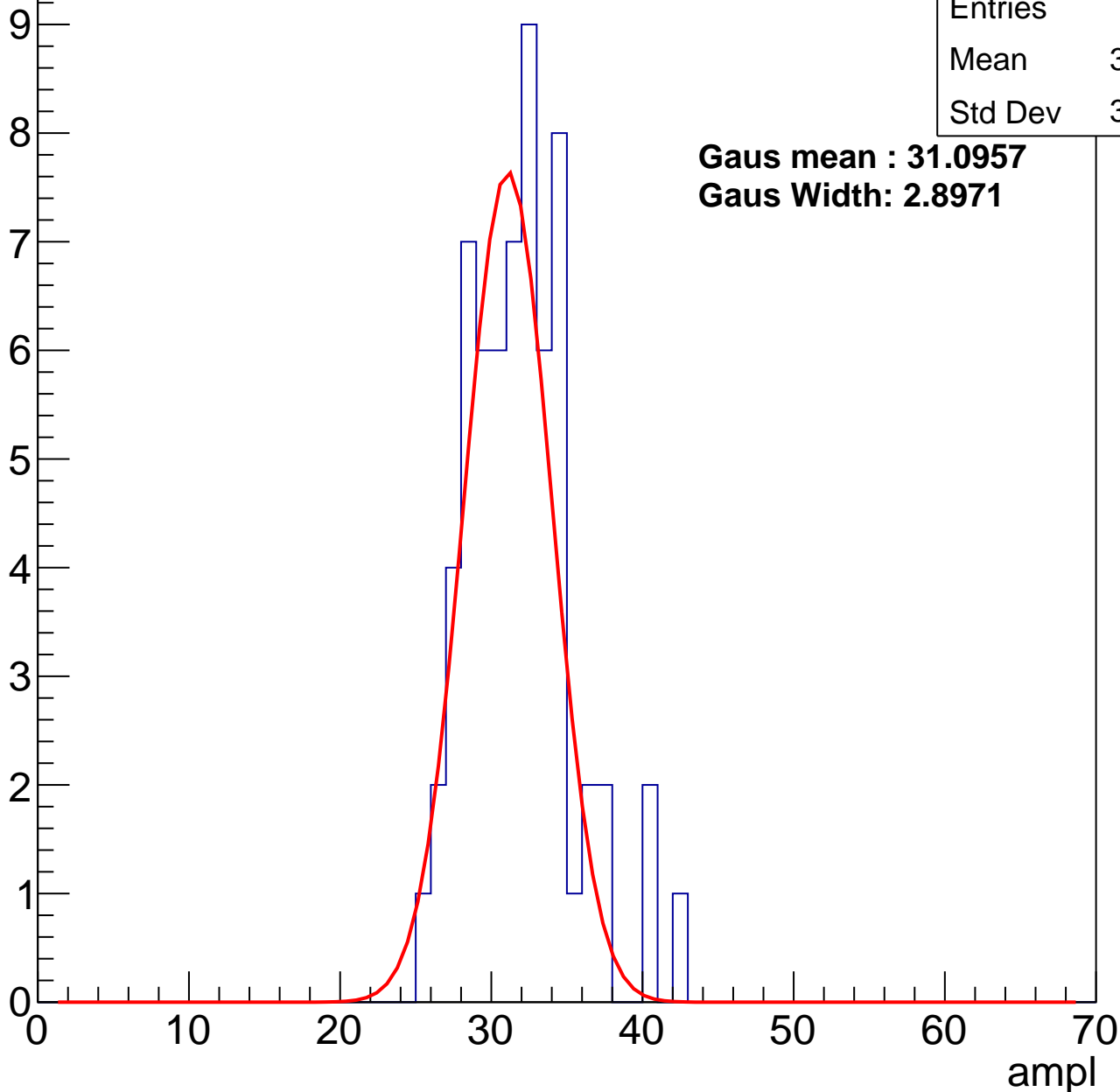
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.45
Std Dev	3.437

Gaus mean : 31.0957

Gaus Width: 2.8971



B0L002S, U4-ch63, adc1

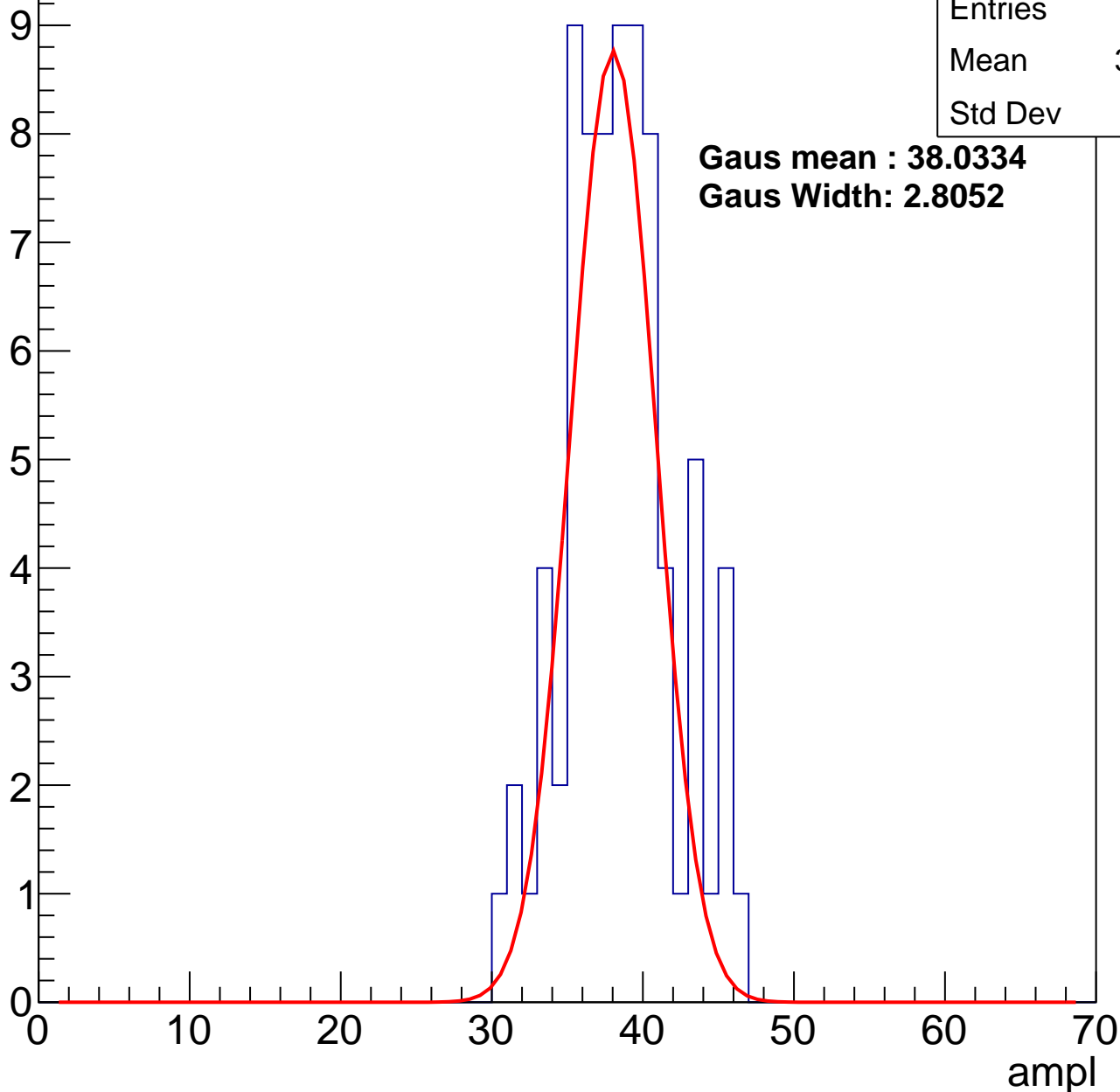
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	38.01
Std Dev	3.54

Gaus mean : 38.0334

Gaus Width: 2.8052



B0L002S, U4-ch63, adc2

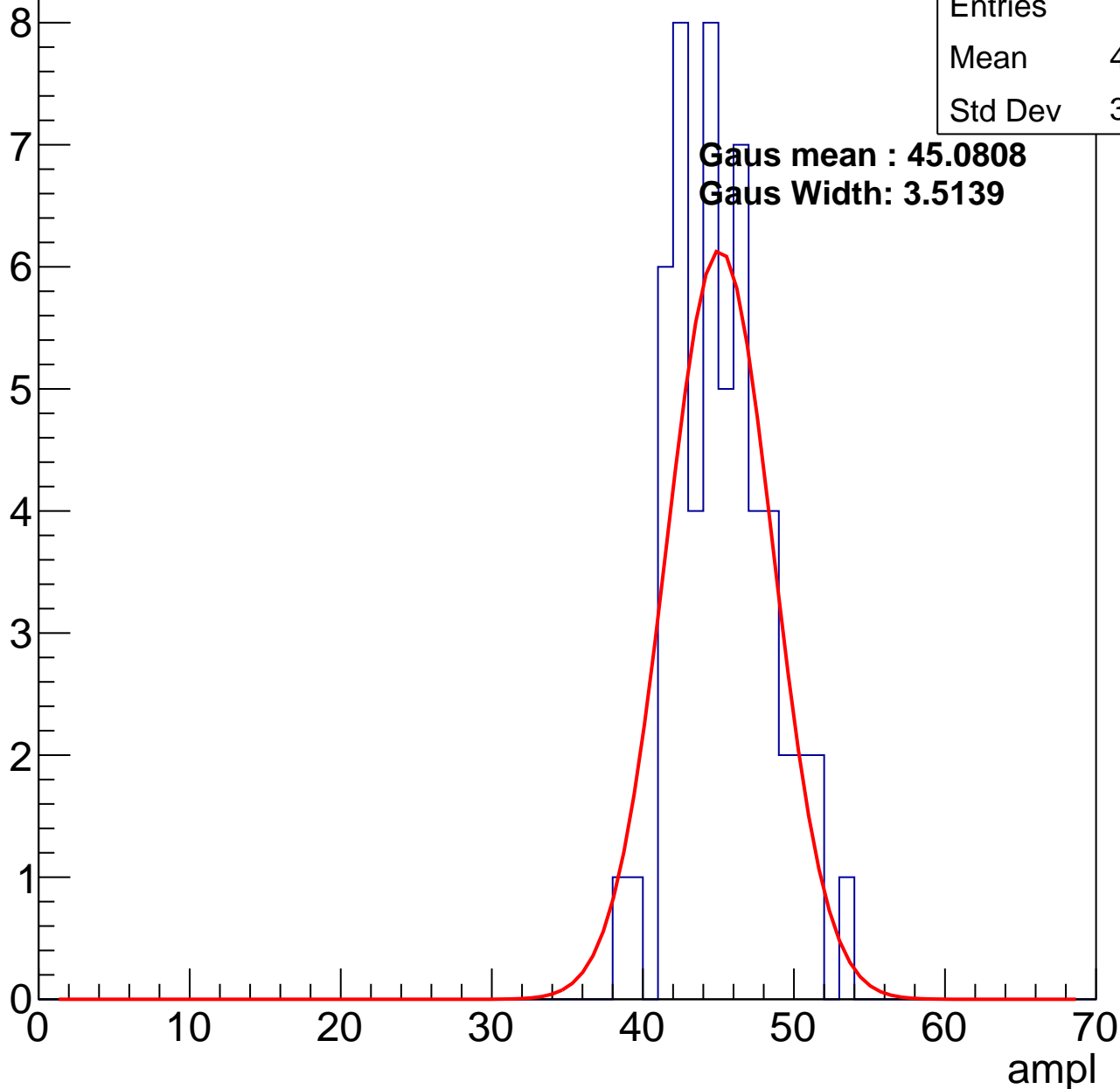
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.78
Std Dev	3.166

Gaus mean : 45.0808

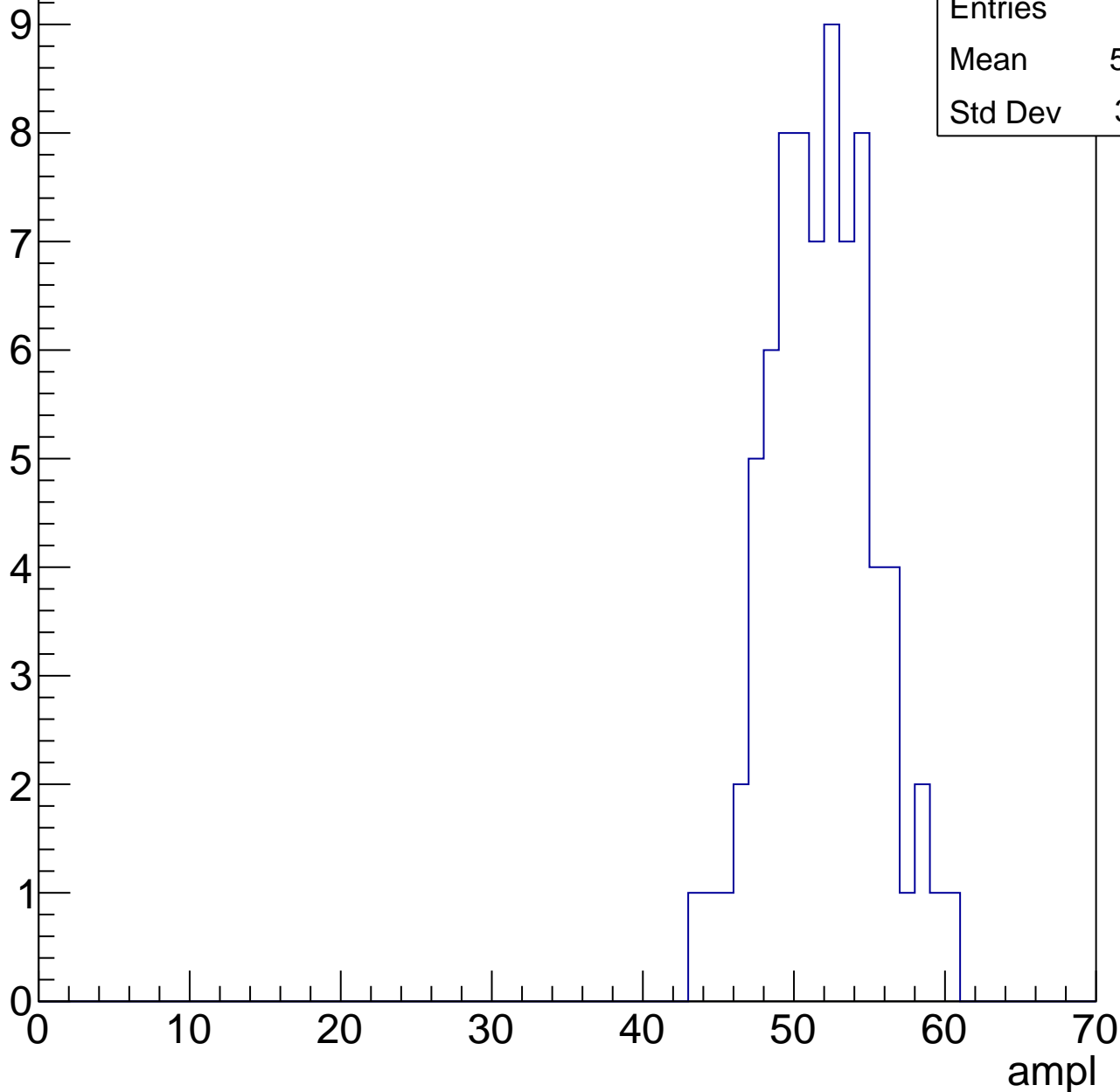
Gaus Width: 3.5139



B0L002S, U4-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



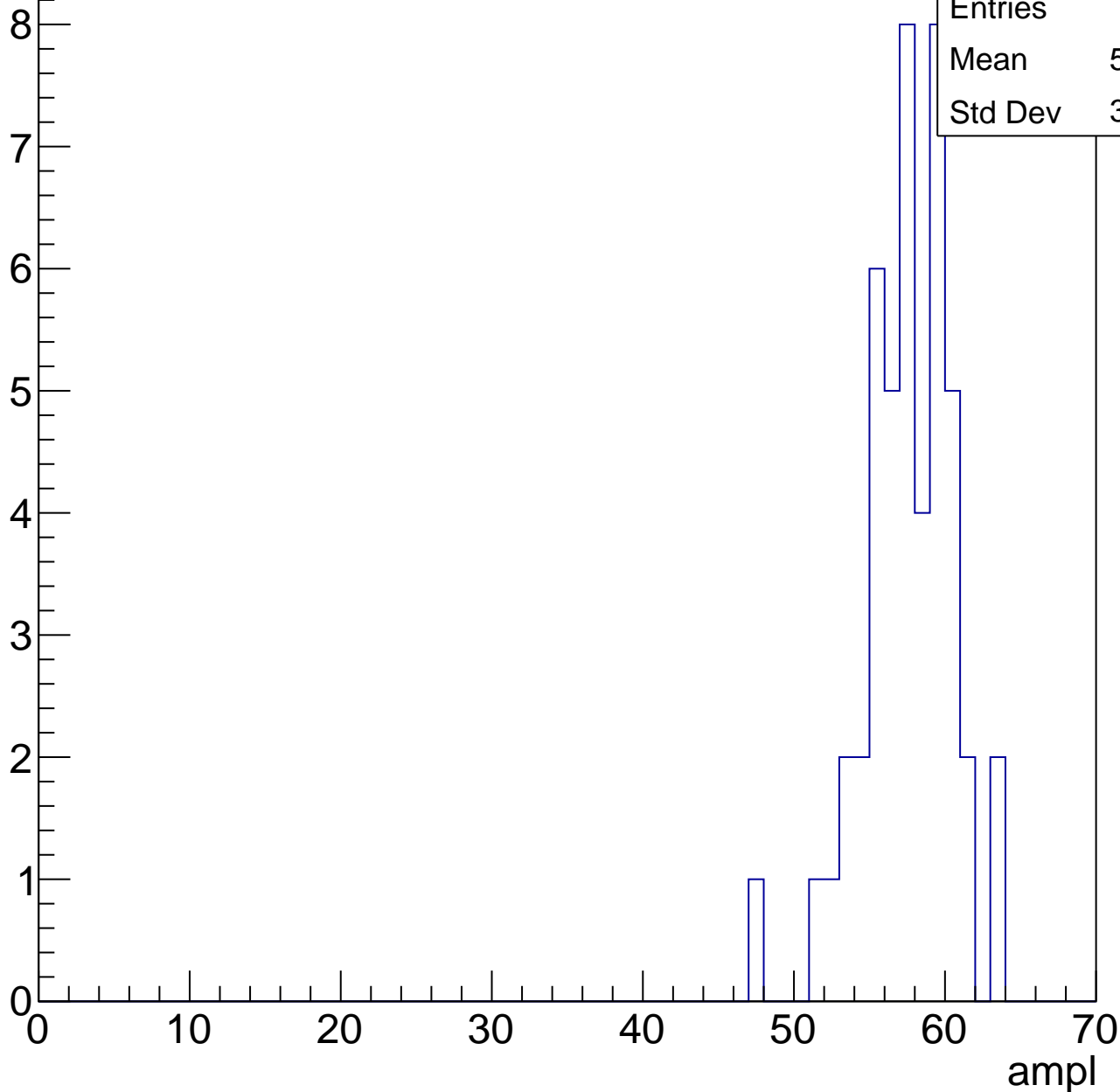
Entries	76
Mean	51.36
Std Dev	3.471

B0L002S, U4-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.06
Std Dev	3.006

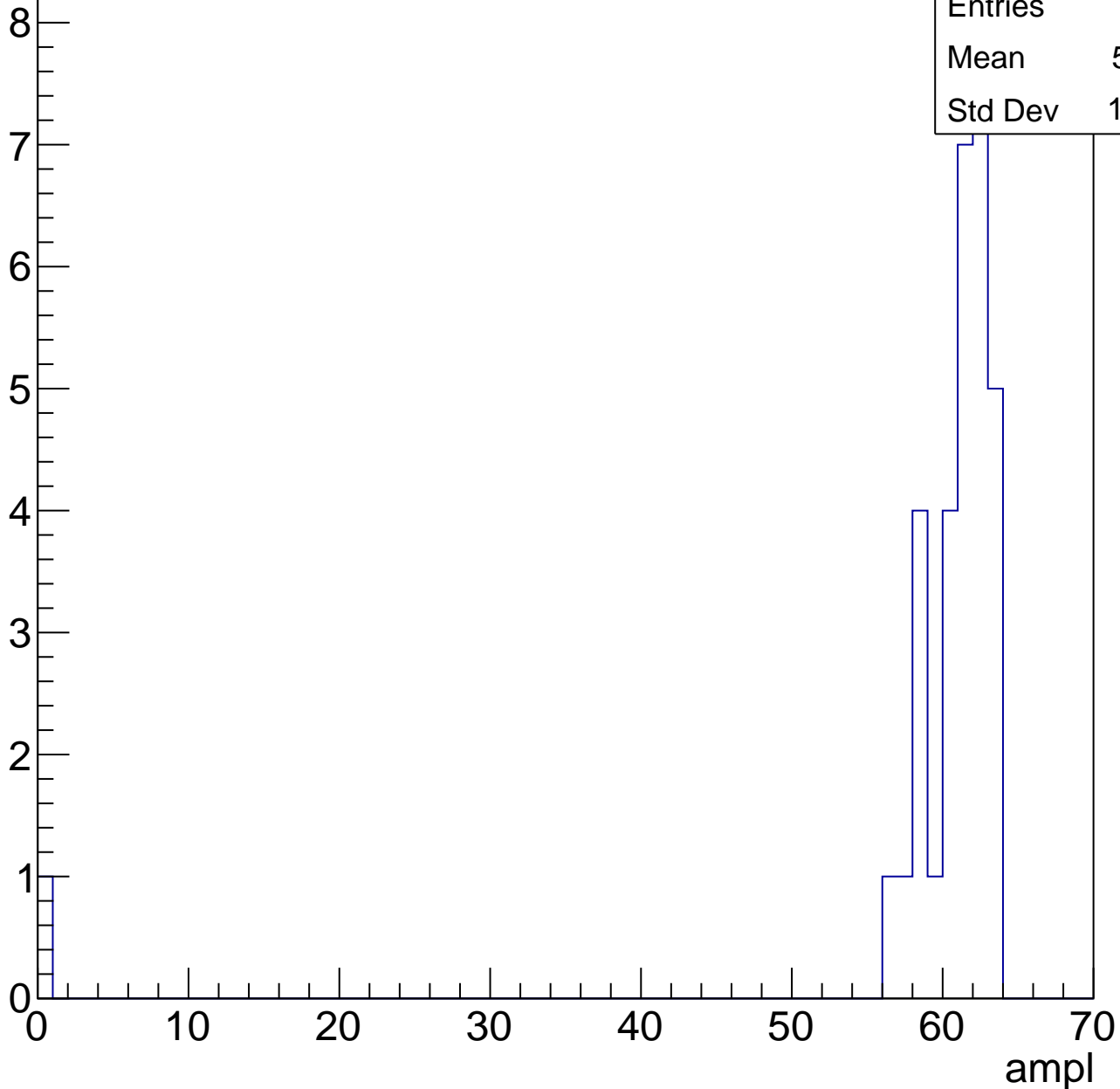


B0L002S, U4-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

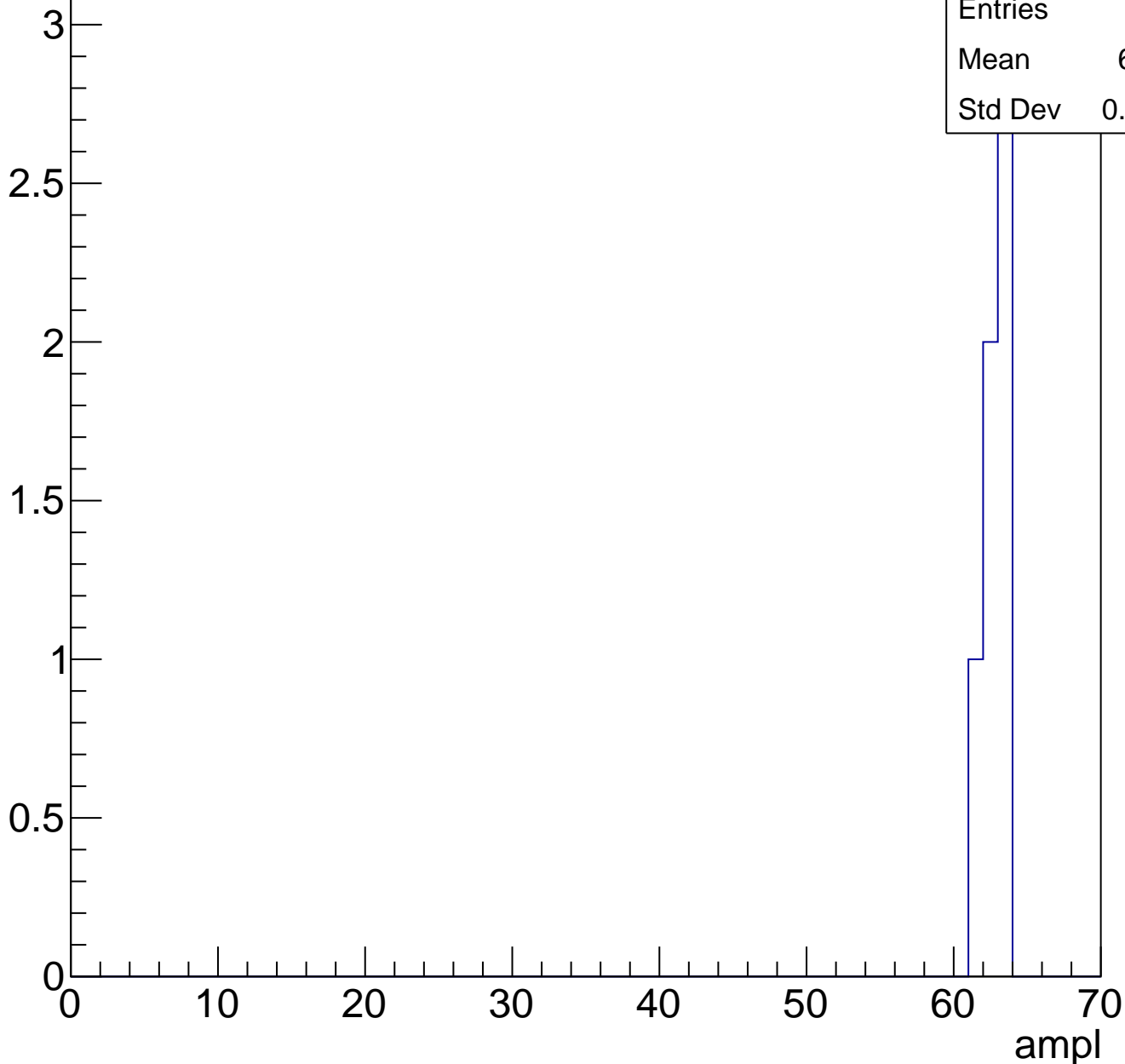
Entries	32
Mean	58.81
Std Dev	10.73



B0L002S, U4-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	62.33
Std Dev	0.7454

B0L002S, U4-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch64, adc0

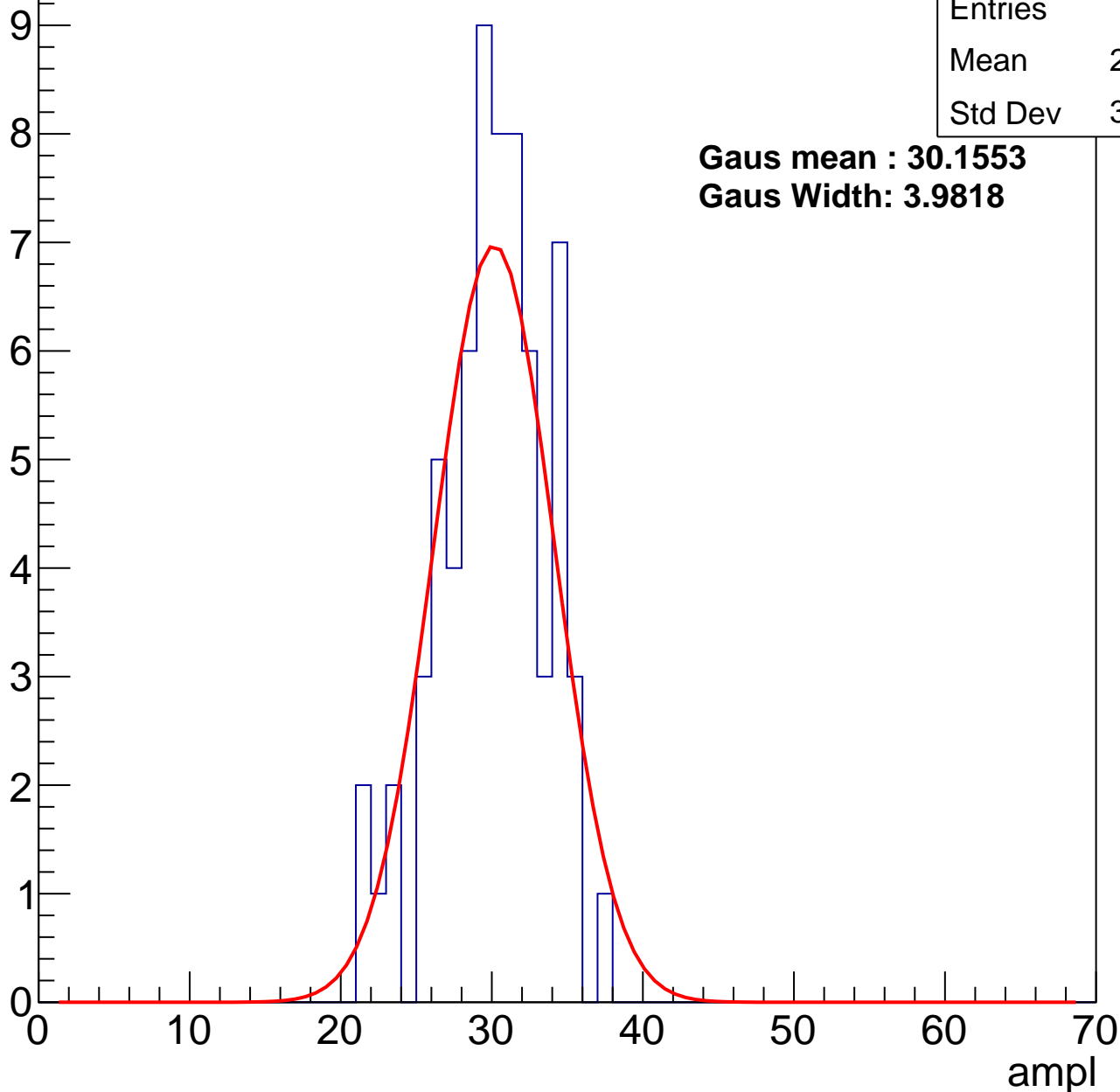
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.57
Std Dev	3.495

Gaus mean : 30.1553

Gaus Width: 3.9818



B0L002S, U4-ch64, adc1

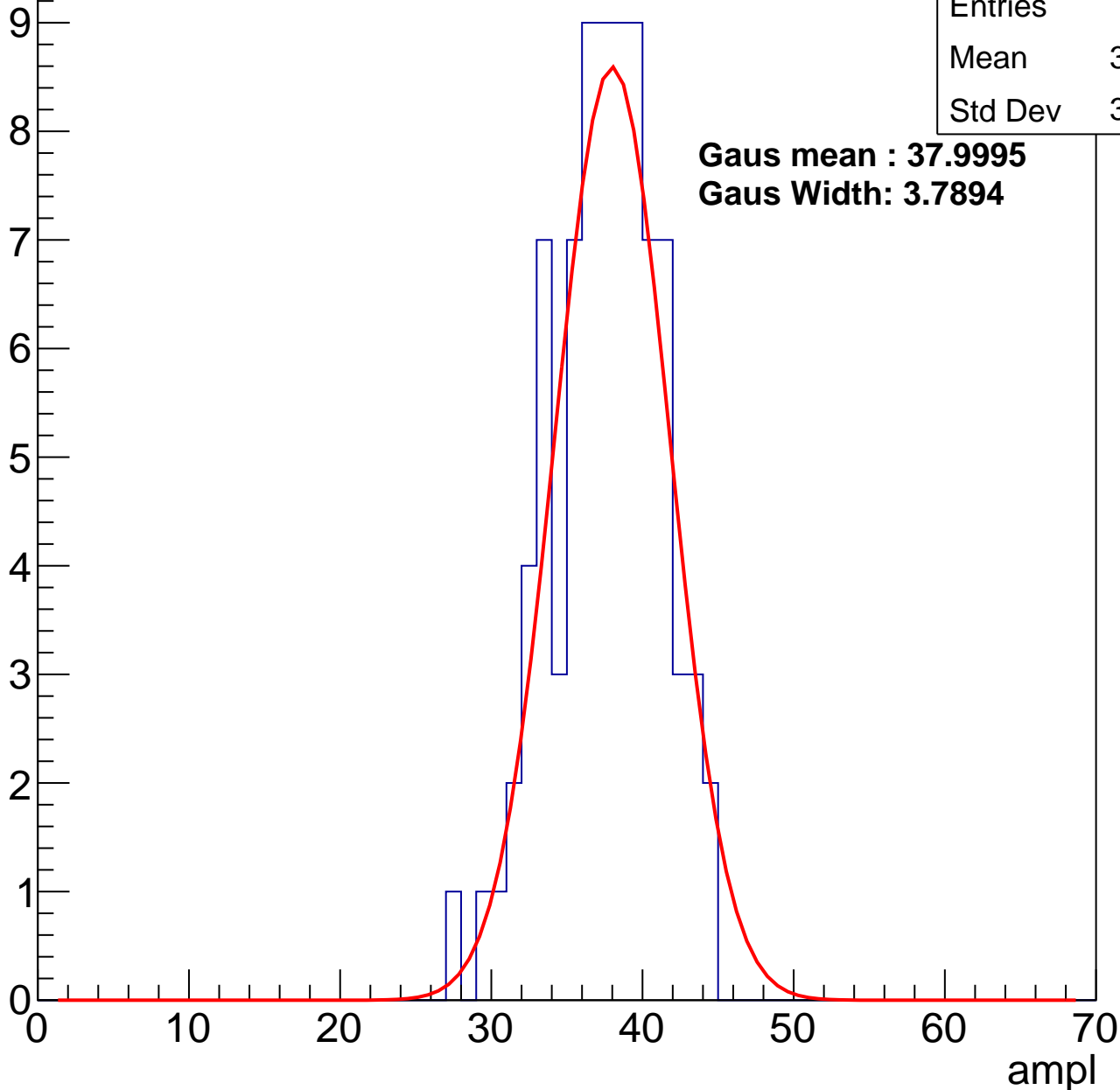
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	84
Mean	37.07
Std Dev	3.555

Gaus mean : 37.9995

Gaus Width: 3.7894

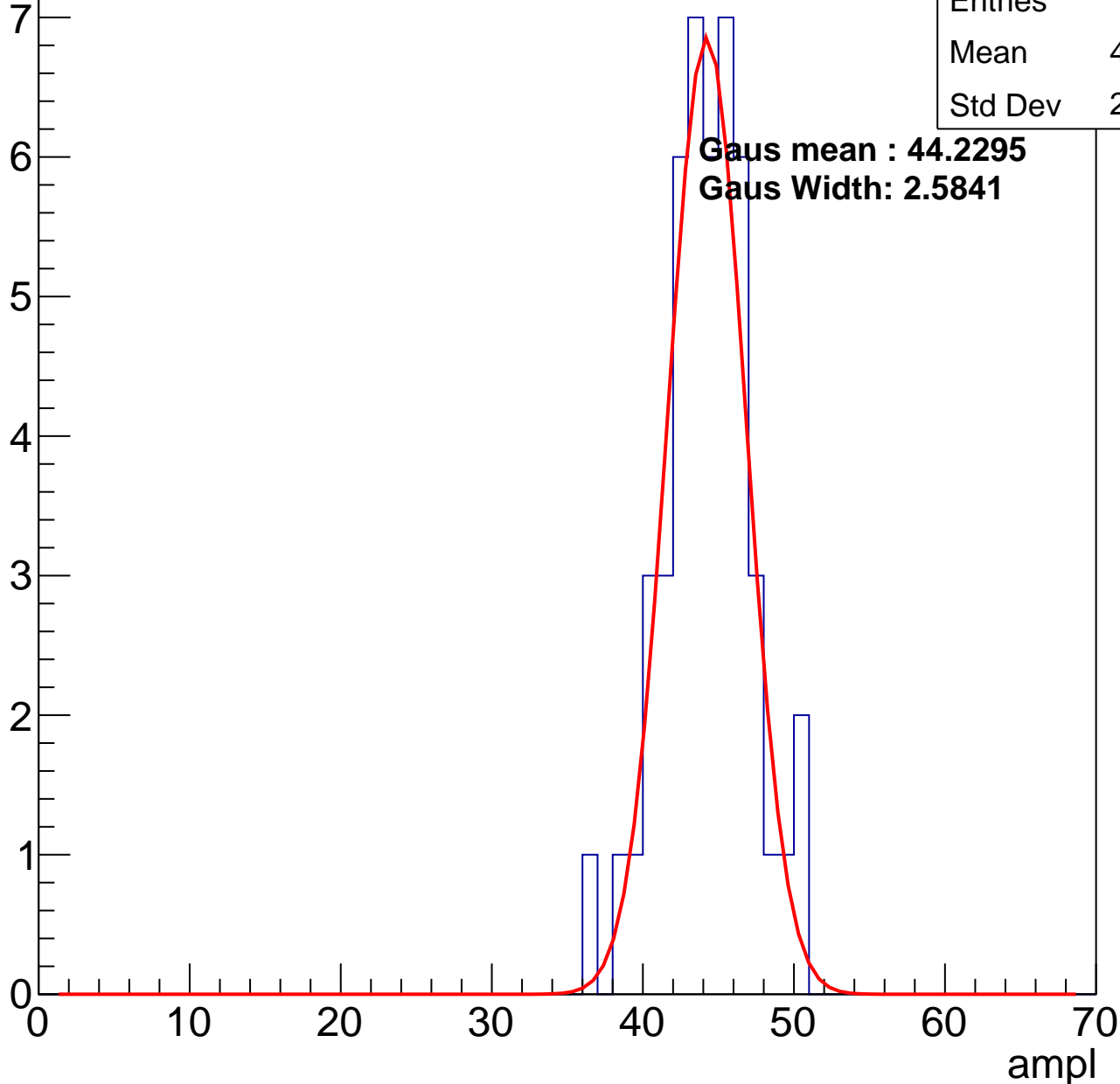


B0L002S, U4-ch64, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.79
Std Dev	2.886

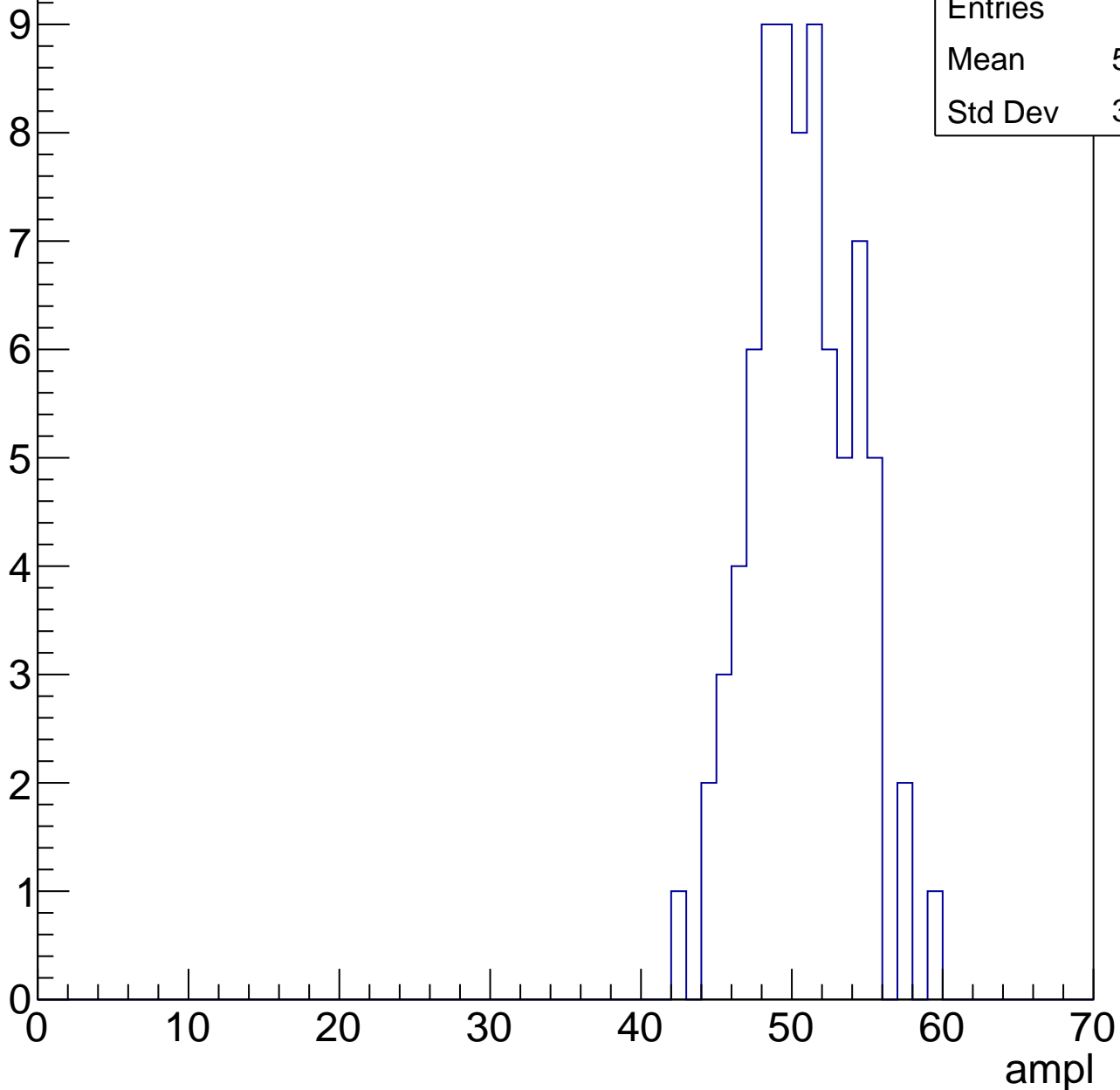


B0L002S, U4-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	50.21
Std Dev	3.351

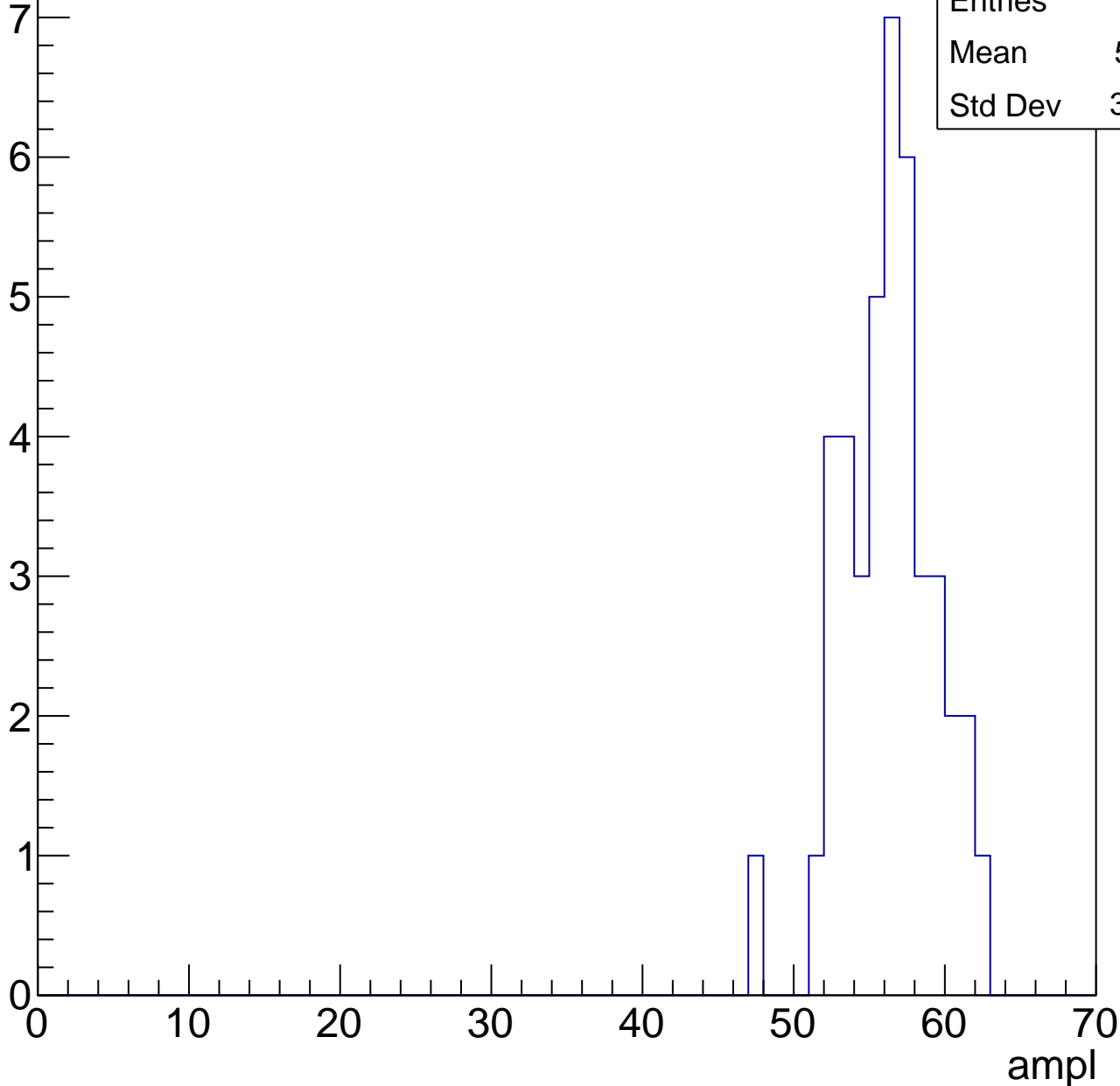


B0L002S, U4-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	55.81
Std Dev	3.026

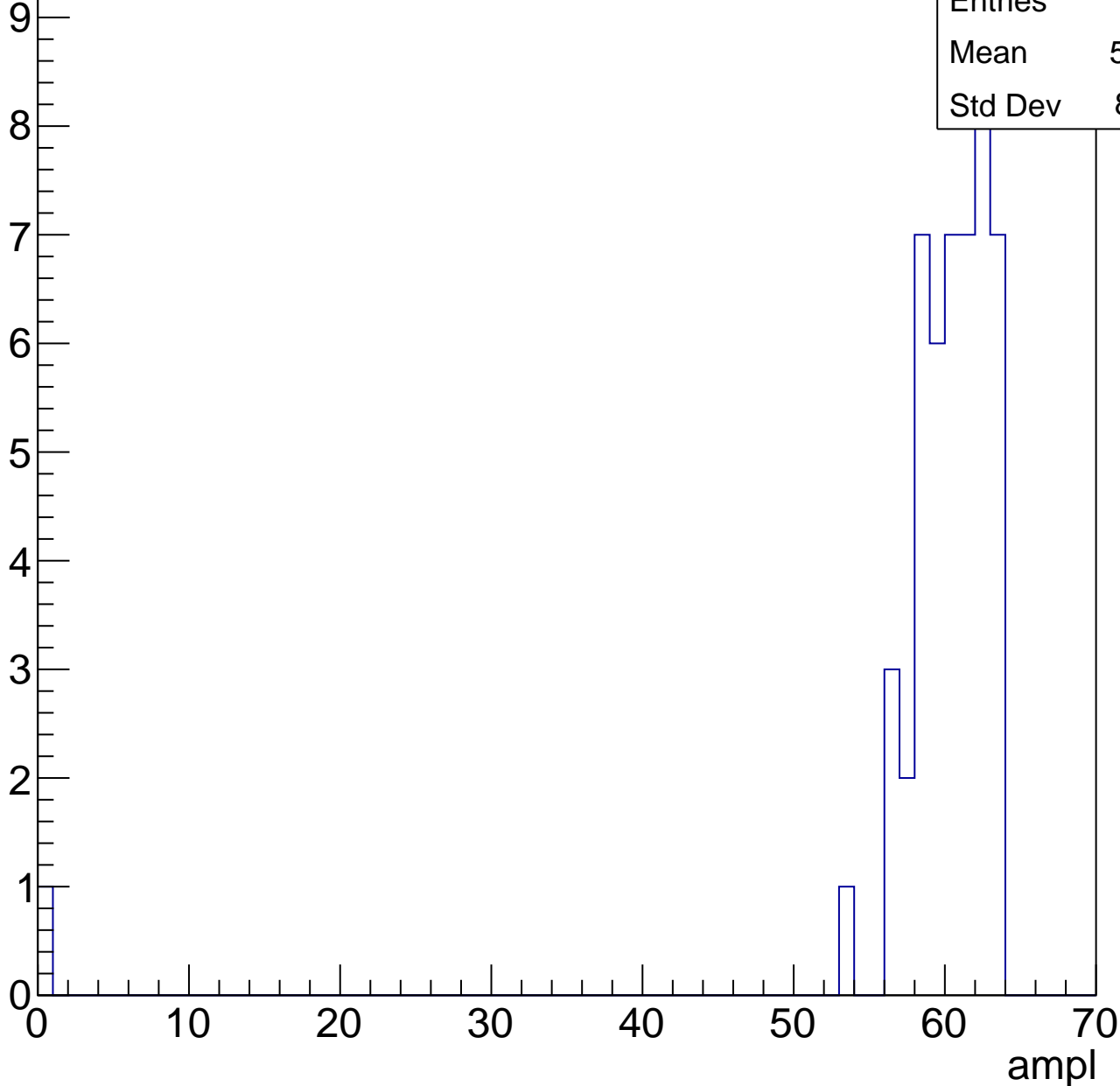


B0L002S, U4-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

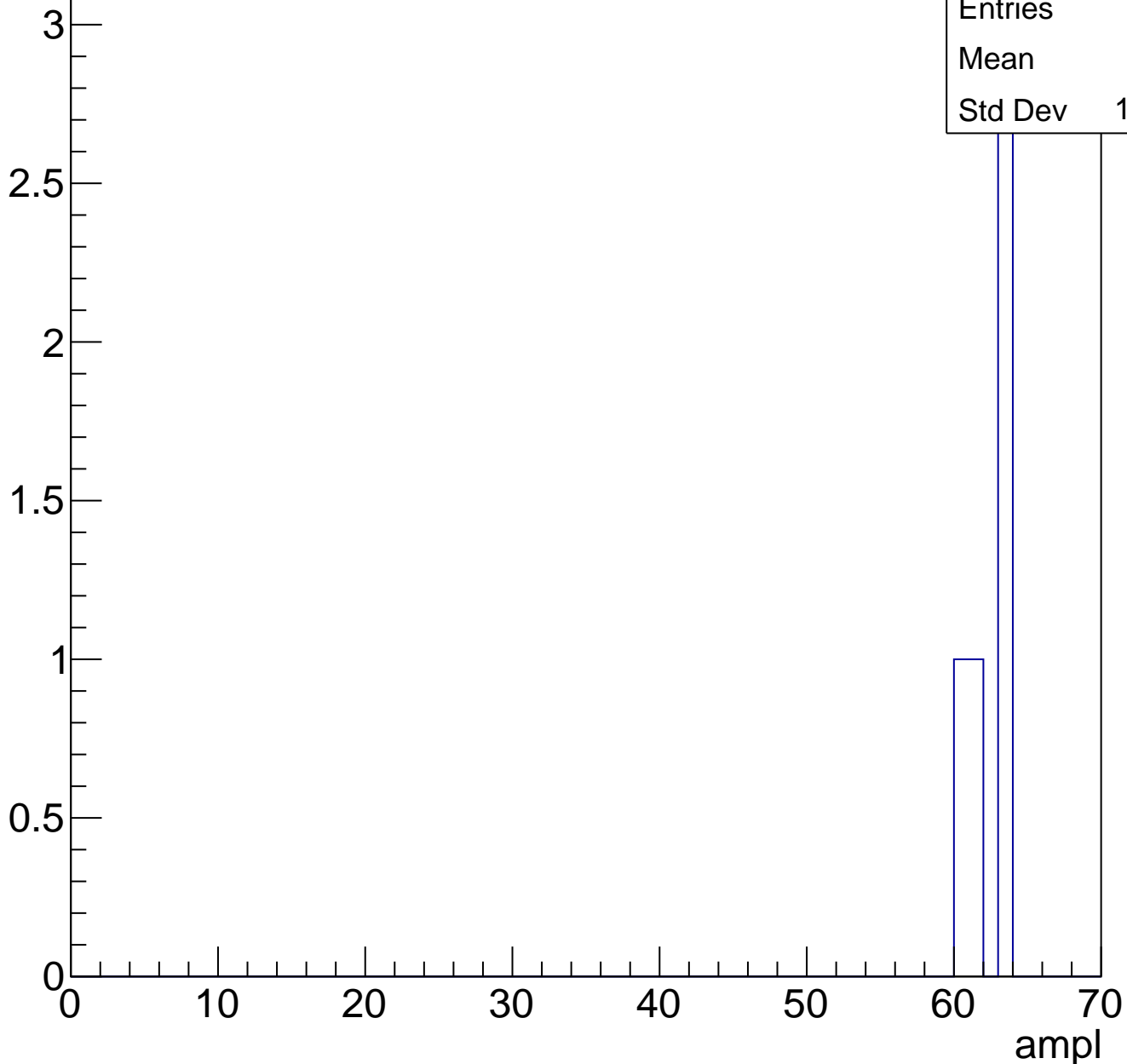
Entries	50
Mean	58.82
Std Dev	8.701



B0L002S, U4-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch65, adc0

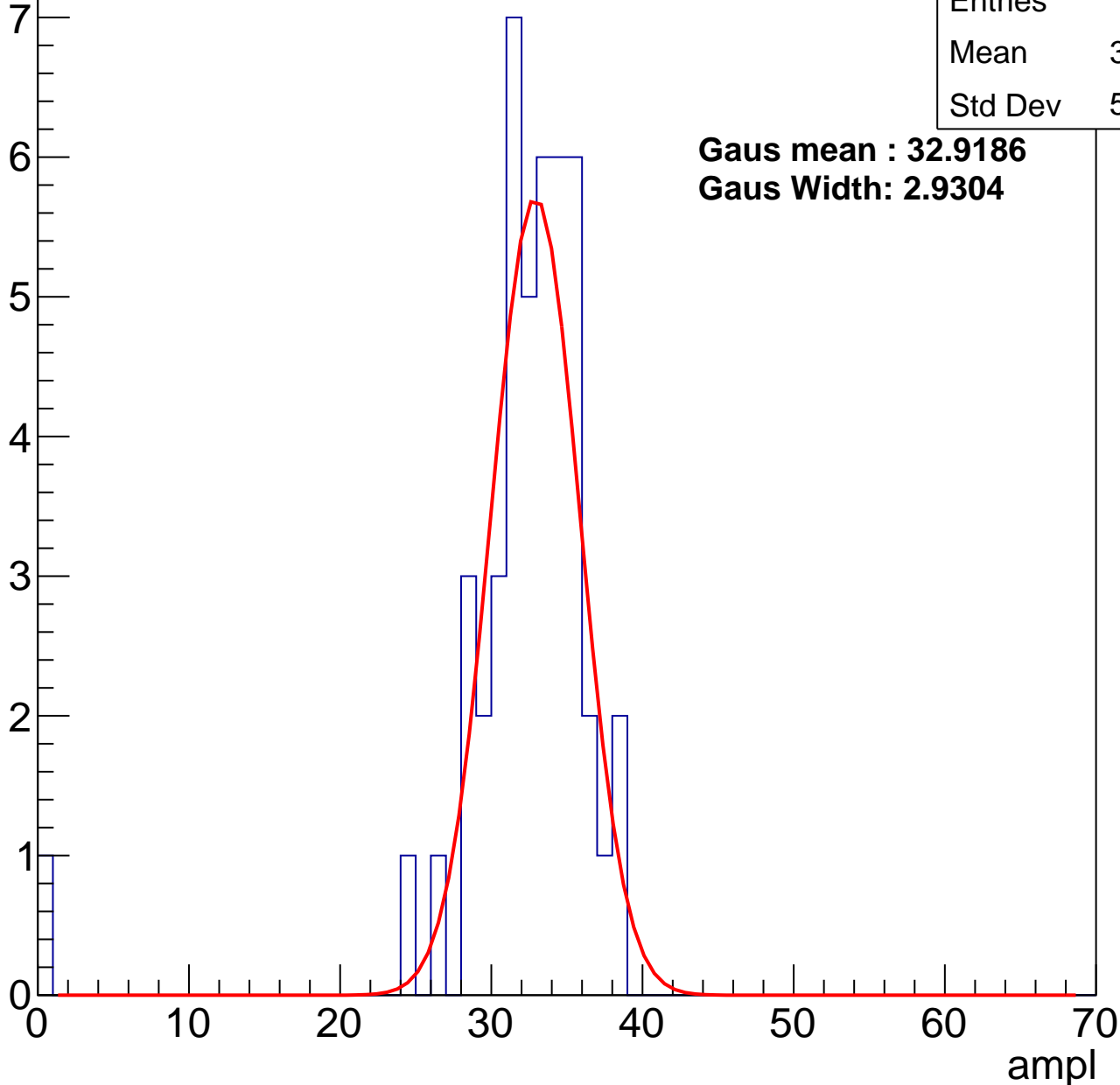
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	31.65
Std Dev	5.549

Gaus mean : 32.9186

Gaus Width: 2.9304



B0L002S, U4-ch65, adc1

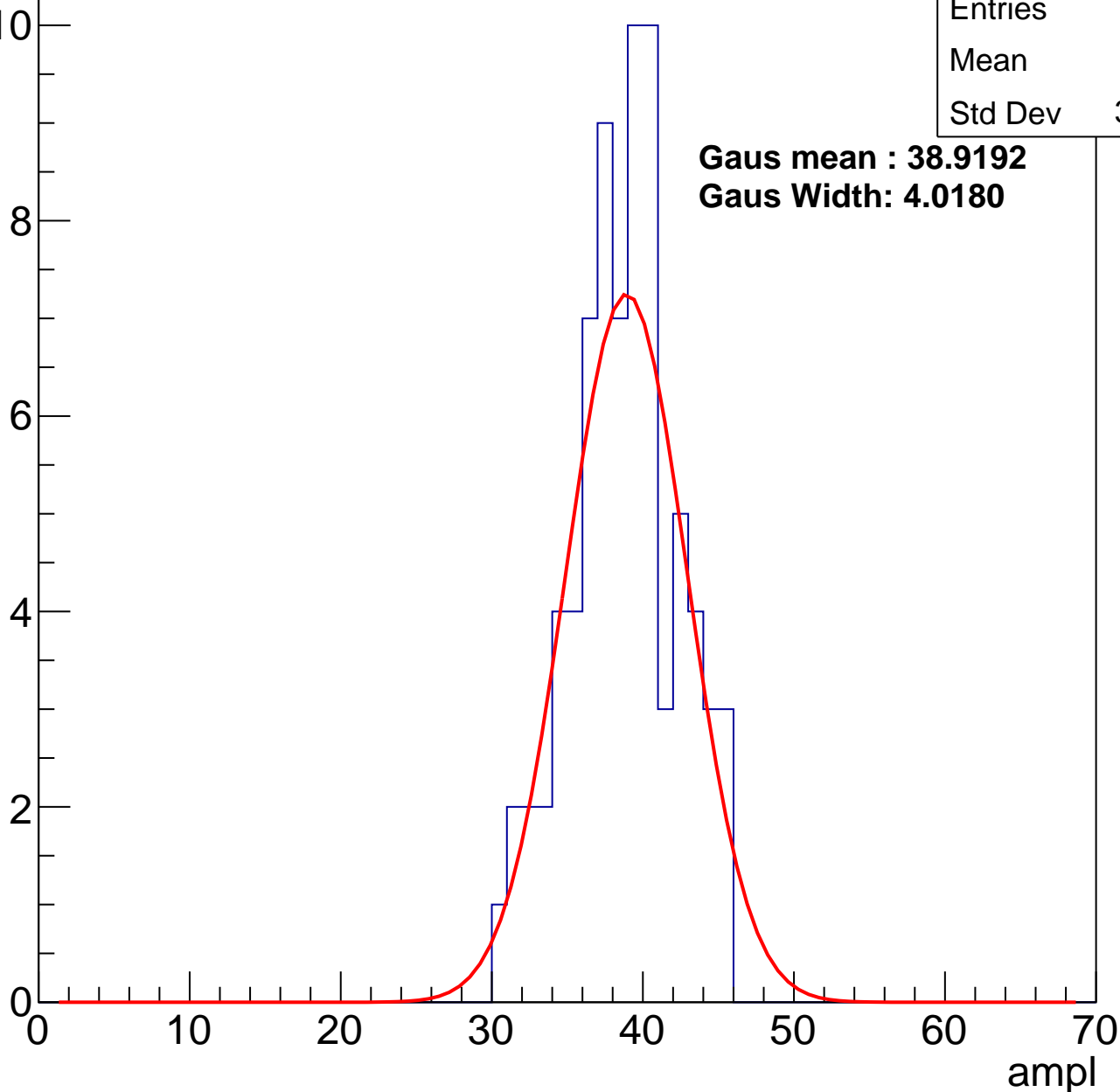
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	38.3
Std Dev	3.491

Gaus mean : 38.9192

Gaus Width: 4.0180



B0L002S, U4-ch65, adc2

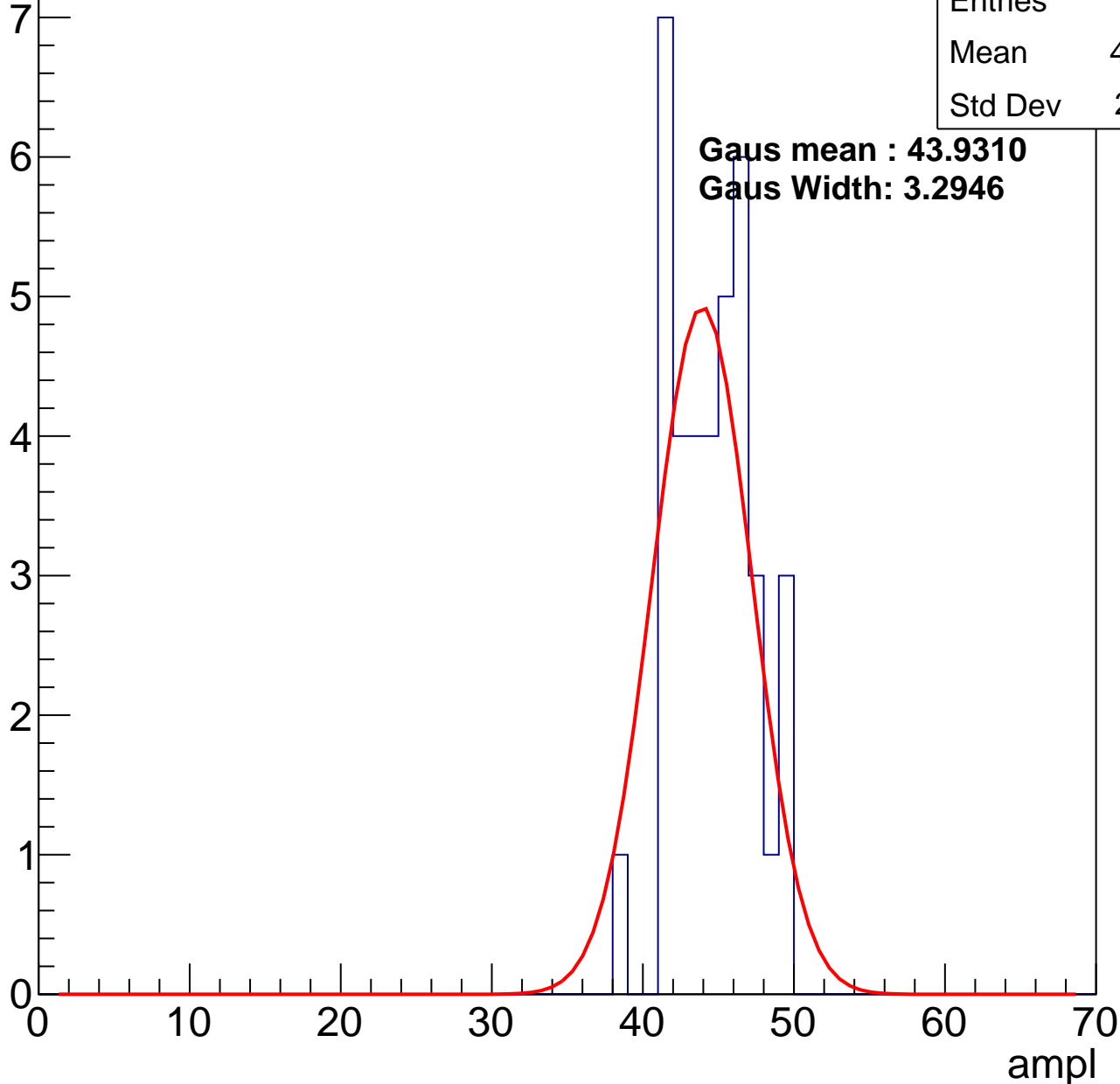
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	44.16
Std Dev	2.641

Gaus mean : 43.9310

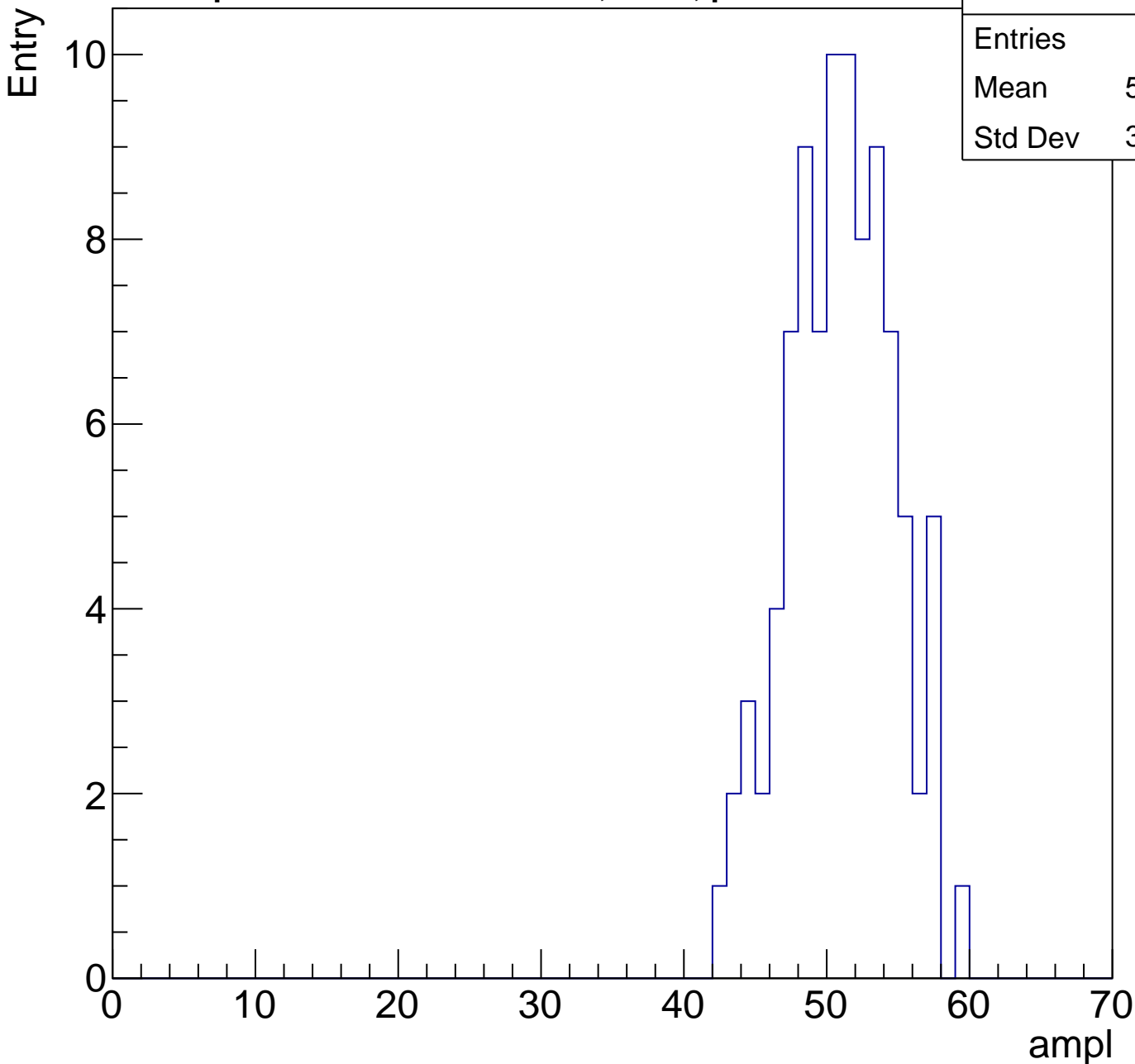
Gaus Width: 3.2946



B0L002S, U4-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

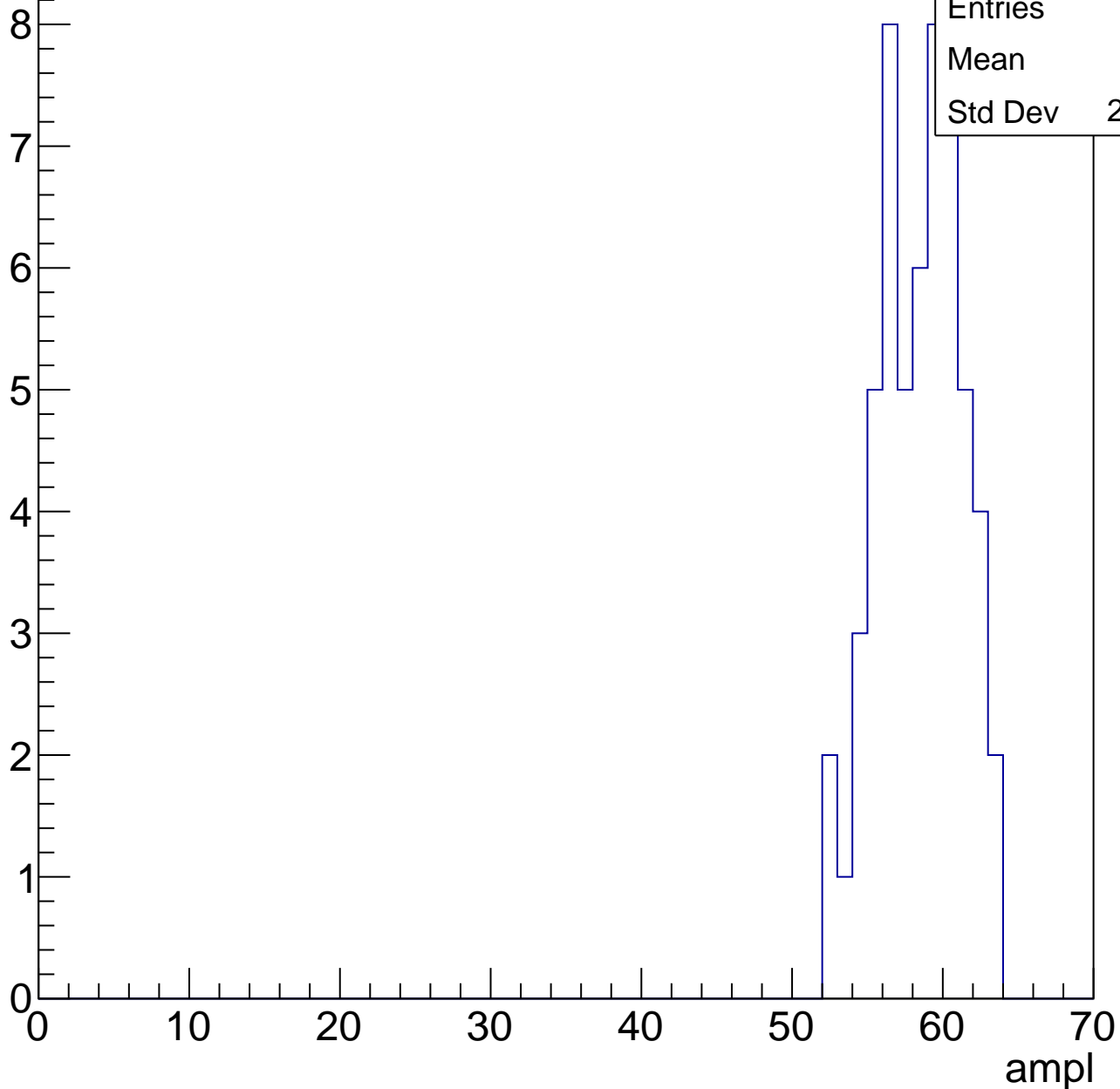
Entries	92
Mean	50.54
Std Dev	3.643



B0L002S, U4-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

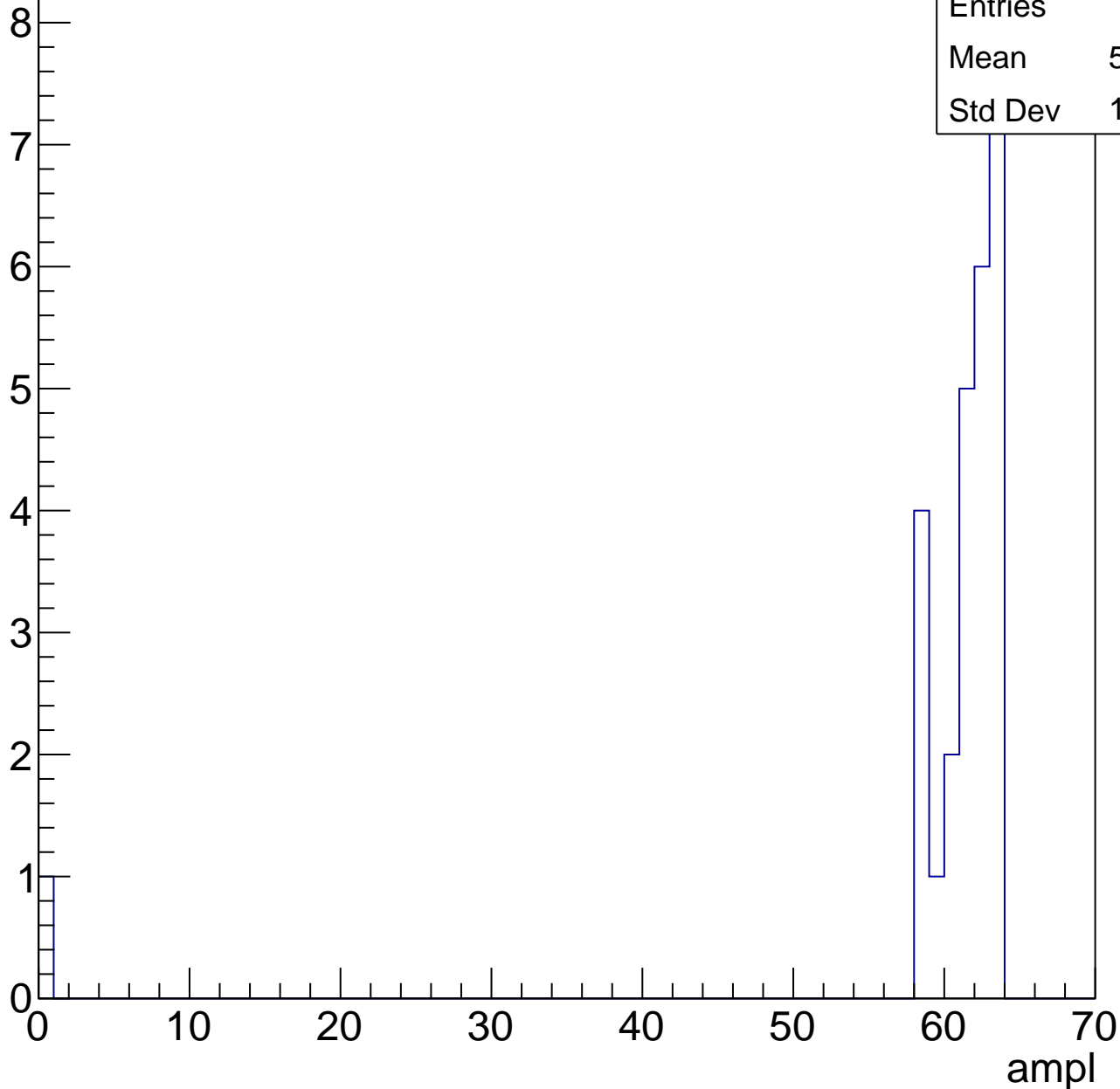


B0L002S, U4-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.96
Std Dev	11.69



B0L002S, U4-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch66, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	96
Mean	30.56
Std Dev	3.891

Gaus mean : 30.5721

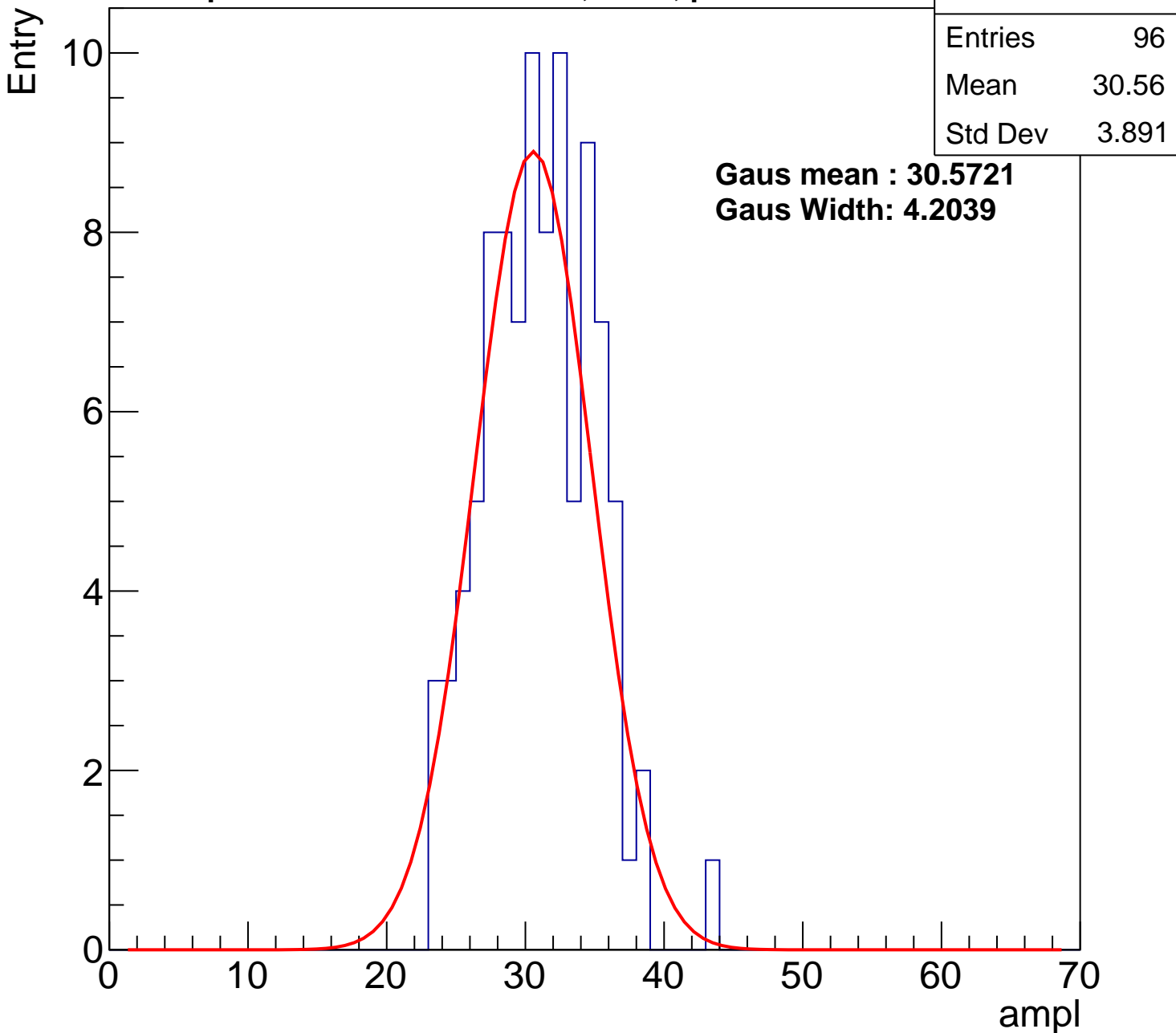
Gaus Width: 4.2039

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch66, adc1

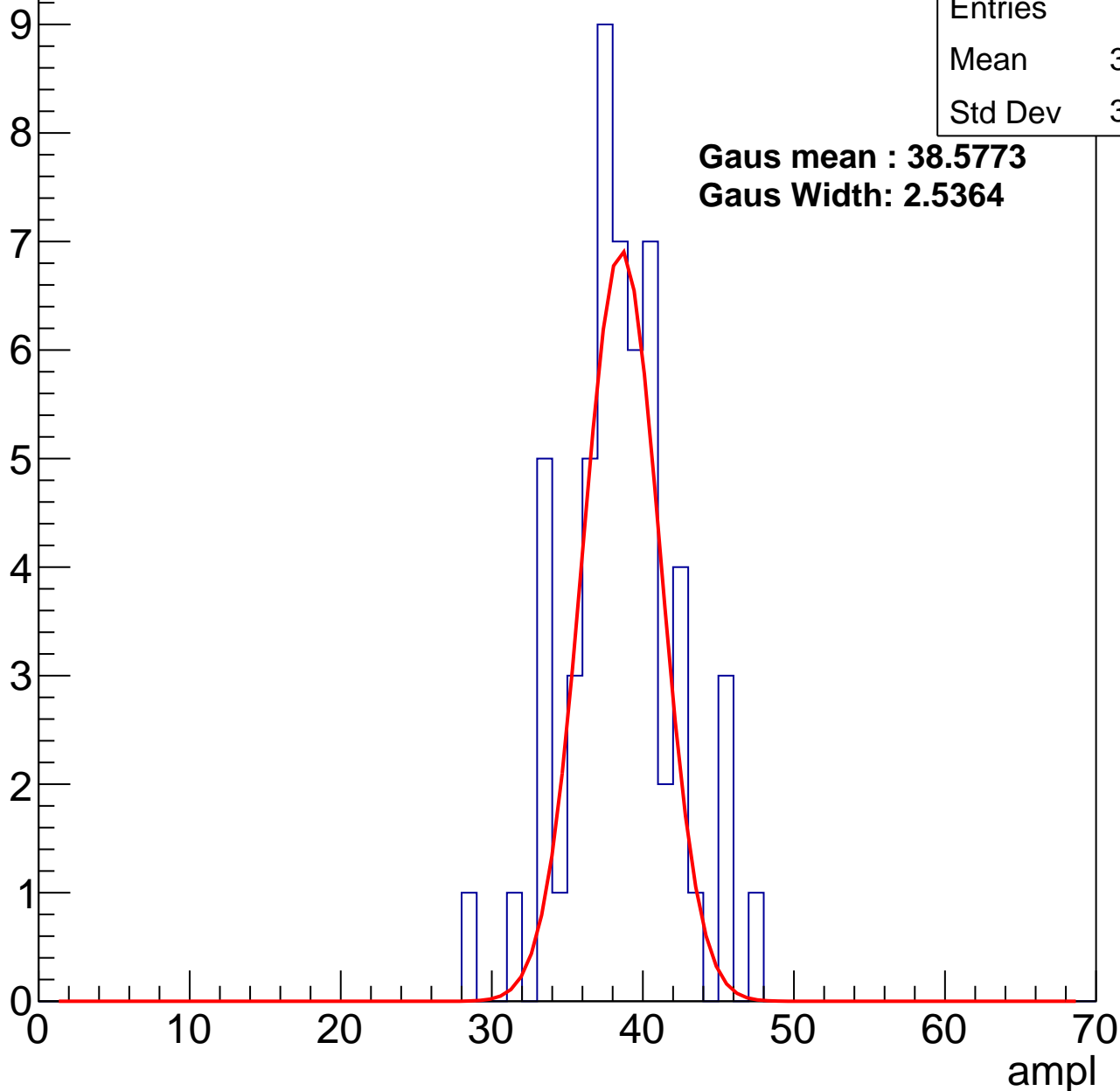
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	38.05
Std Dev	3.573

Gaus mean : 38.5773

Gaus Width: 2.5364



B0L002S, U4-ch66, adc2

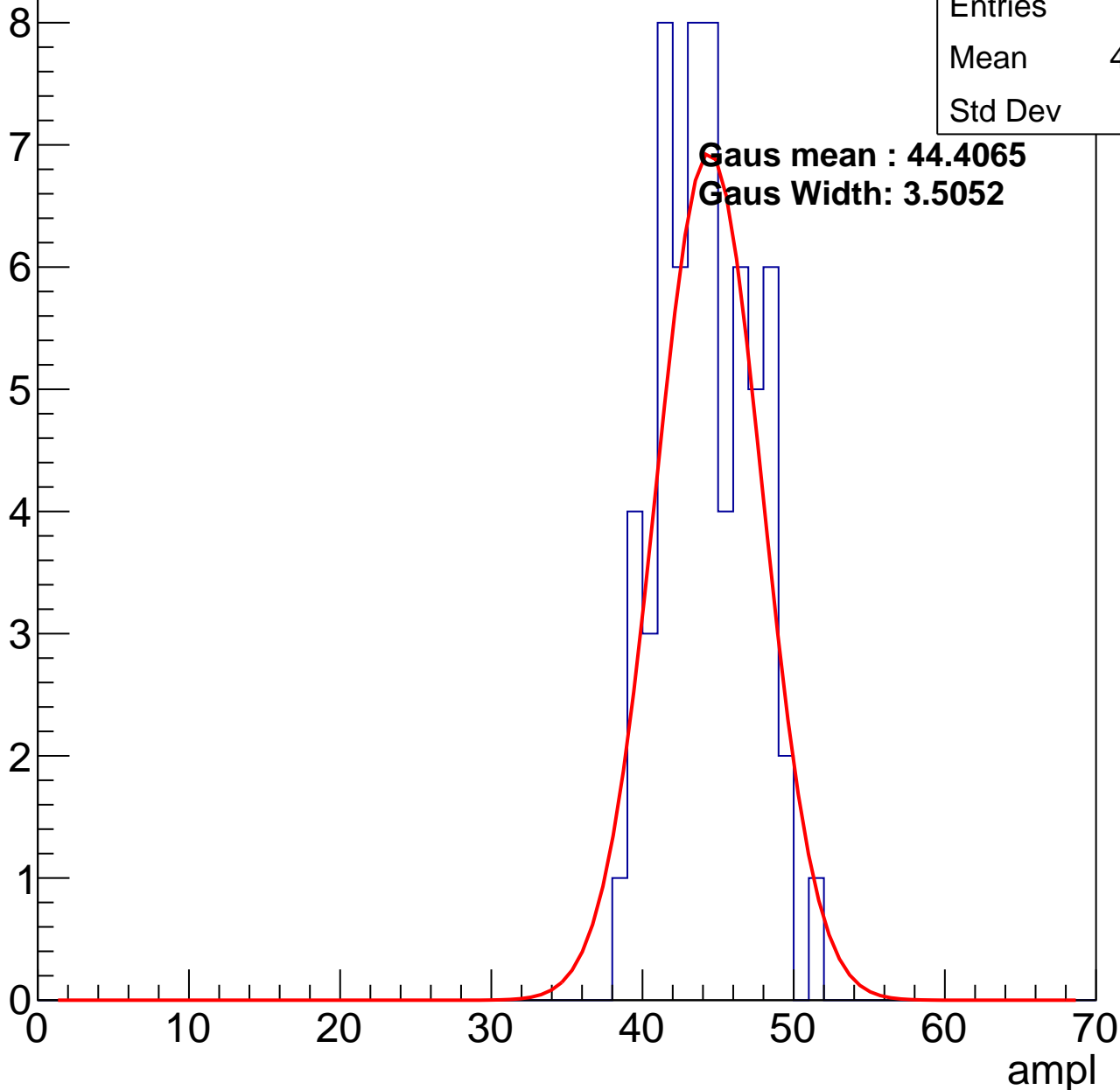
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.84
Std Dev	2.99

Gaus mean : 44.4065

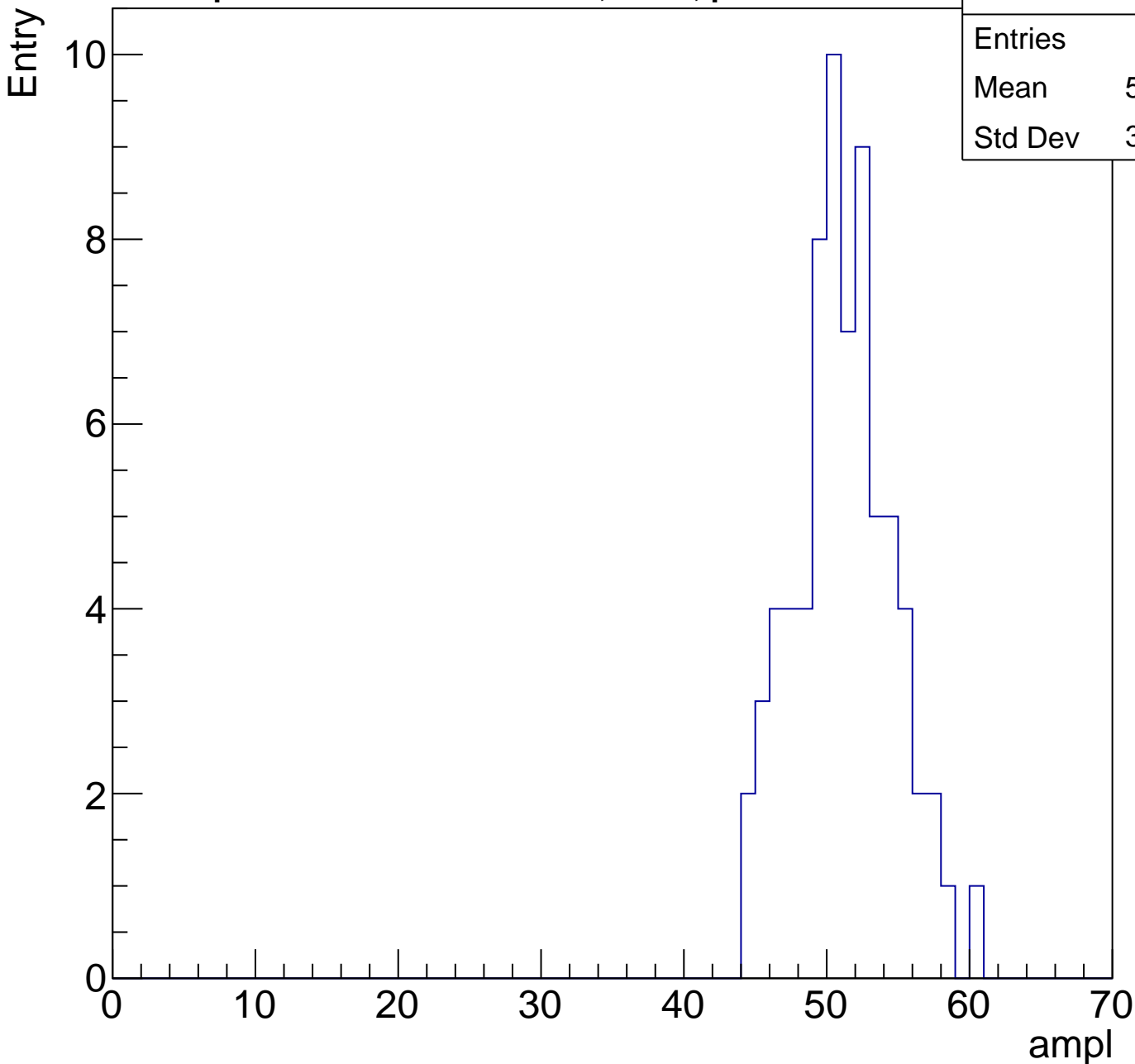
Gaus Width: 3.5052



B0L002S, U4-ch66, adc3

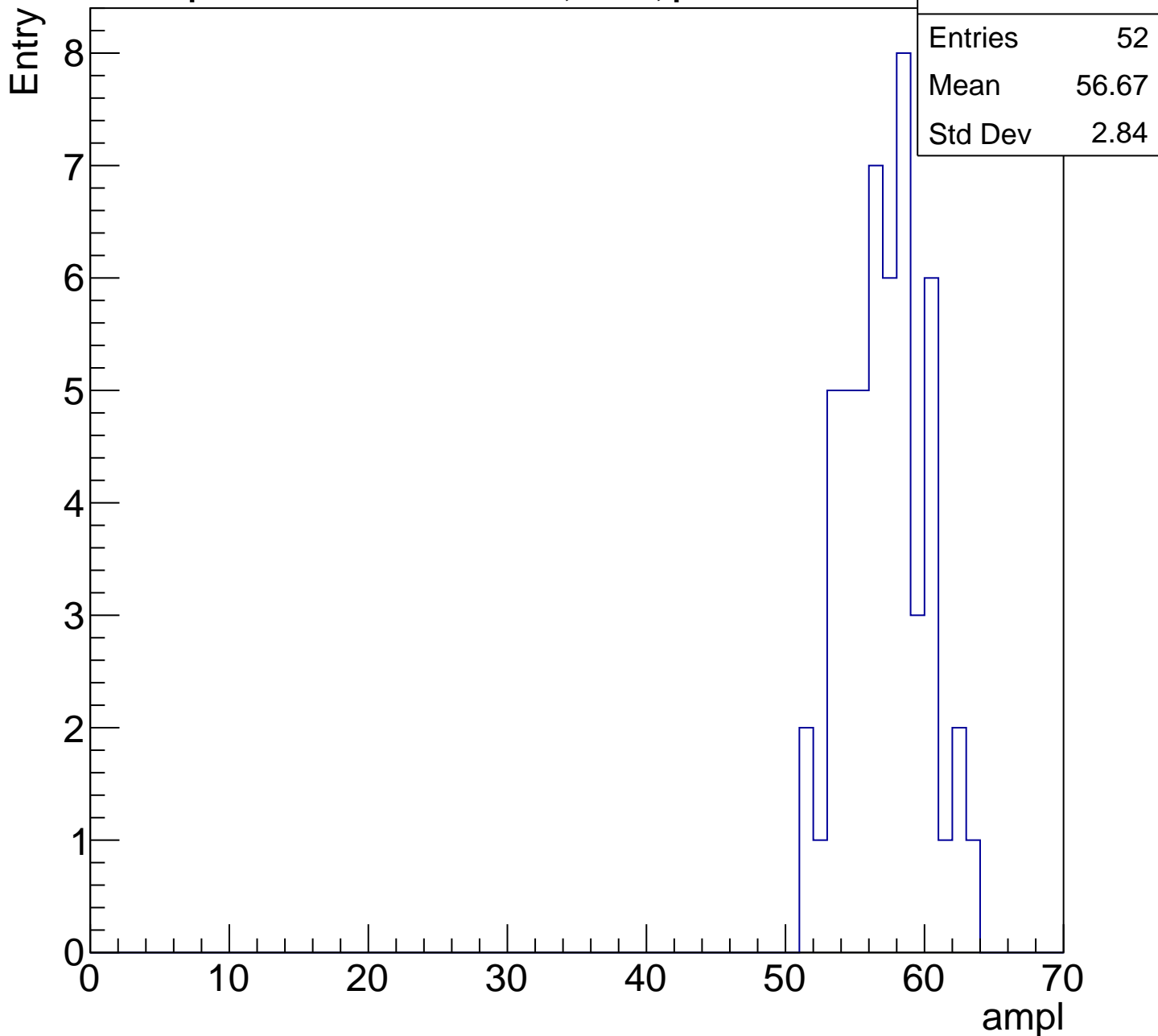
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	50.75
Std Dev	3.426



B0L002S, U4-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

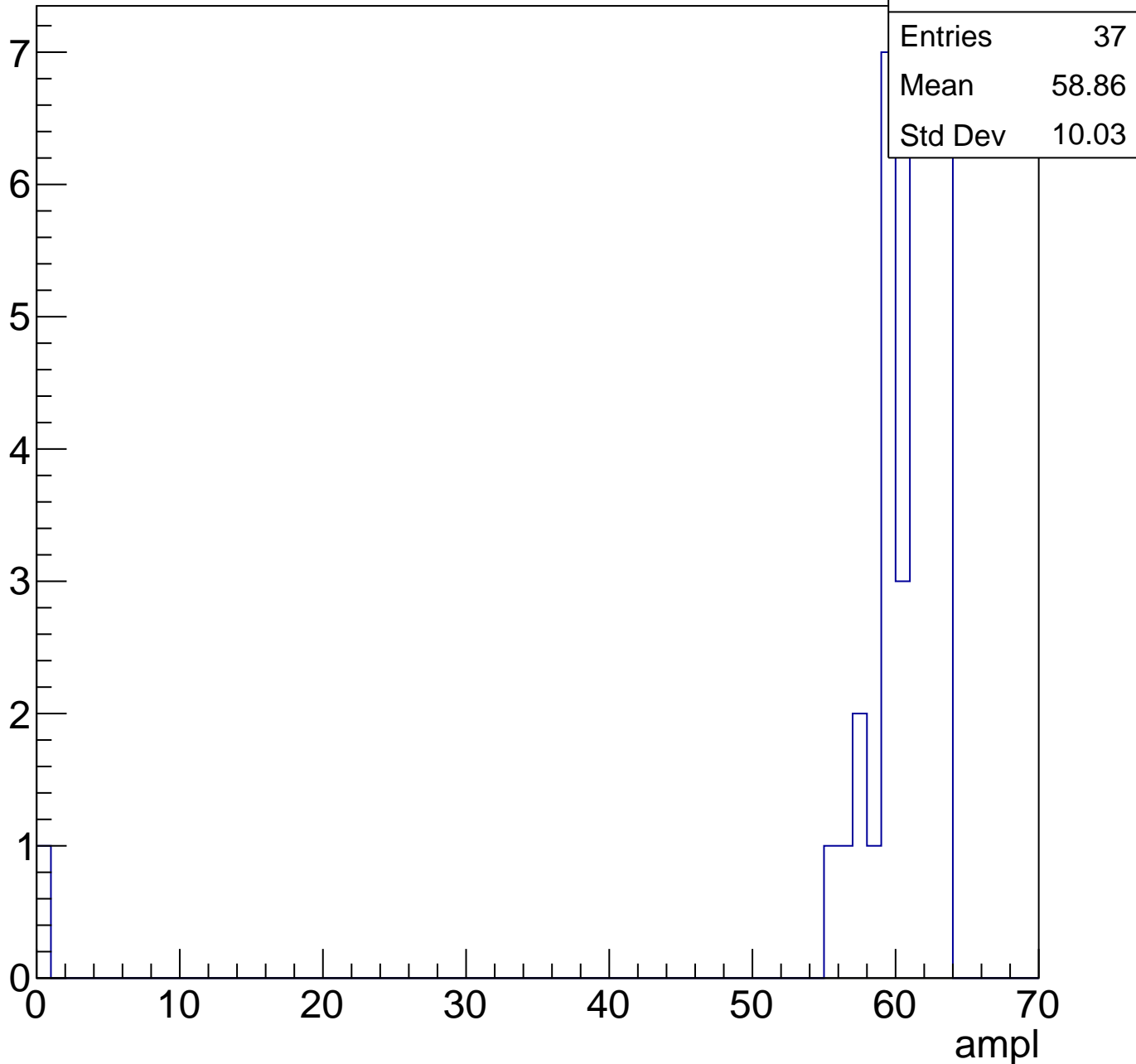
37

Mean

58.86

Std Dev

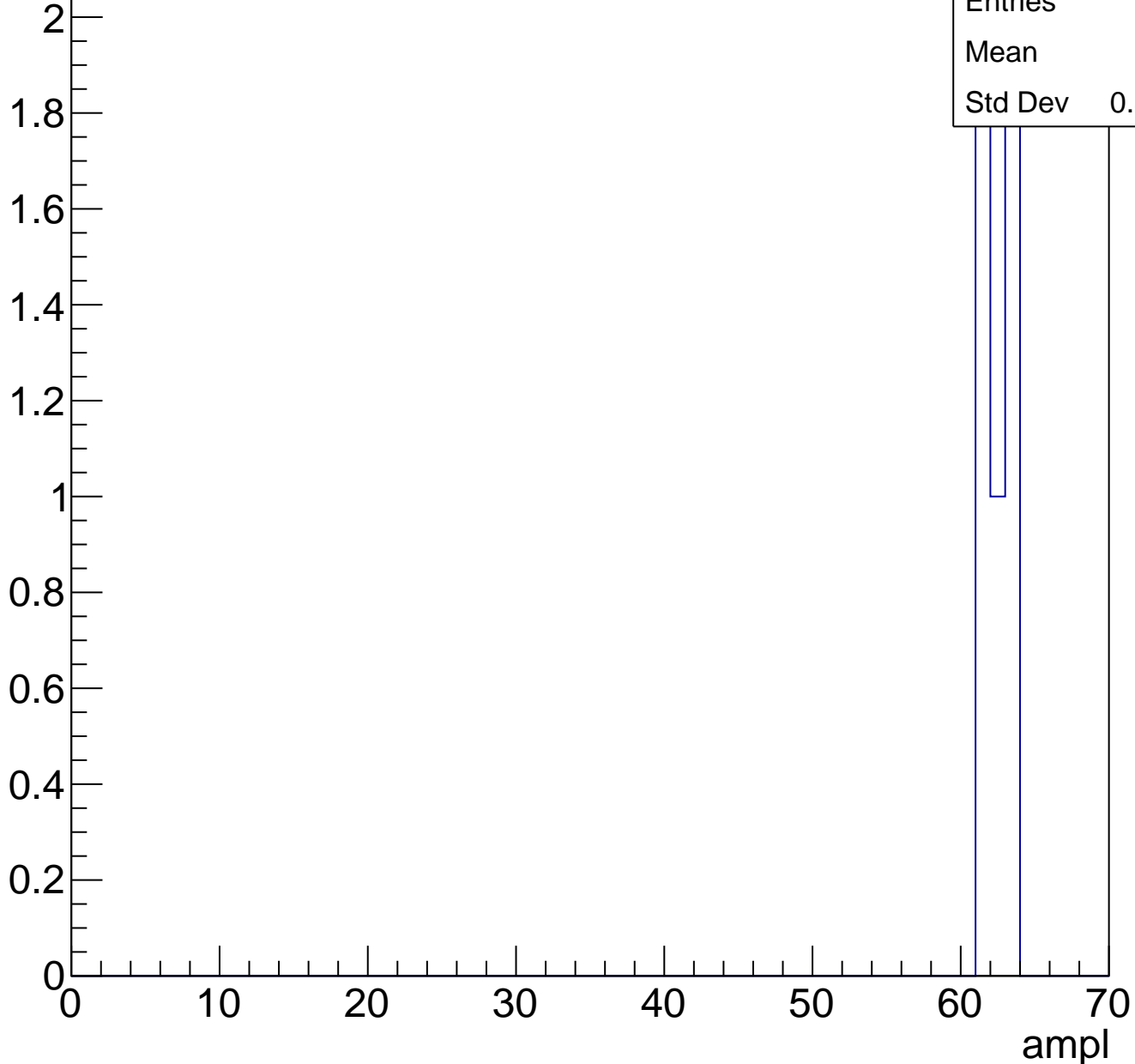
10.03



B0L002S, U4-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch67, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	28.96
Std Dev	3.054

Gaus mean : 29.3001

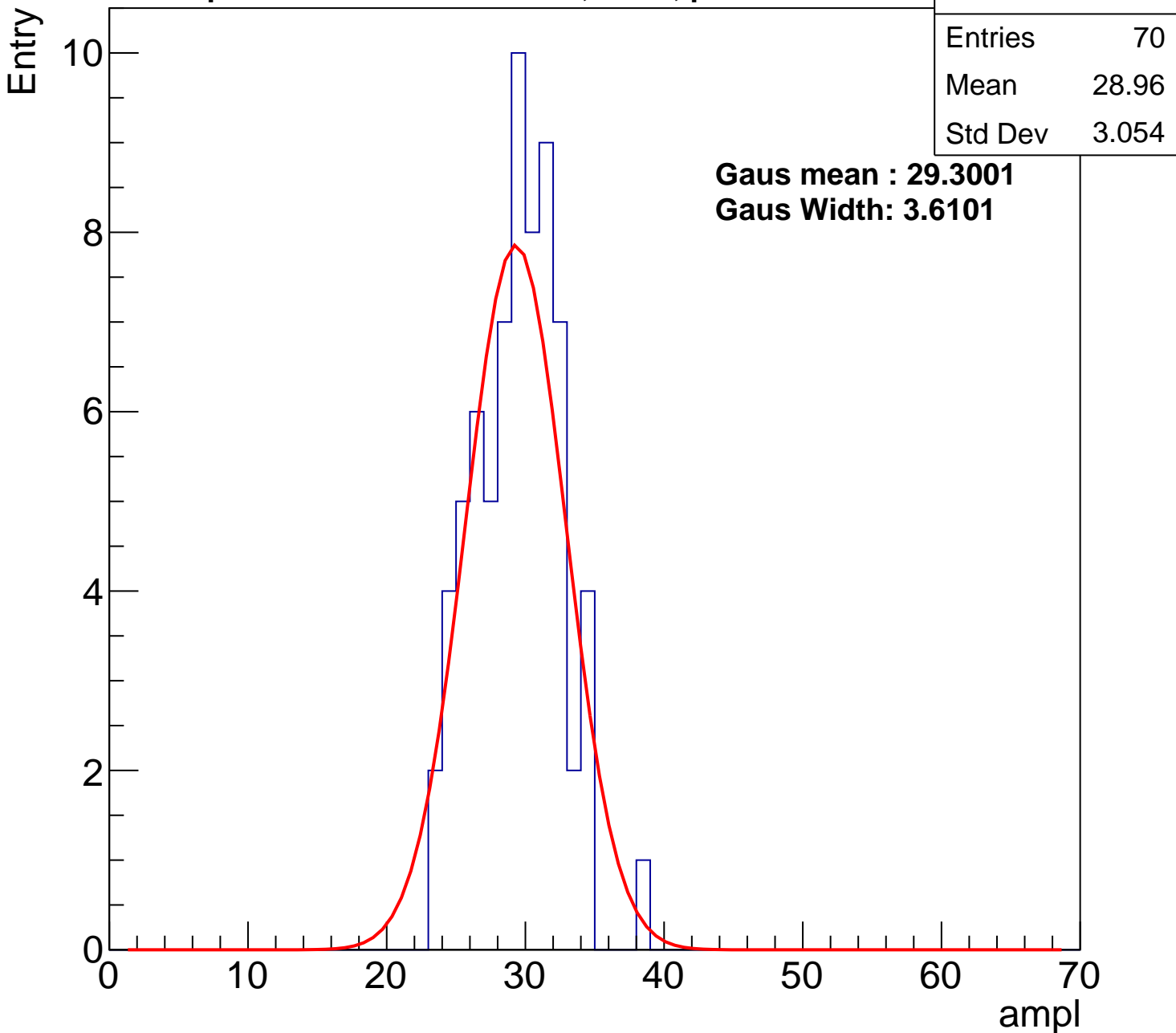
Gaus Width: 3.6101

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch67, adc1

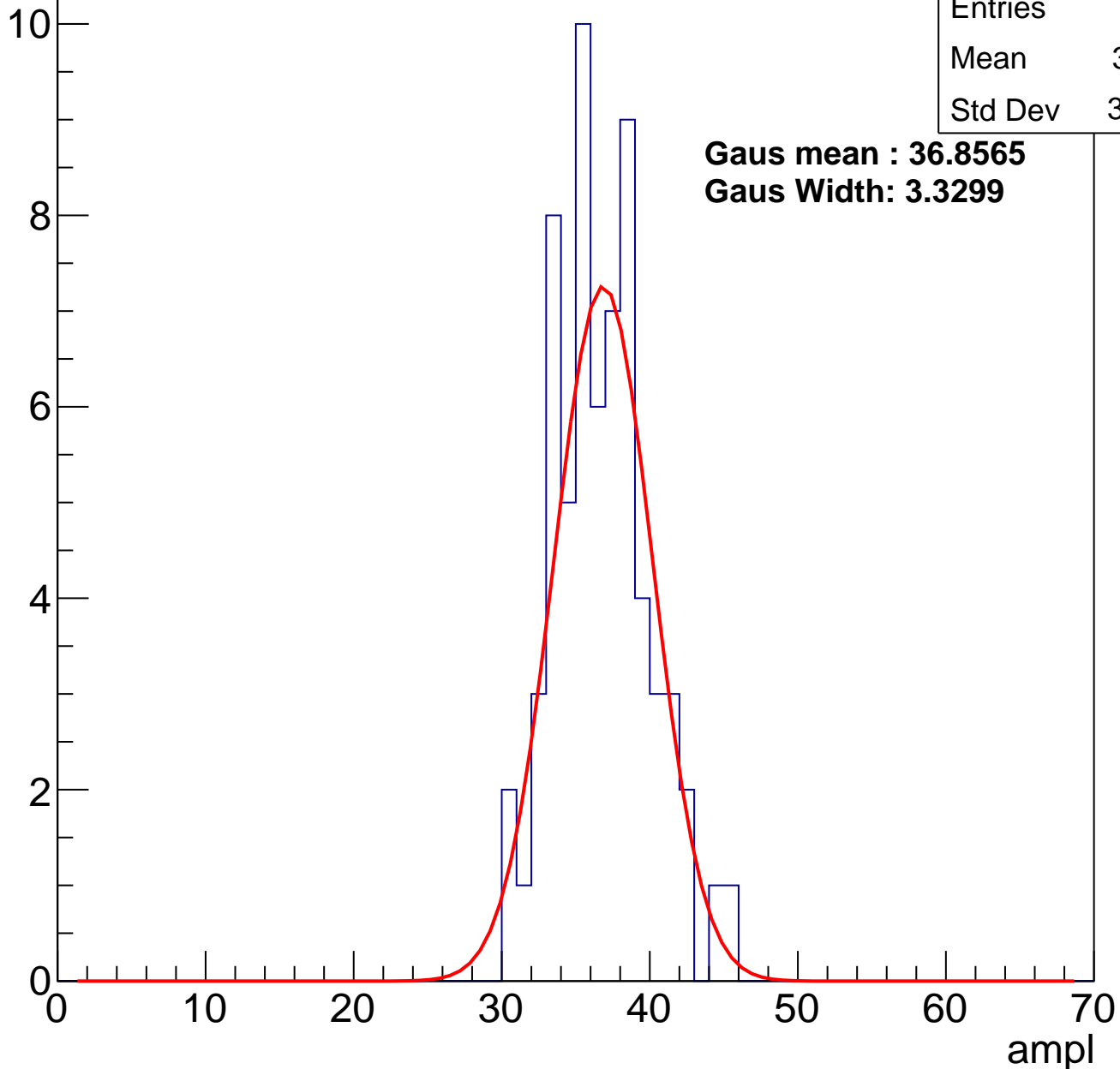
calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	36.31
Std Dev	3.186

Gaus mean : 36.8565

Gaus Width: 3.3299

Entry



B0L002S, U4-ch67, adc2

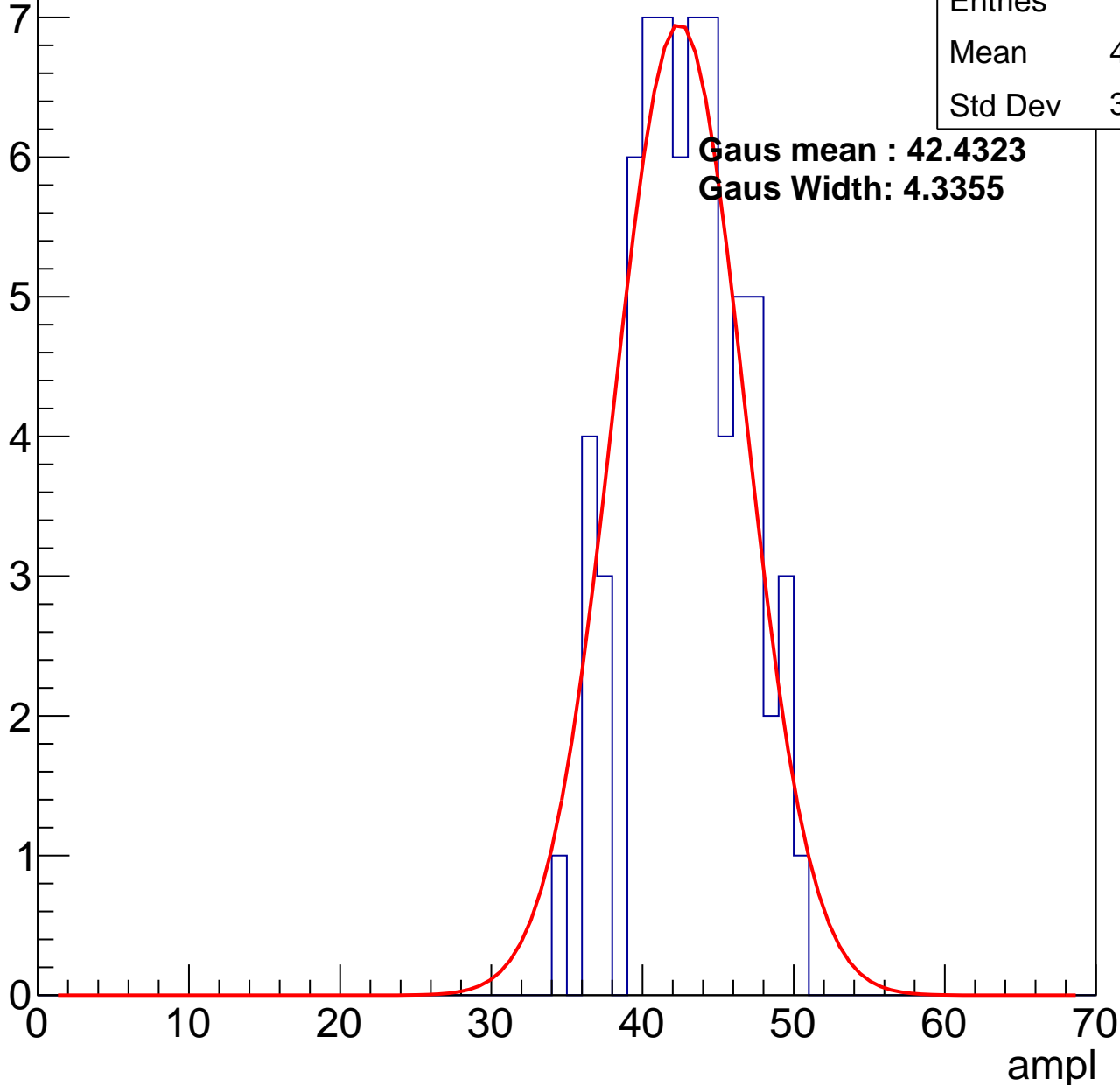
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.49
Std Dev	3.668

Gaus mean : 42.4323

Gaus Width: 4.3355

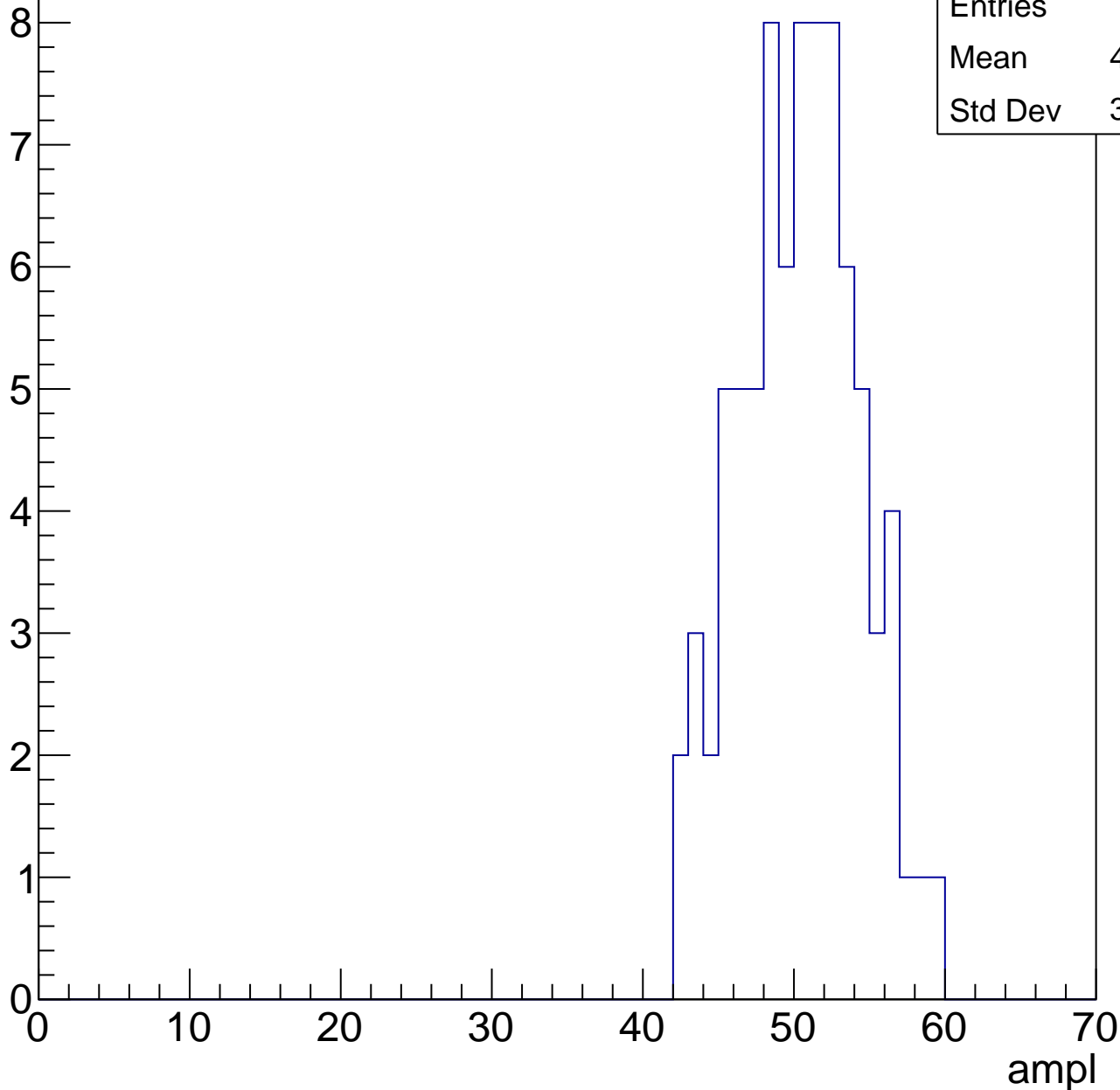


B0L002S, U4-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	49.93
Std Dev	3.883

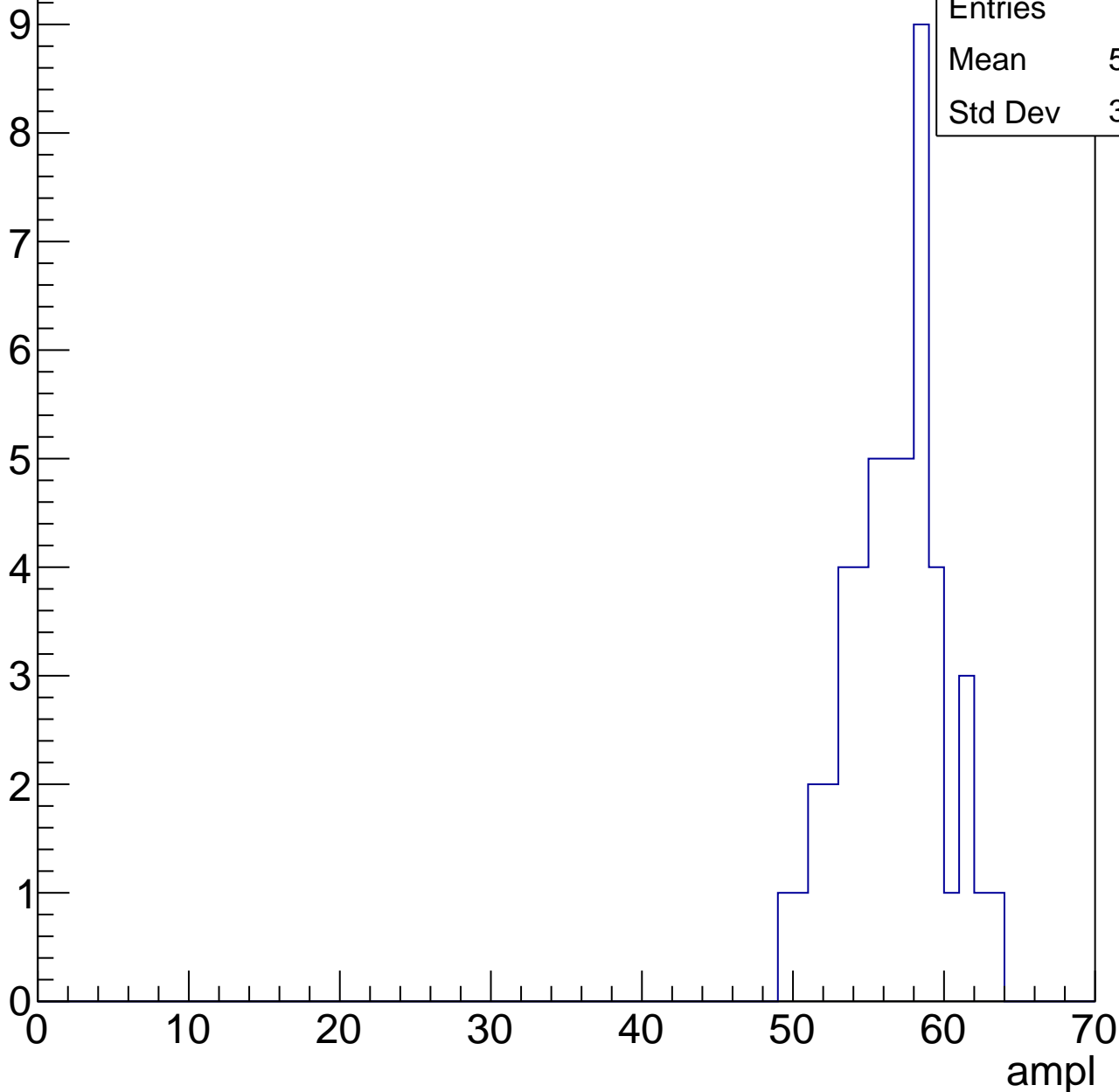


B0L002S, U4-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	56.23
Std Dev	3.144



B0L002S, U4-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	60.2
Std Dev	2.41

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L002S, U4-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

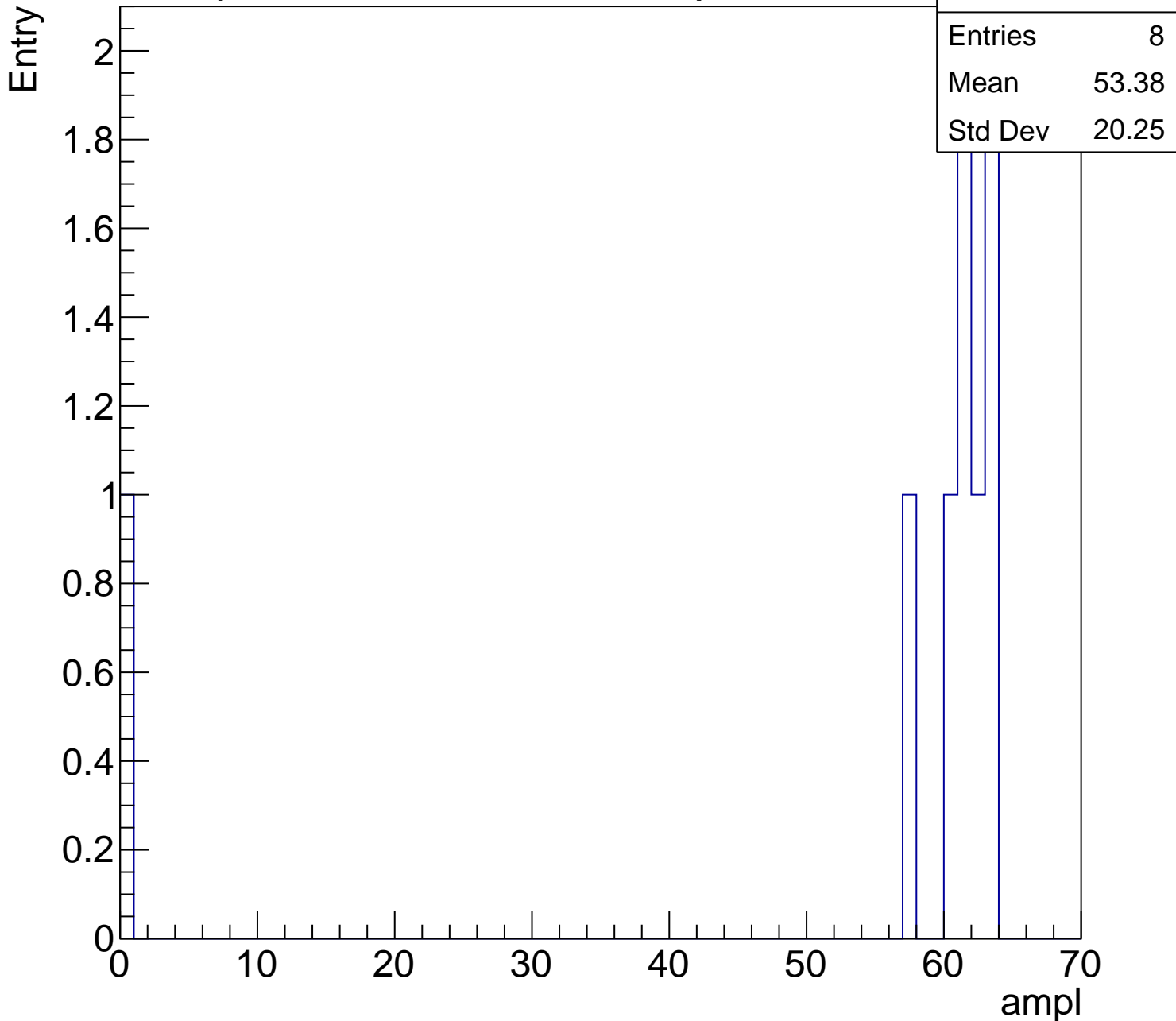
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.38
Std Dev	20.25

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch68, adc0

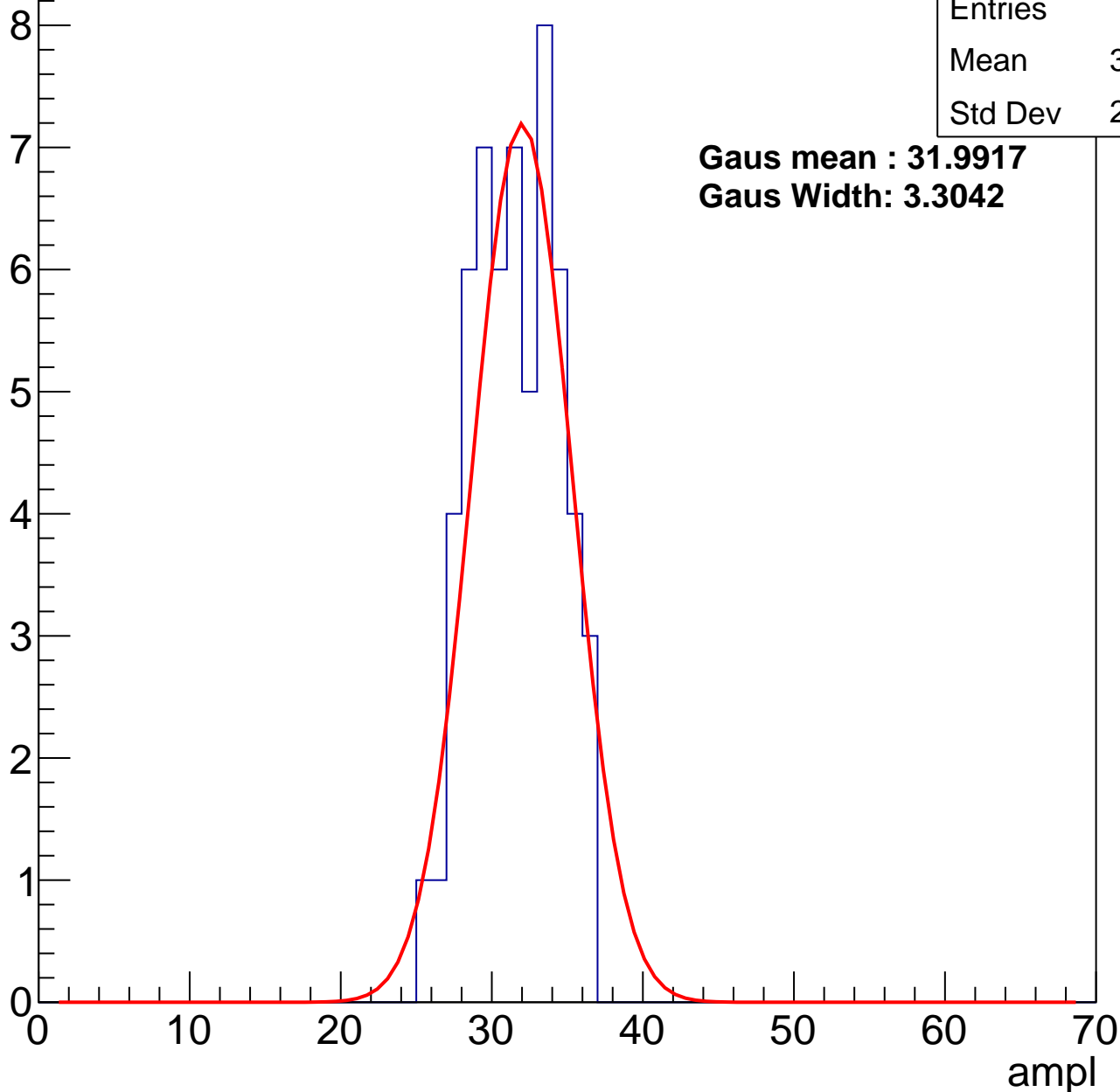
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.09
Std Dev	2.762

Gaus mean : 31.9917

Gaus Width: 3.3042



B0L002S, U4-ch68, adc1

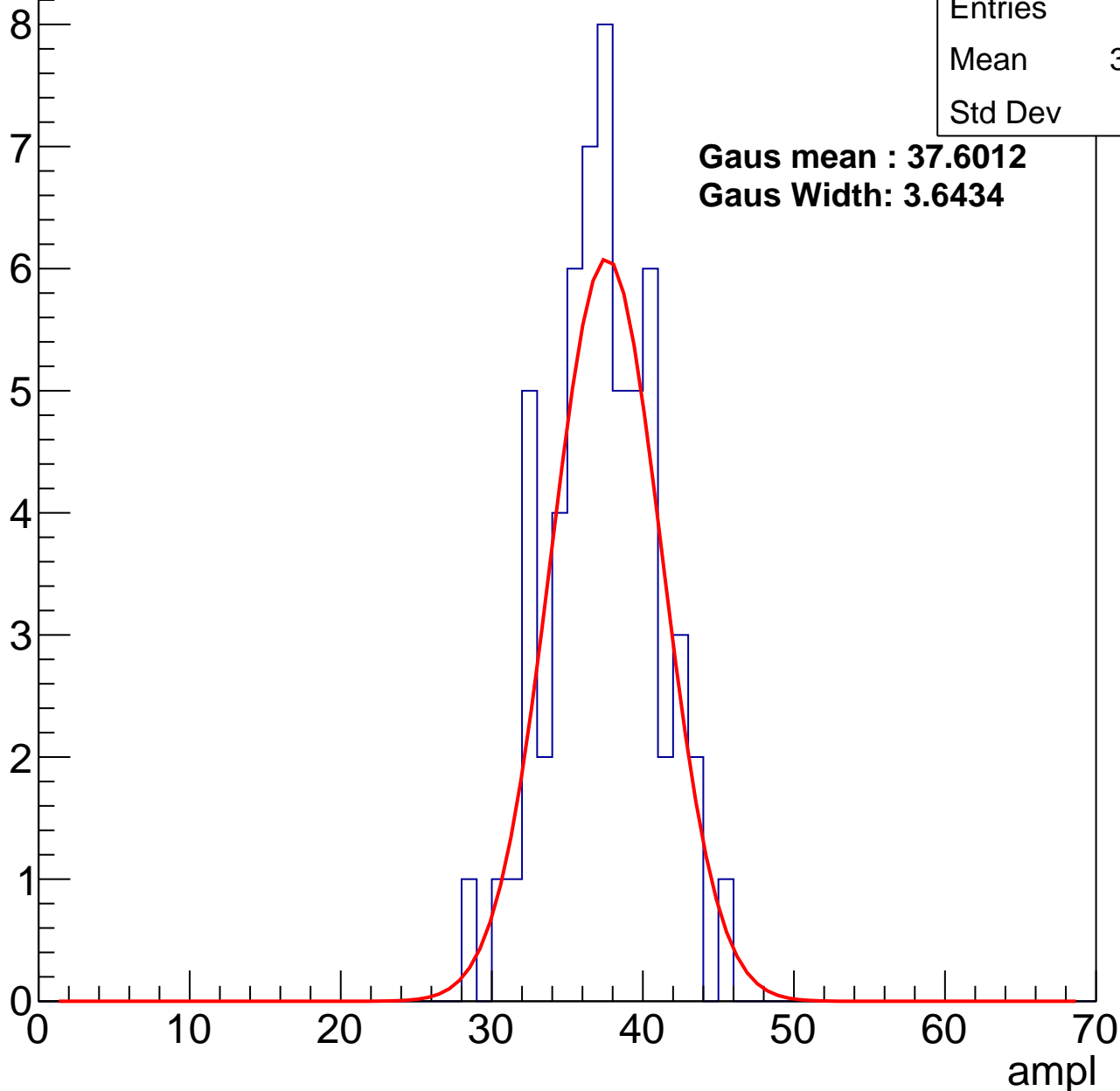
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.83
Std Dev	3.47

Gaus mean : 37.6012

Gaus Width: 3.6434



B0L002S, U4-ch68, adc2

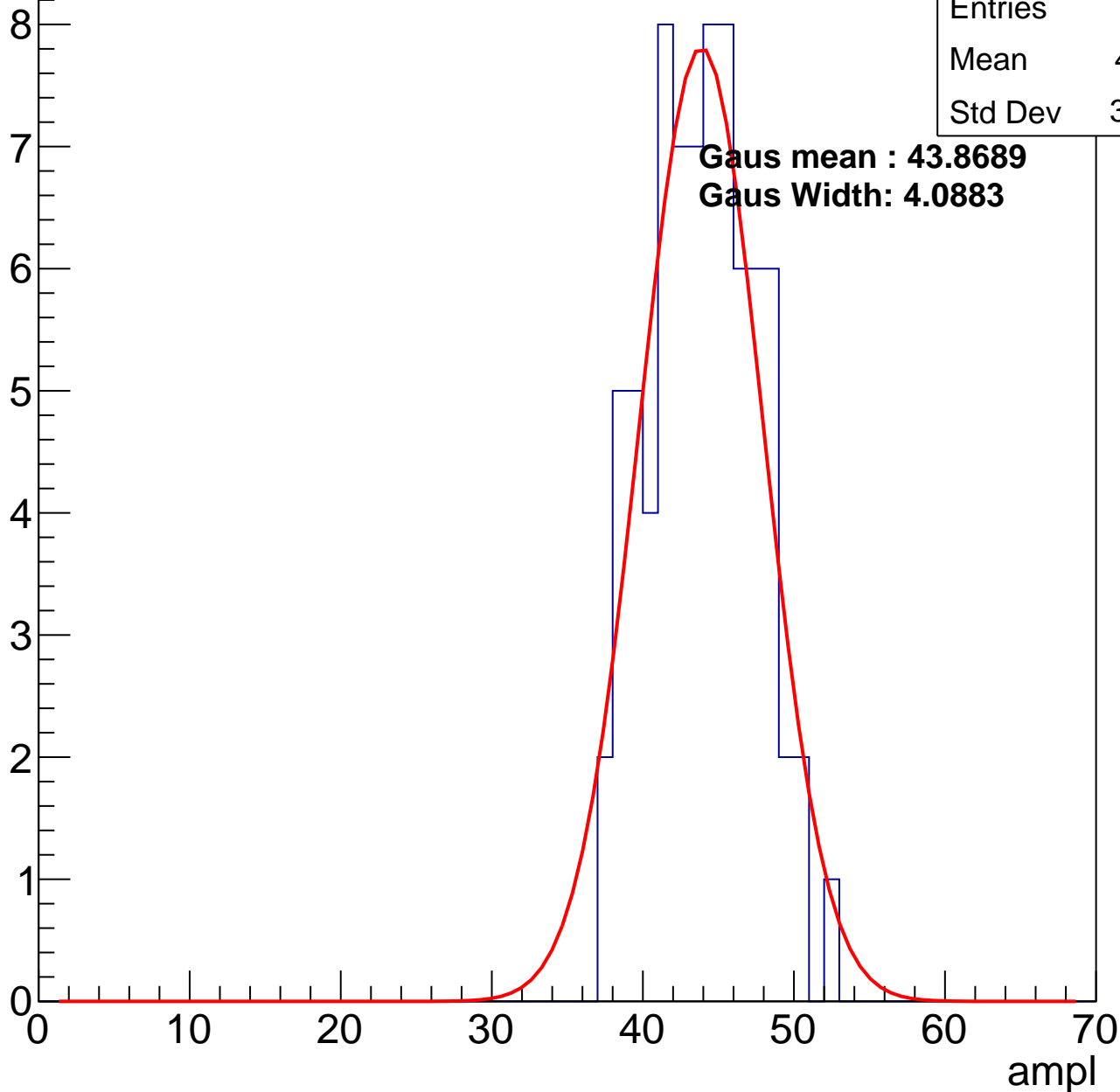
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.51
Std Dev	3.463

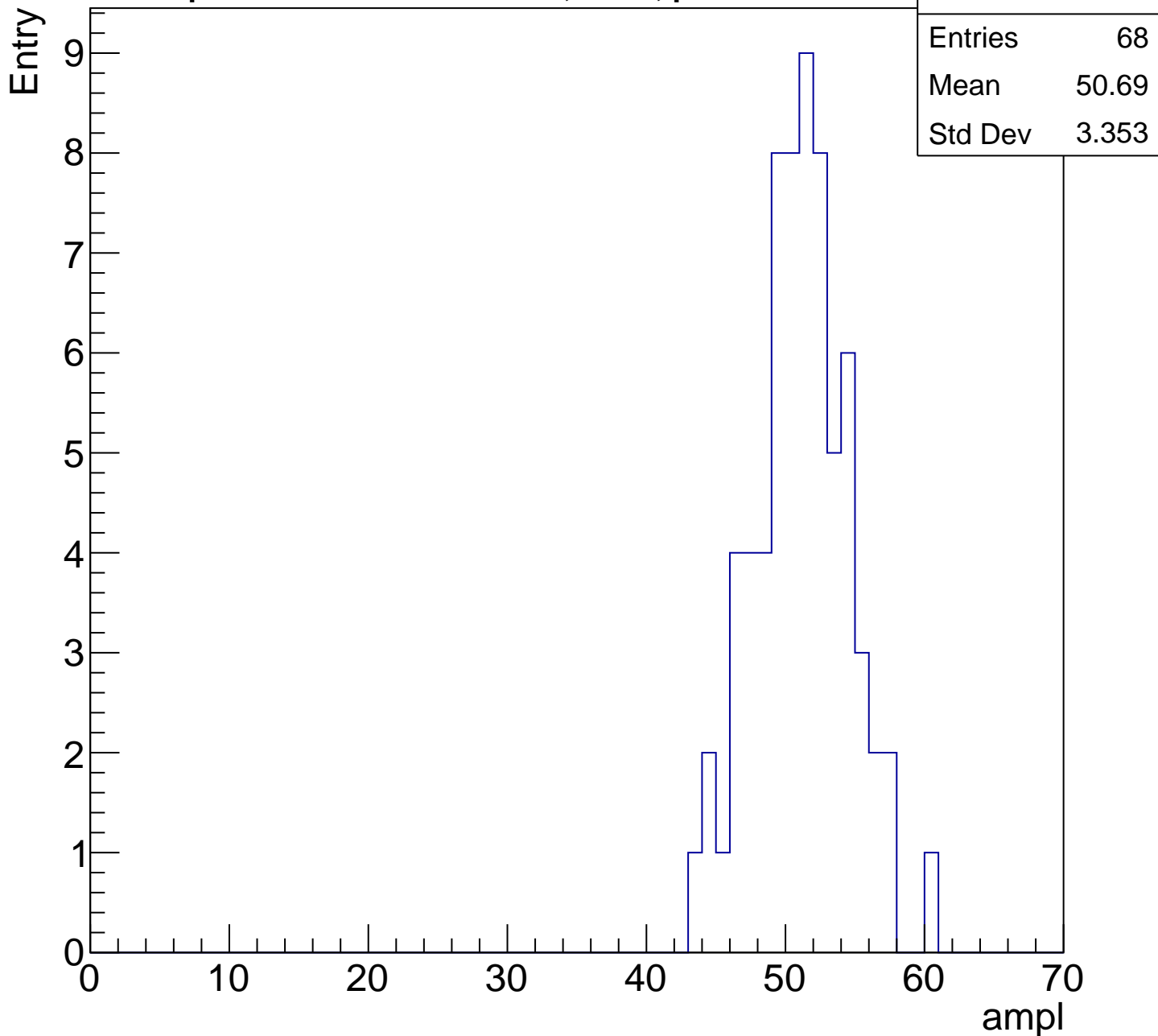
Gaus mean : 43.8689

Gaus Width: 4.0883



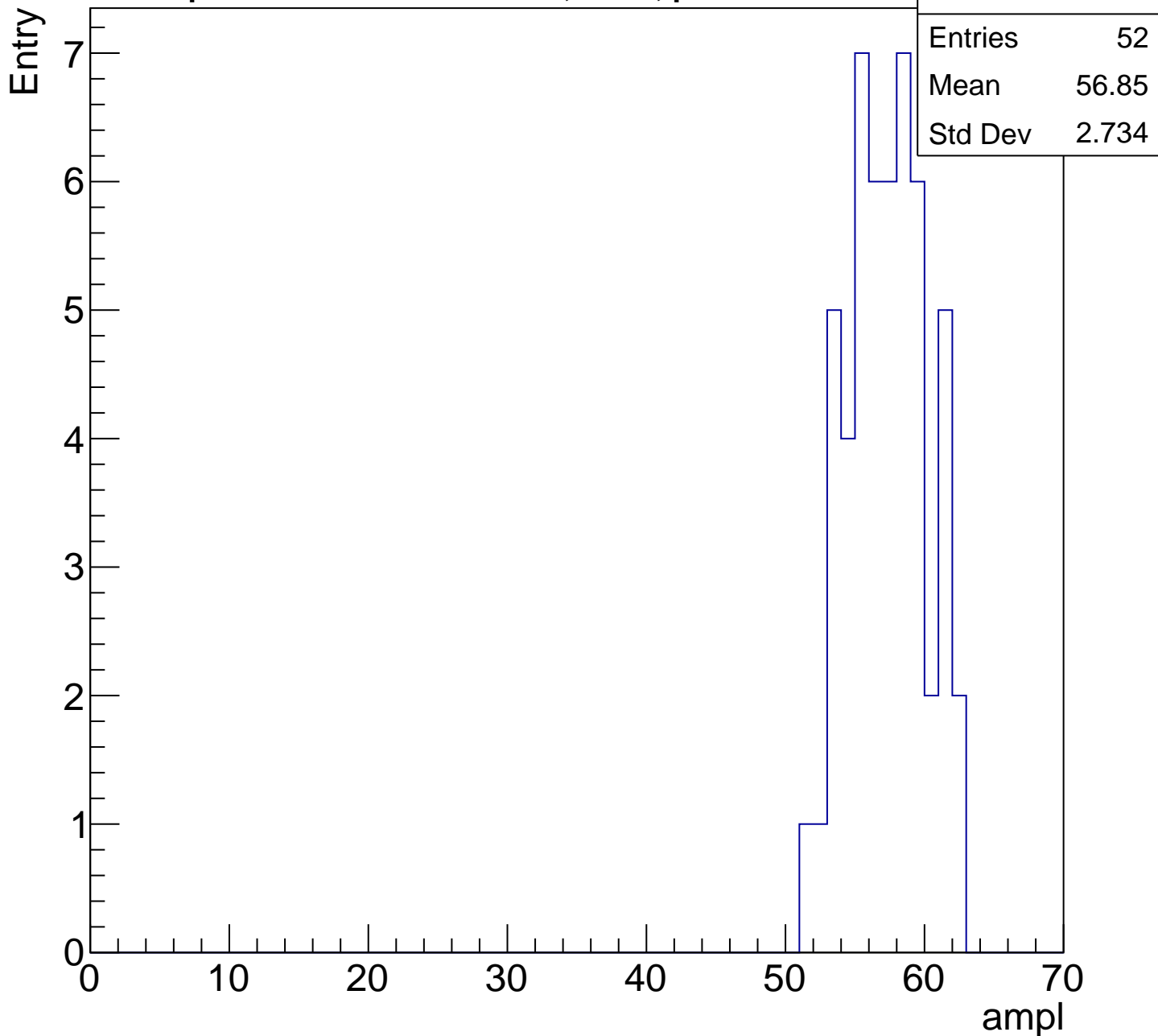
B0L002S, U4-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch68, adc4

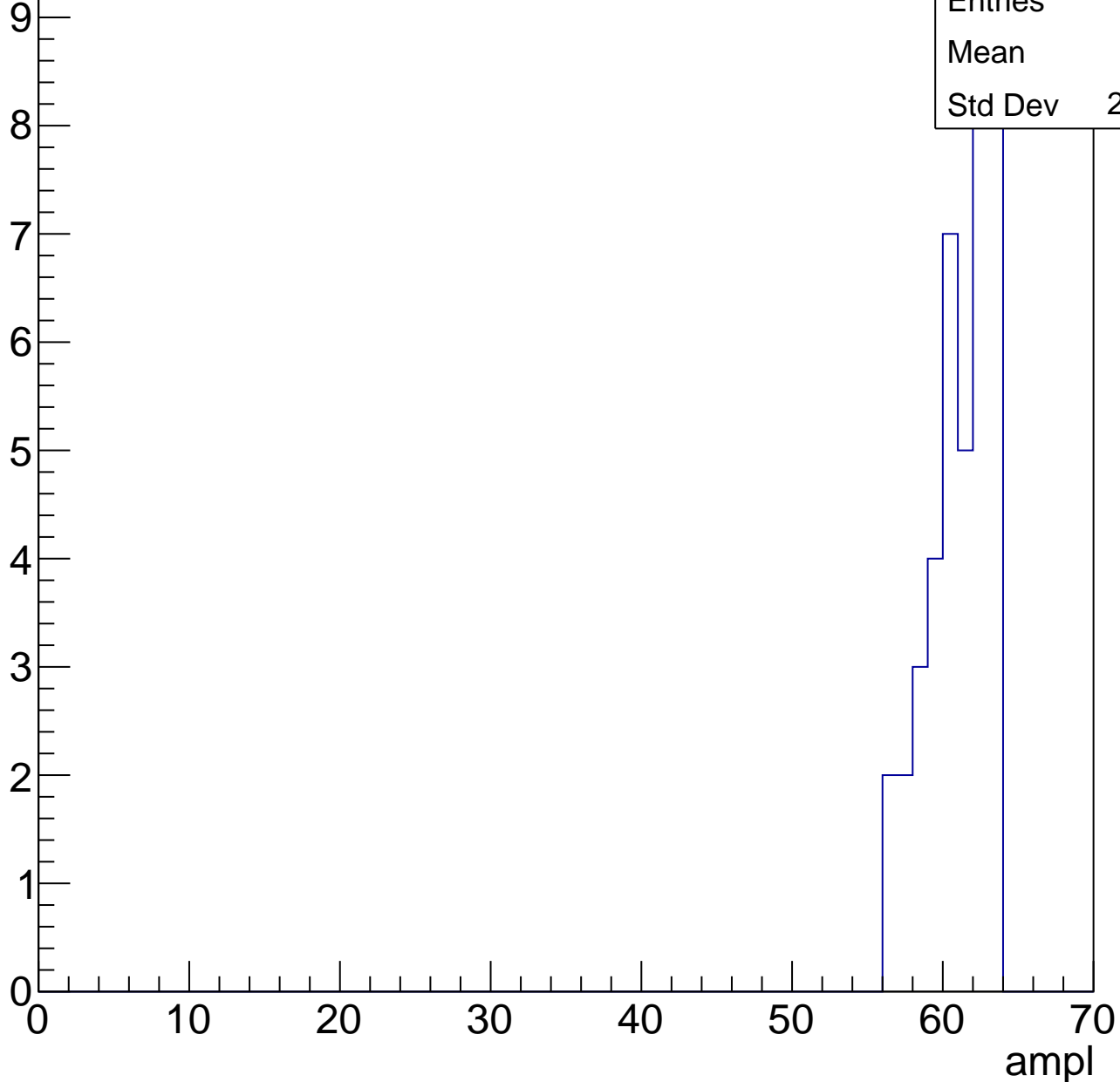
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	40
Mean	60.6
Std Dev	2.059

B0L002S, U4-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	25
Std Dev	0

B0L002S, U4-ch69, adc0

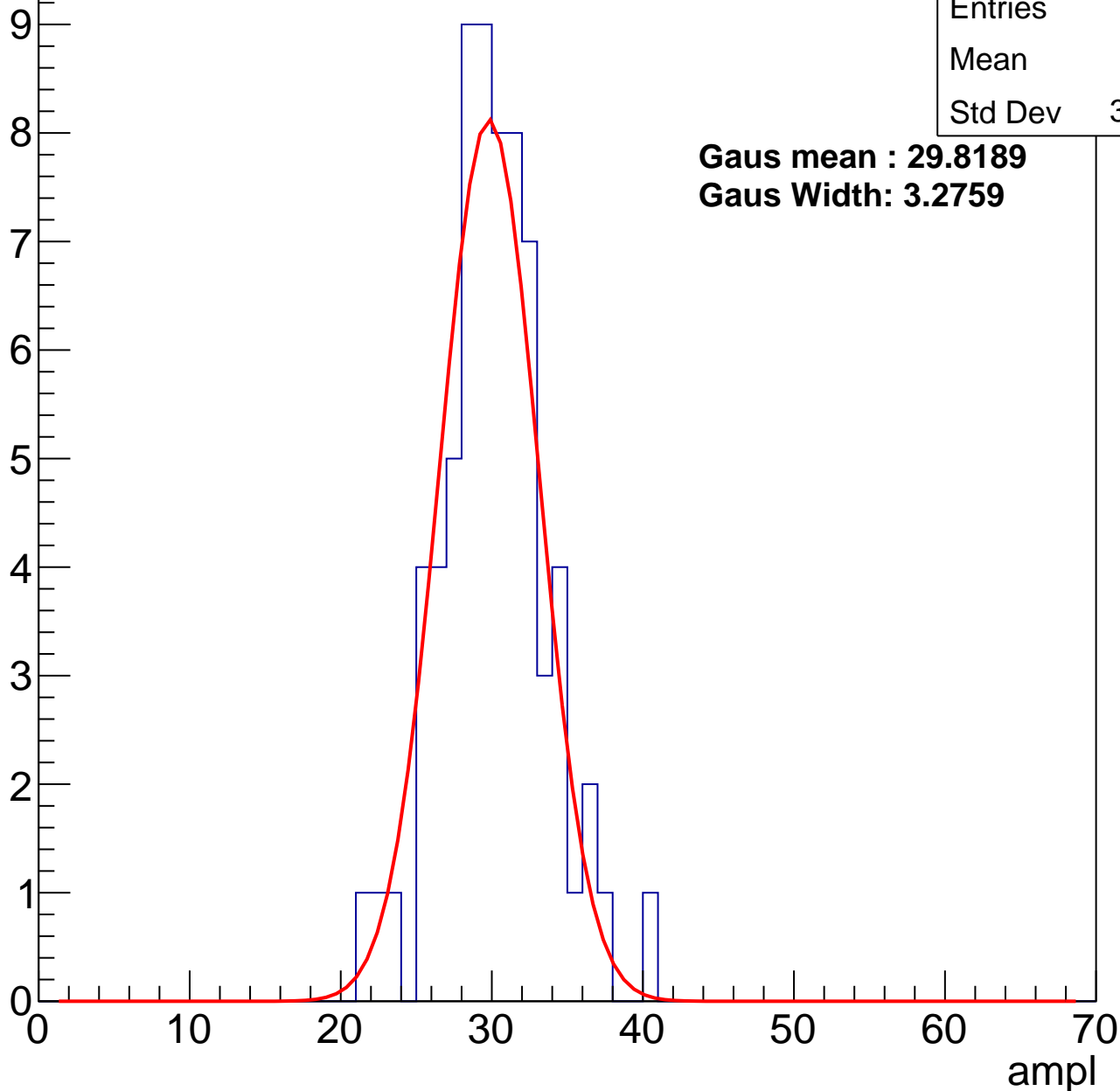
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.7
Std Dev	3.436

Gaus mean : 29.8189

Gaus Width: 3.2759



B0L002S, U4-ch69, adc1

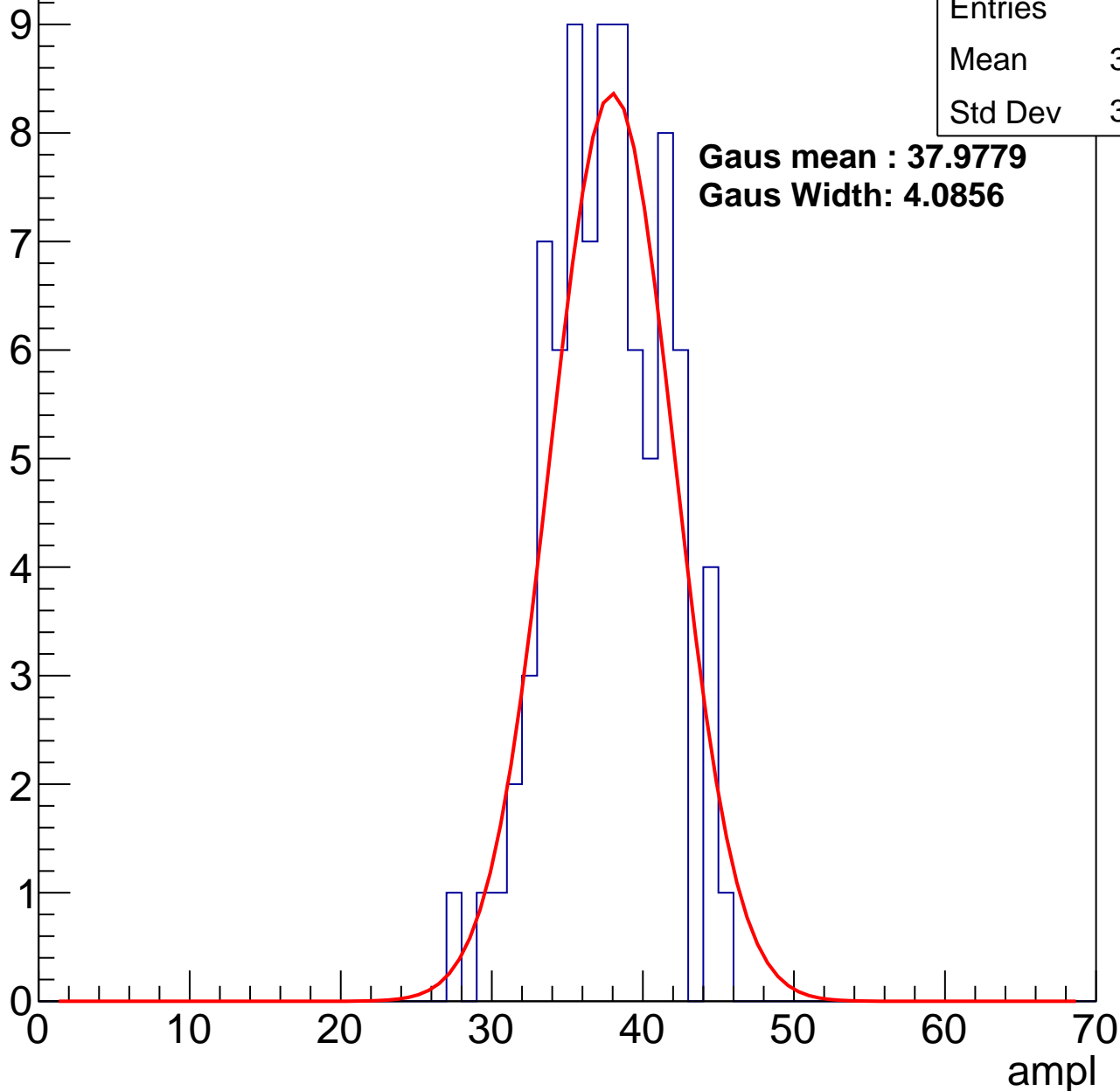
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	37.13
Std Dev	3.732

Gaus mean : 37.9779

Gaus Width: 4.0856



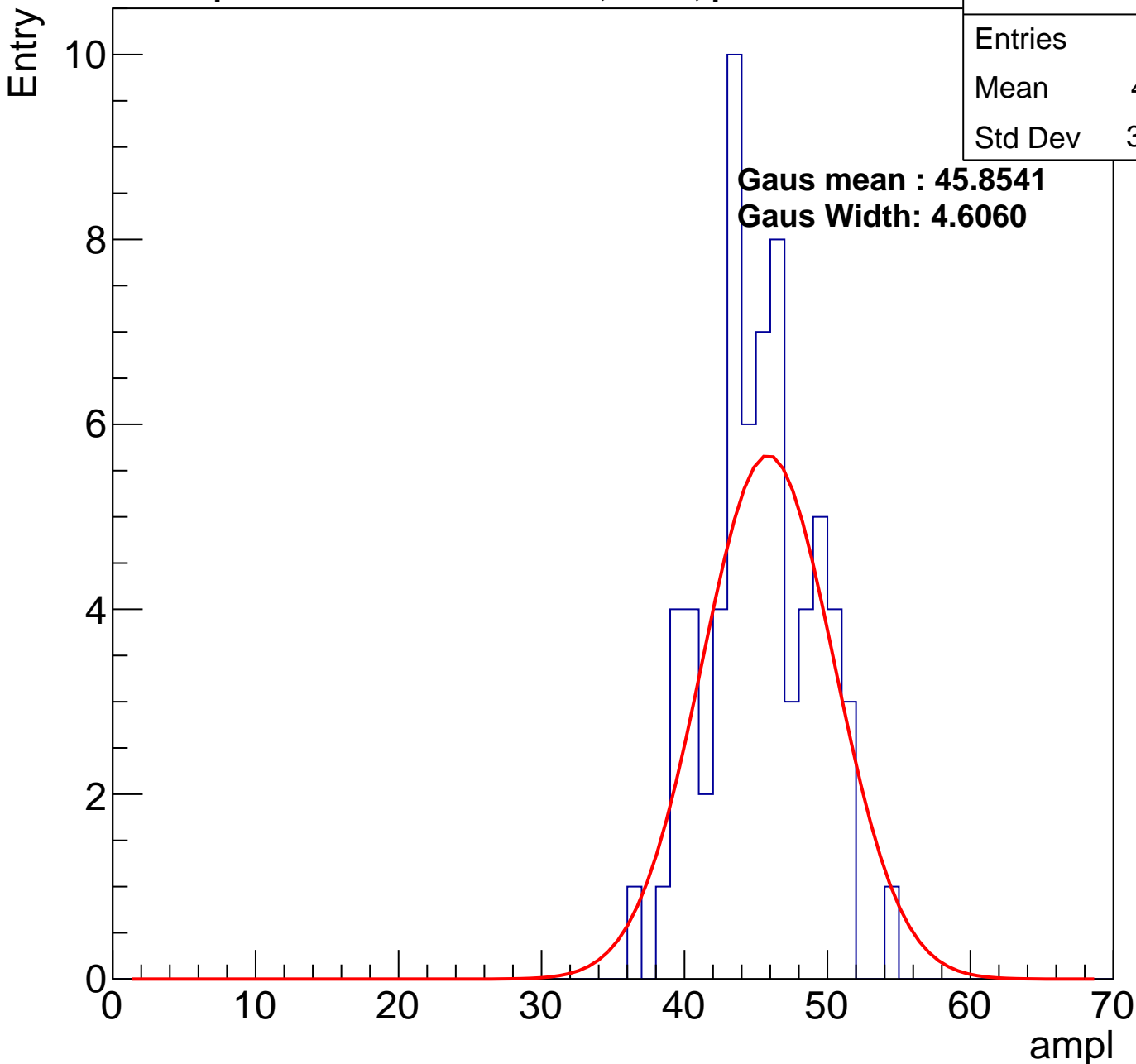
B0L002S, U4-ch69, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	44.81
Std Dev	3.686

Gaus mean : 45.8541

Gaus Width: 4.6060

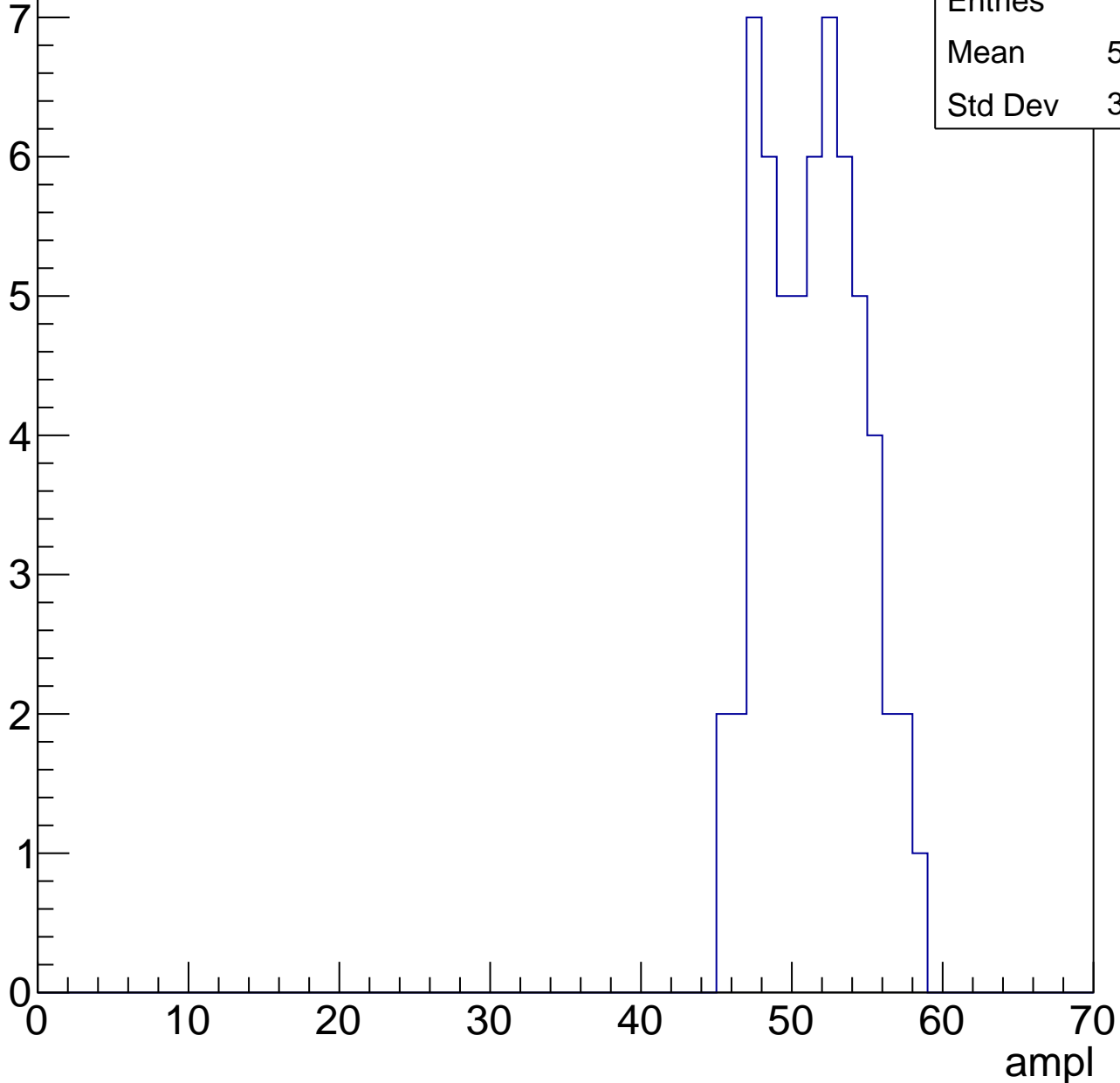


B0L002S, U4-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.93
Std Dev	3.224



B0L002S, U4-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 52

Mean 56.73

Std Dev 2.923

ampl

0

10

20

30

40

50

60

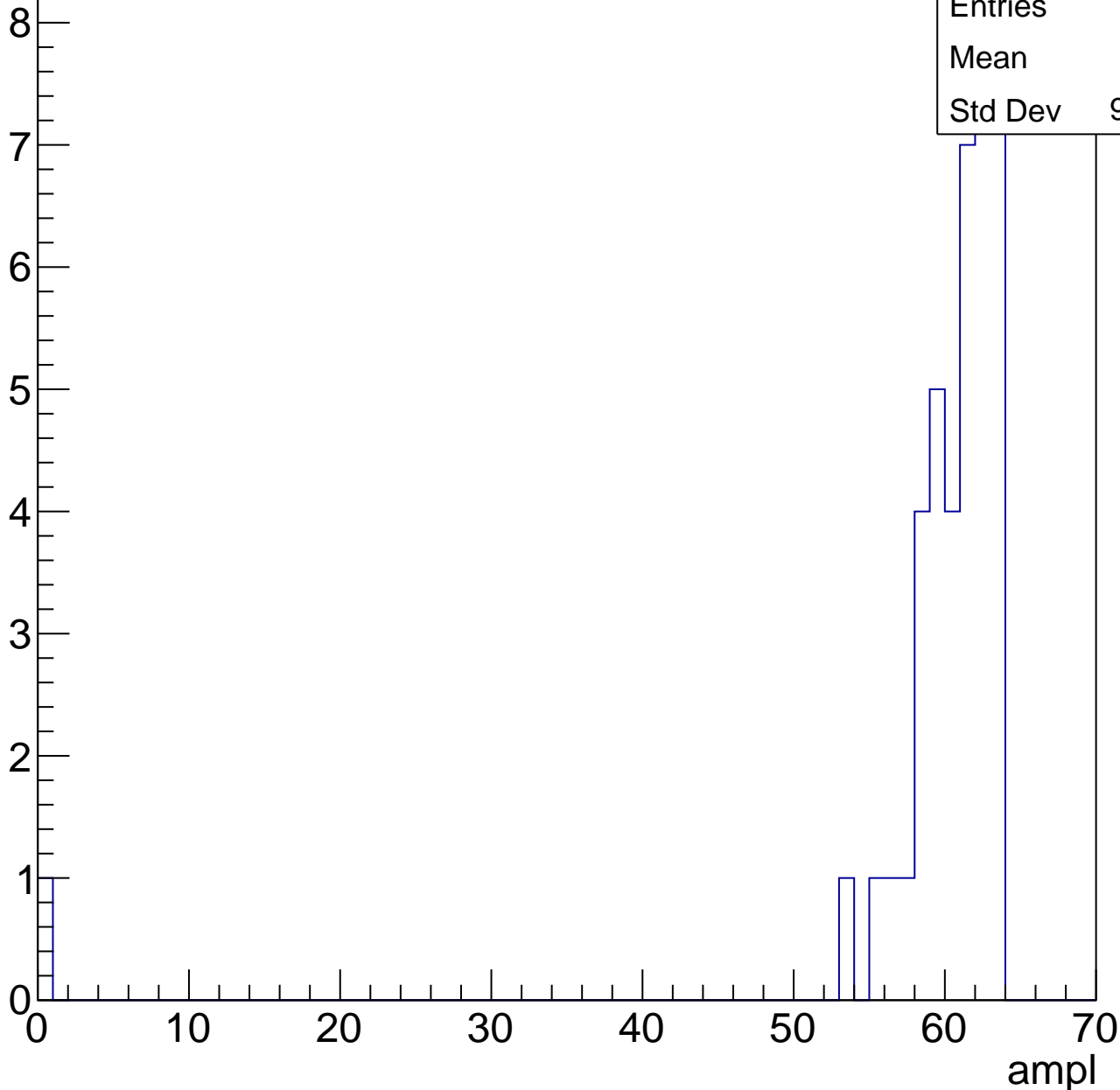
70

B0L002S, U4-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	58.9
Std Dev	9.604



B0L002S, U4-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

ampl

B0L002S, U4-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch70, adc0

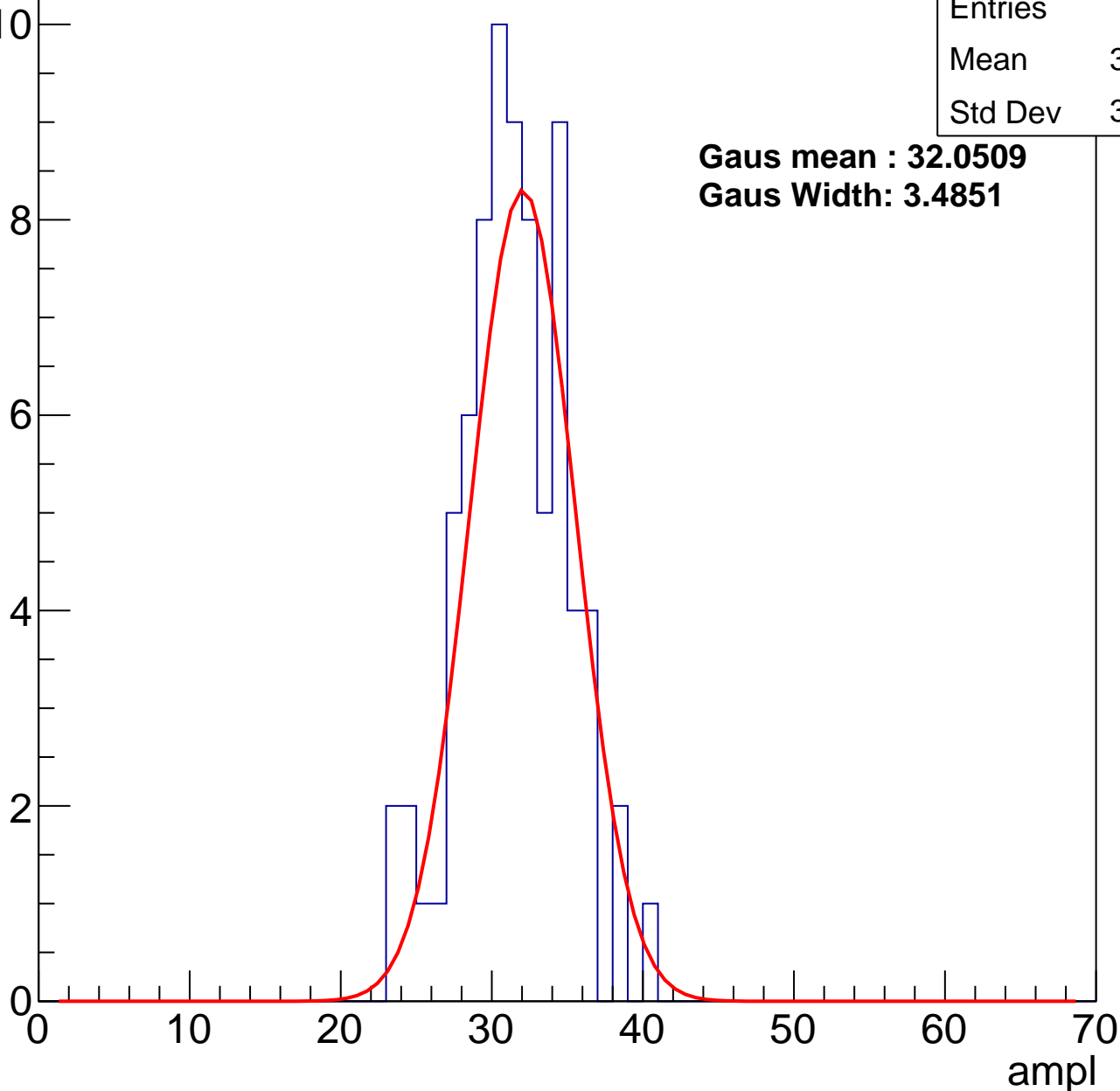
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.99
Std Dev	3.447

Gaus mean : 32.0509

Gaus Width: 3.4851



B0L002S, U4-ch70, adc1

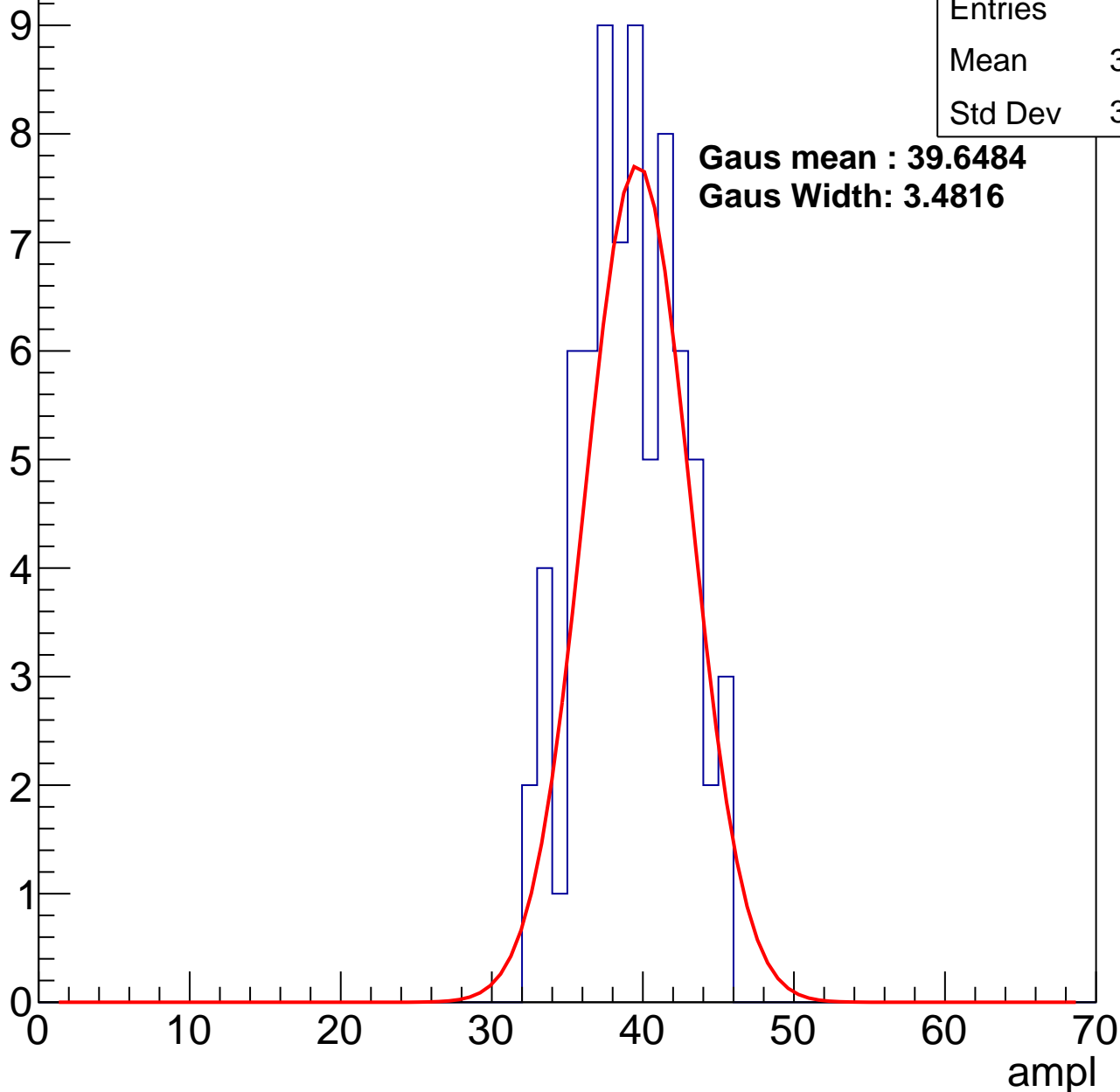
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	38.68
Std Dev	3.272

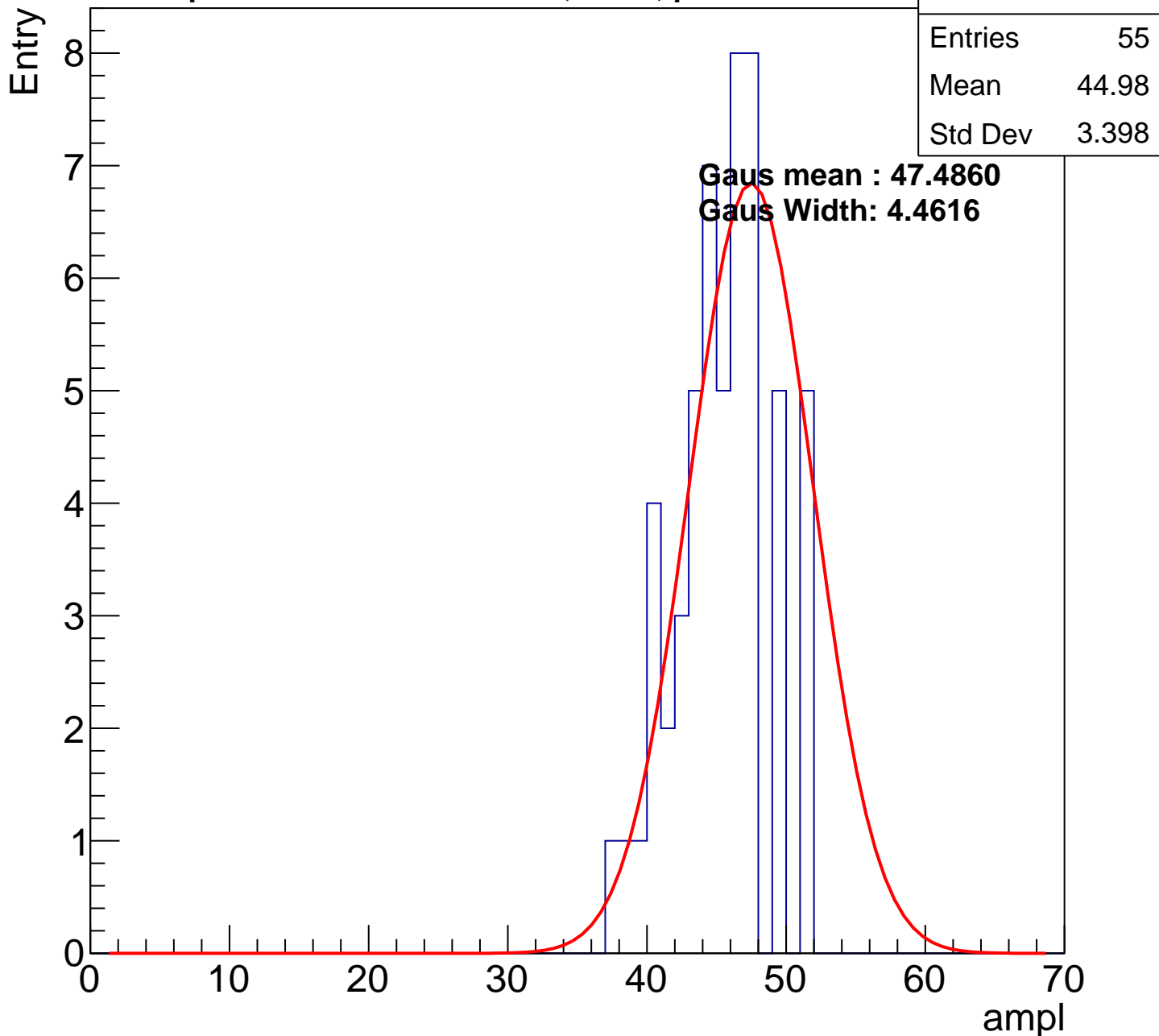
Gaus mean : 39.6484

Gaus Width: 3.4816



B0L002S, U4-ch70, adc2

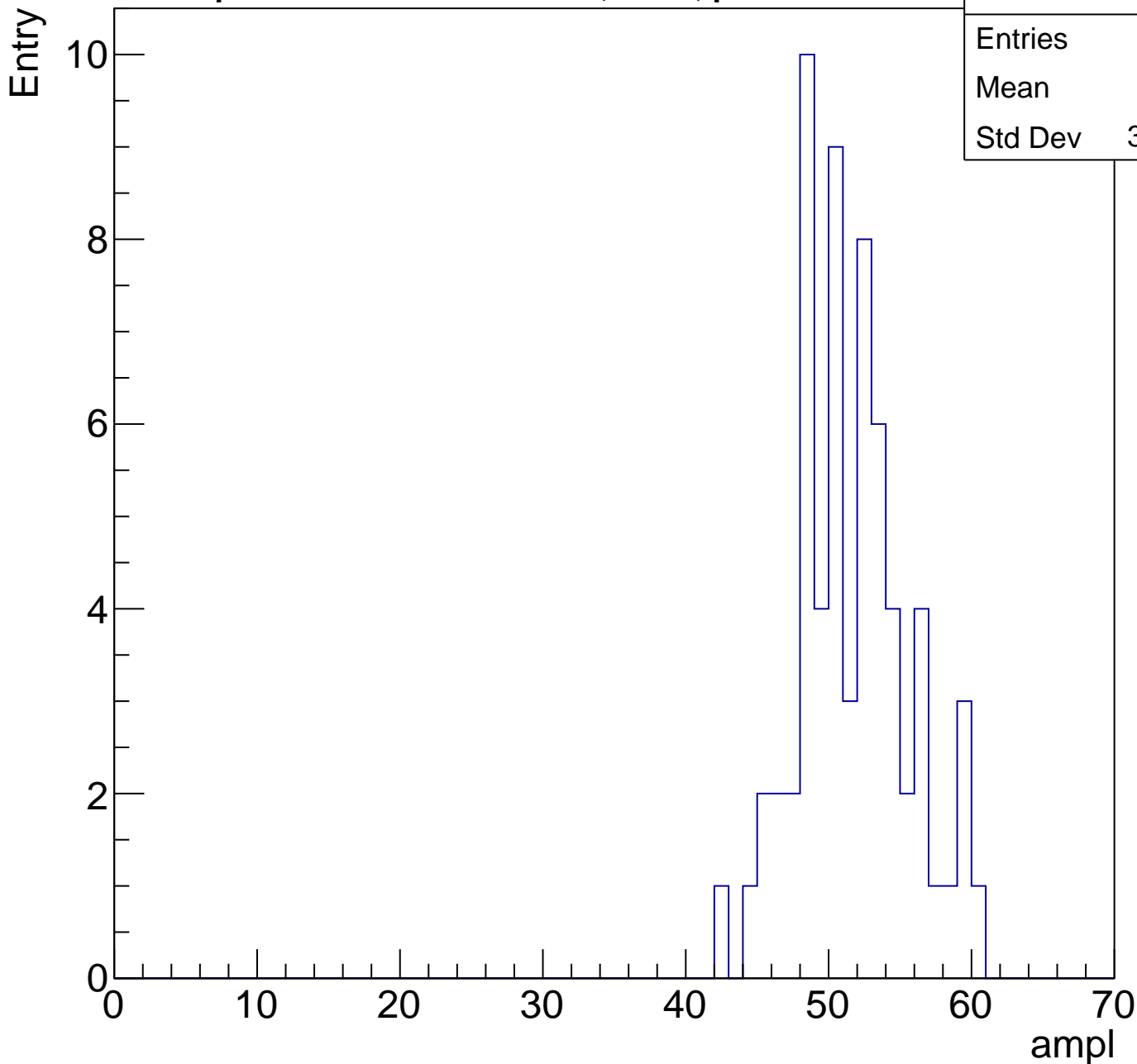
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

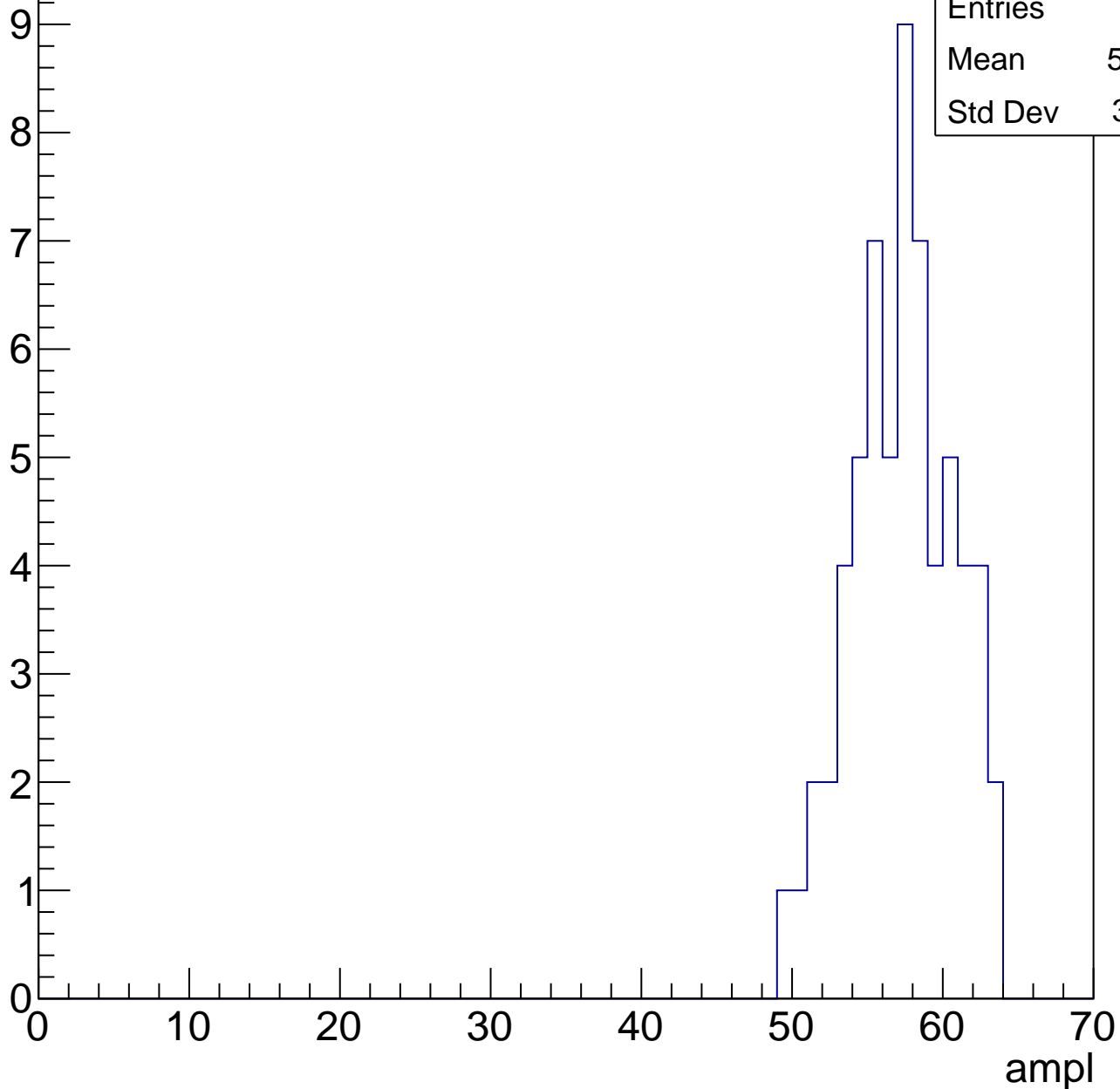
Entries	64
Mean	51.2
Std Dev	3.862



B0L002S, U4-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

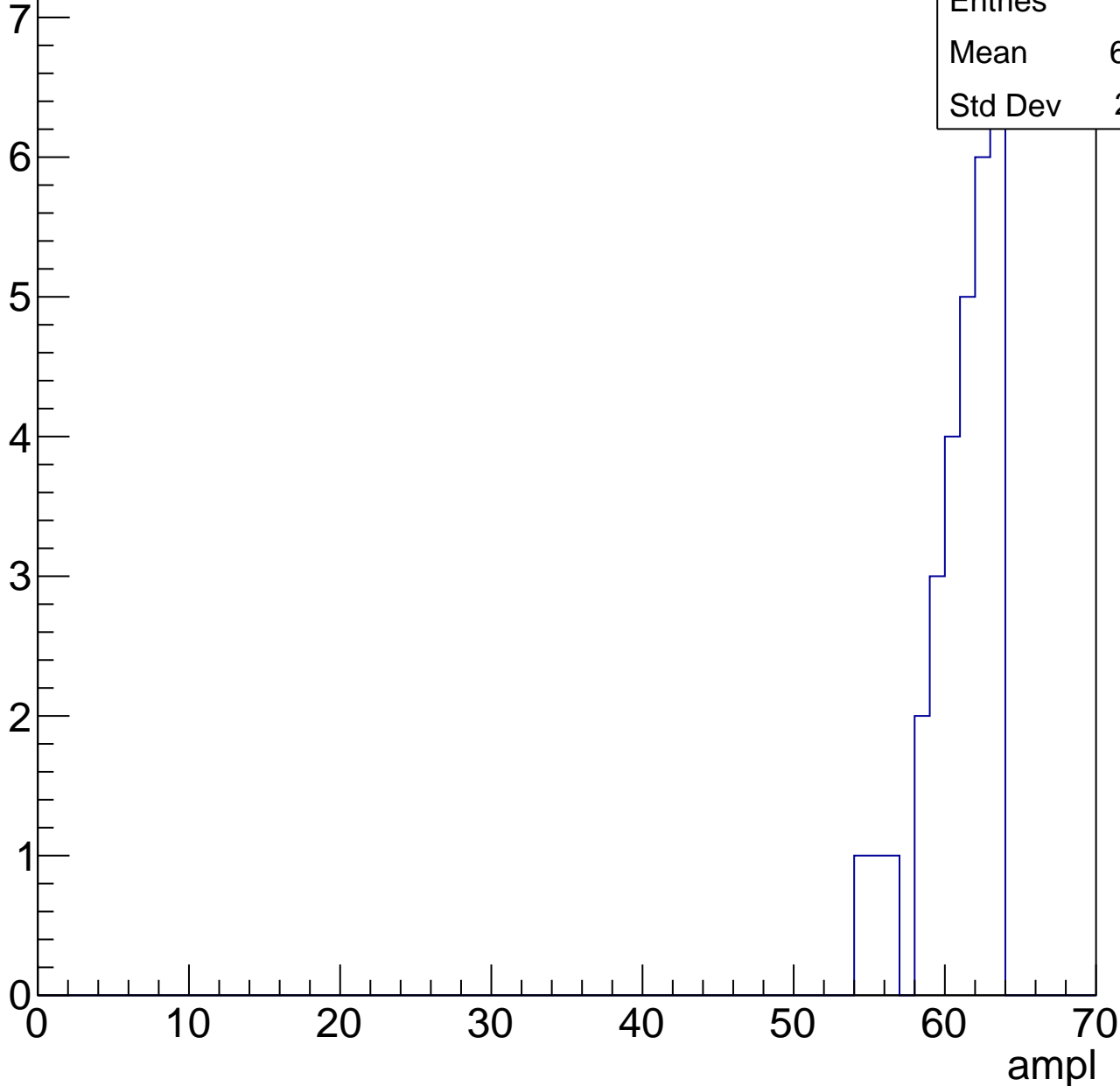


B0L002S, U4-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.53
Std Dev	2.391



B0L002S, U4-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

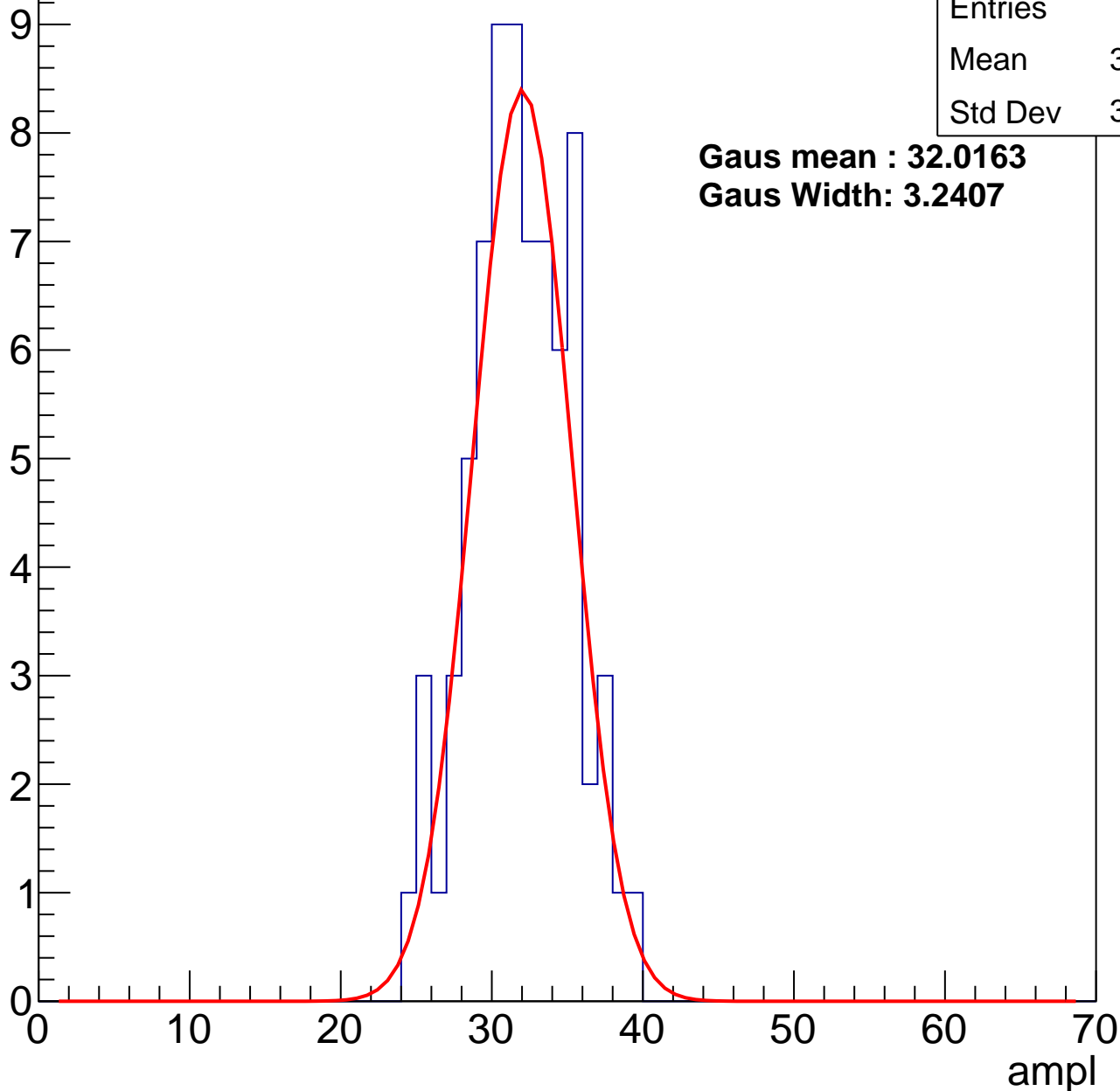


Entries	2
Mean	11
Std Dev	11

B0L002S, U4-ch71, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch71, adc1

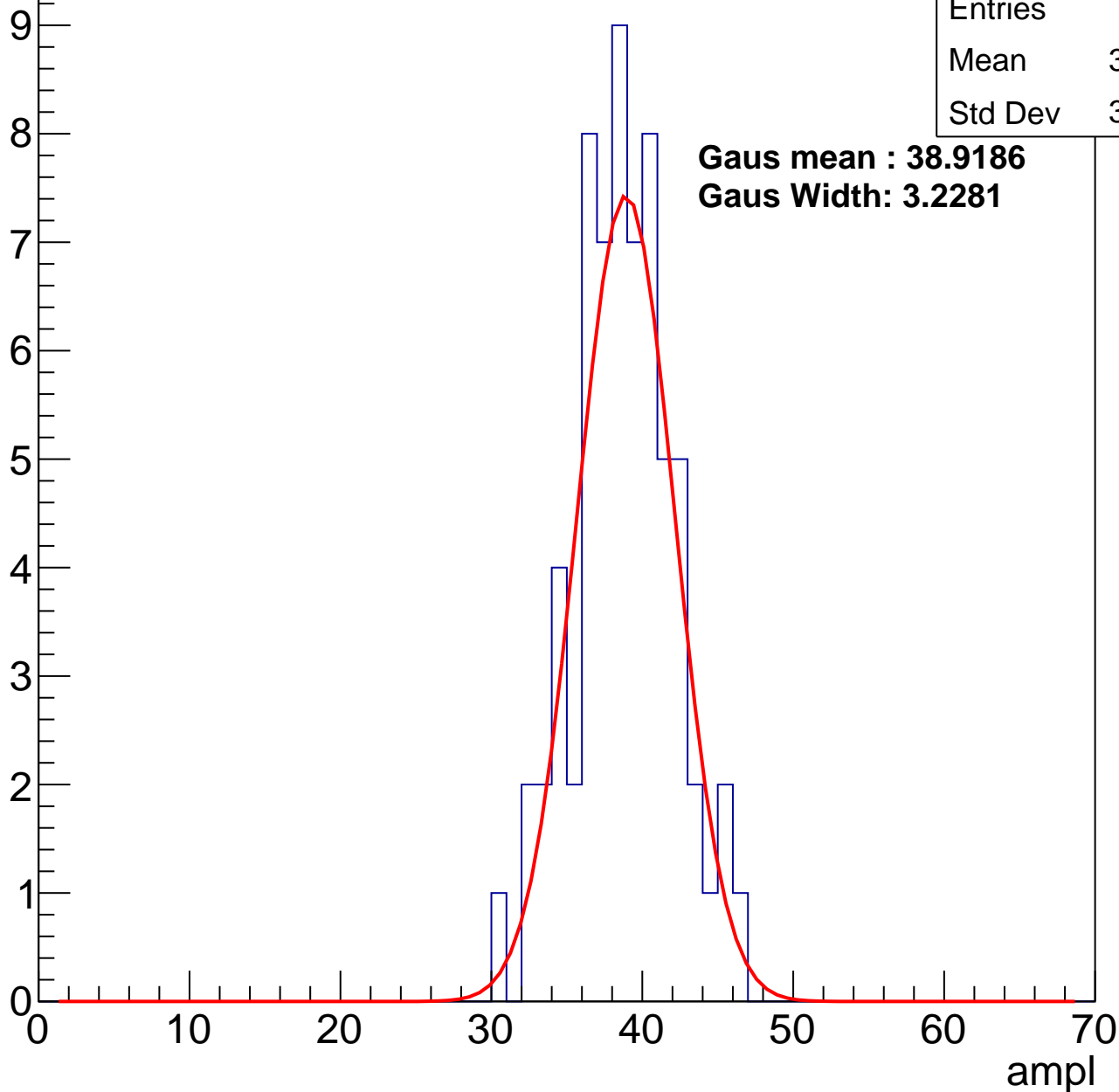
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.32
Std Dev	3.285

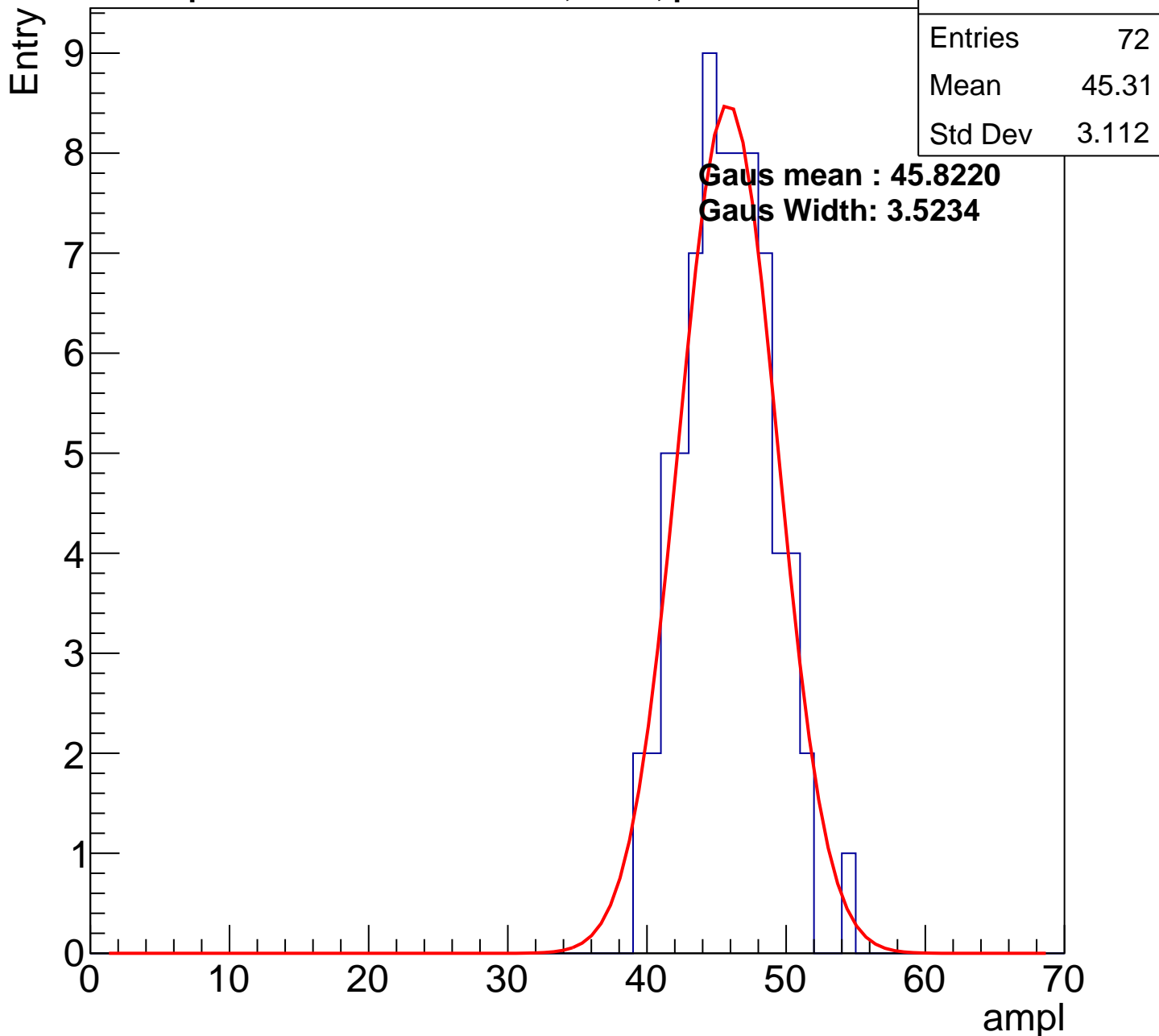
Gaus mean : 38.9186

Gaus Width: 3.2281



B0L002S, U4-ch71, adc2

calib_packv5_042523_0143.root, FC#8, port C1

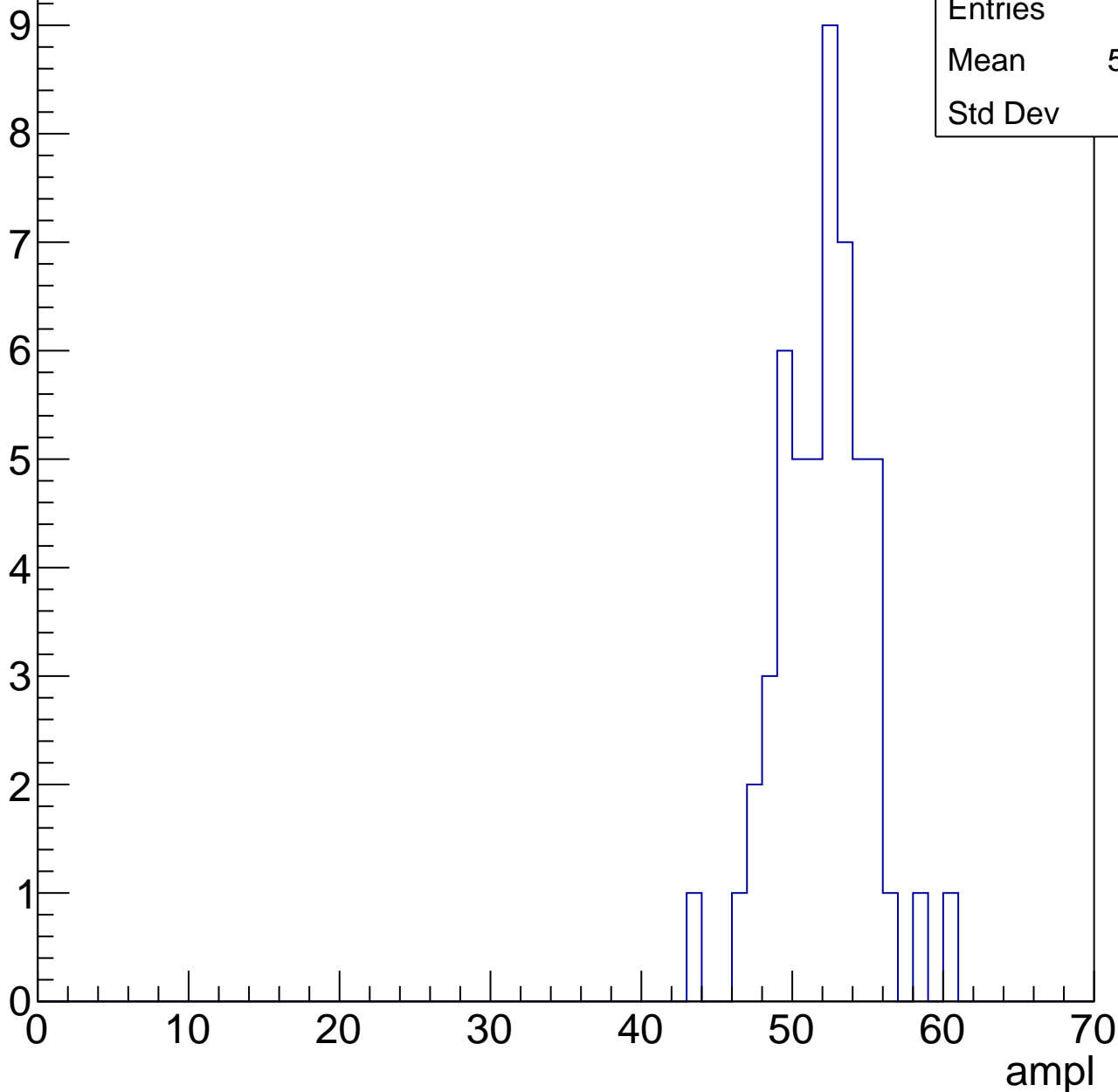


B0L002S, U4-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

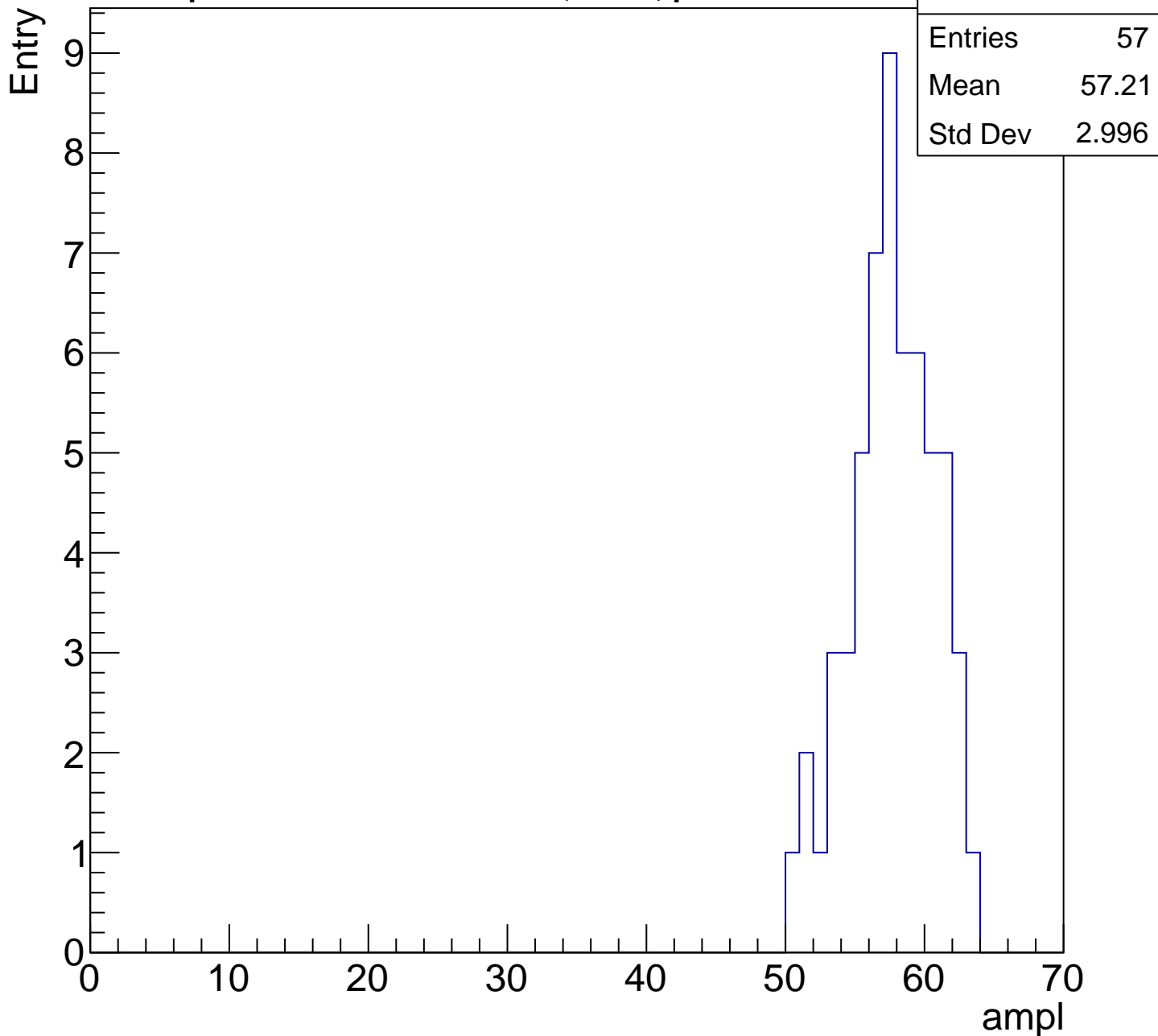
Entry

Entries	52
Mean	51.62
Std Dev	3.02



B0L002S, U4-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

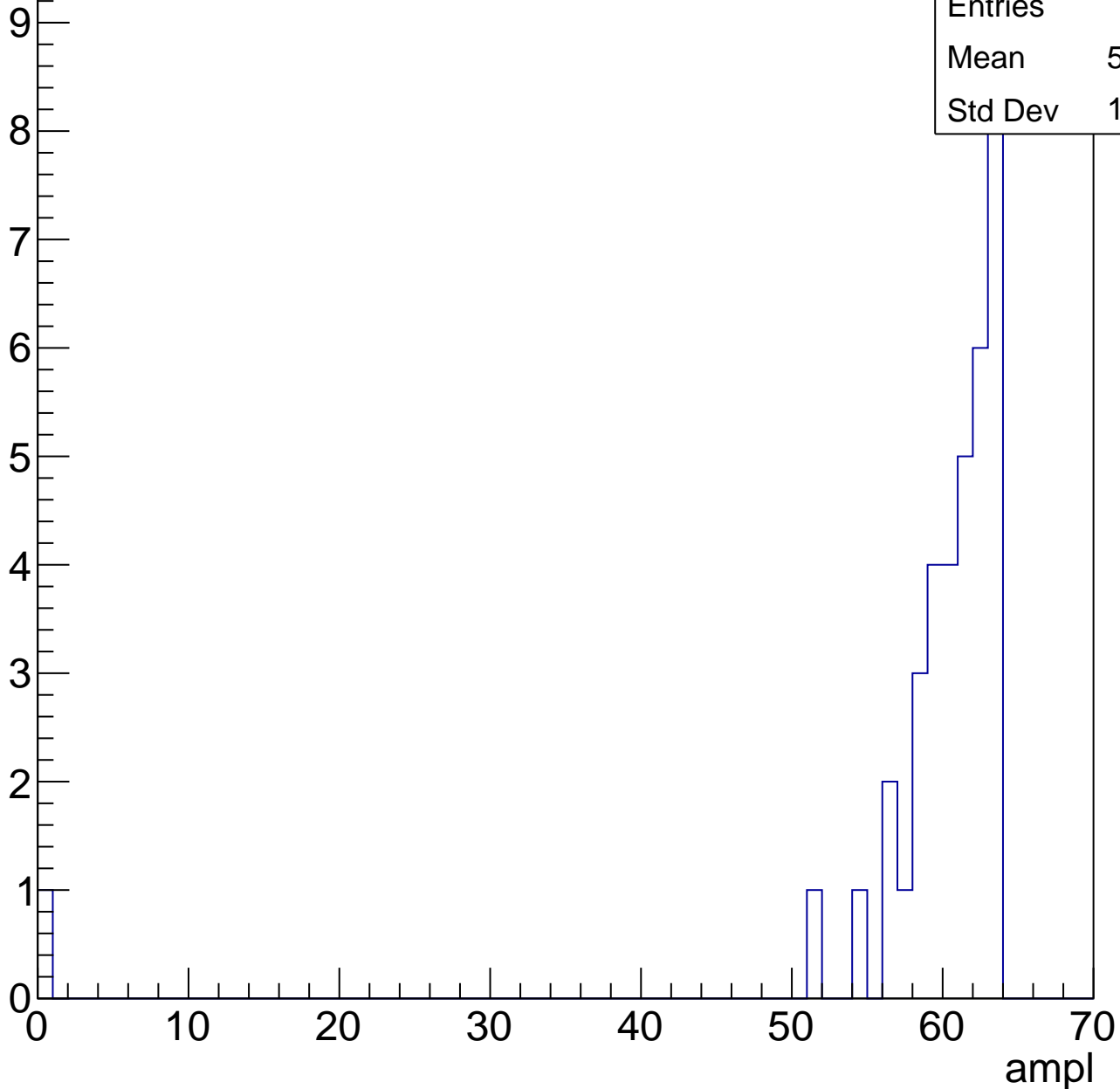


B0L002S, U4-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.59
Std Dev	10.15



B0L002S, U4-ch71, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

B0L002S, U4-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U4-ch72, adc0

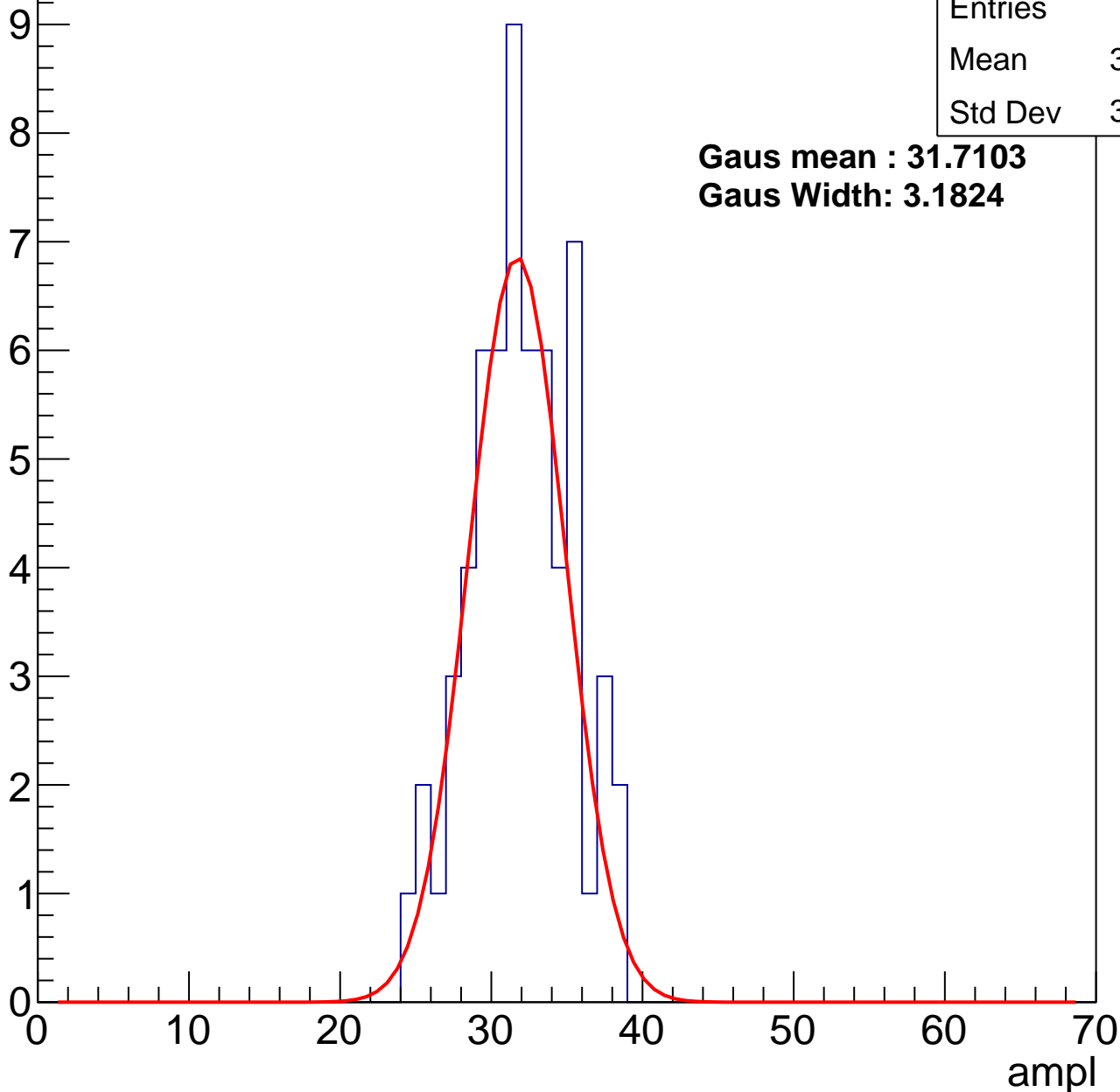
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.48
Std Dev	3.277

Gaus mean : 31.7103

Gaus Width: 3.1824



B0L002S, U4-ch72, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	37.76
Std Dev	3.302

Gaus mean : 38.5646

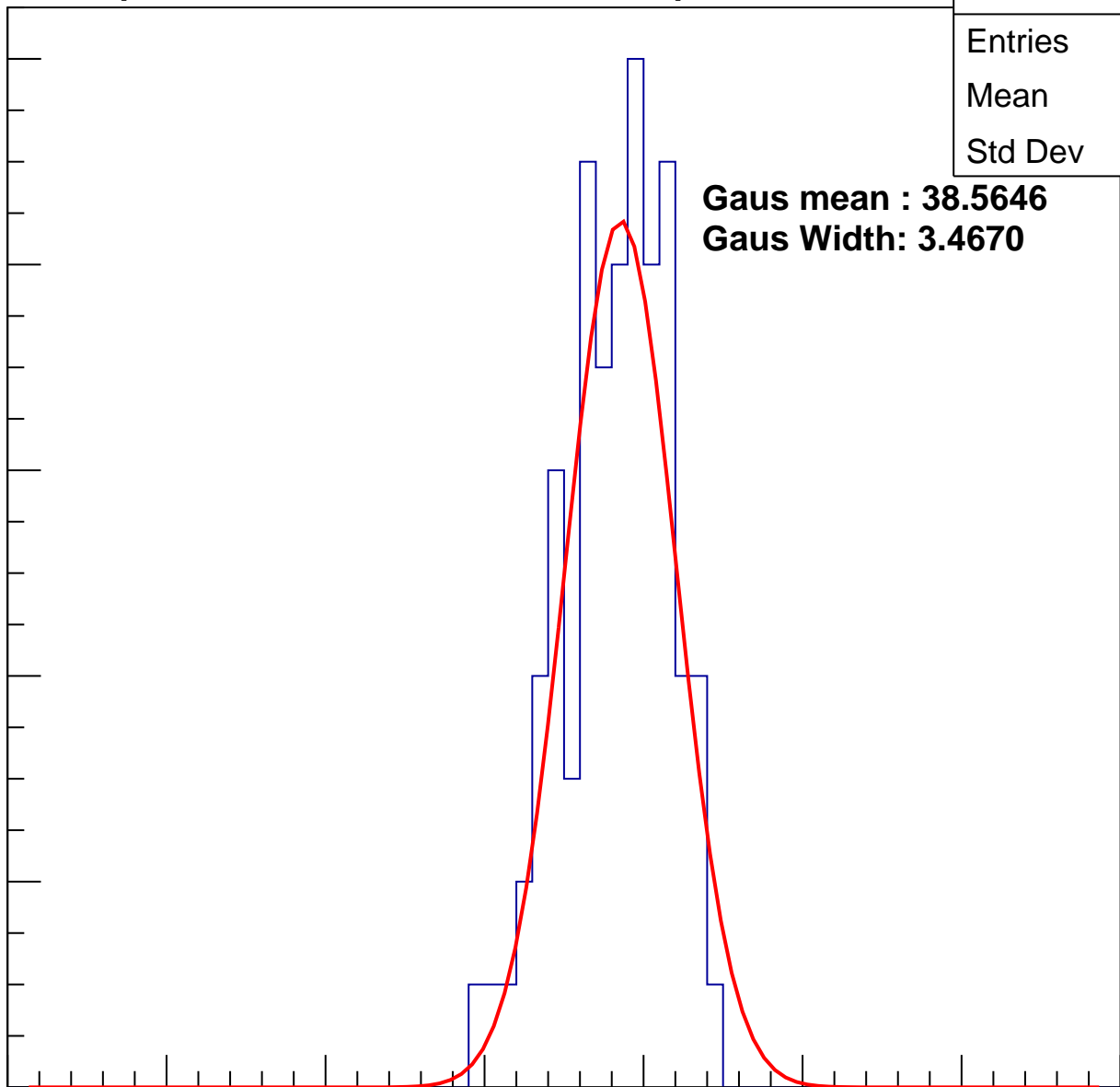
Gaus Width: 3.4670

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch72, adc2

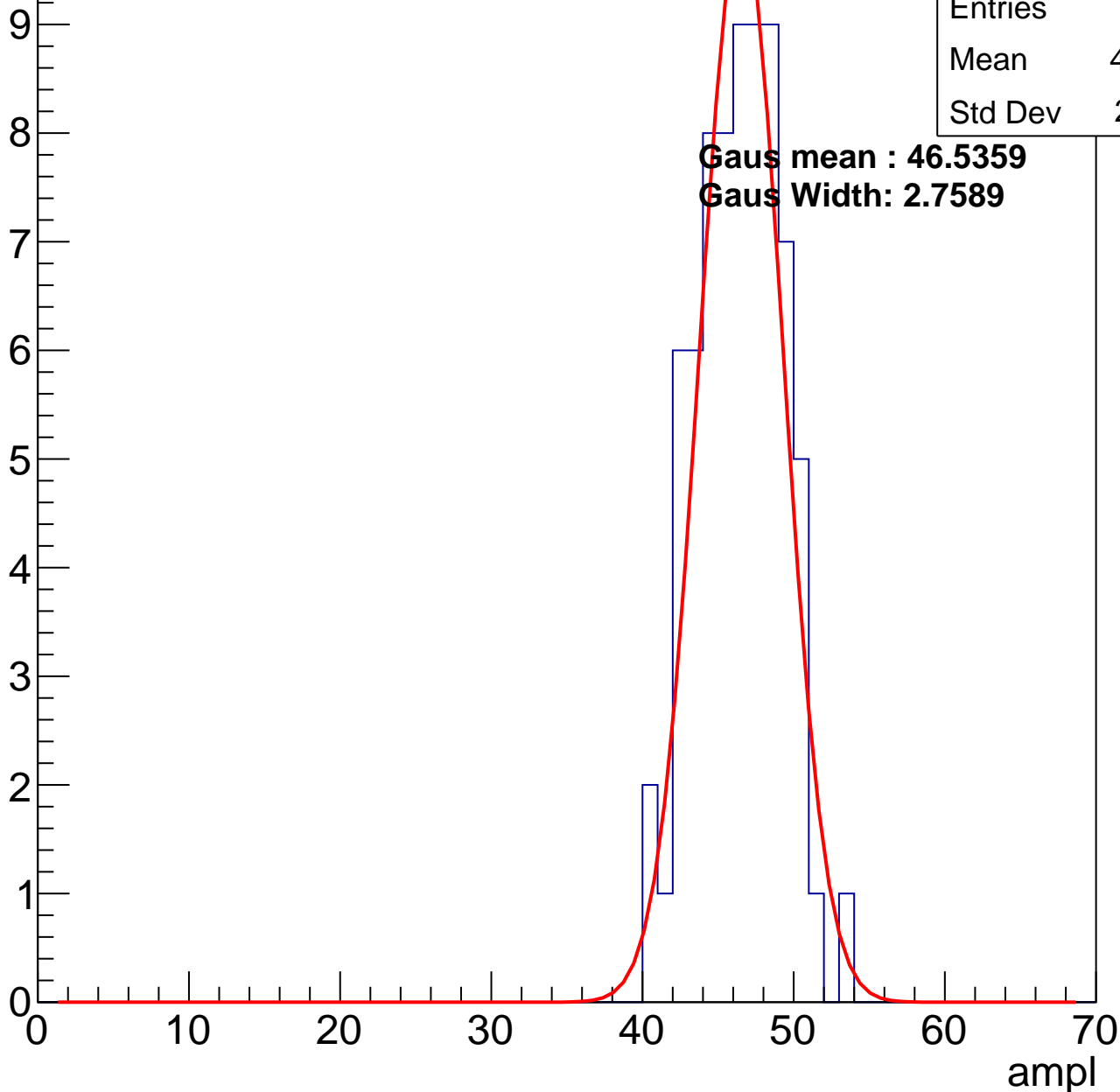
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	45.96
Std Dev	2.761

Gaus mean : 46.5359

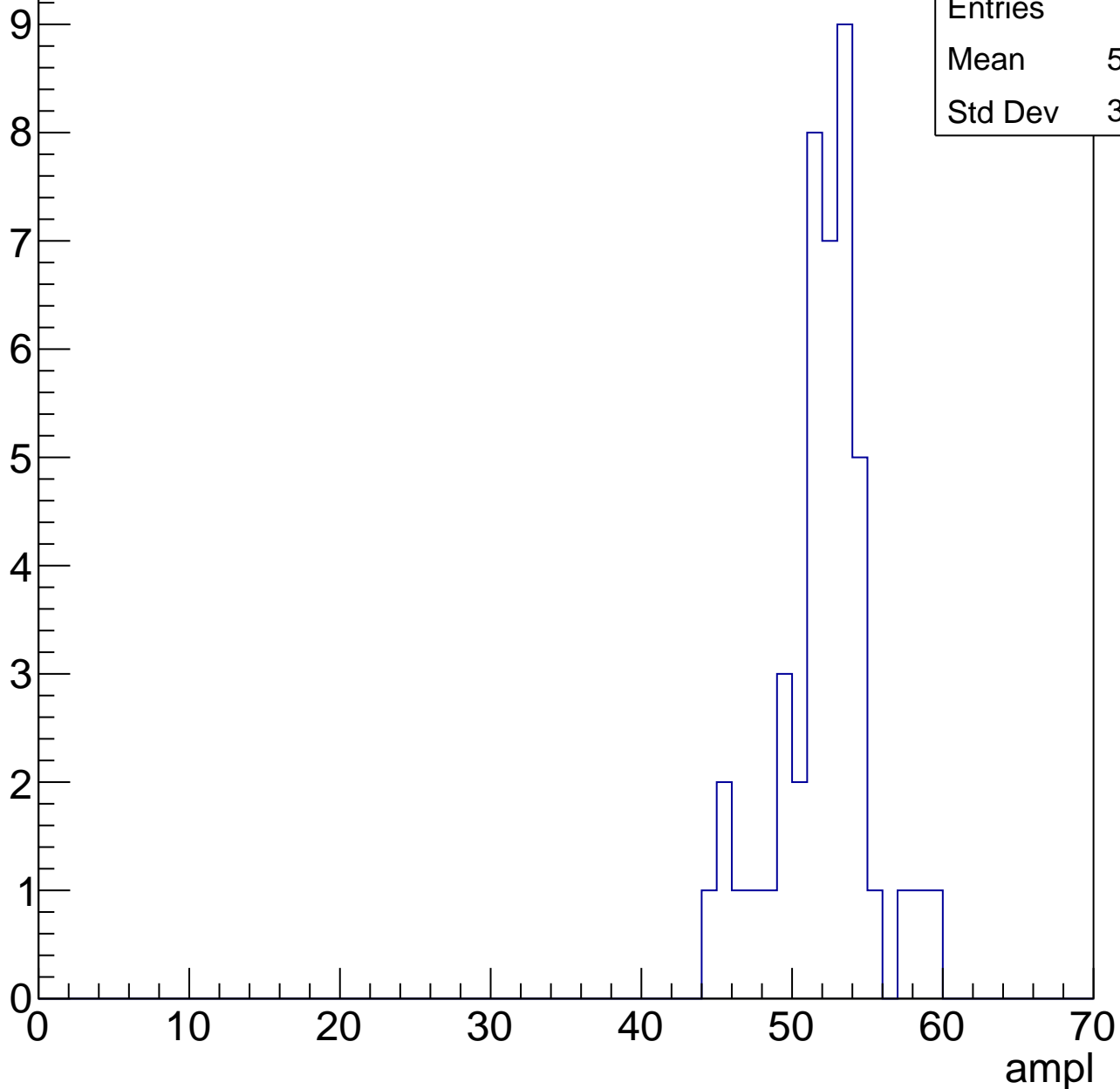
Gaus Width: 2.7589



B0L002S, U4-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



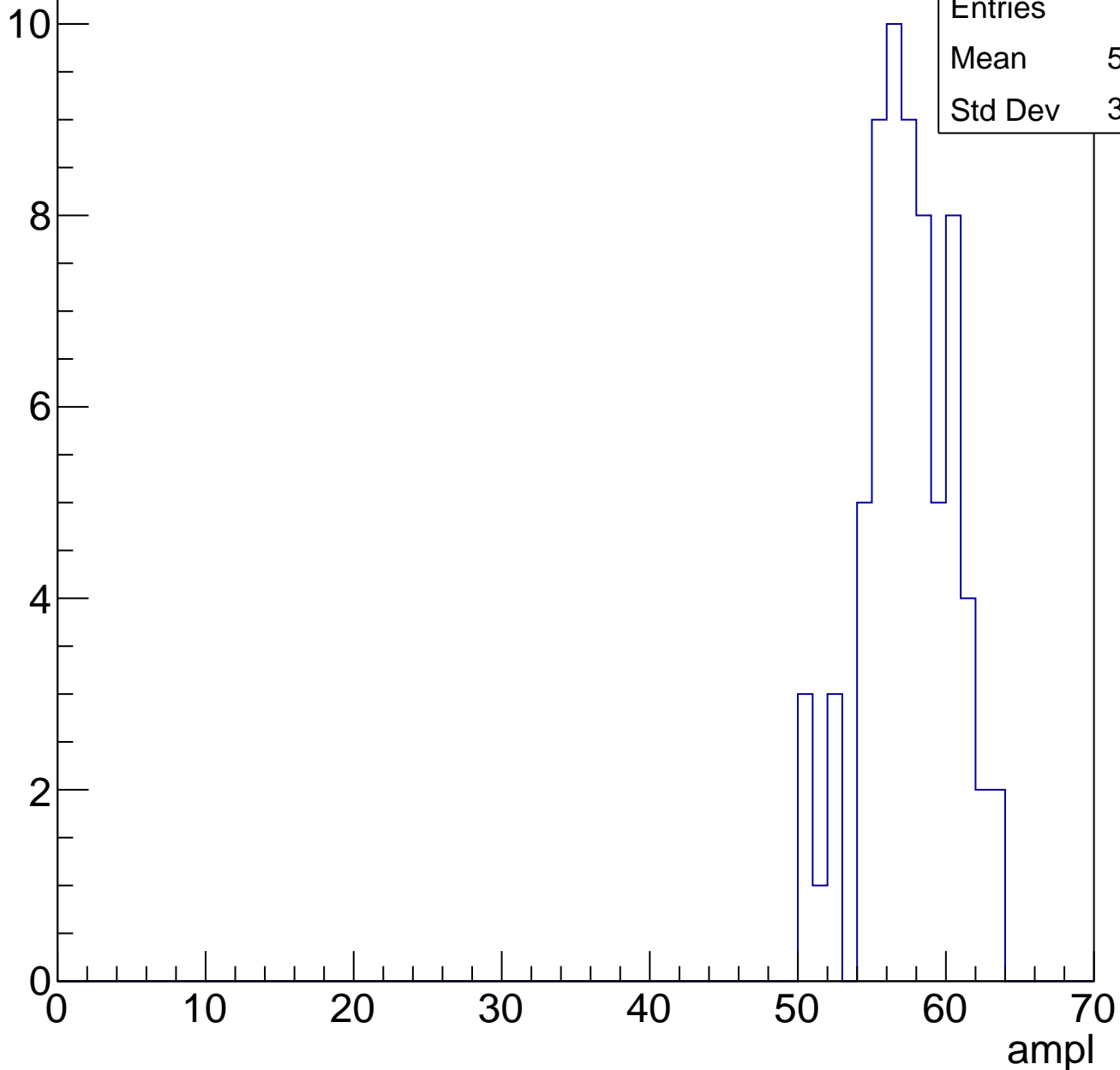
Entries	44
Mean	51.59
Std Dev	3.099

B0L002S, U4-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	56.93
Std Dev	3.033

Entry

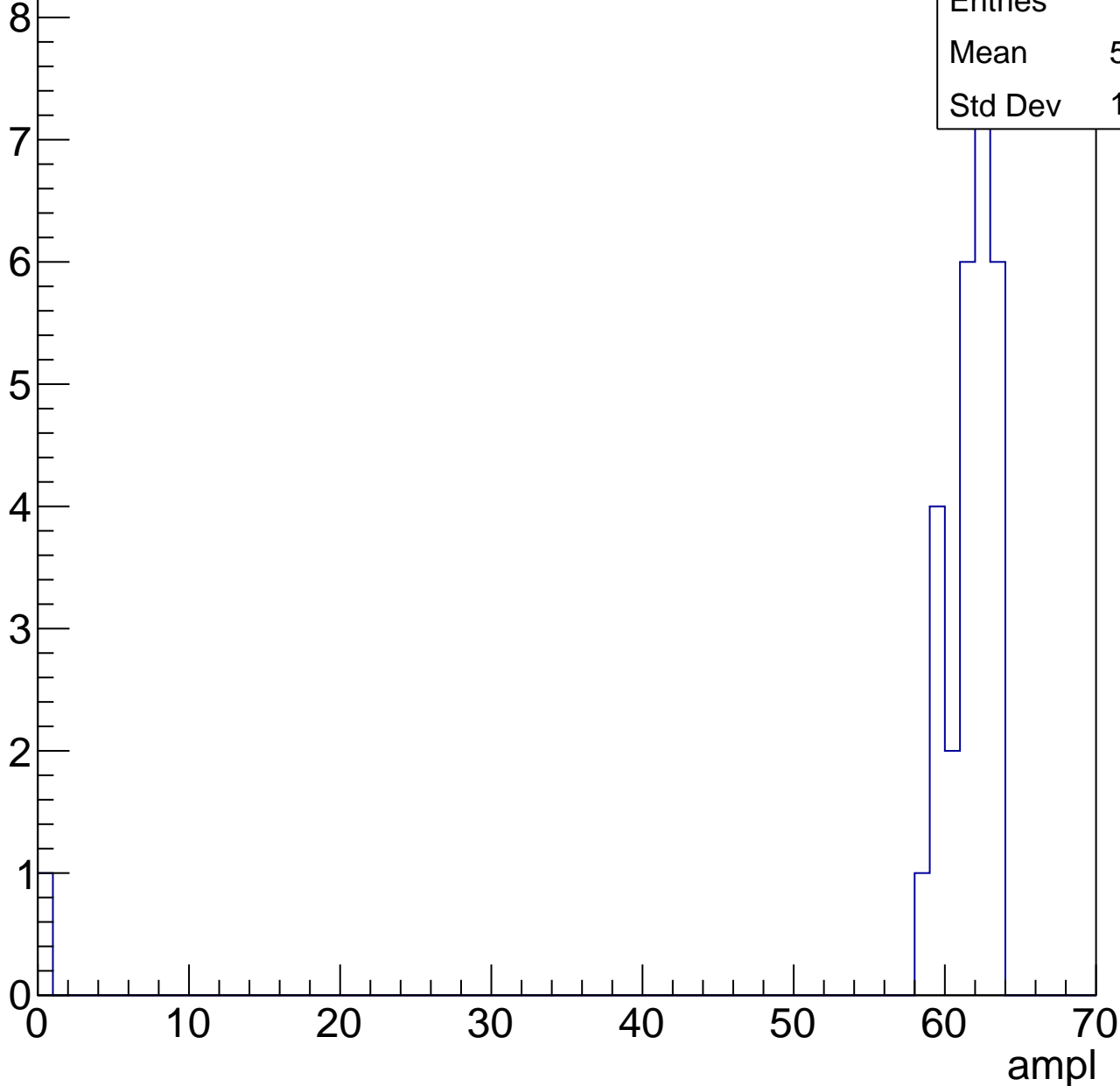


B0L002S, U4-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	59.07
Std Dev	11.46



B0L002S, U4-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch73, adc0

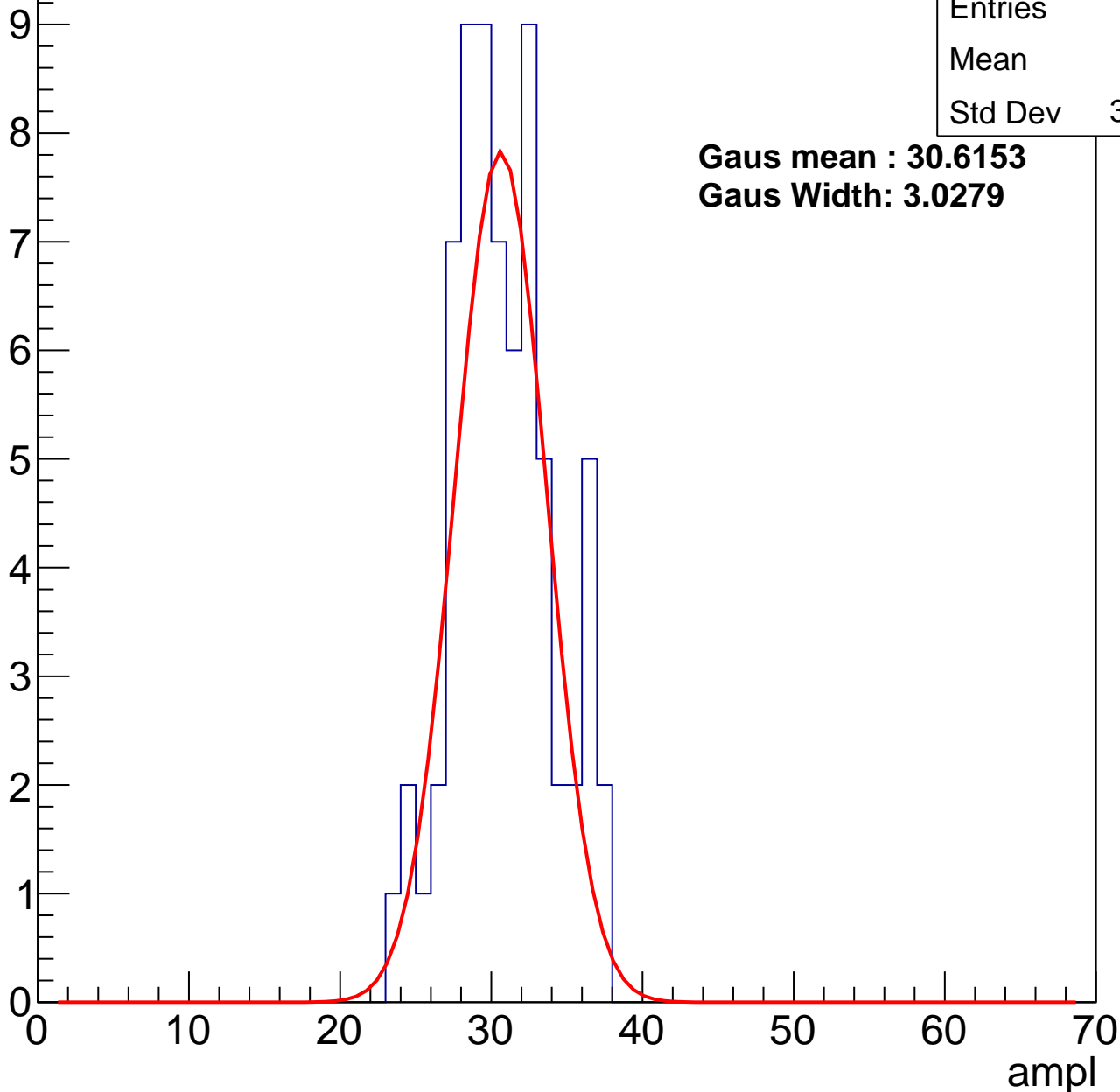
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.3
Std Dev	3.245

Gaus mean : 30.6153

Gaus Width: 3.0279



B0L002S, U4-ch73, adc1

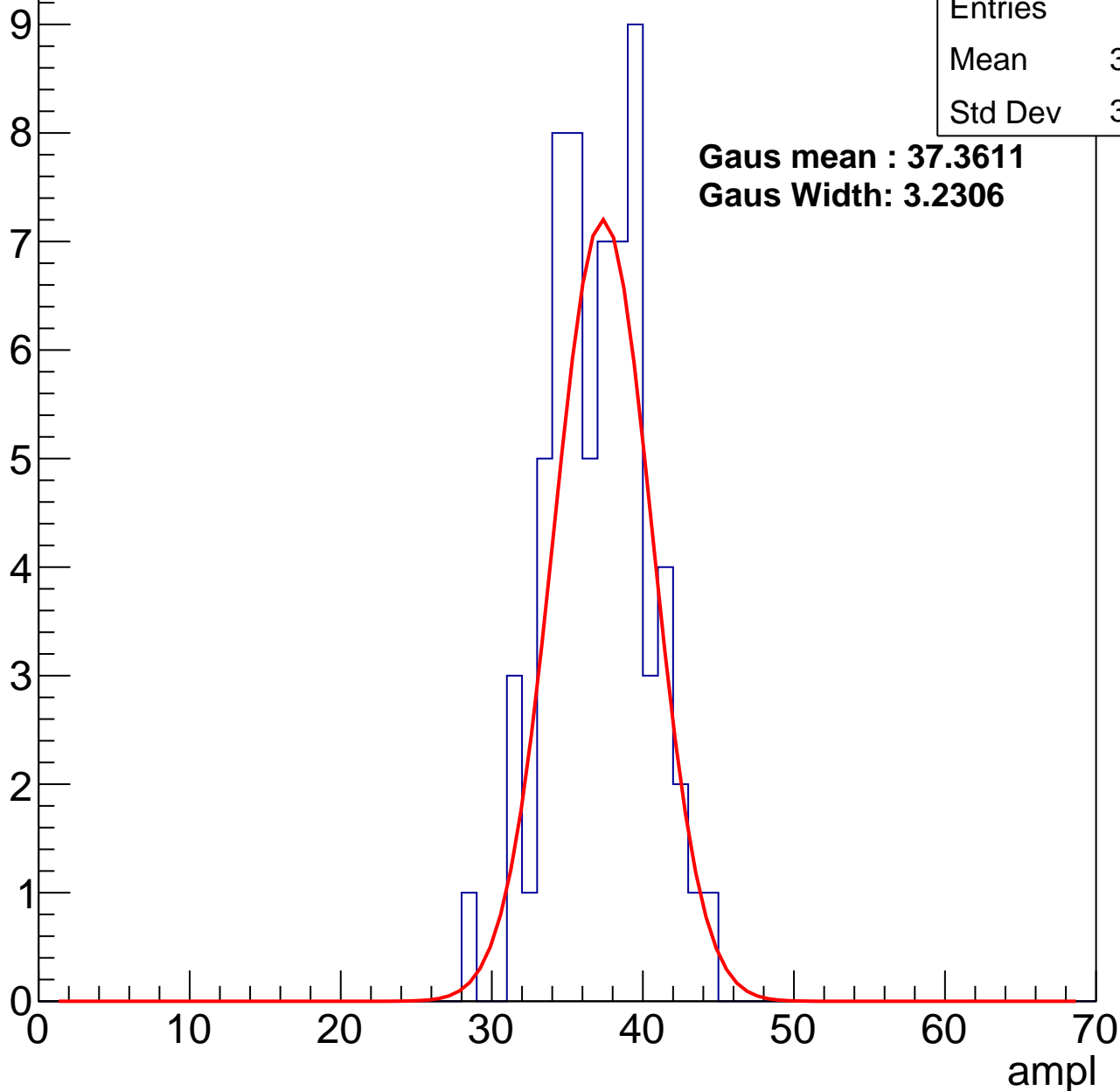
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.63
Std Dev	3.194

Gaus mean : 37.3611

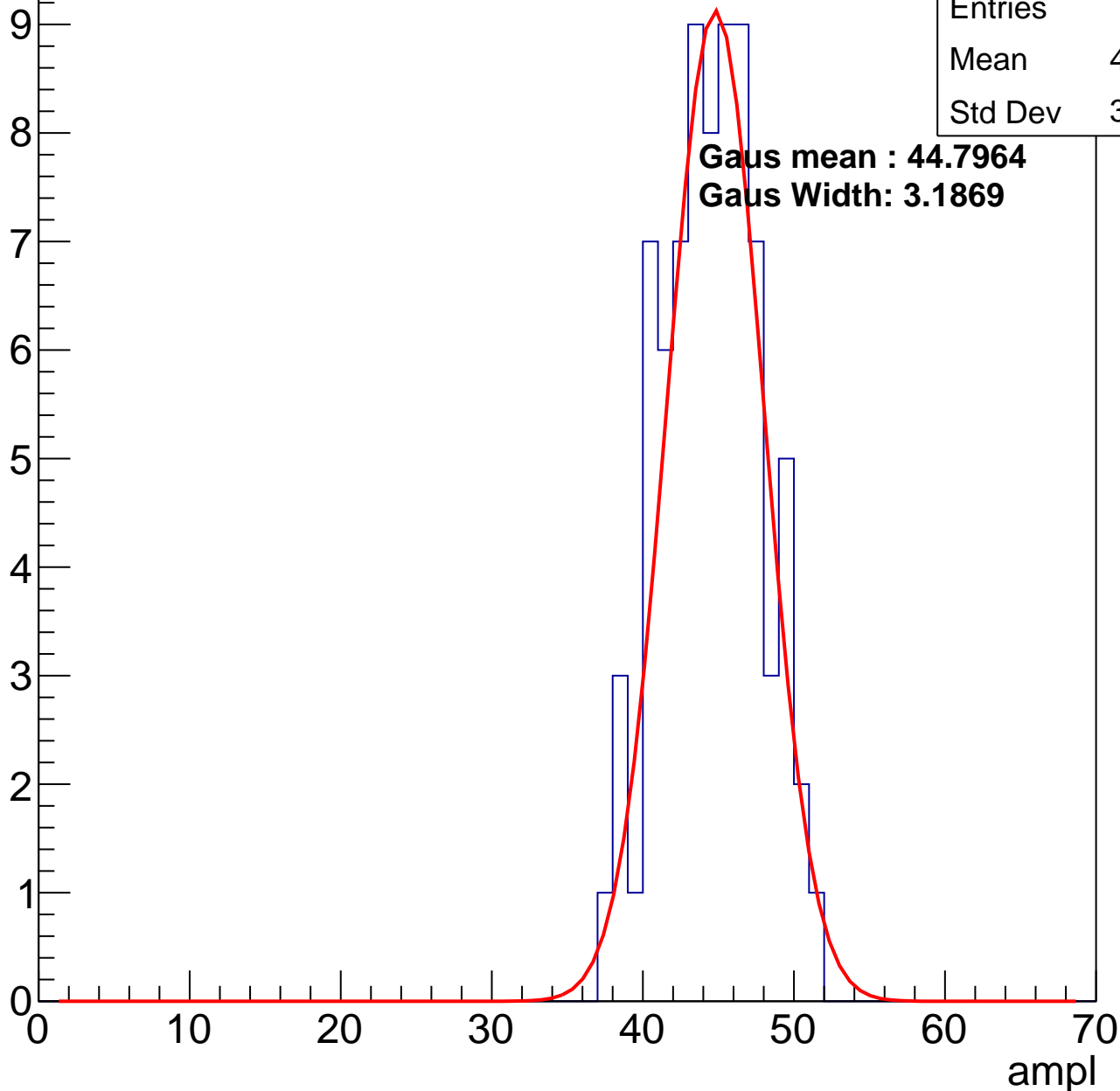
Gaus Width: 3.2306



B0L002S, U4-ch73, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

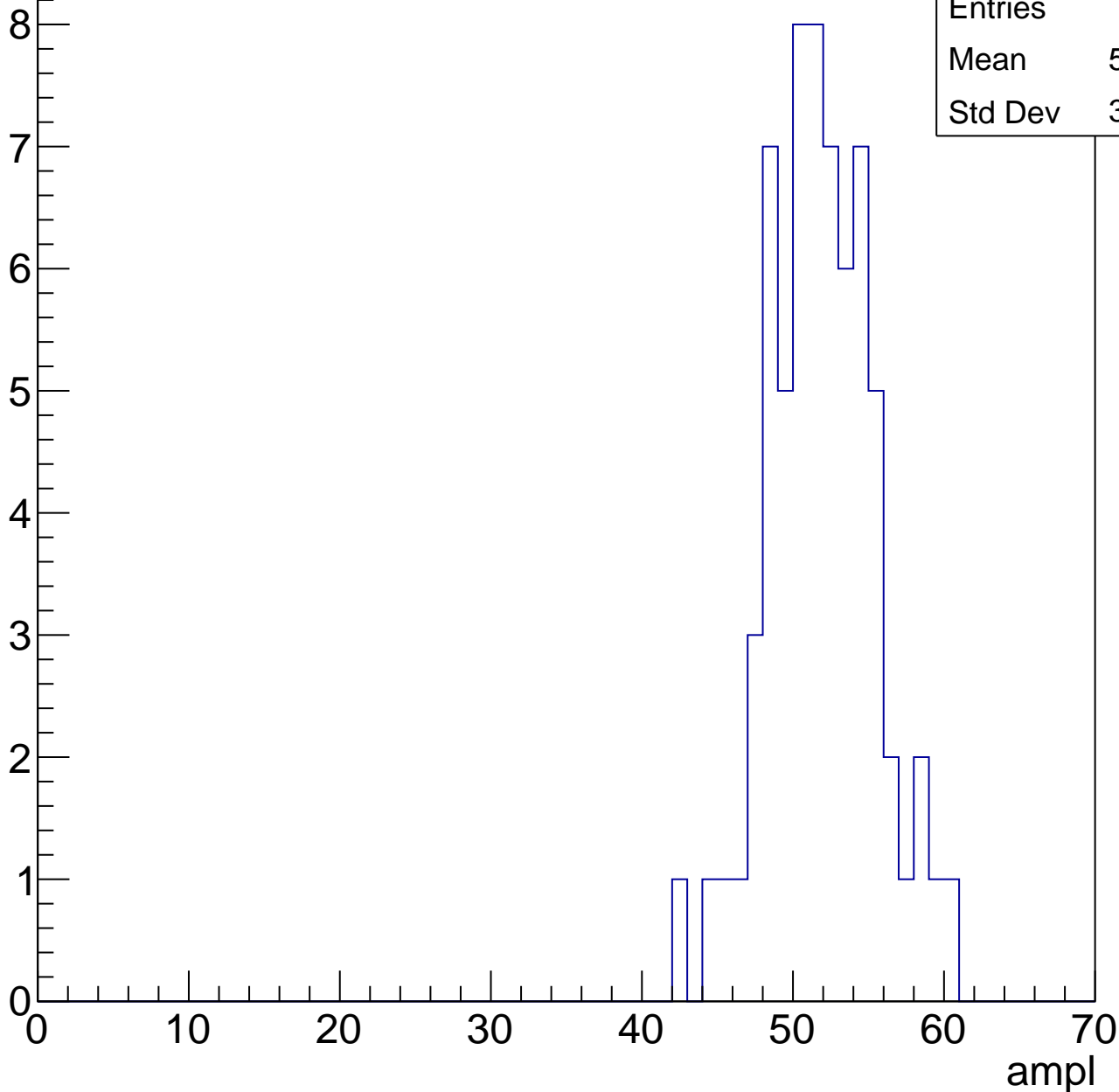


B0L002S, U4-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

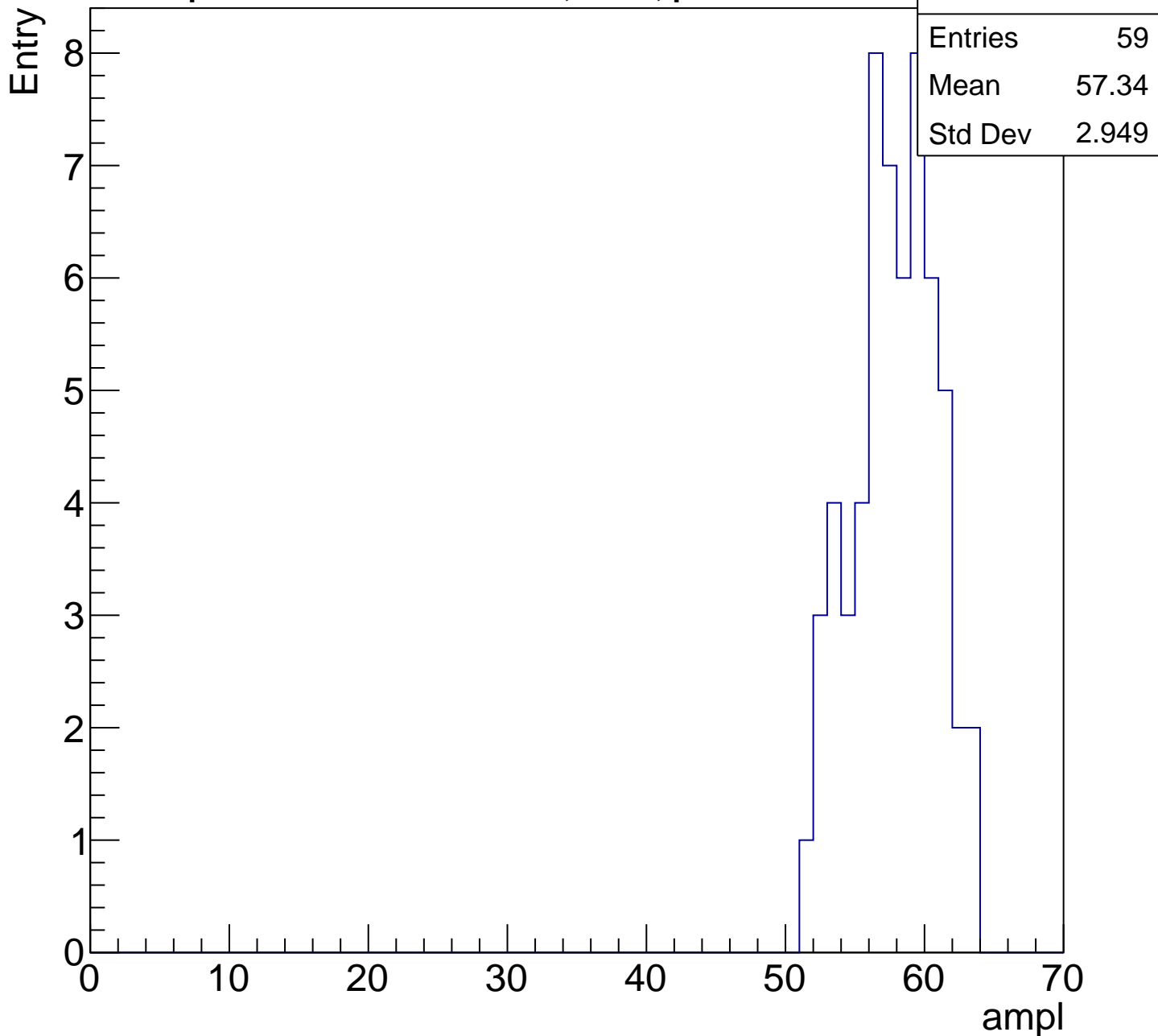
Entry

Entries	67
Mean	51.43
Std Dev	3.499



B0L002S, U4-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

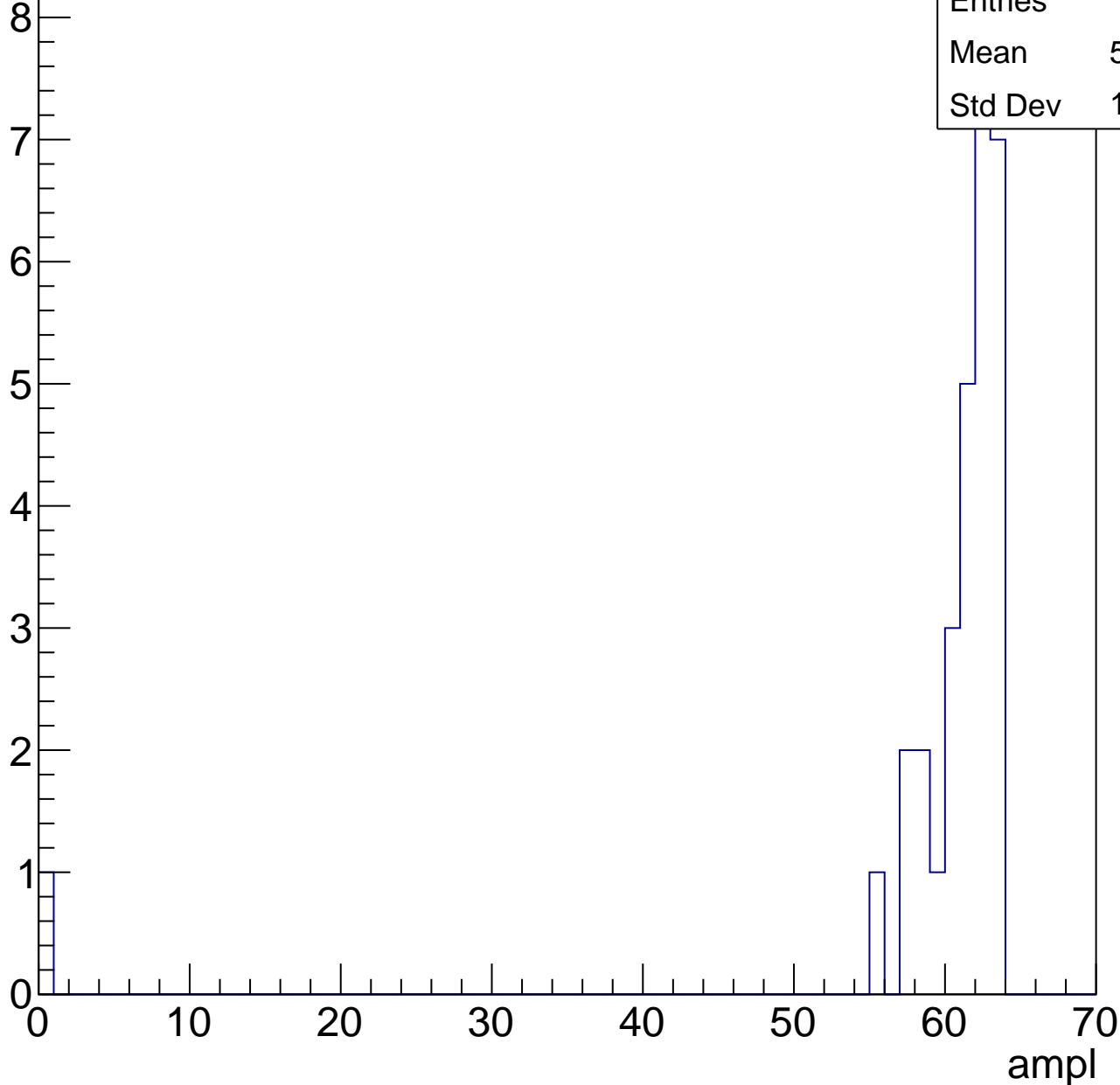


B0L002S, U4-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.87
Std Dev	11.13



B0L002S, U4-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B0L002S, U4-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch74, adc0

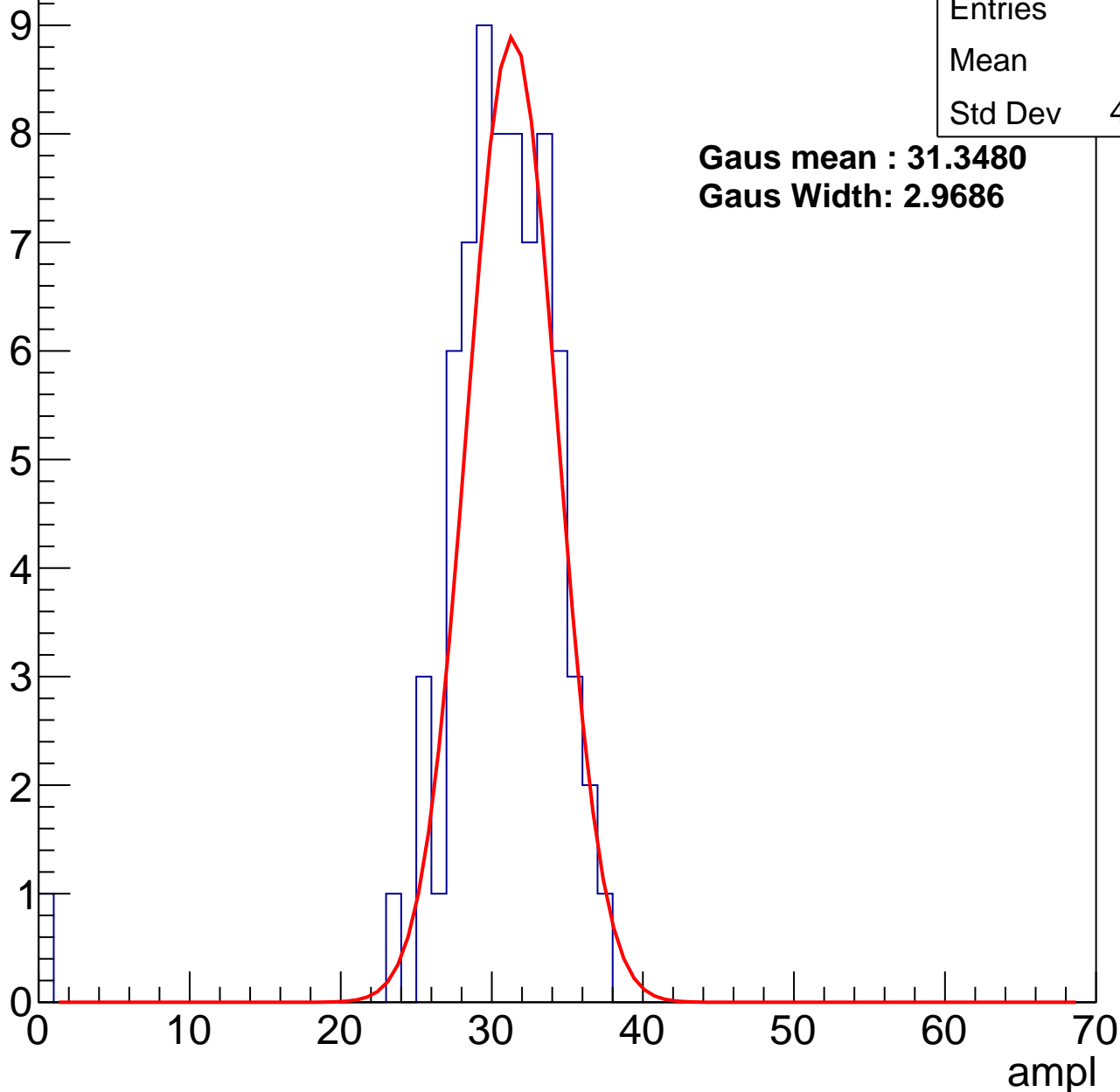
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.1
Std Dev	4.643

Gaus mean : 31.3480

Gaus Width: 2.9686



B0L002S, U4-ch74, adc1

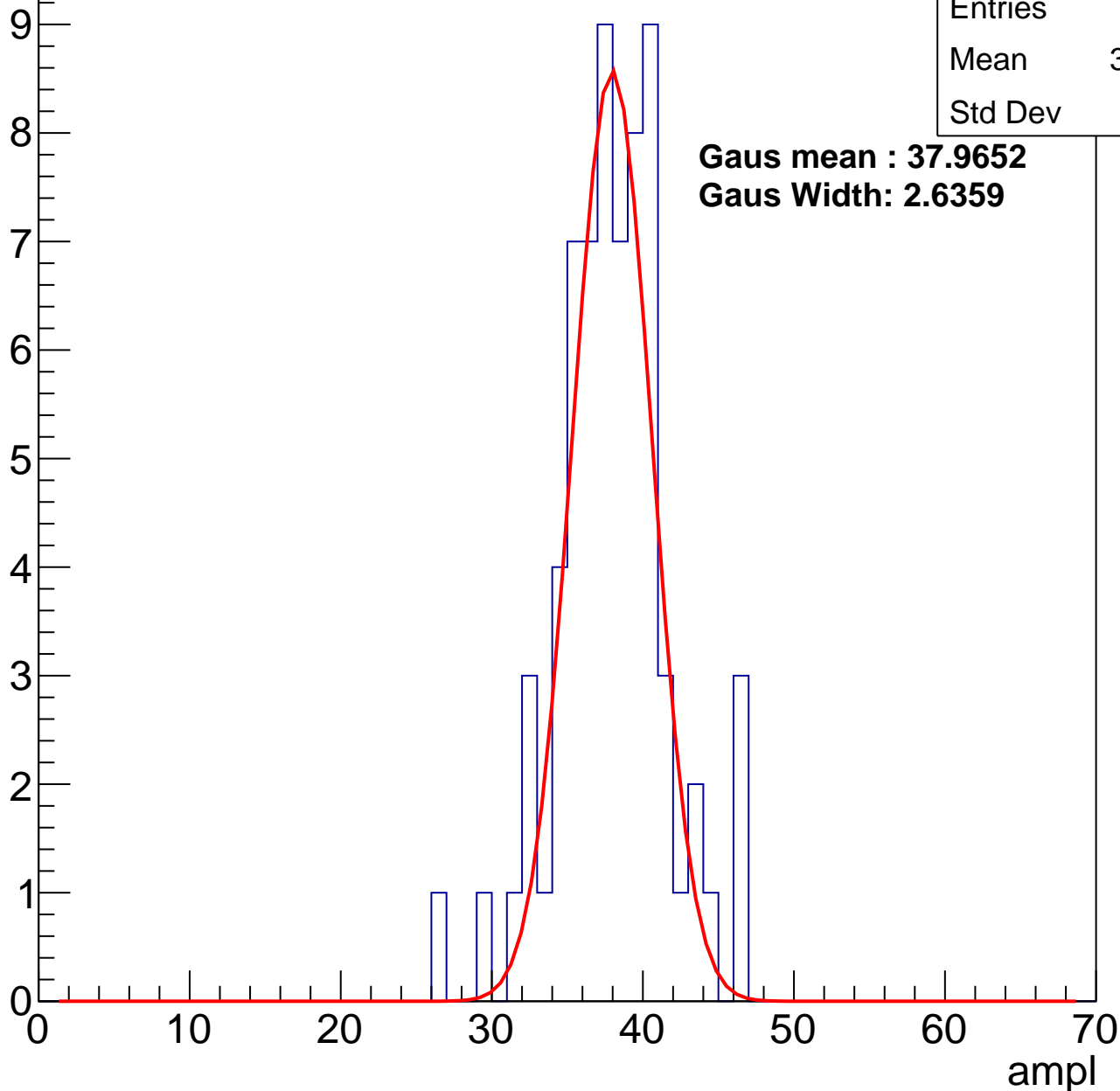
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	37.53
Std Dev	3.68

Gaus mean : 37.9652

Gaus Width: 2.6359



B0L002S, U4-ch74, adc2

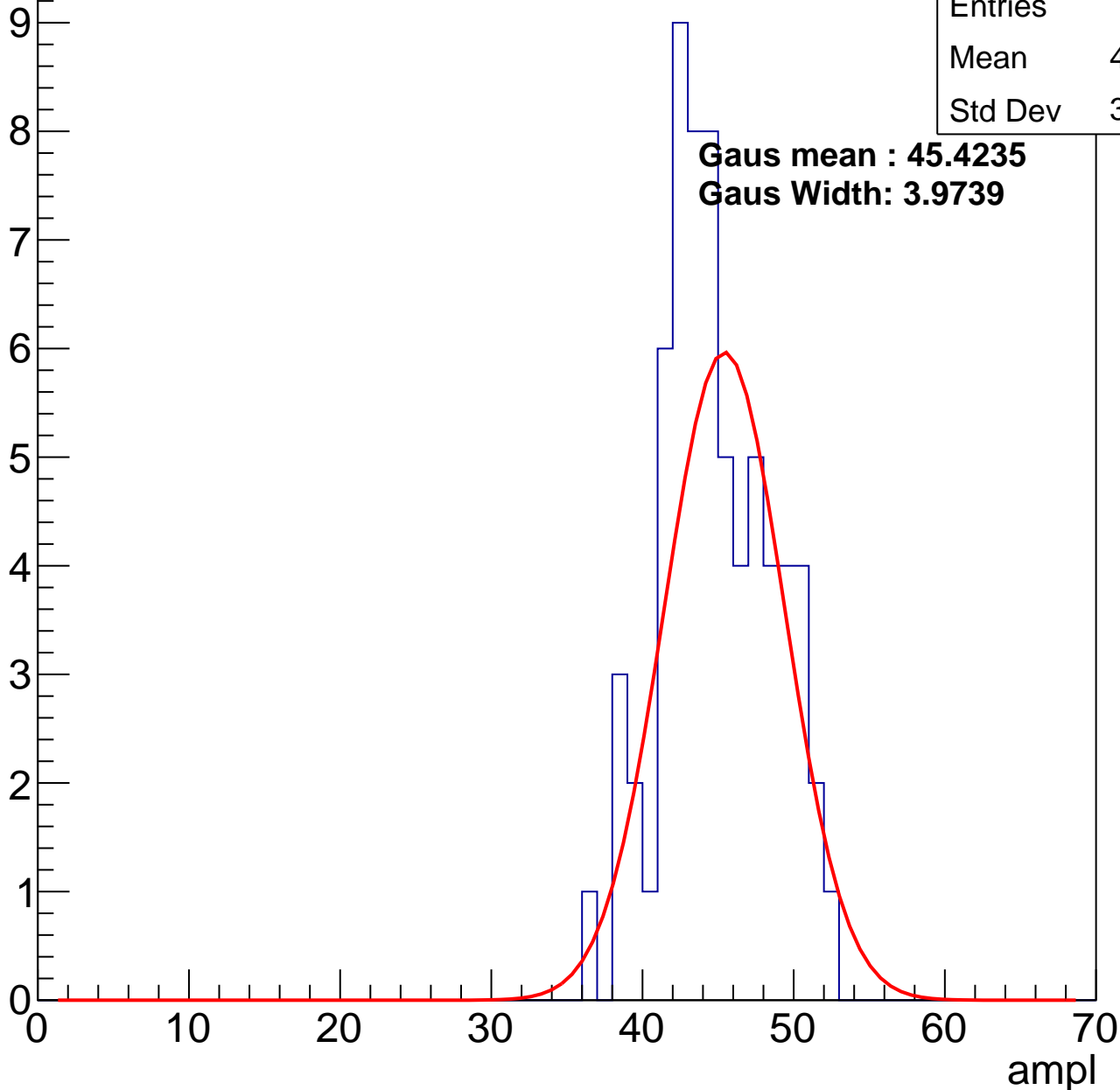
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.39
Std Dev	3.587

Gaus mean : 45.4235

Gaus Width: 3.9739

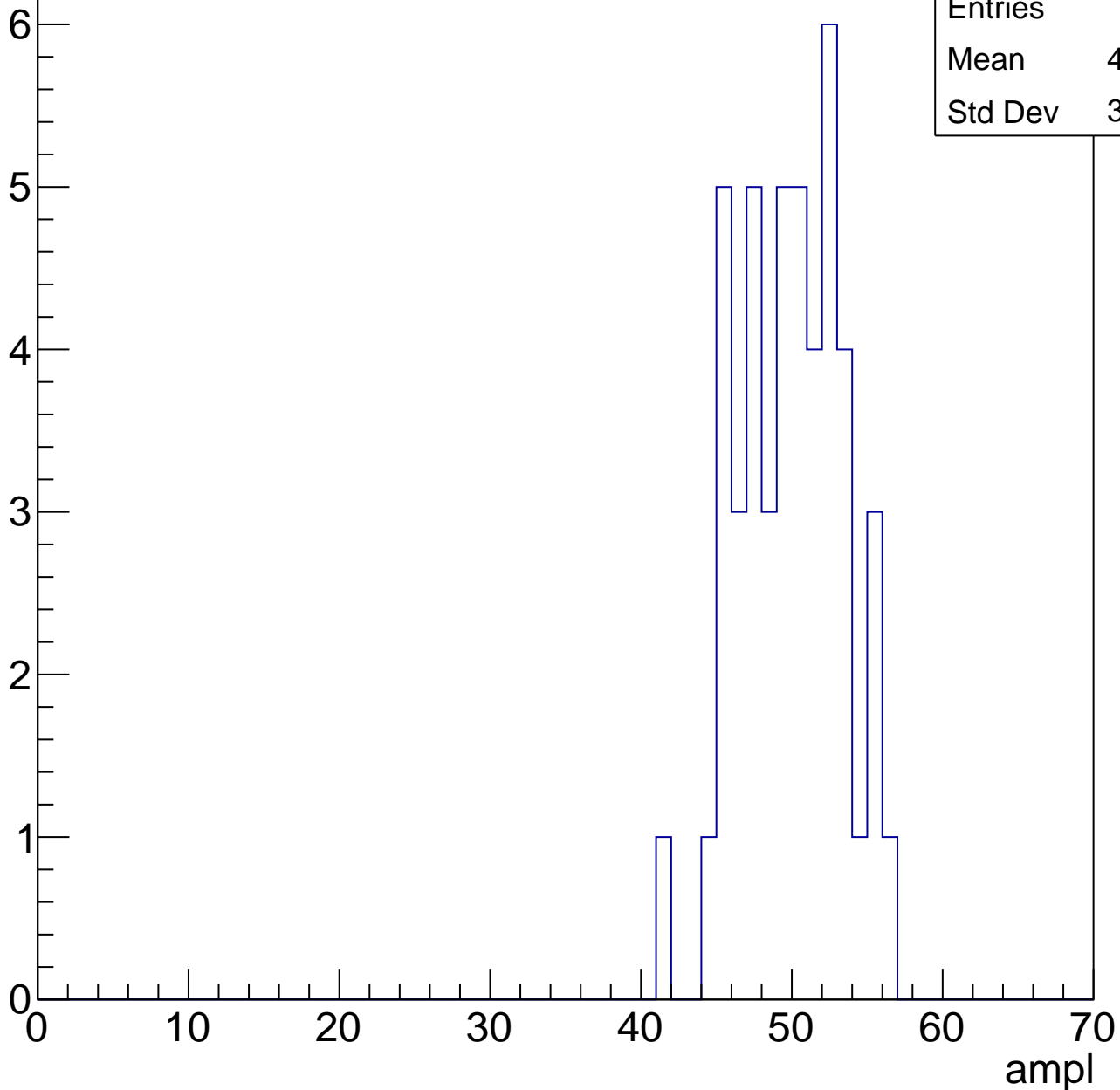


B0L002S, U4-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

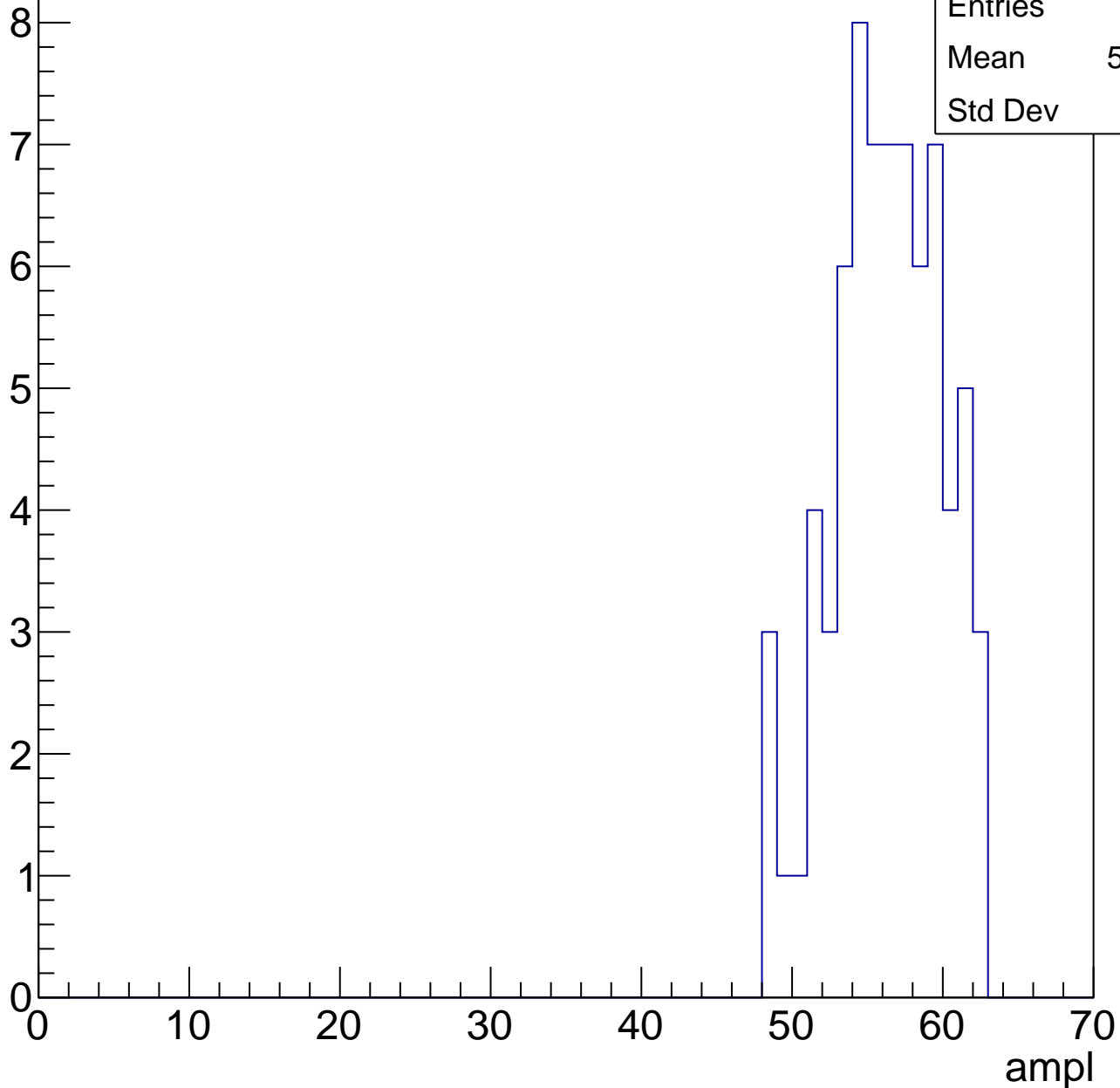
Entries	47
Mean	49.47
Std Dev	3.357



B0L002S, U4-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



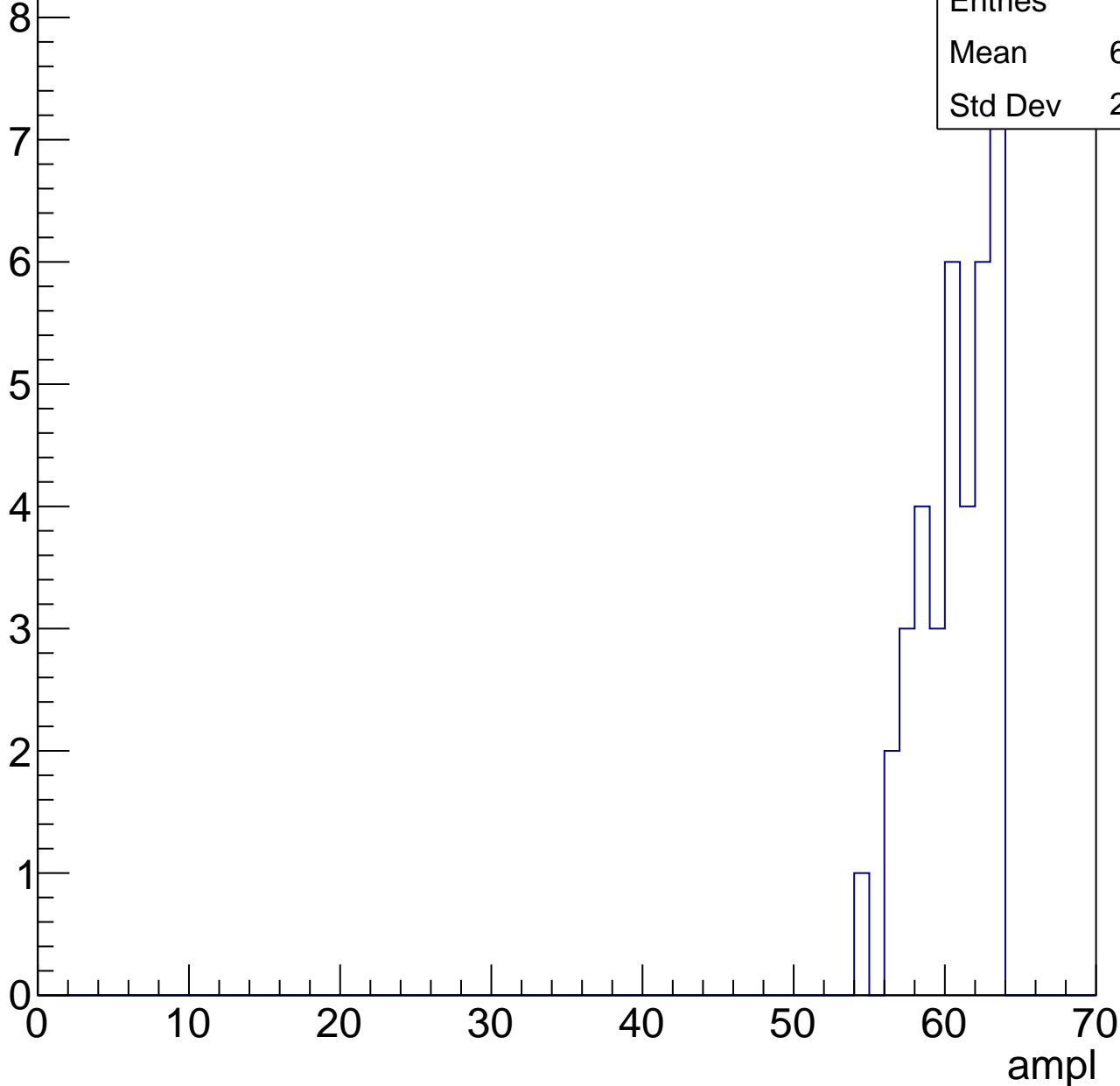
Entries	72
Mean	55.85
Std Dev	3.55

B0L002S, U4-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

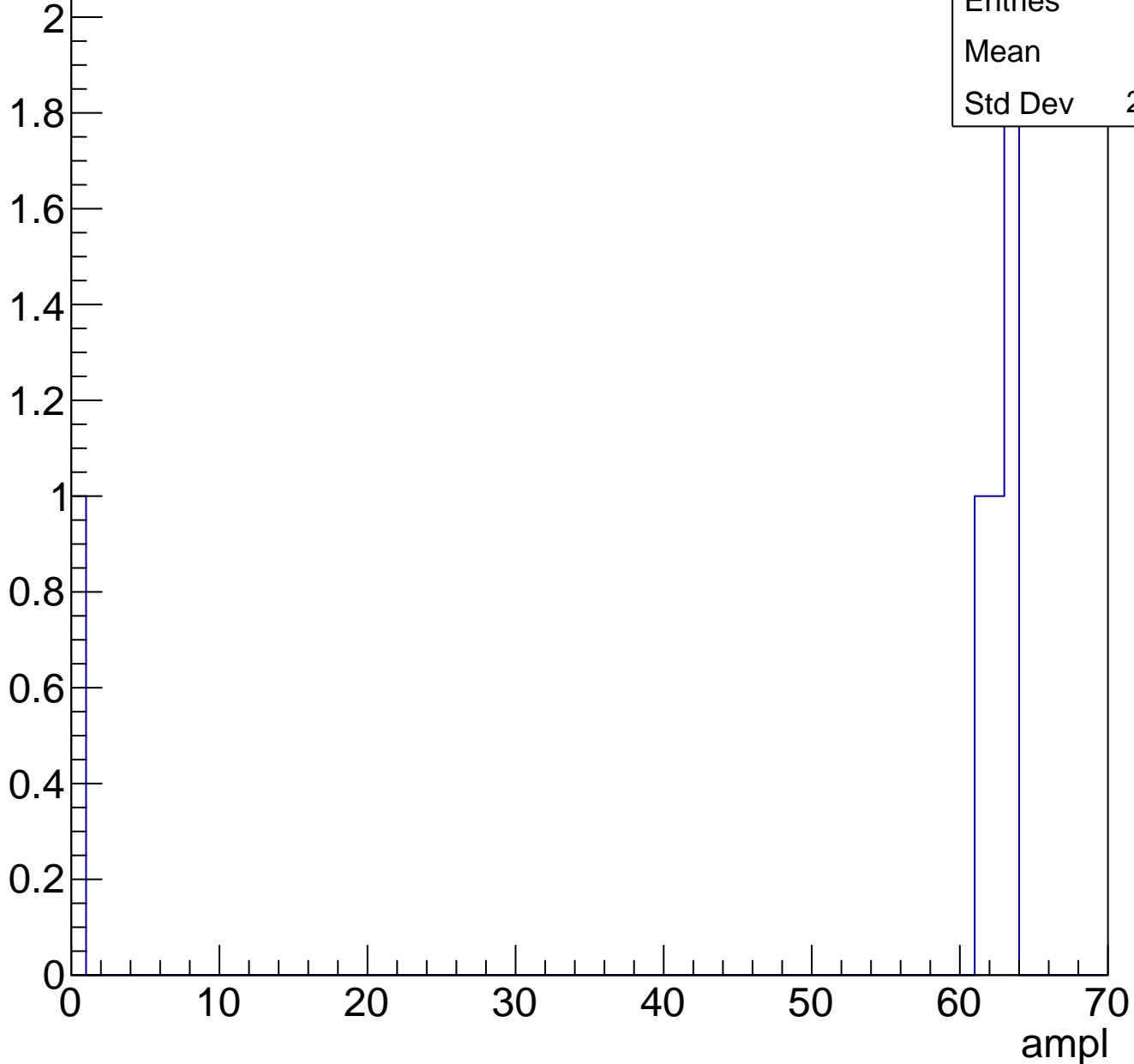
Entries	37
Mean	60.16
Std Dev	2.399



B0L002S, U4-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	49.8
Std Dev	24.91

B0L002S, U4-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch75, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	31.2
Std Dev	4.989

Gaus mean : 32.1132

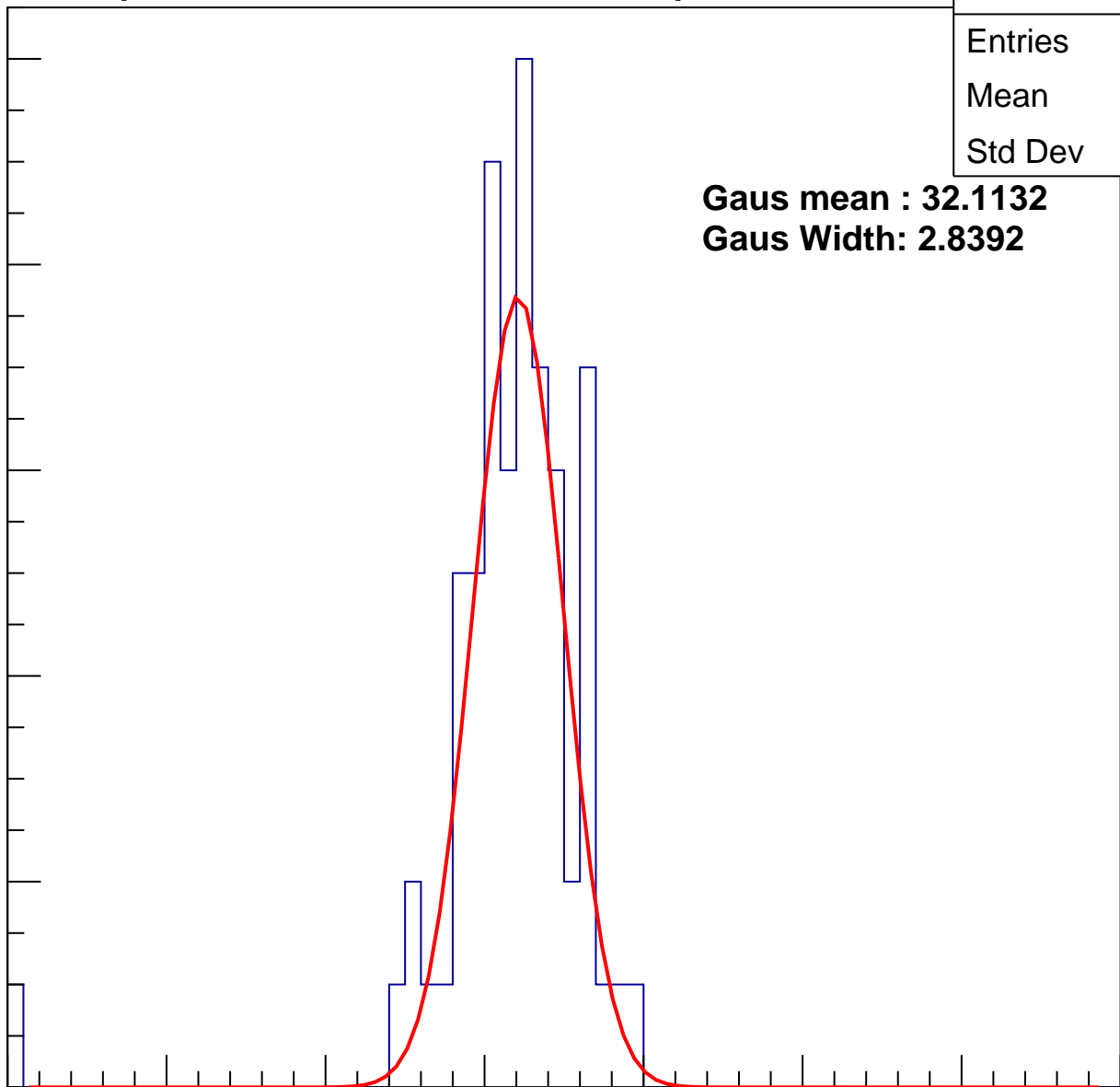
Gaus Width: 2.8392

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch75, adc1

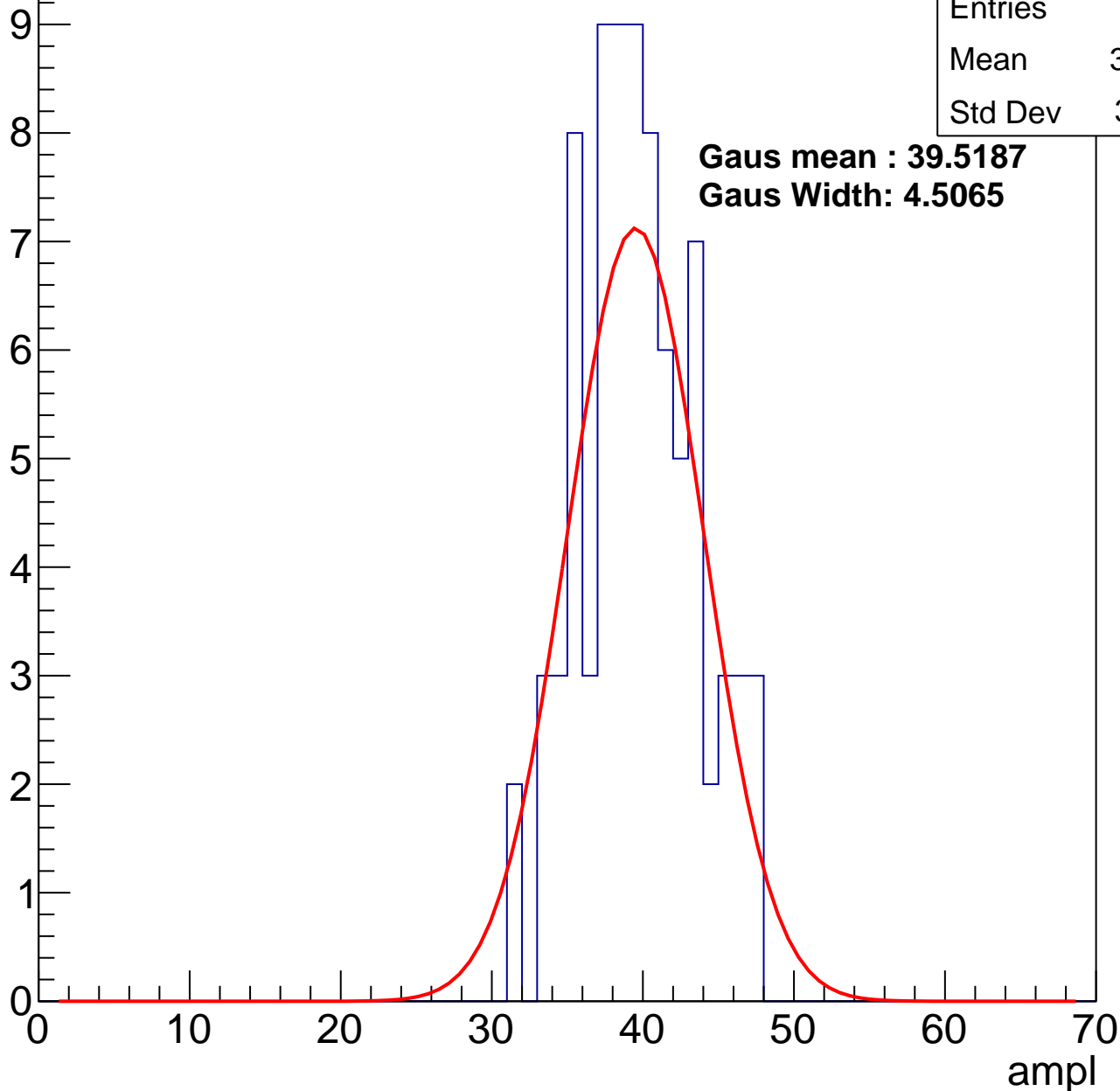
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	39.23
Std Dev	3.791

Gaus mean : 39.5187

Gaus Width: 4.5065



B0L002S, U4-ch75, adc2

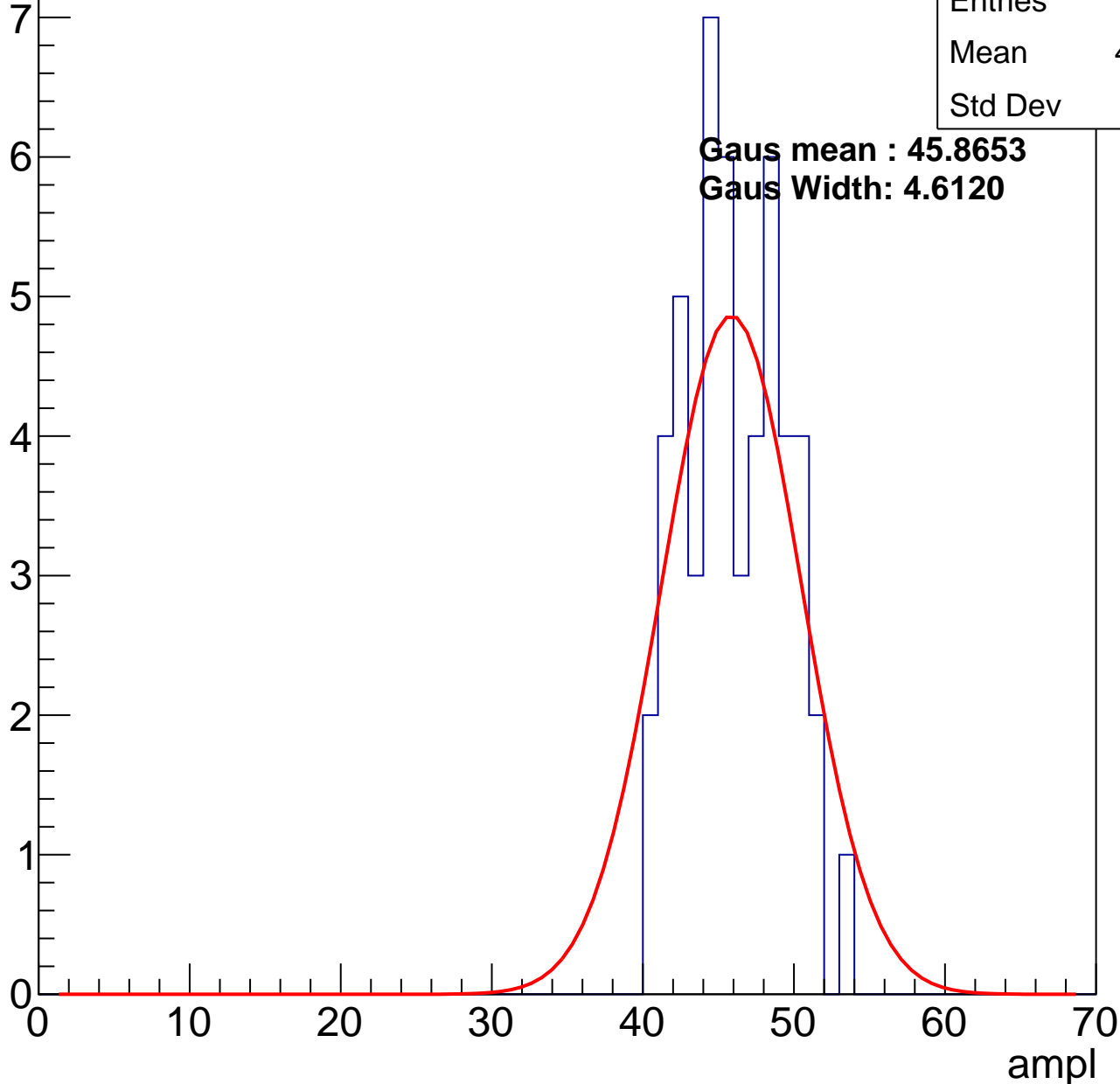
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	45.61
Std Dev	3.23

Gaus mean : 45.8653

Gaus Width: 4.6120

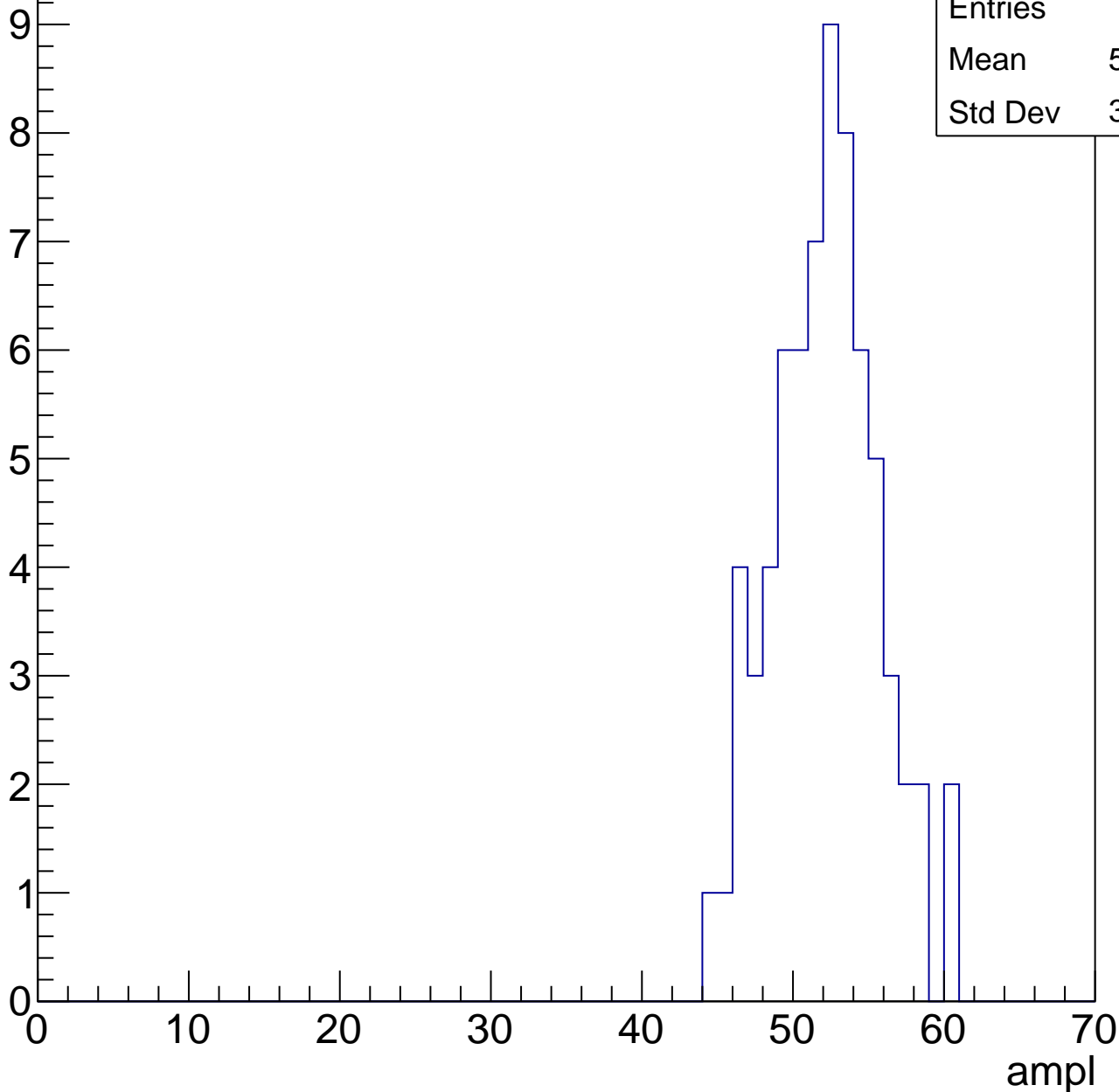


B0L002S, U4-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	51.68
Std Dev	3.508



B0L002S, U4-ch75, adc4

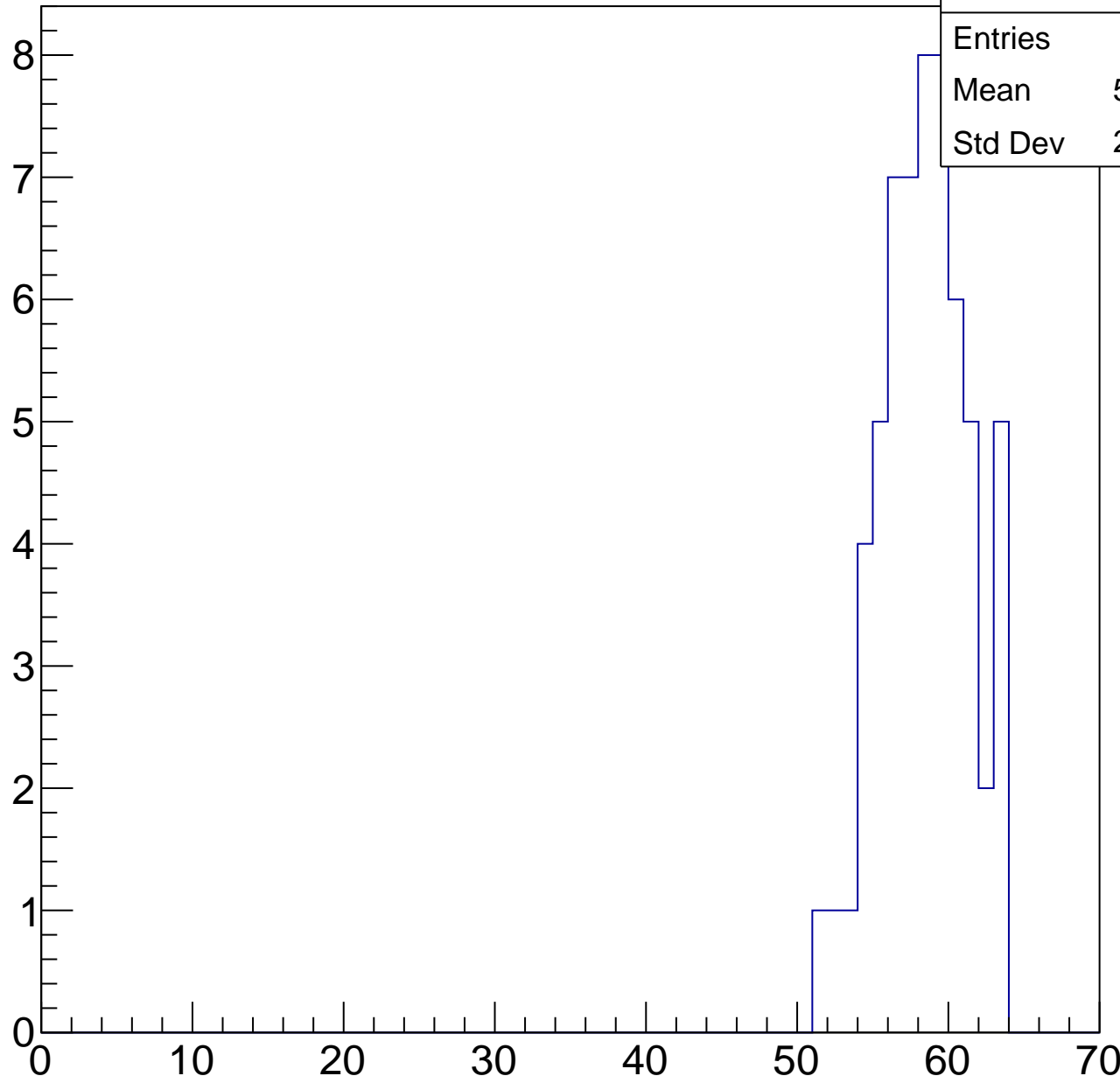
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	60
Mean	57.97
Std Dev	2.852

ampl

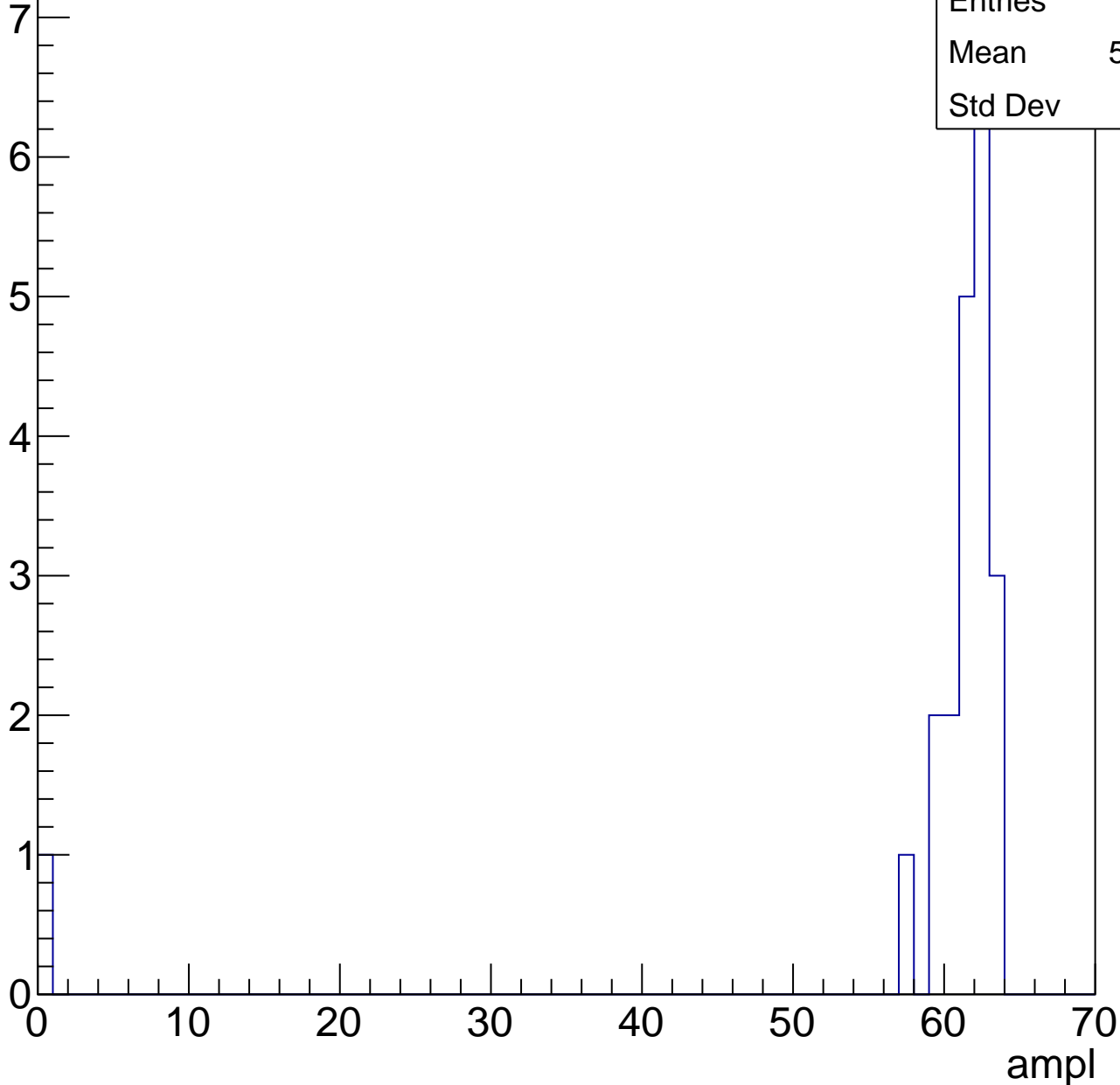


B0L002S, U4-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.24
Std Dev	13.1



B0L002S, U4-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U4-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch76, adc0

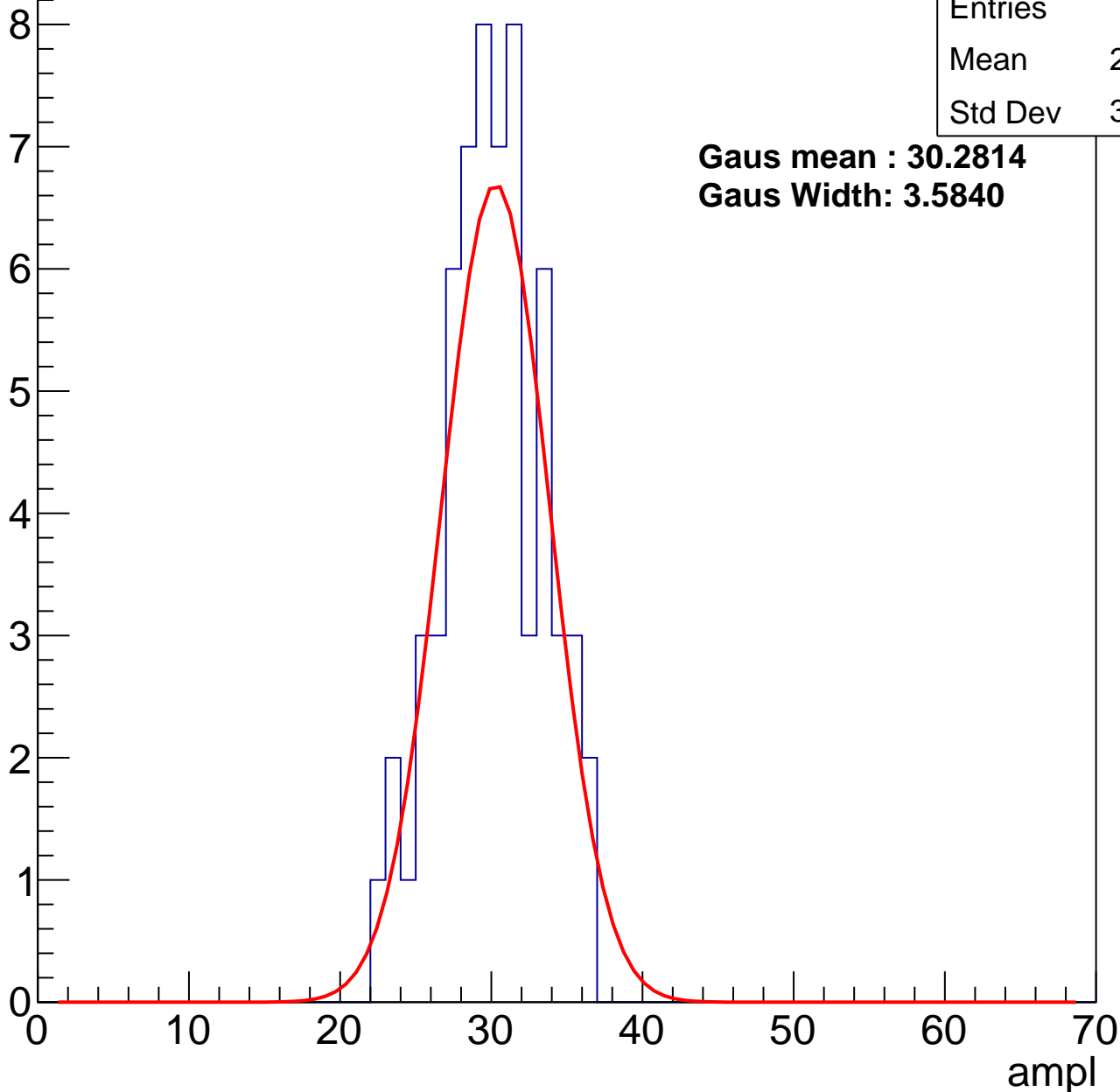
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.62
Std Dev	3.263

Gaus mean : 30.2814

Gaus Width: 3.5840



B0L002S, U4-ch76, adc1

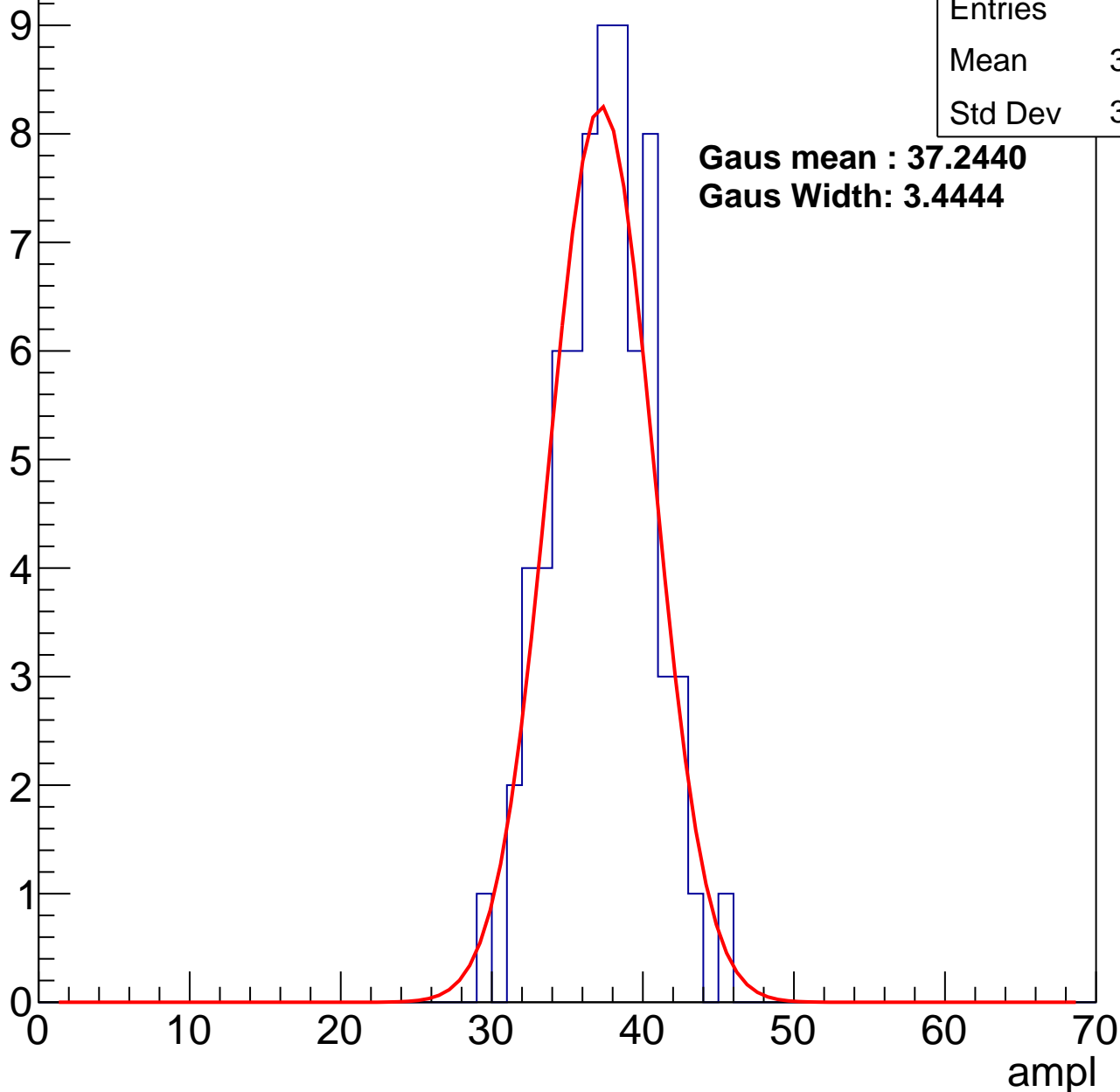
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.89
Std Dev	3.169

Gaus mean : 37.2440

Gaus Width: 3.4444

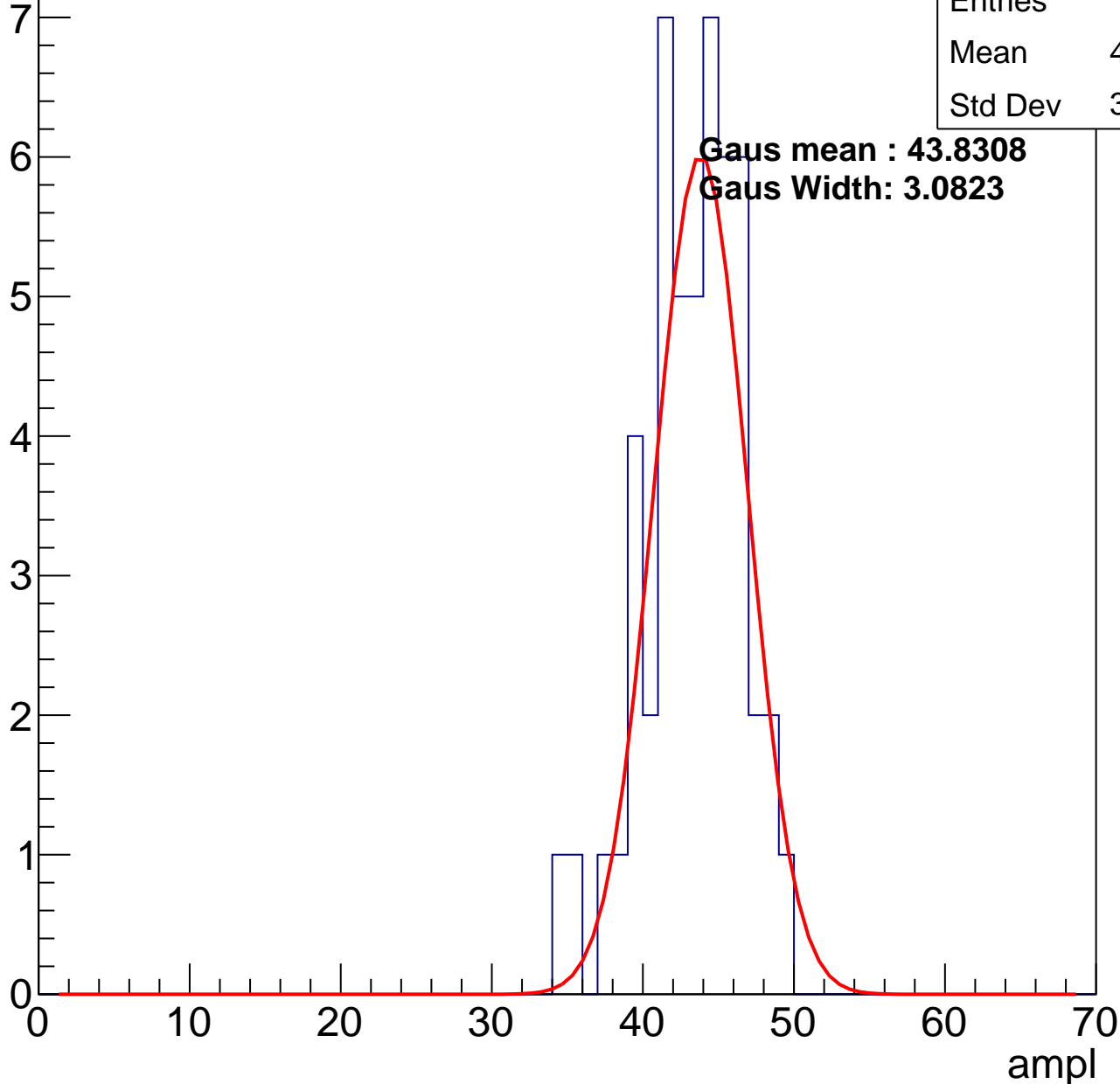


B0L002S, U4-ch76, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	42.84
Std Dev	3.202



B0L002S, U4-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

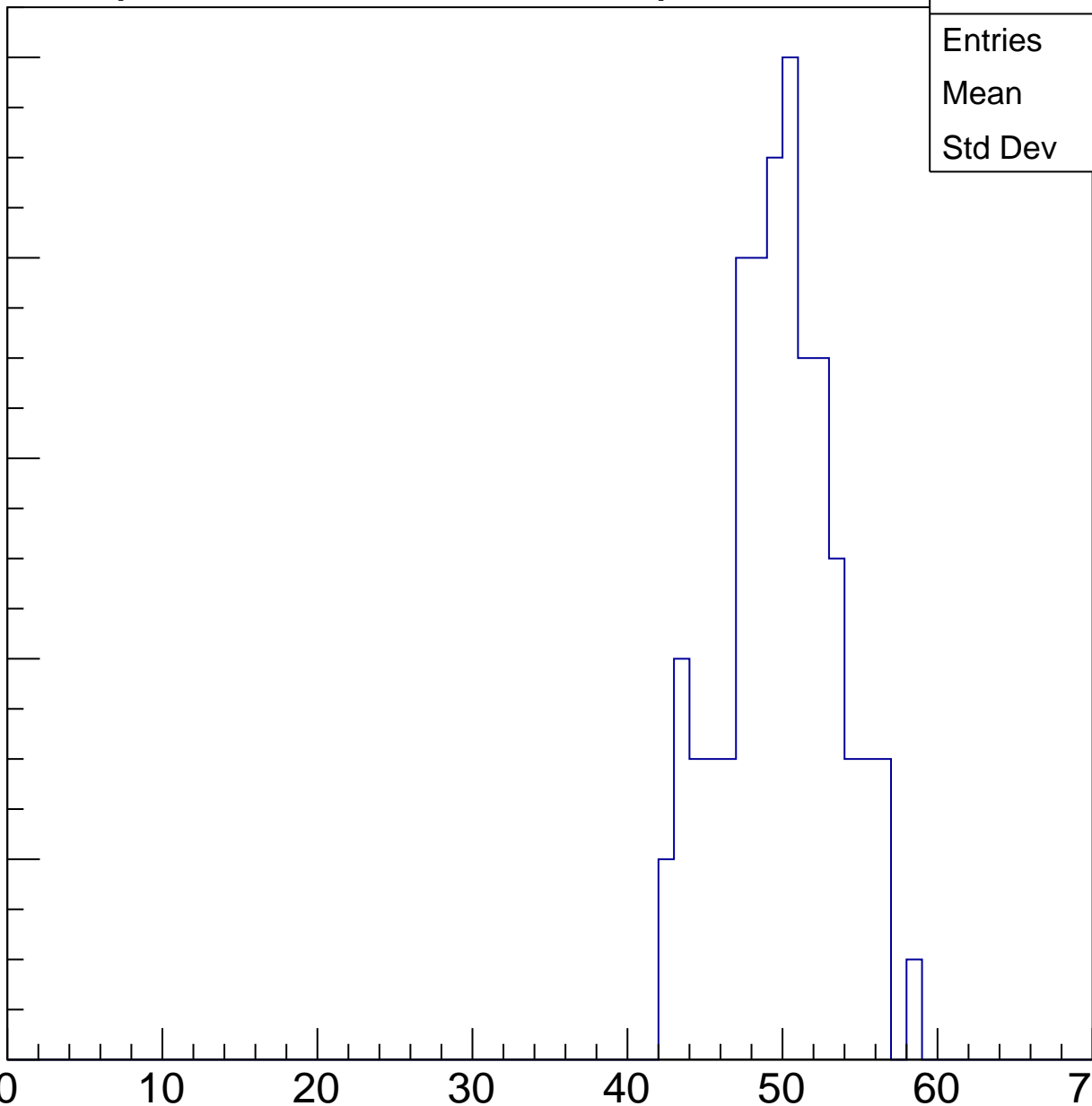
Entries	79
Mean	49.38
Std Dev	3.58

Entry

10
8
6
4
2
0

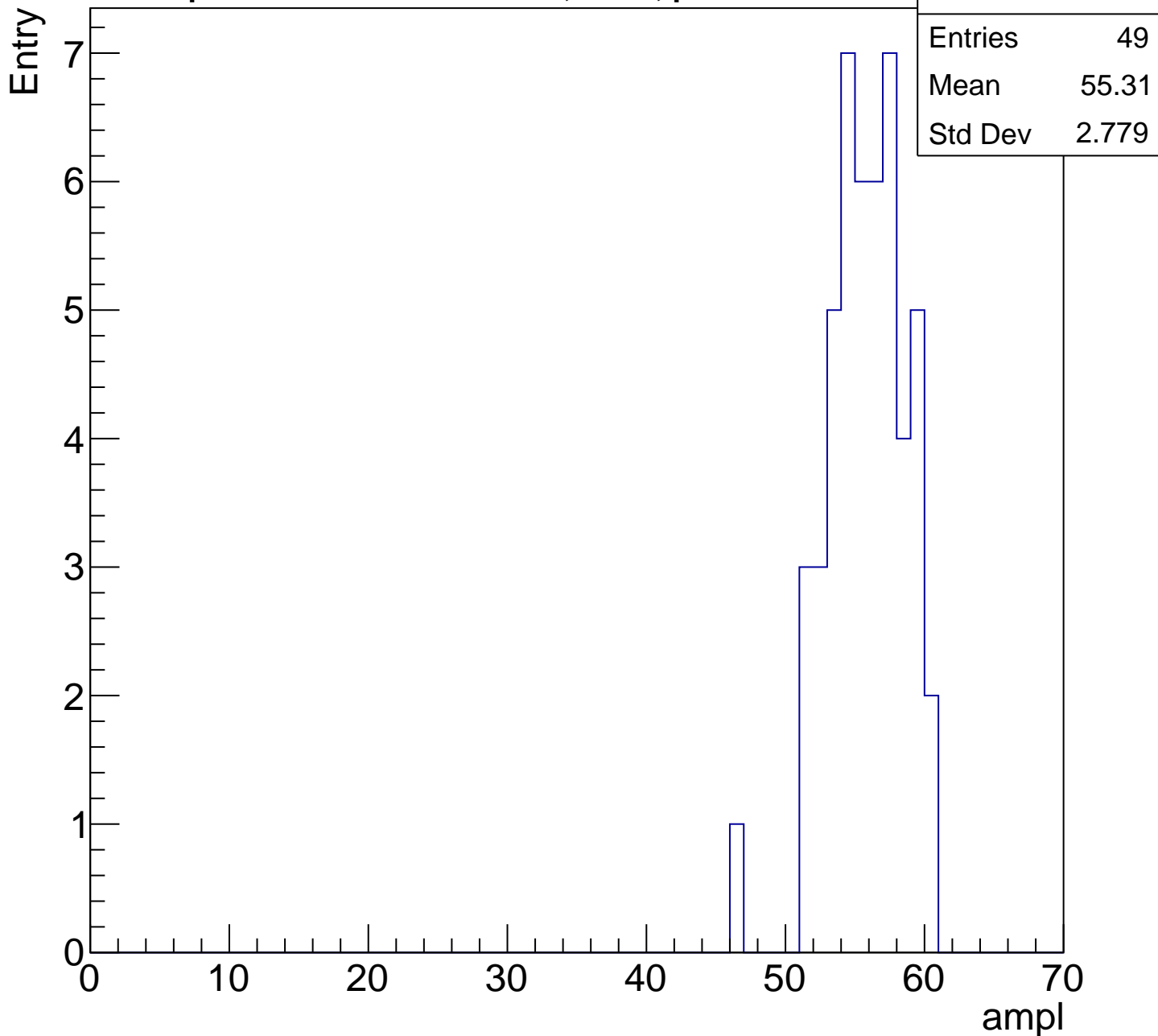
0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch76, adc4

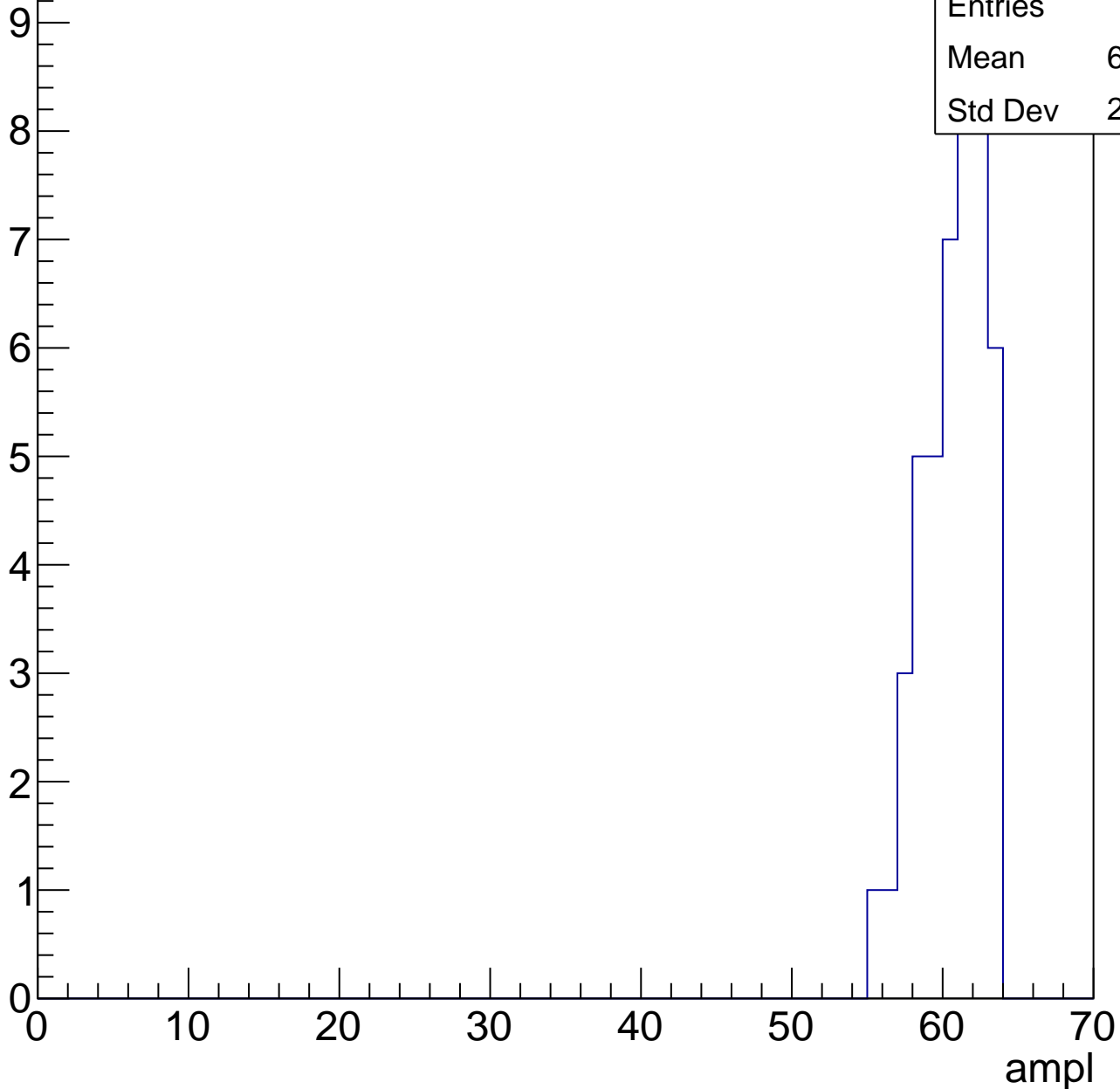
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

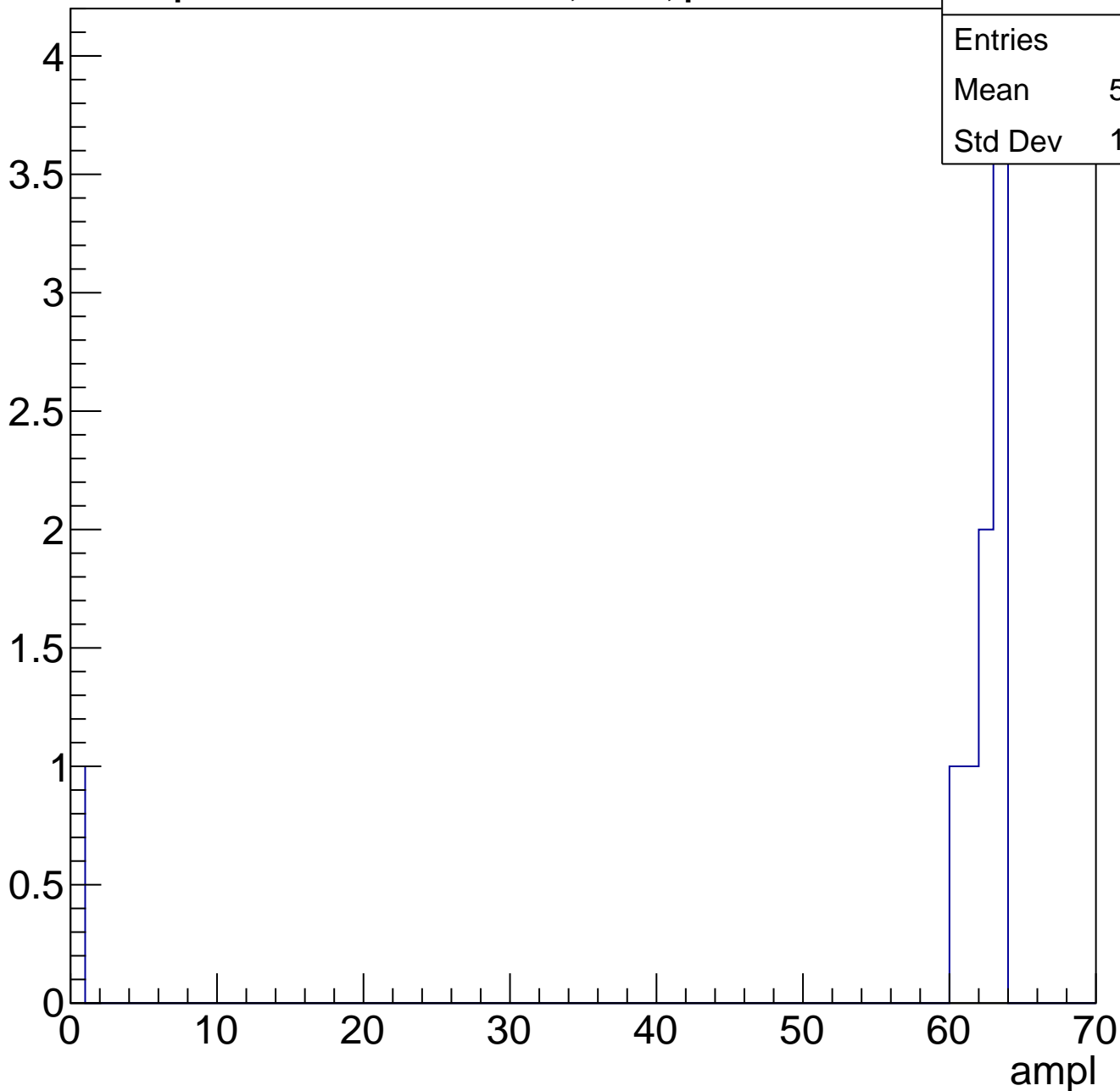
Entry



B0L002S, U4-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch77, adc0

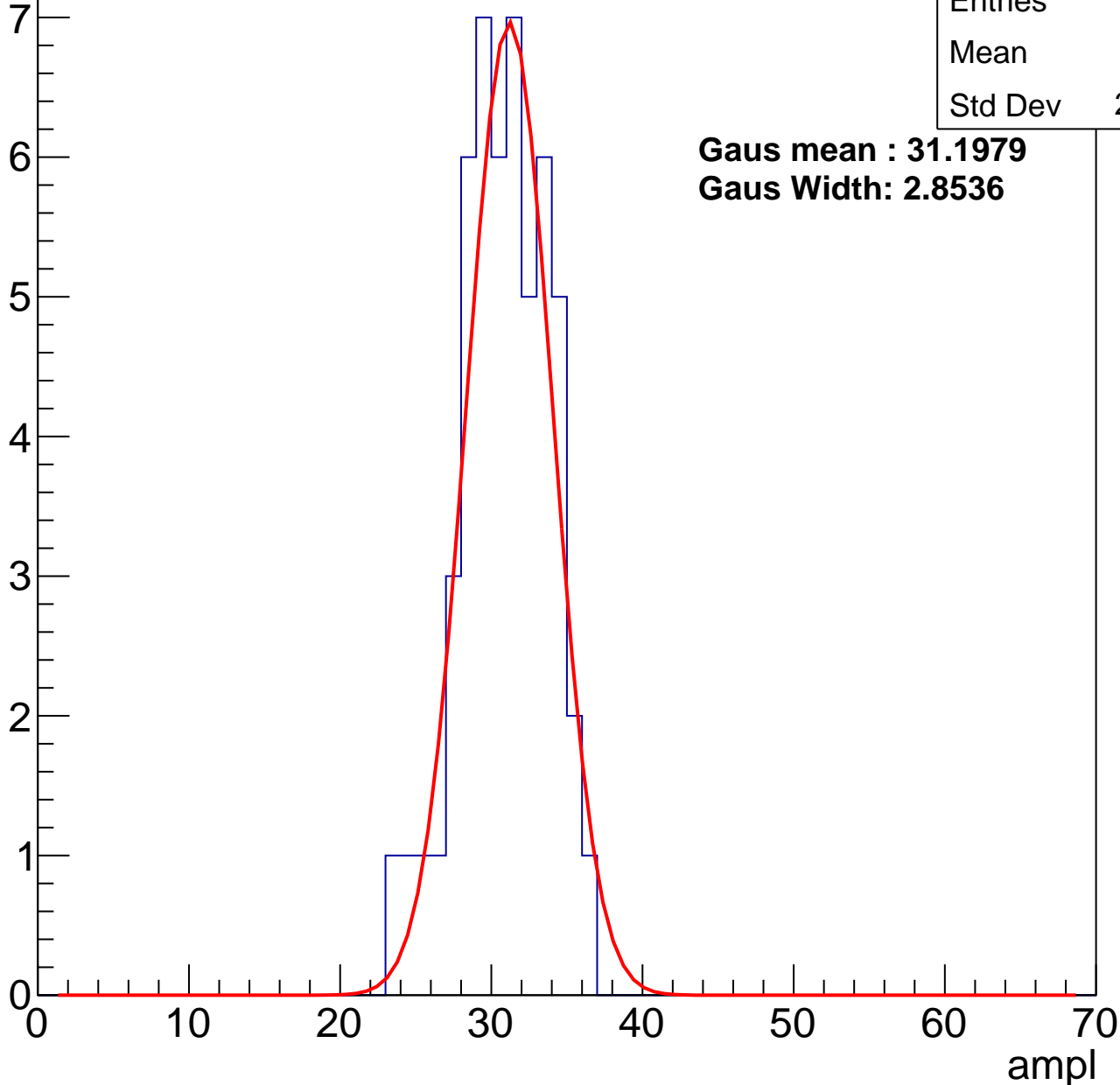
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.4
Std Dev	2.851

Gaus mean : 31.1979

Gaus Width: 2.8536



B0L002S, U4-ch77, adc1

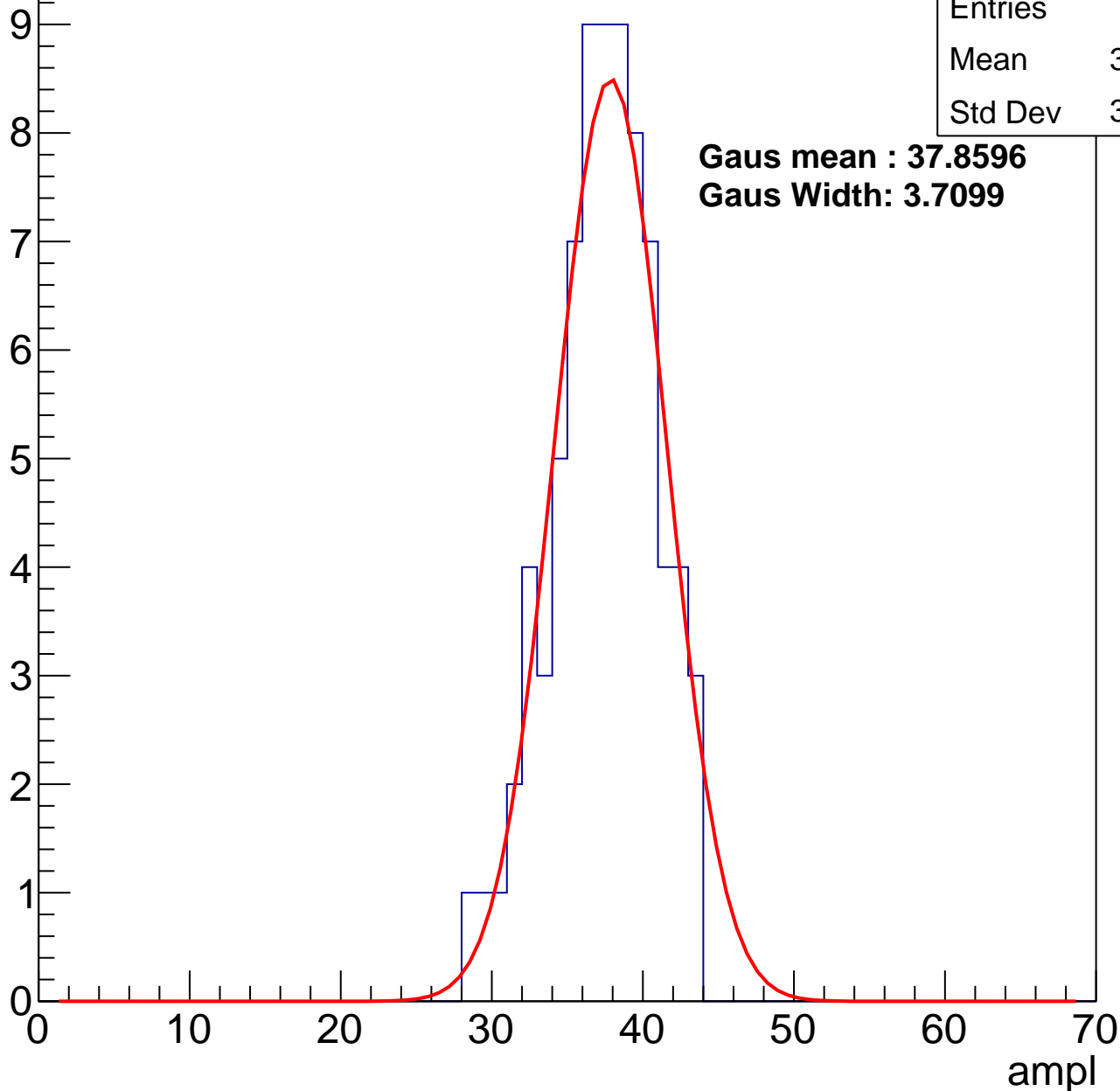
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.92
Std Dev	3.364

Gaus mean : 37.8596

Gaus Width: 3.7099



B0L002S, U4-ch77, adc2

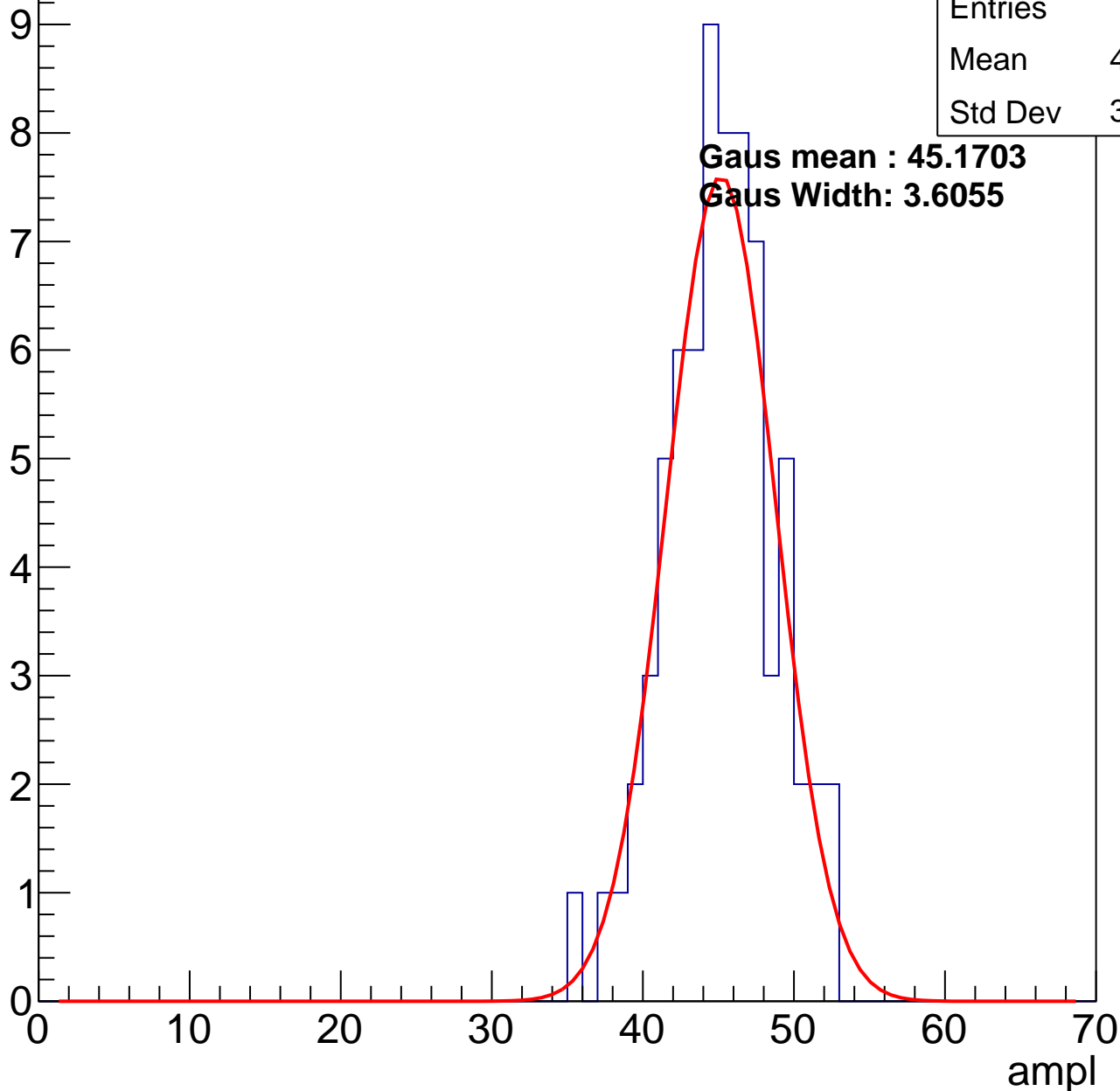
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	44.66
Std Dev	3.524

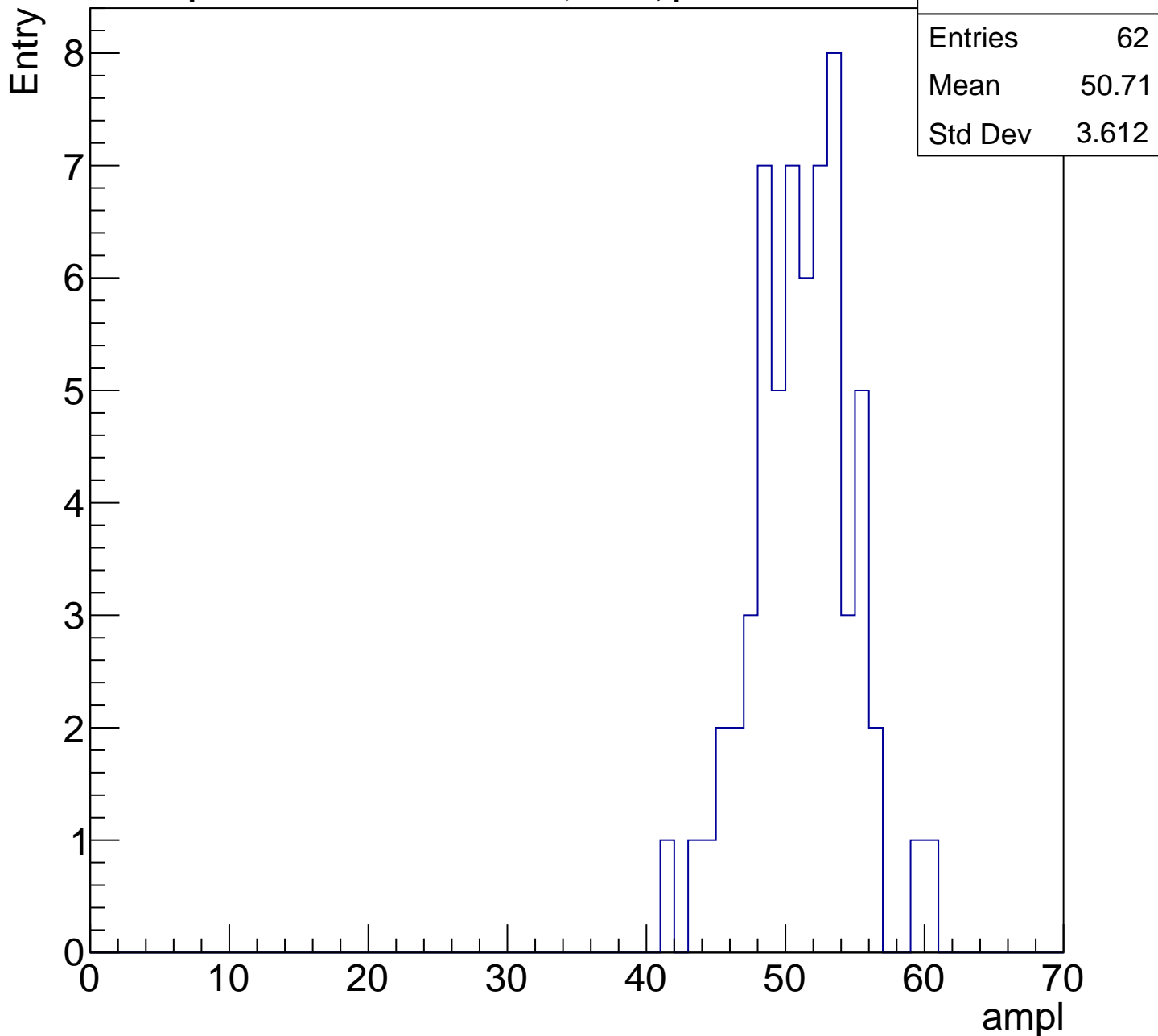
Gaus mean : 45.1703

Gaus Width: 3.6055



B0L002S, U4-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

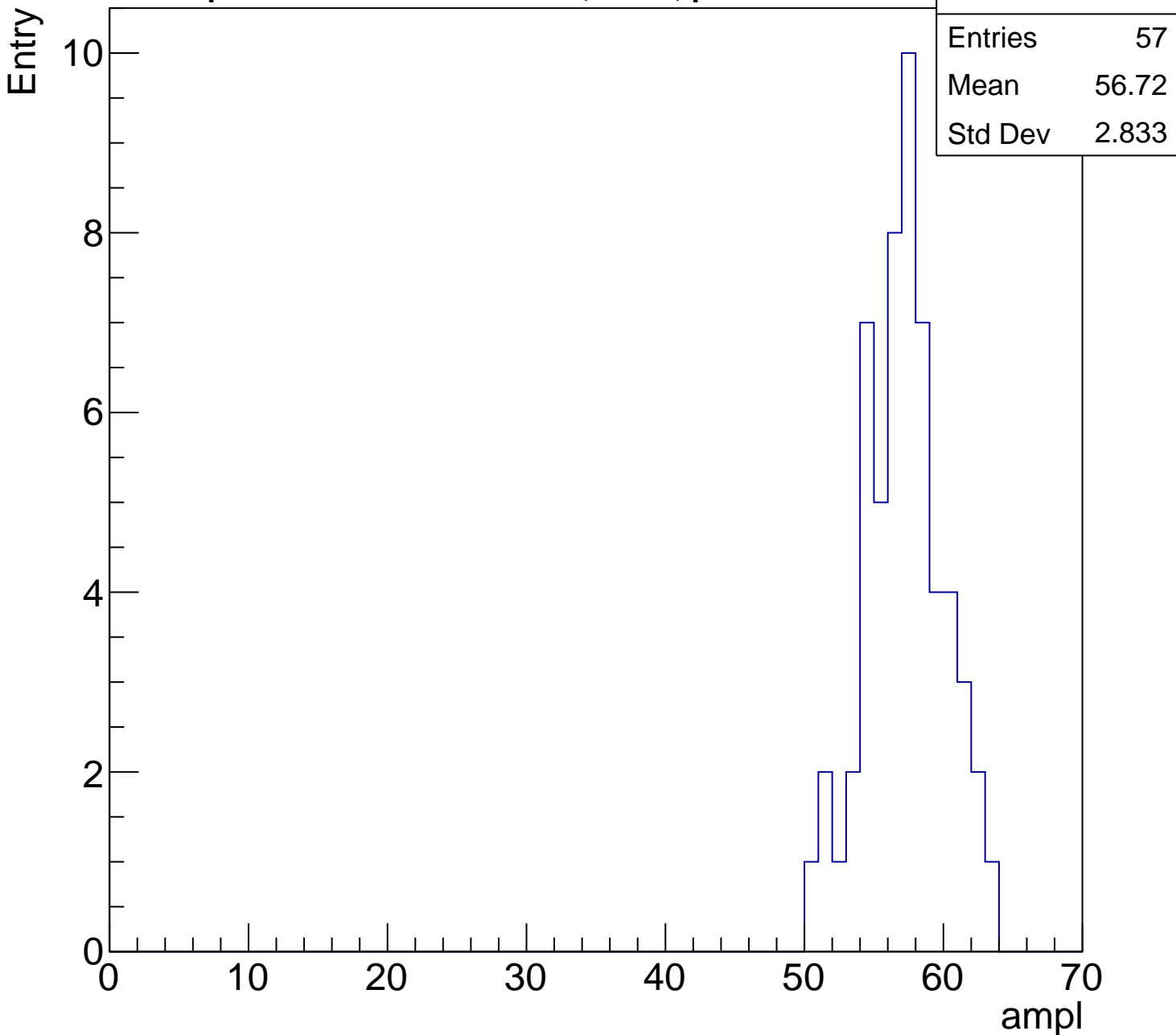
Entries	57
Mean	56.72
Std Dev	2.833

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

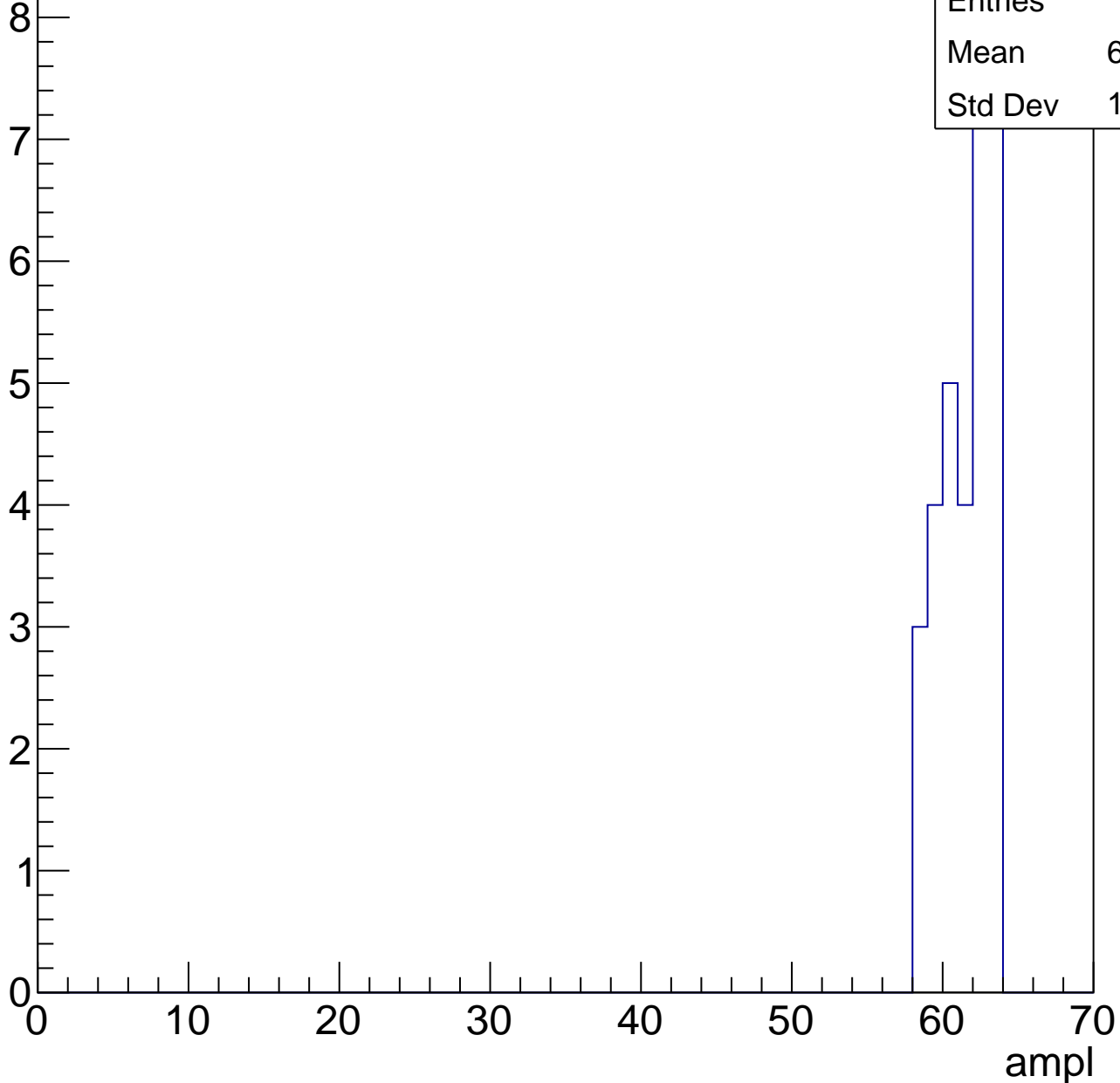


B0L002S, U4-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

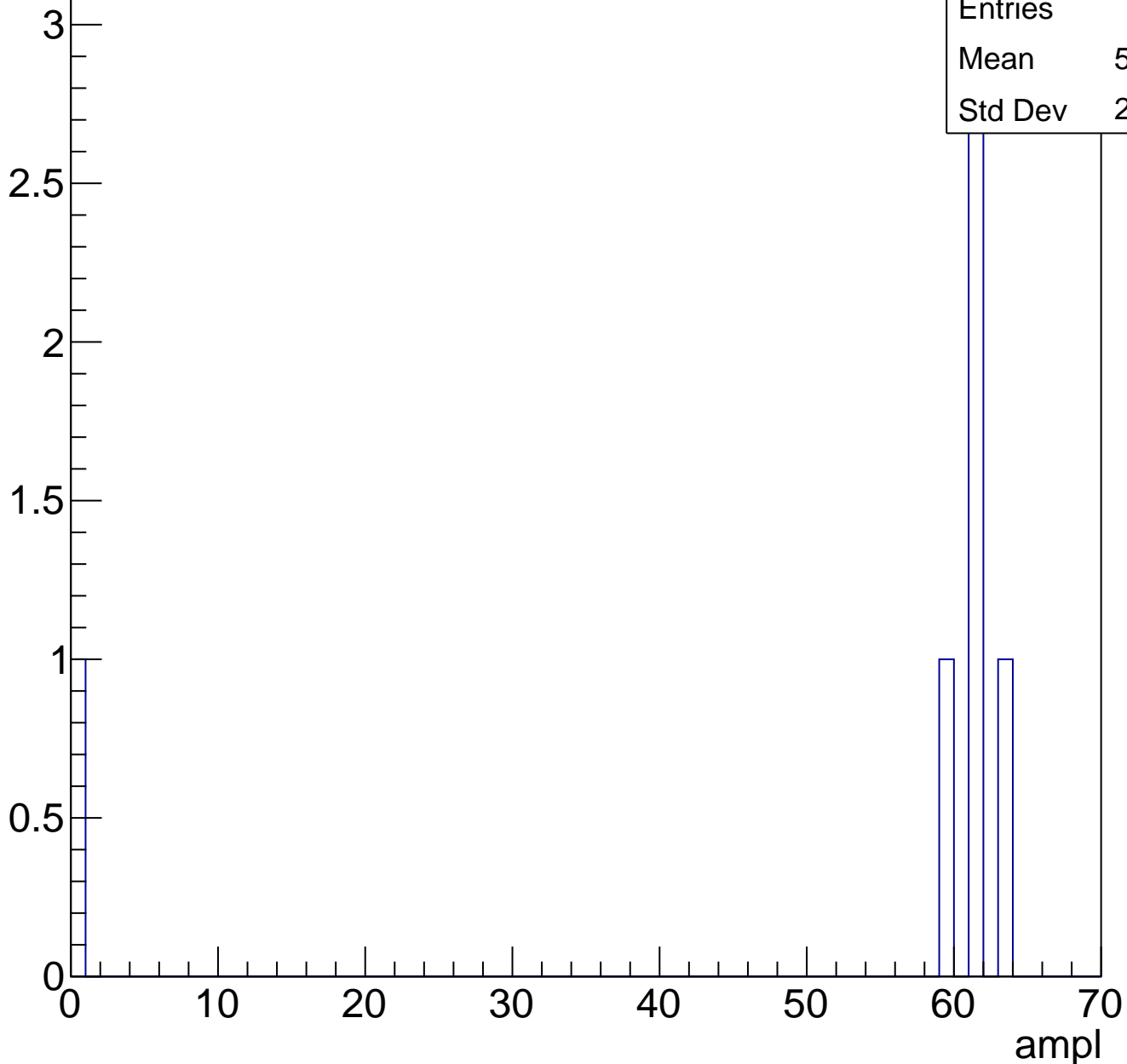
Entries	32
Mean	61.06
Std Dev	1.657



B0L002S, U4-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch78, adc0

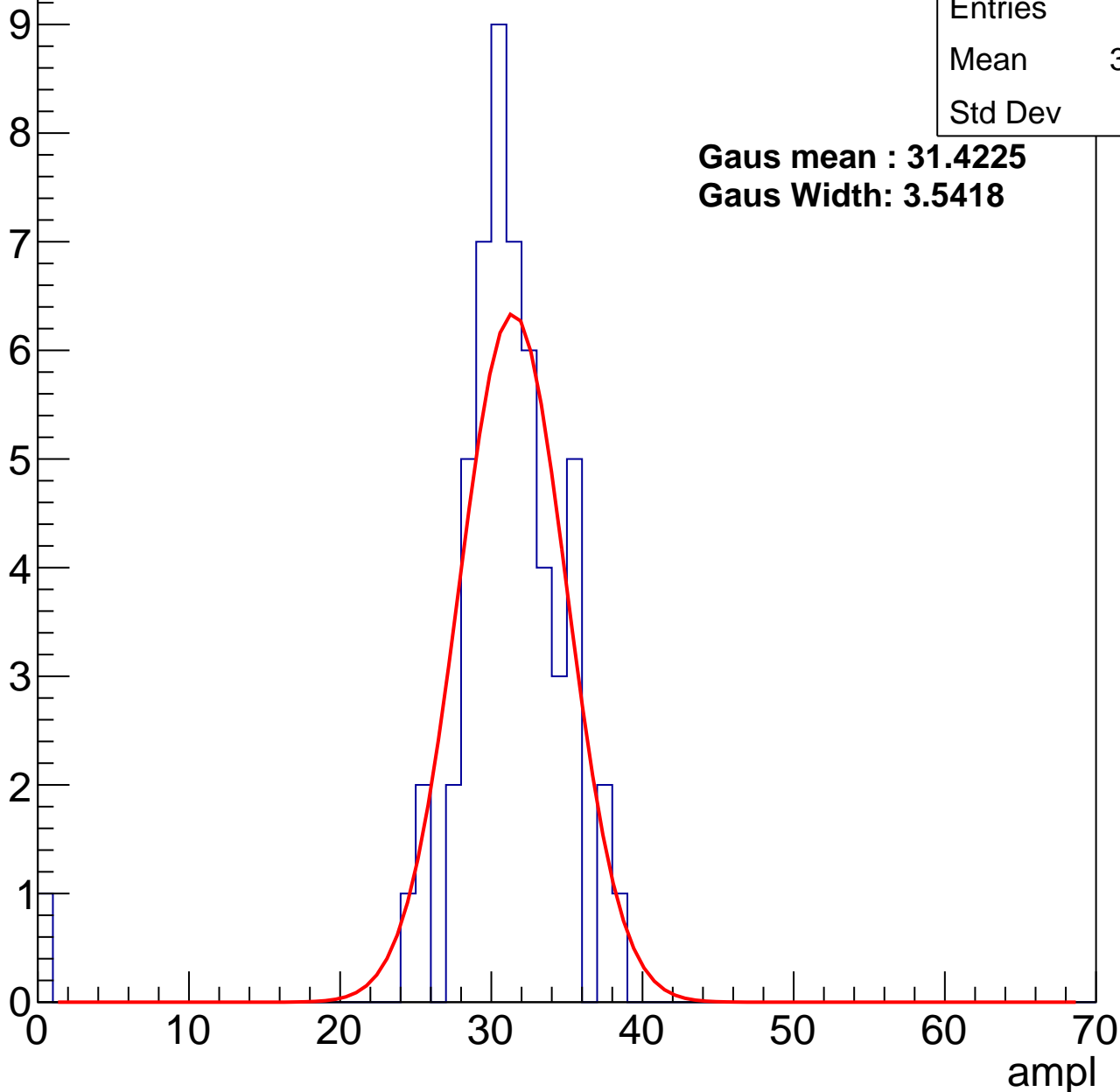
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	30.38
Std Dev	5.09

Gaus mean : 31.4225

Gaus Width: 3.5418



B0L002S, U4-ch78, adc1

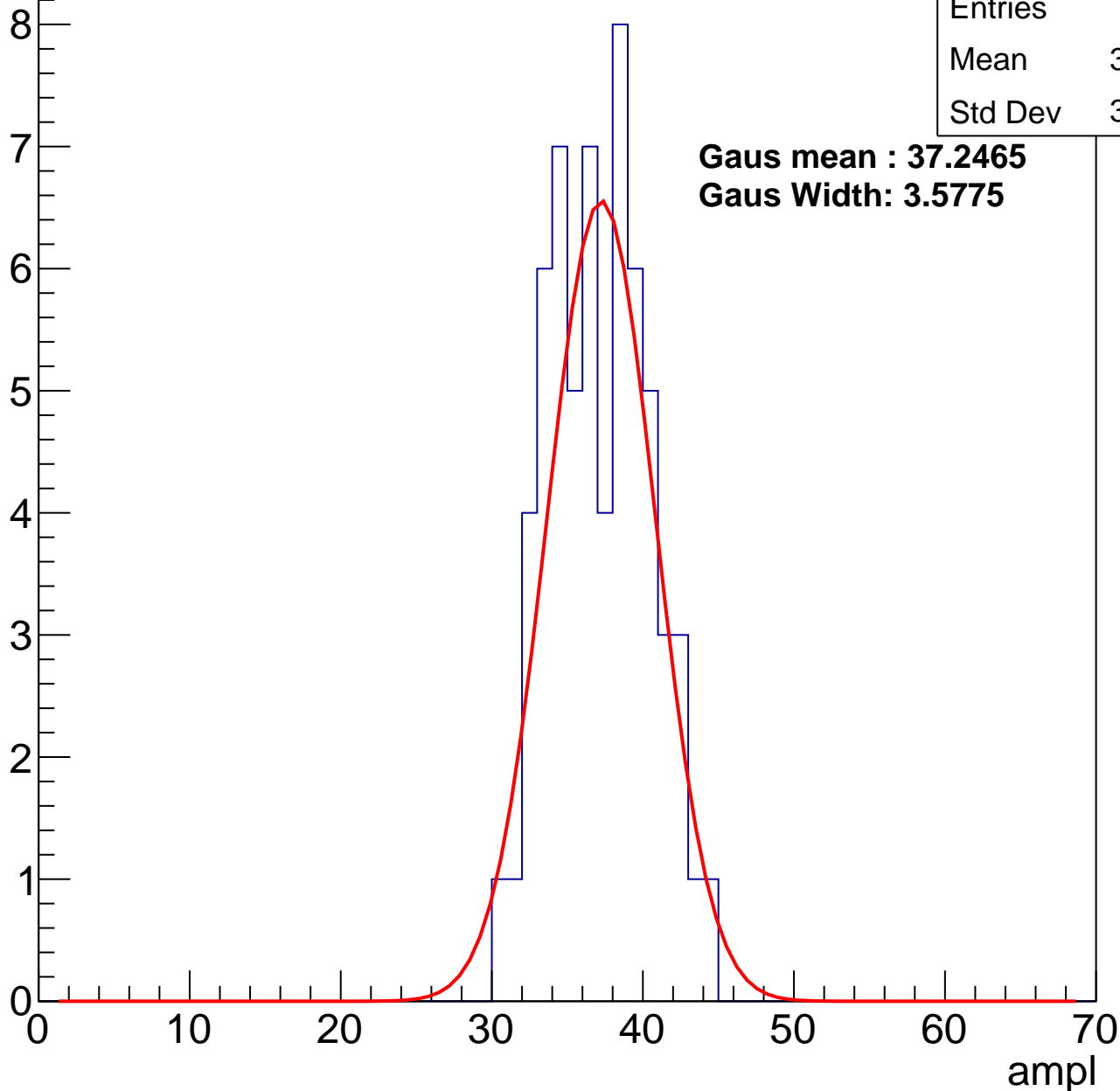
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.68
Std Dev	3.237

Gaus mean : 37.2465

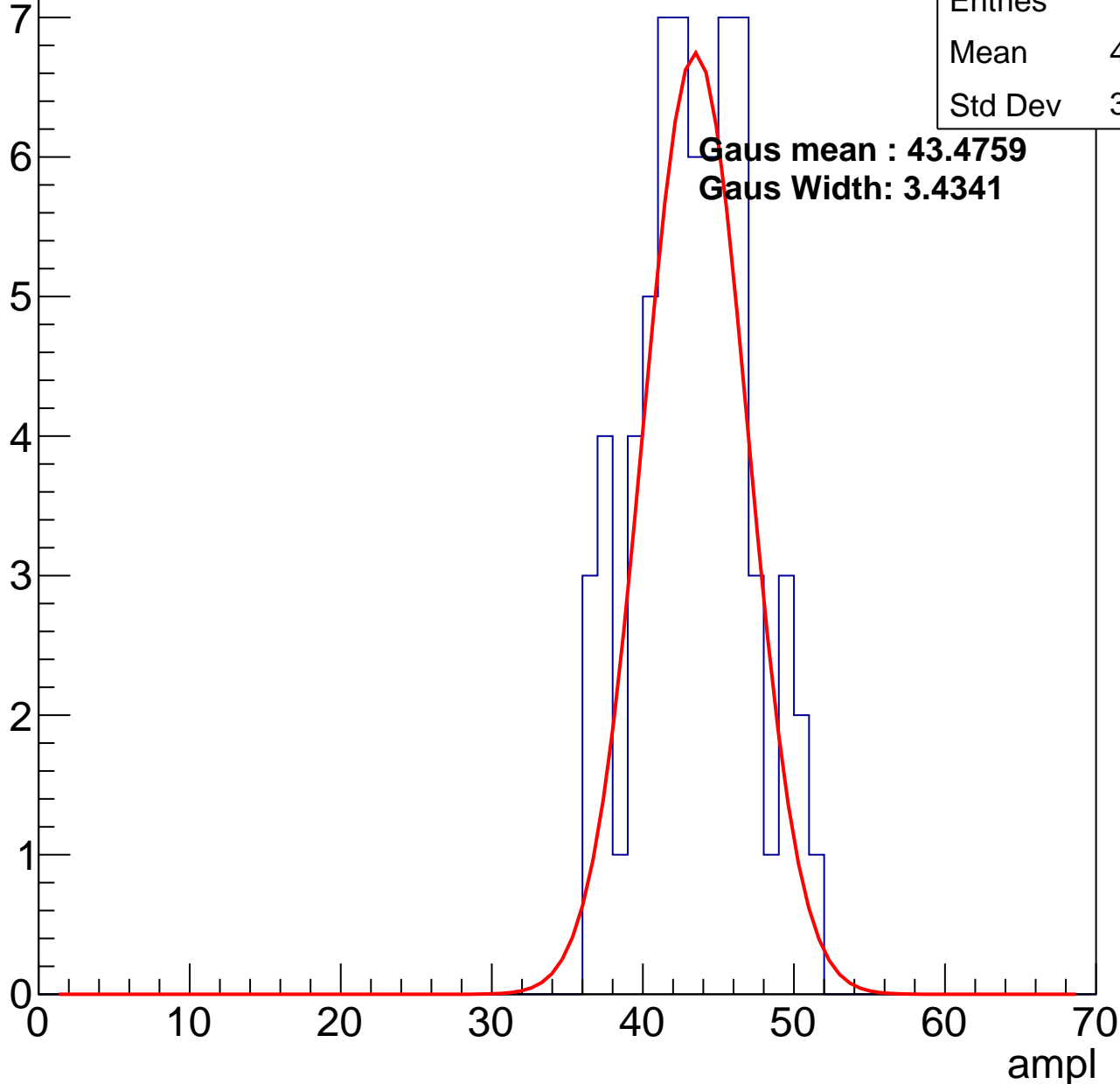
Gaus Width: 3.5775



B0L002S, U4-ch78, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

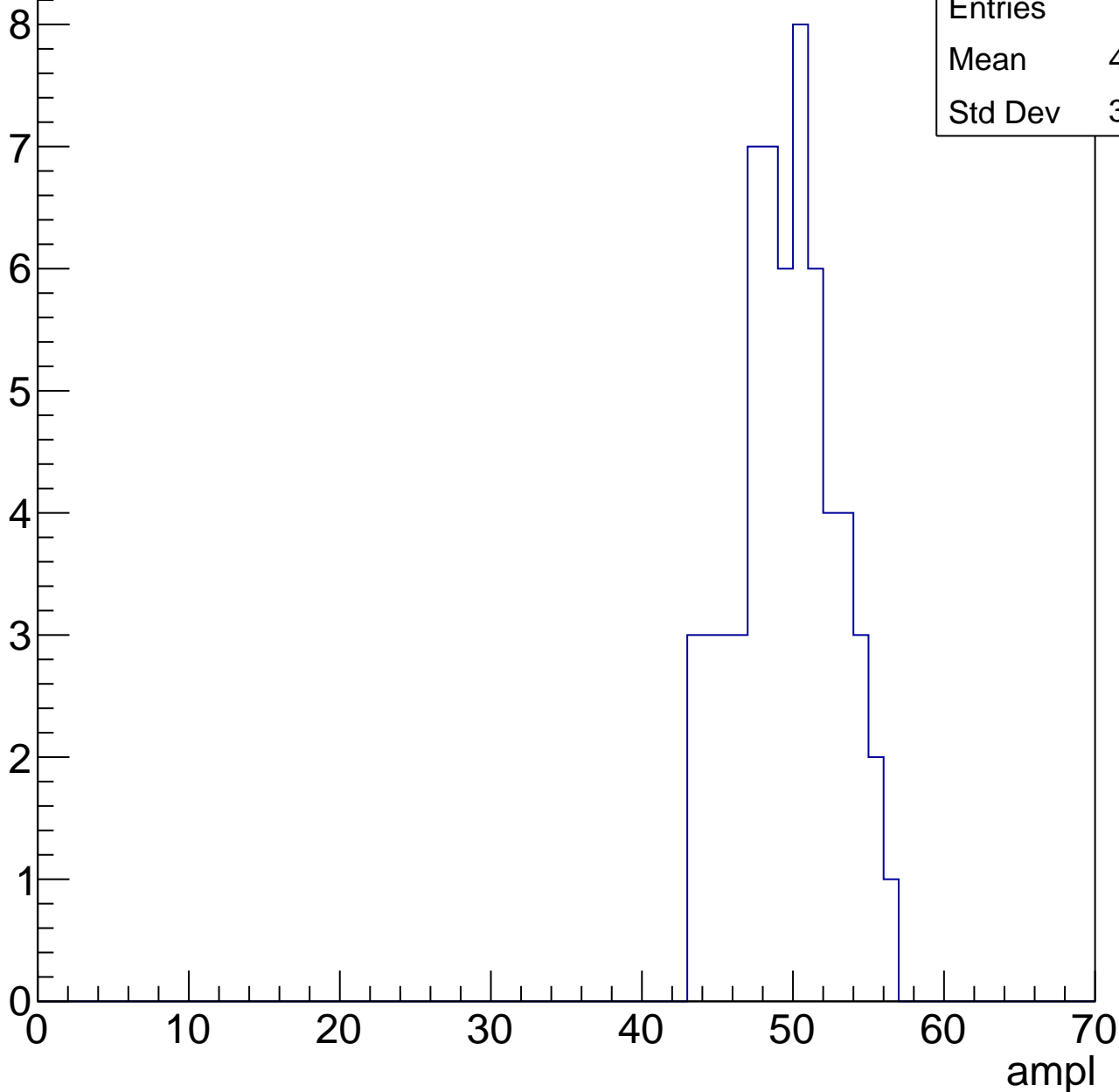


B0L002S, U4-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

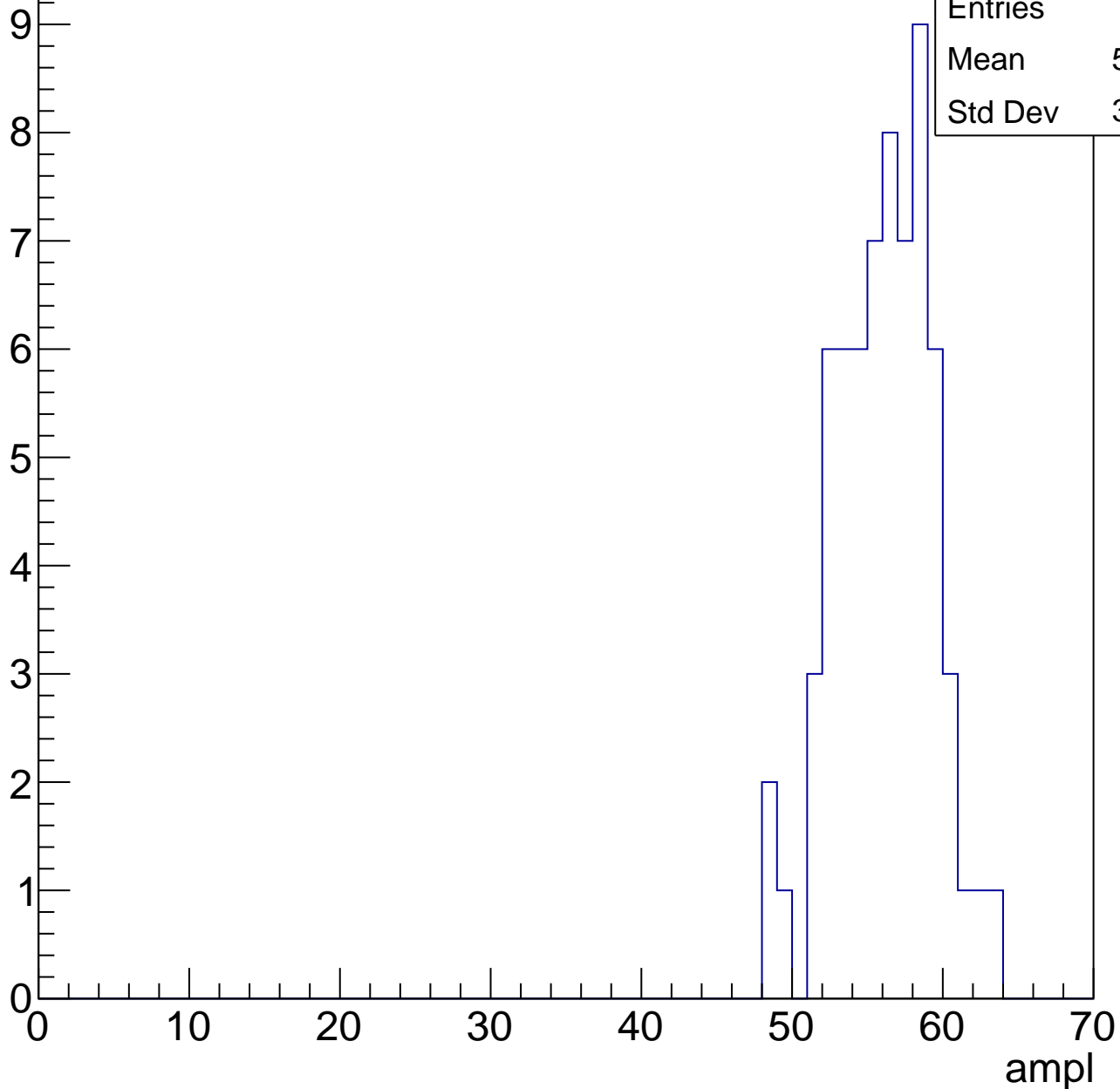
Entries	60
Mean	49.12
Std Dev	3.215



B0L002S, U4-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



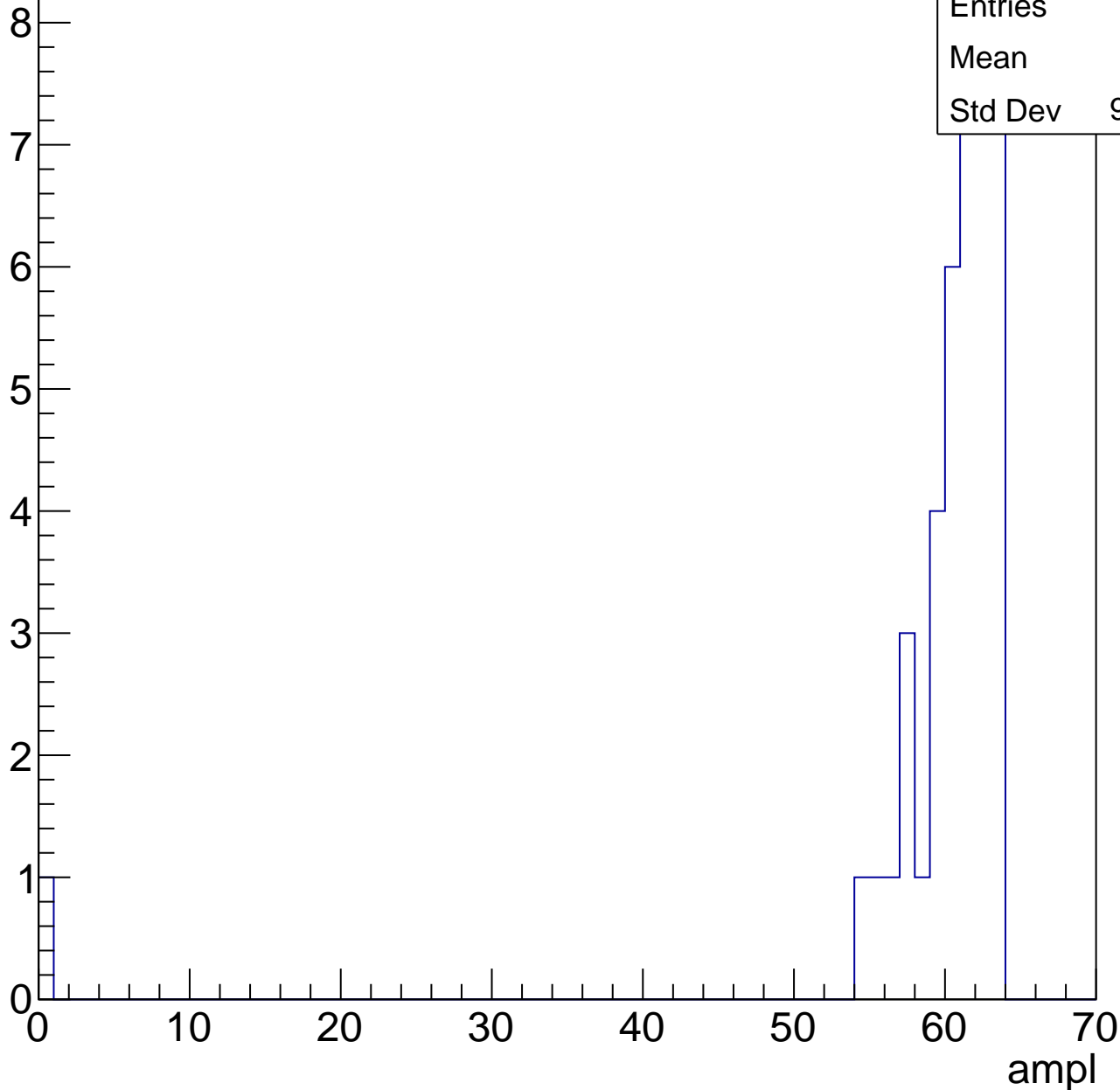
Entries	67
Mean	55.61
Std Dev	3.181

B0L002S, U4-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59
Std Dev	9.489



B0L002S, U4-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch79, adc0

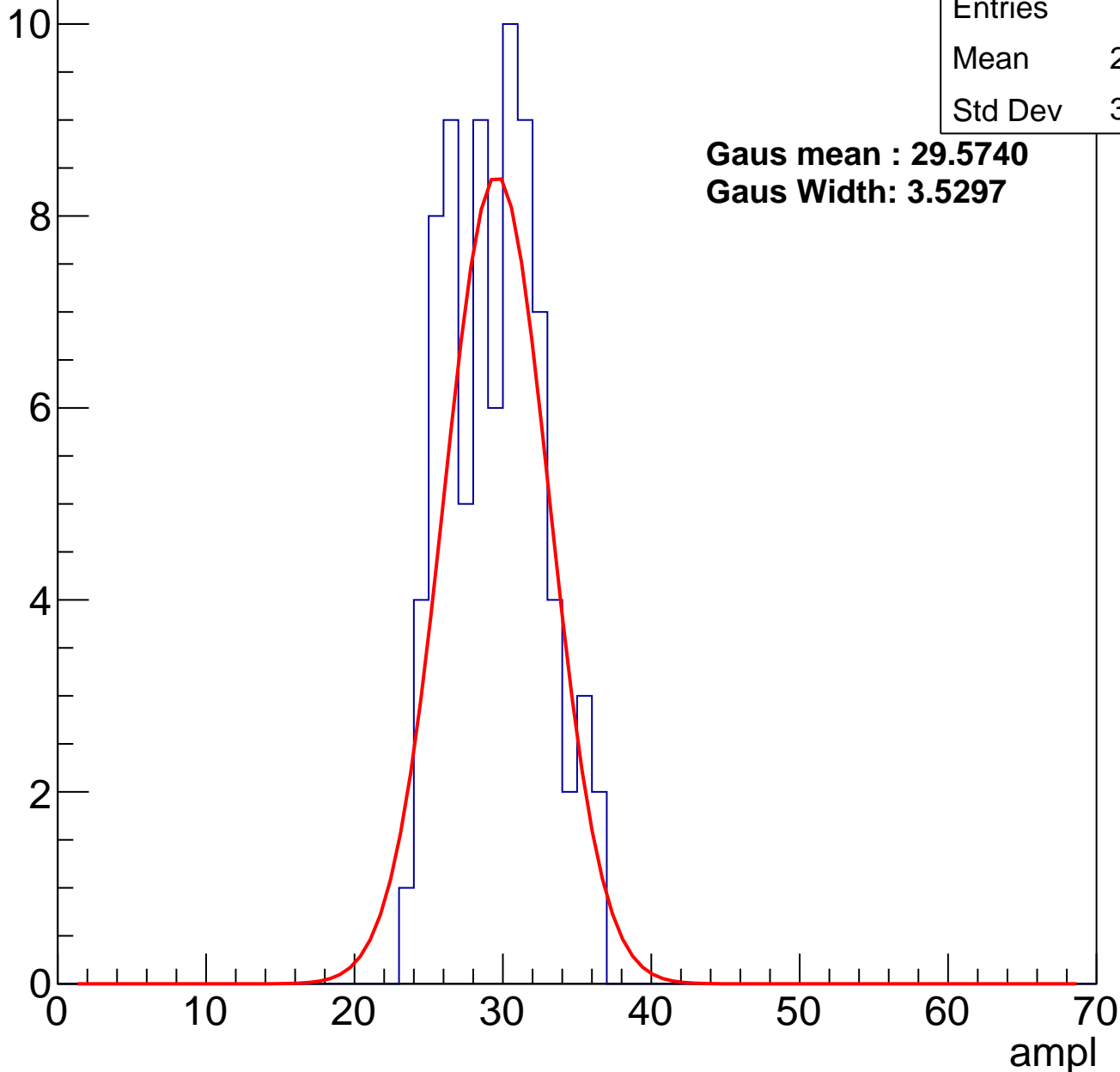
calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	29.04
Std Dev	3.188

Gaus mean : 29.5740

Gaus Width: 3.5297

Entry



B0L002S, U4-ch79, adc1

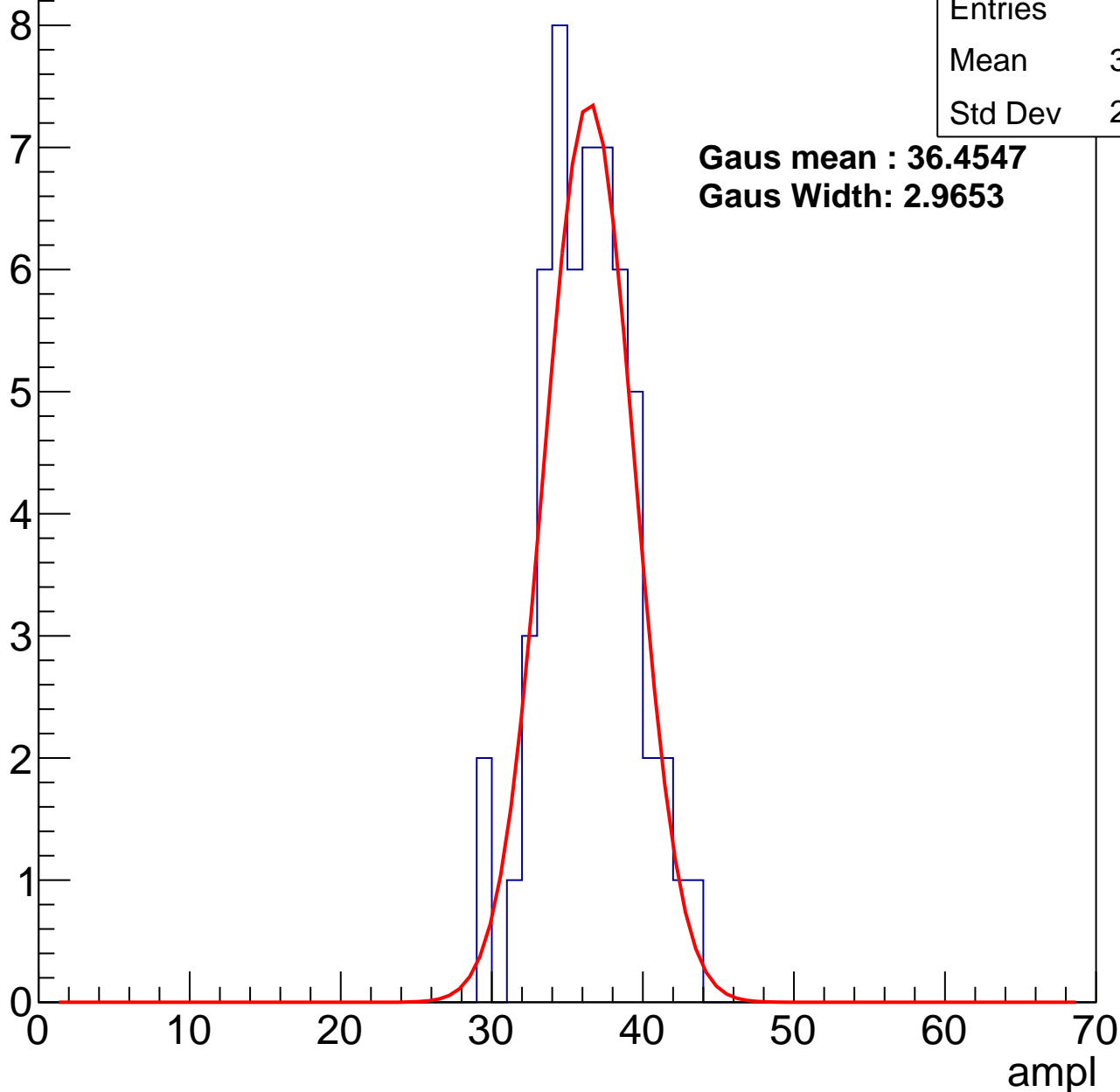
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	35.89
Std Dev	2.978

Gaus mean : 36.4547

Gaus Width: 2.9653



B0L002S, U4-ch79, adc2

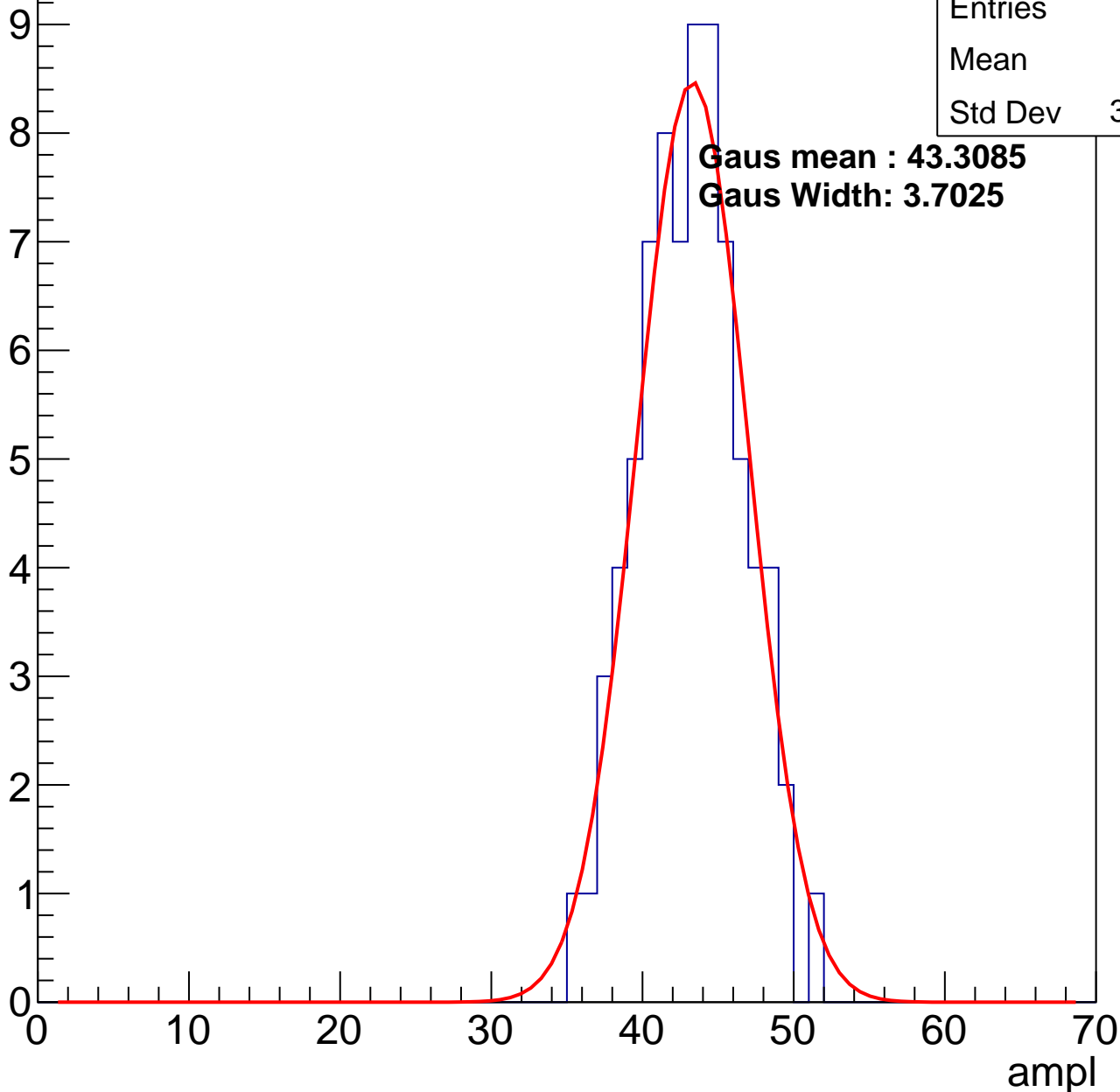
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	42.7
Std Dev	3.377

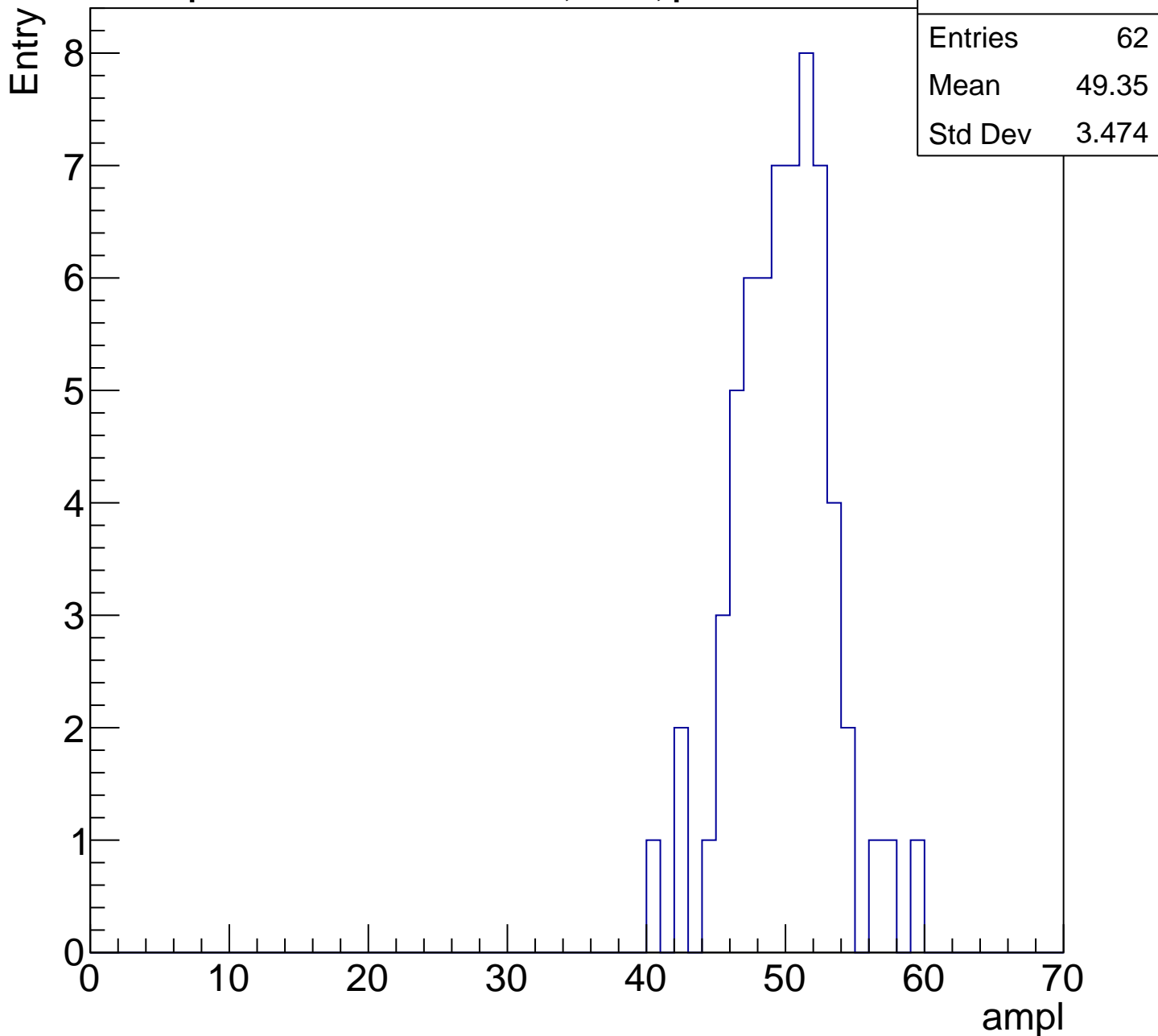
Gaus mean : 43.3085

Gaus Width: 3.7025



B0L002S, U4-ch79, adc3

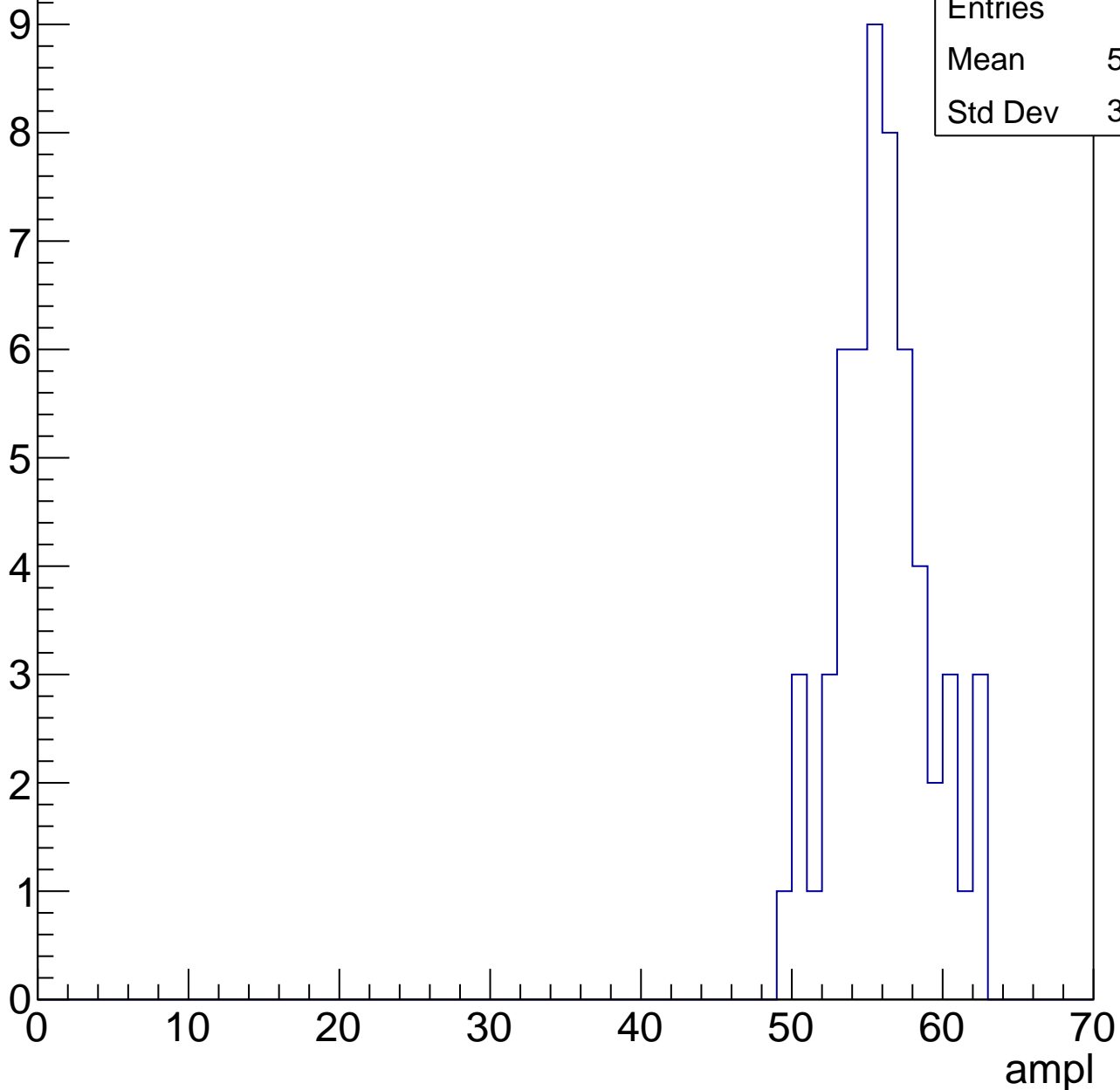
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



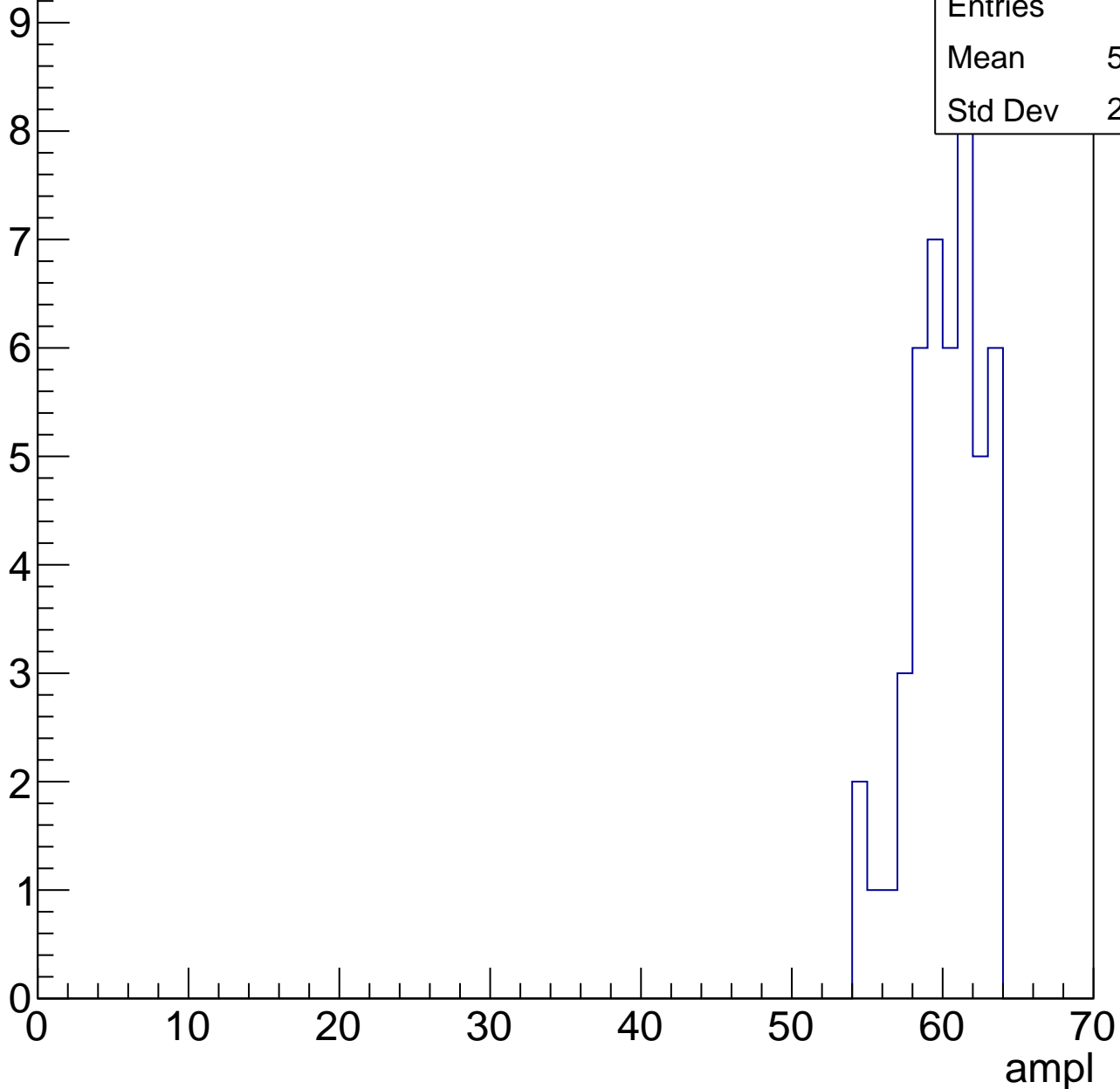
Entries	56
Mean	55.54
Std Dev	3.065

B0L002S, U4-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

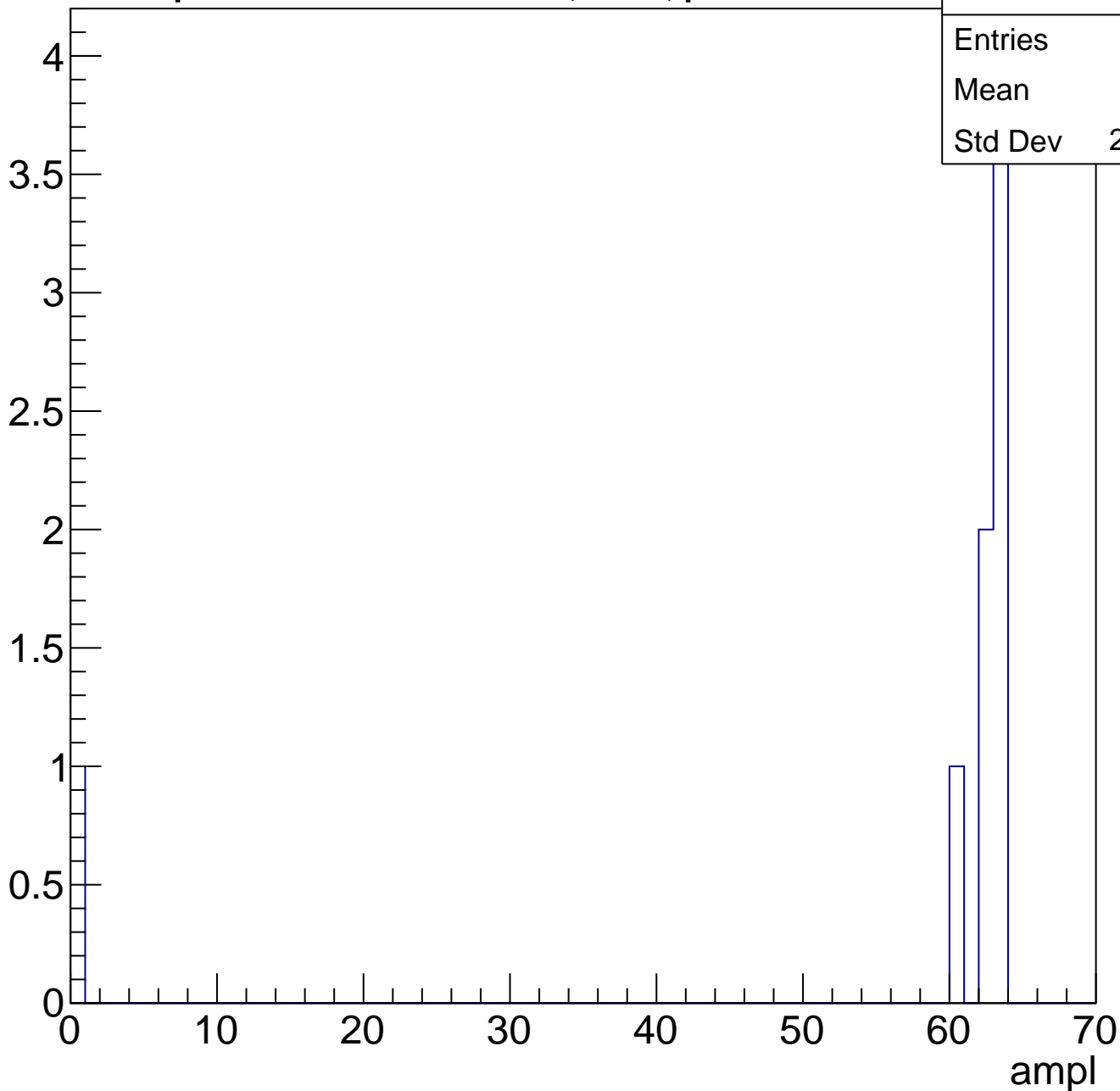
Entries	46
Mean	59.74
Std Dev	2.335



B0L002S, U4-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch80, adc0

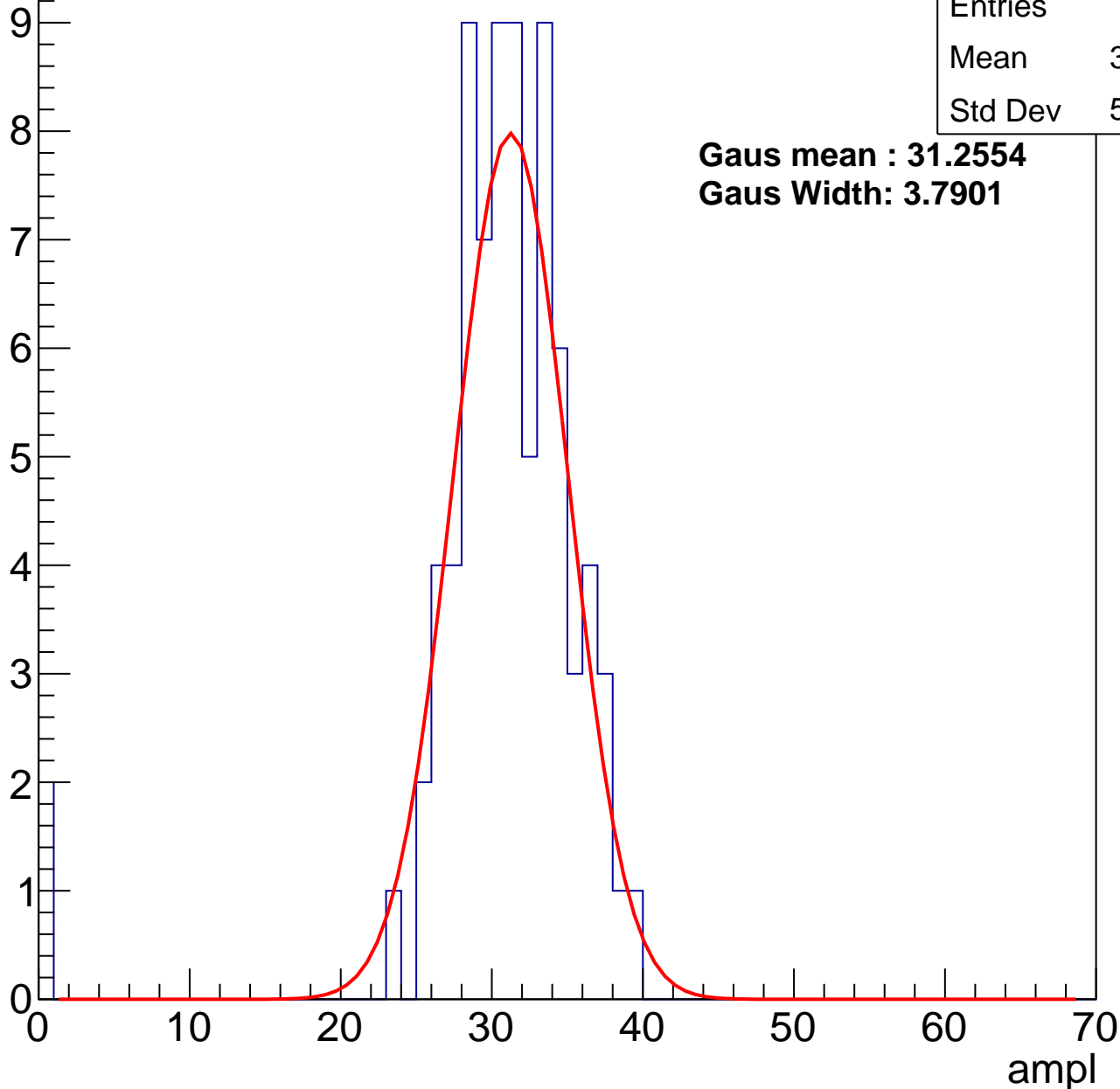
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	30.22
Std Dev	5.906

Gaus mean : 31.2554

Gaus Width: 3.7901



B0L002S, U4-ch80, adc1

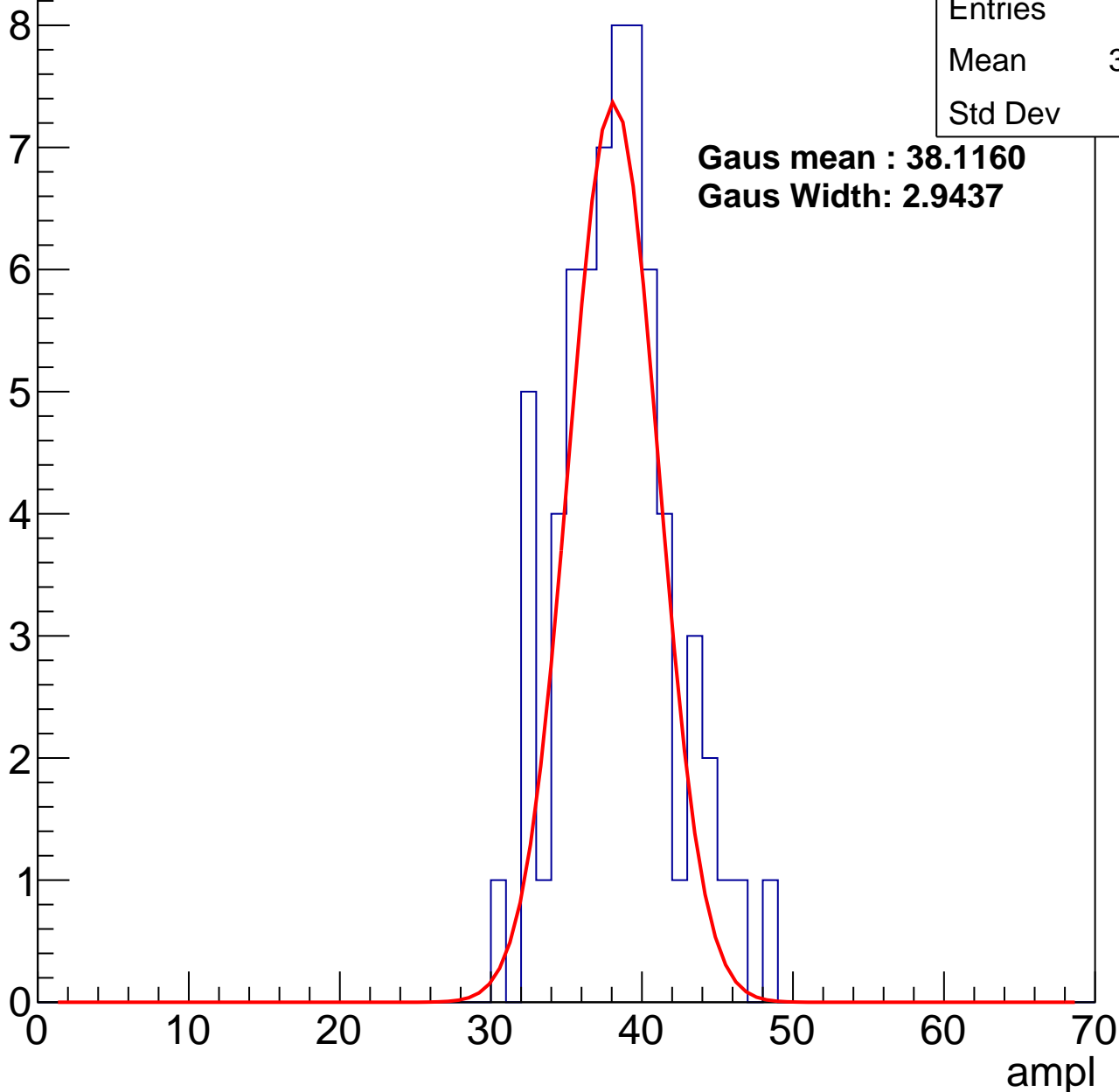
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.88
Std Dev	3.64

Gaus mean : 38.1160

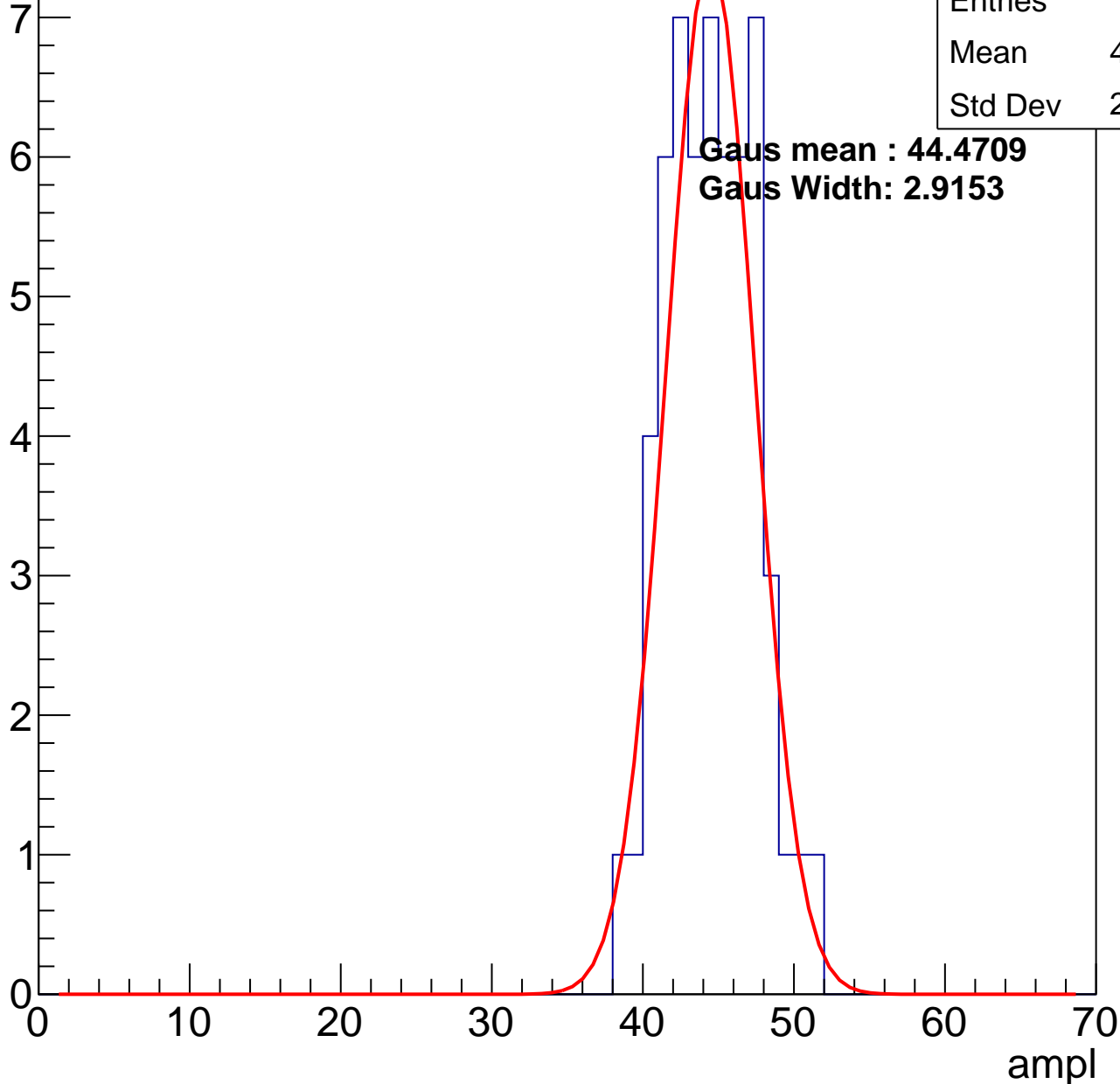
Gaus Width: 2.9437



B0L002S, U4-ch80, adc2

calib_packv5_042523_0143.root, FC#8, port C1

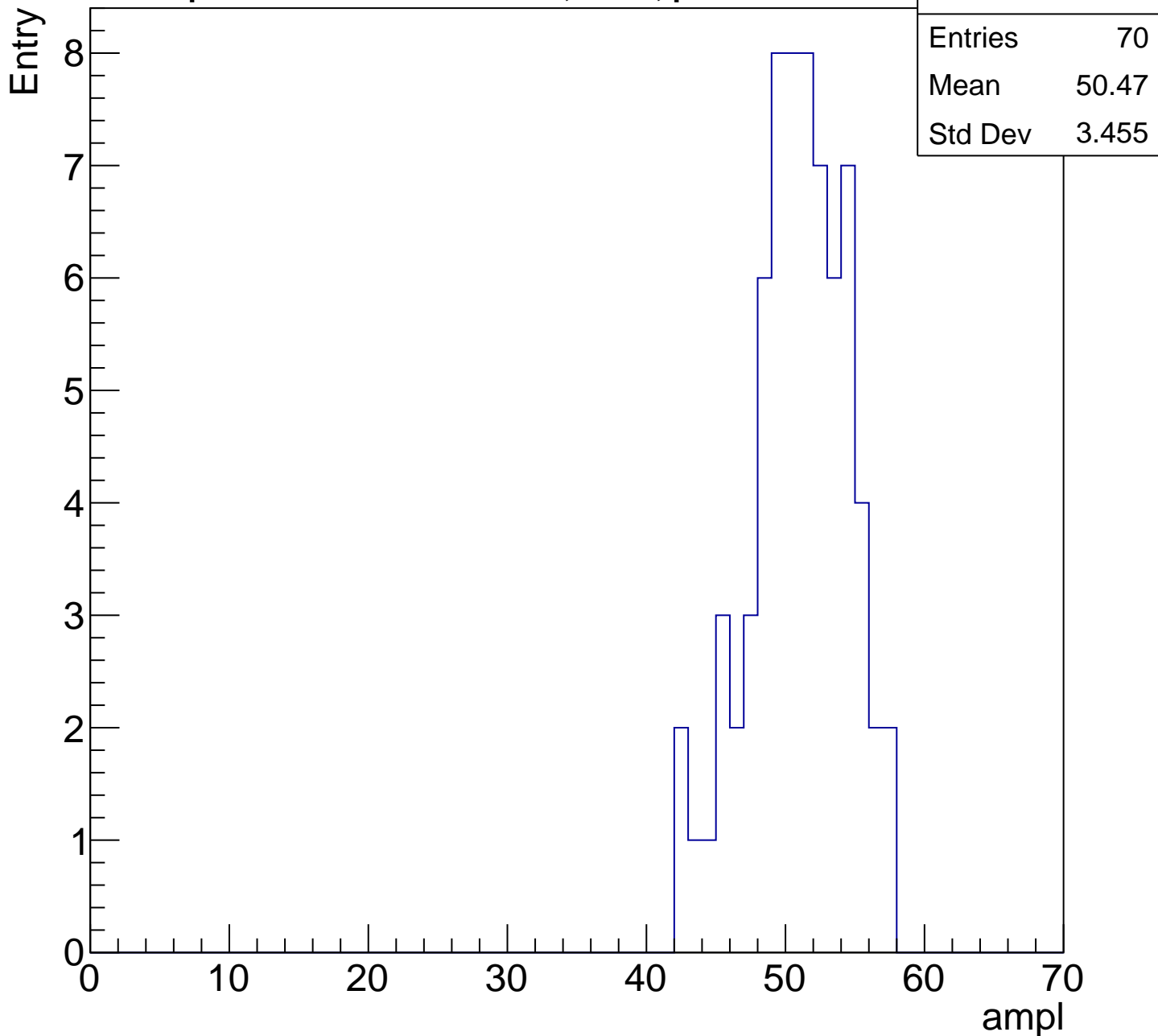
Entry



Entries	57
Mean	44.07
Std Dev	2.852

B0L002S, U4-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

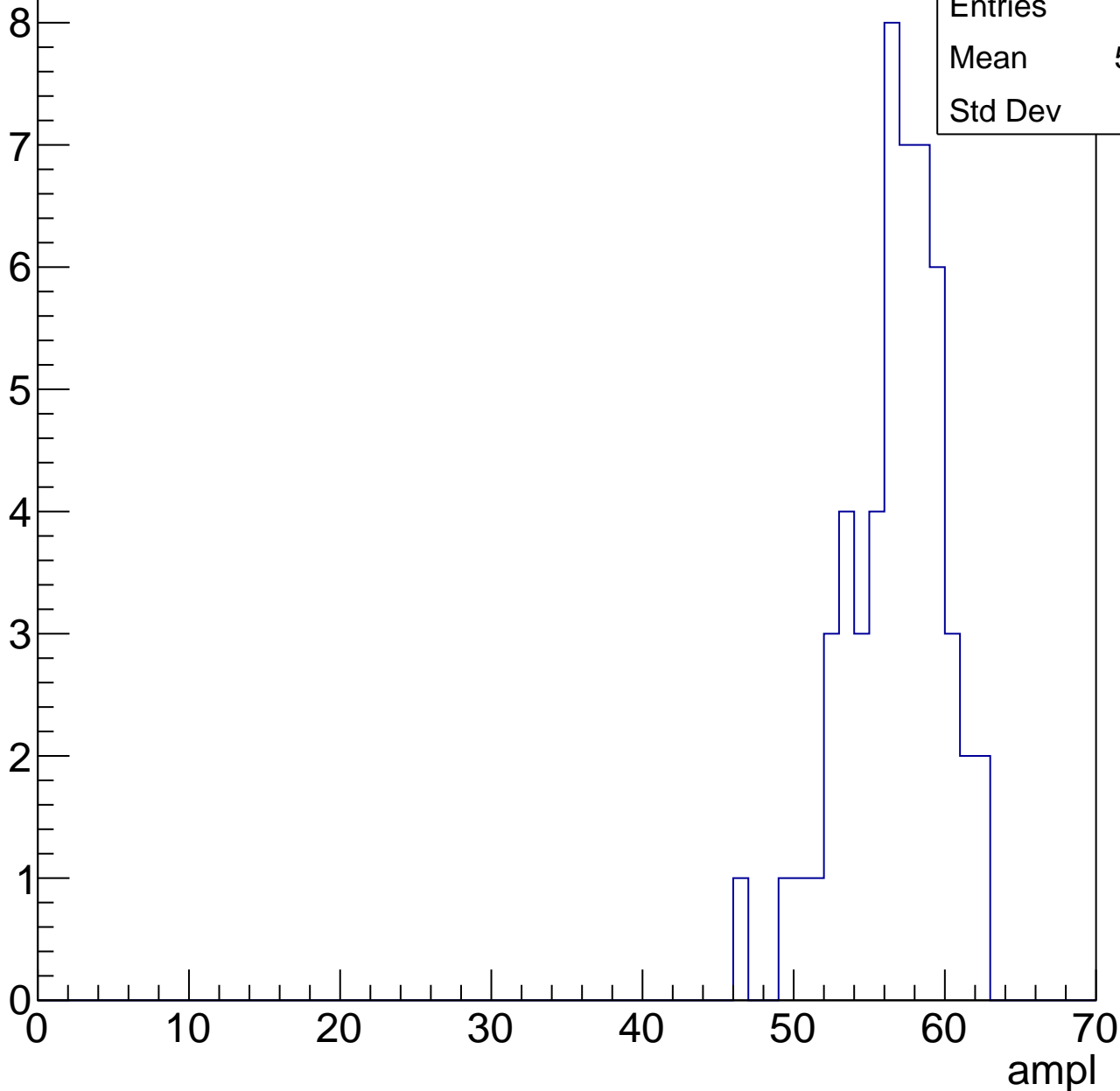


B0L002S, U4-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.21
Std Dev	3.27

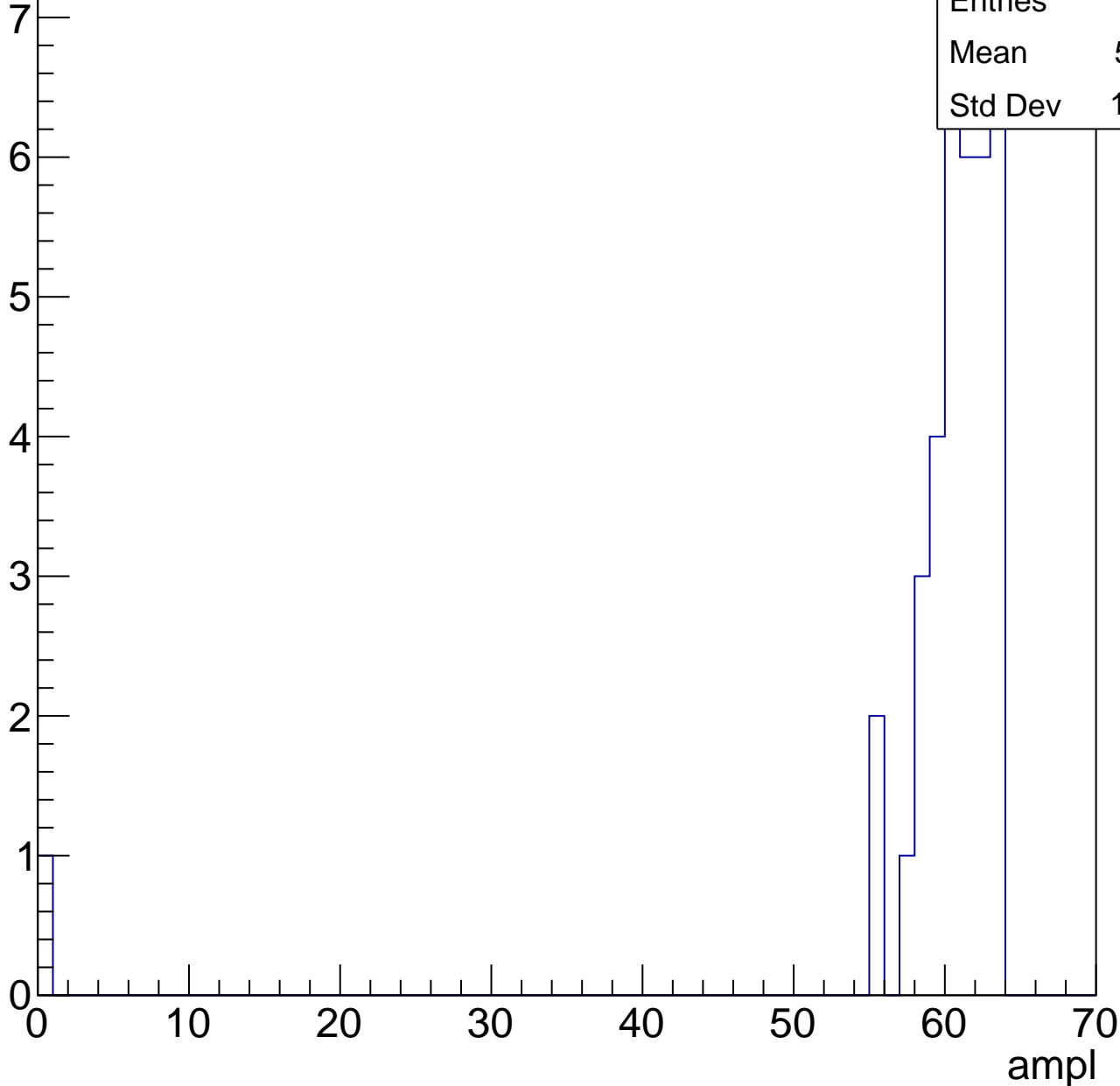


B0L002S, U4-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

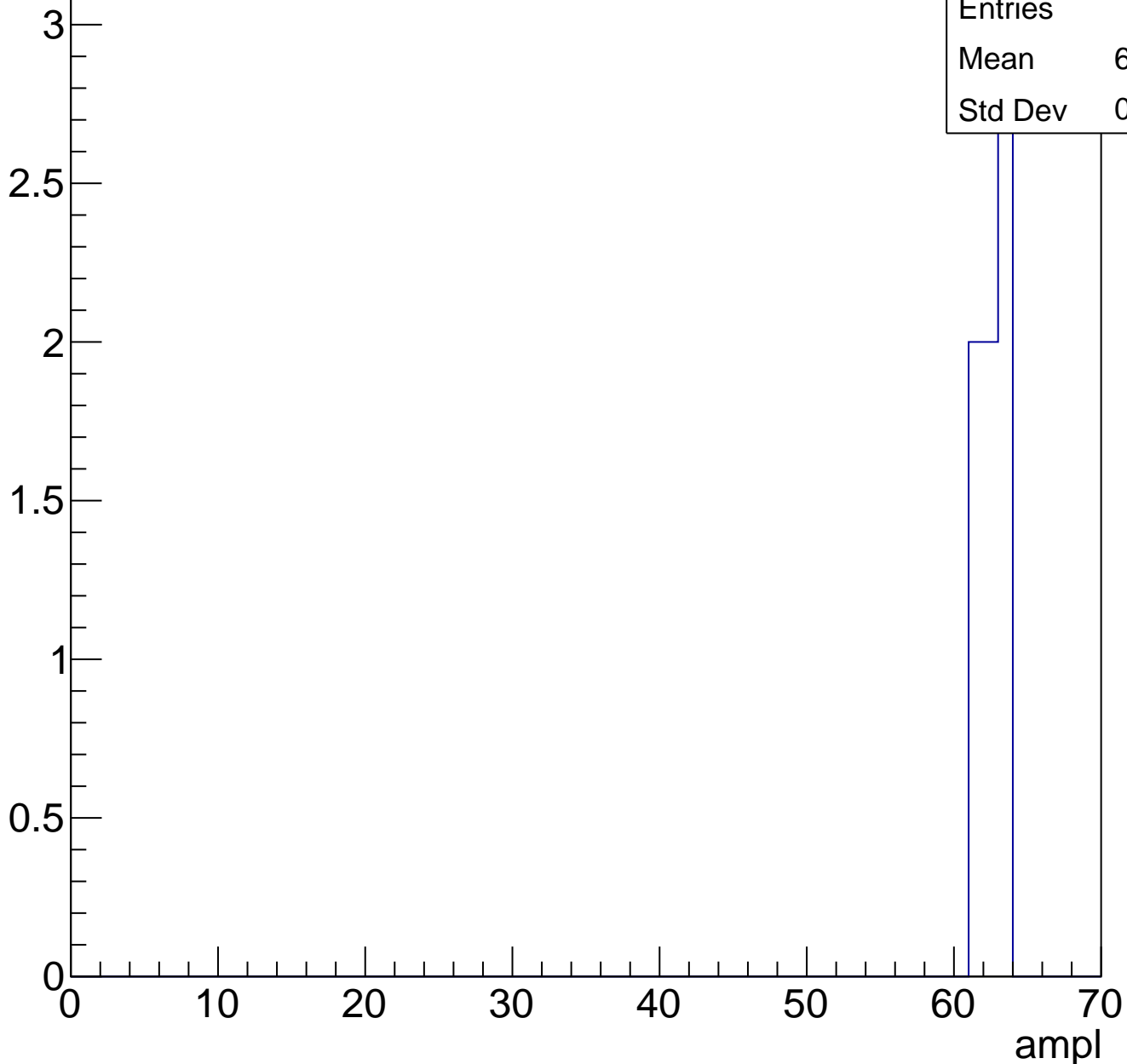
Entries	37
Mean	58.81
Std Dev	10.02



B0L002S, U4-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch81, adc0

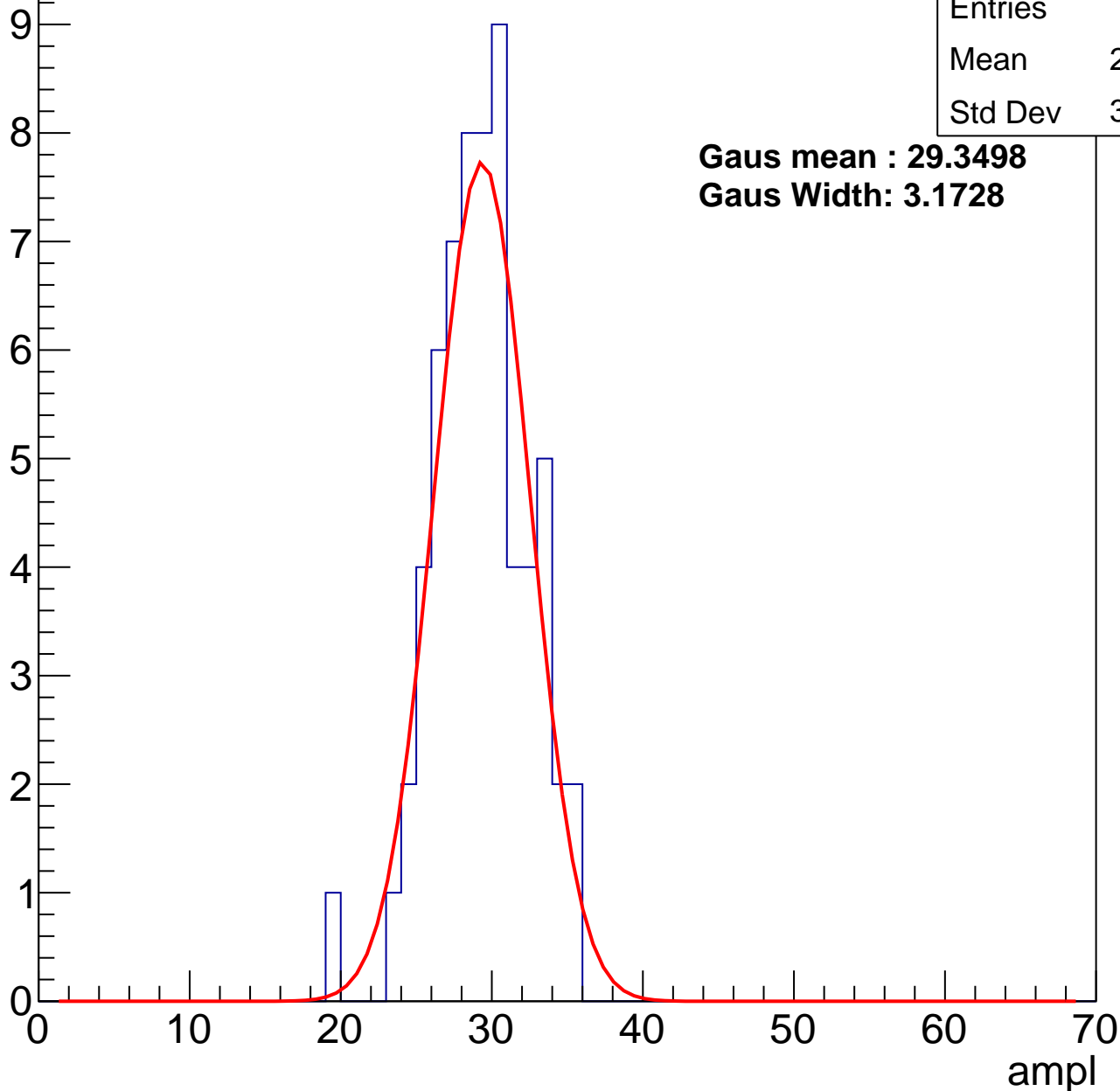
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	28.83
Std Dev	3.089

Gaus mean : 29.3498

Gaus Width: 3.1728



B0L002S, U4-ch81, adc1

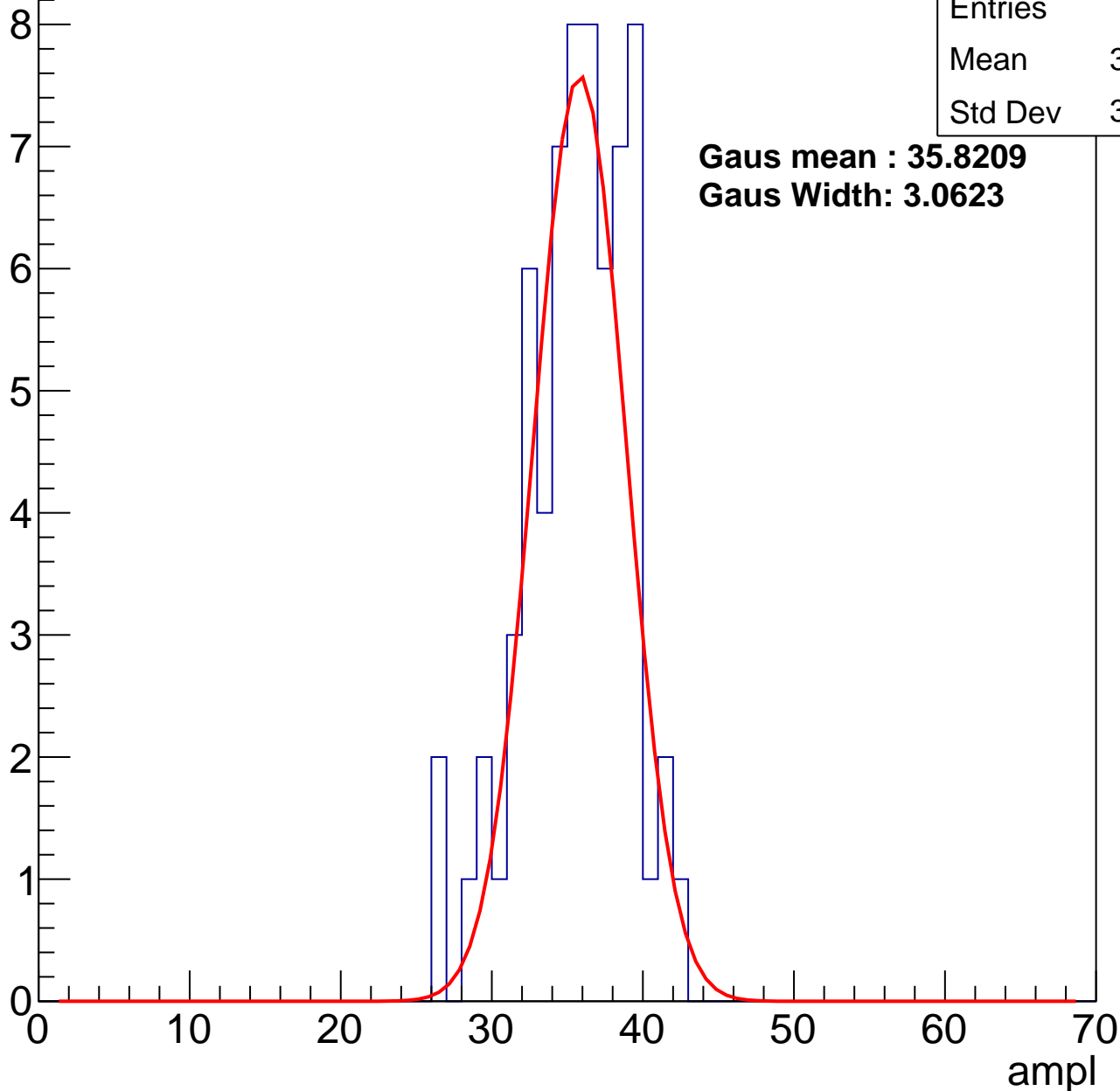
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.15
Std Dev	3.457

Gaus mean : 35.8209

Gaus Width: 3.0623



B0L002S, U4-ch81, adc2

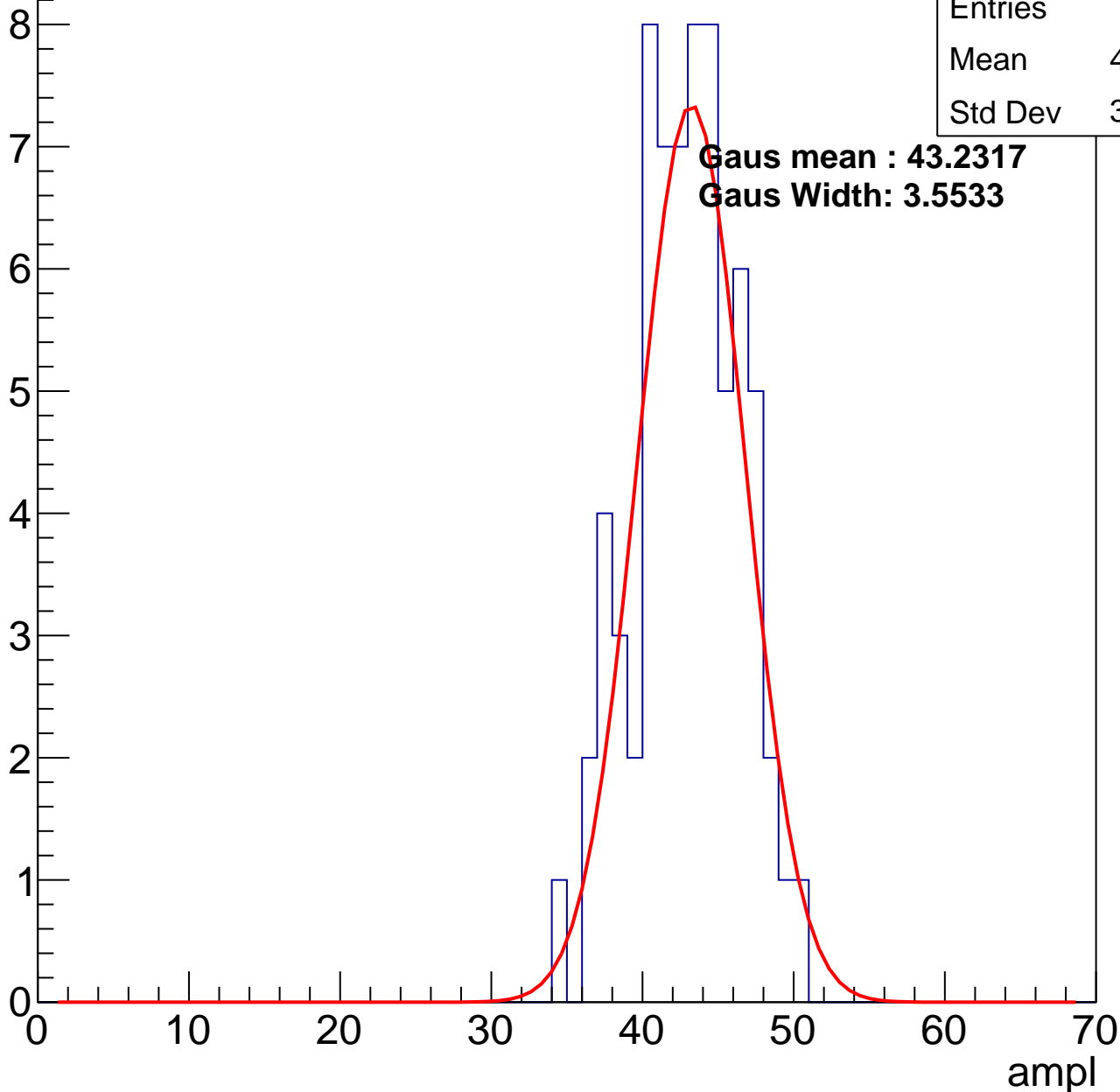
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.49
Std Dev	3.413

Gaus mean : 43.2317

Gaus Width: 3.5533

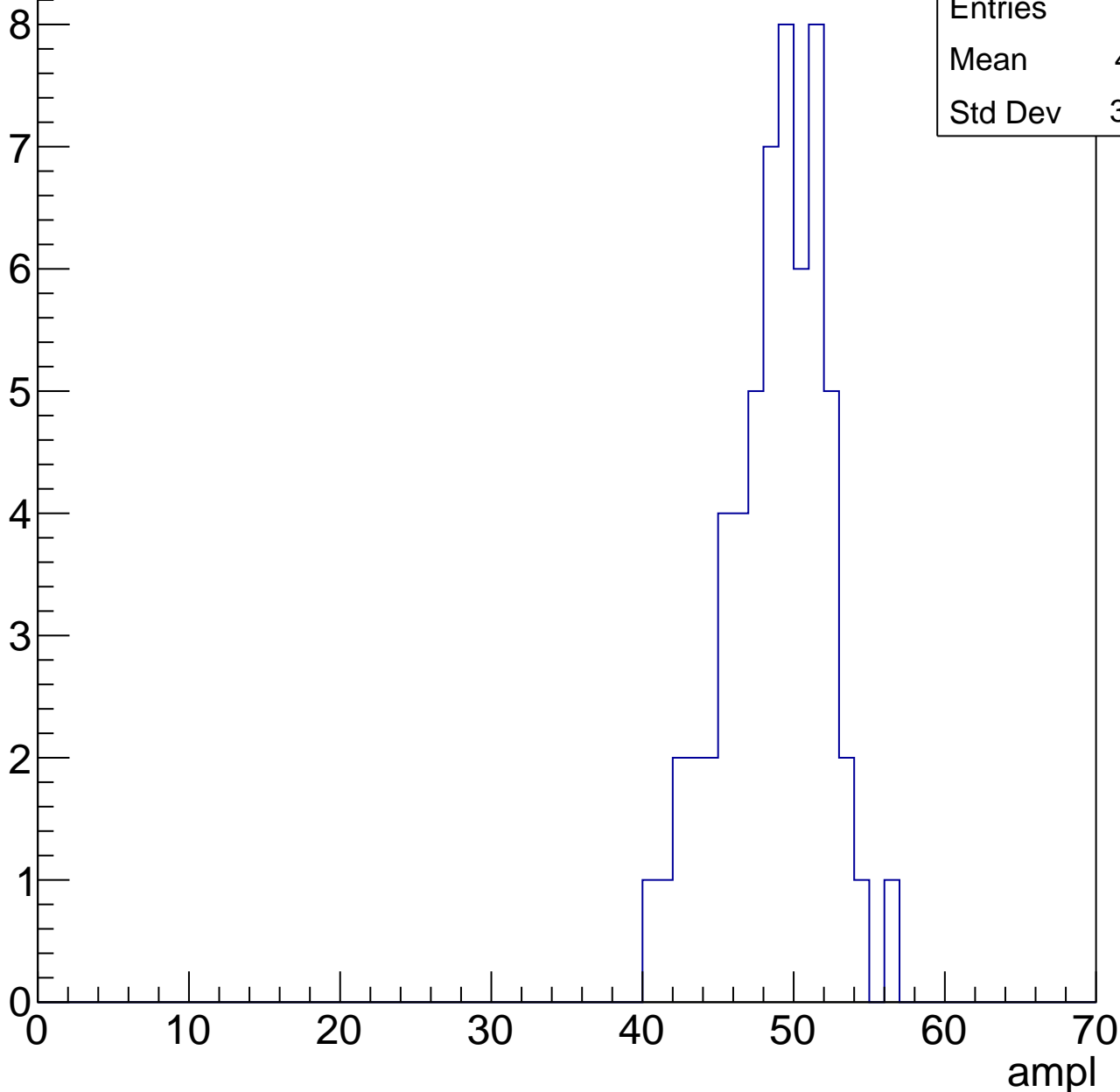


B0L002S, U4-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	48.31
Std Dev	3.326

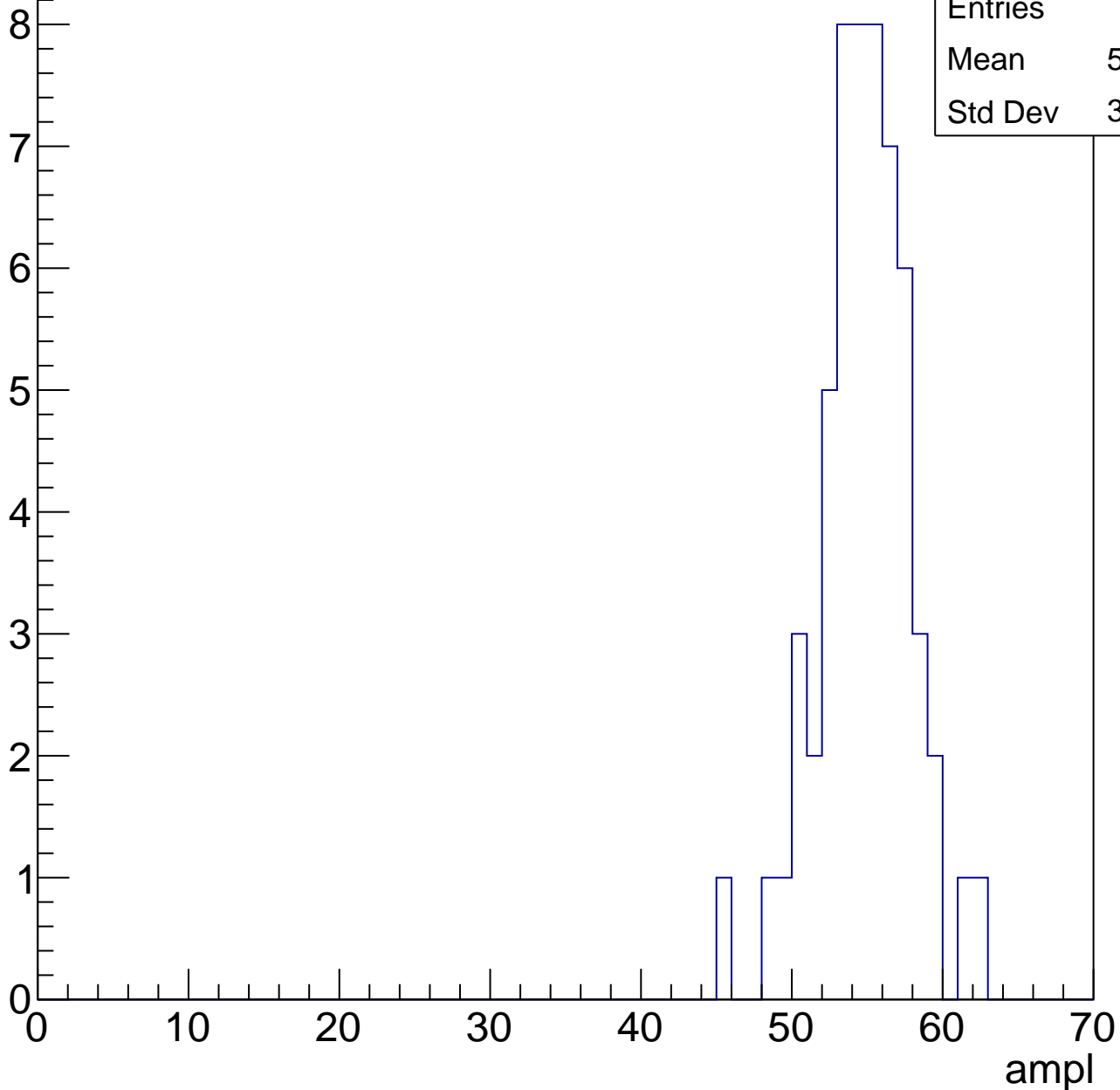


B0L002S, U4-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	54.37
Std Dev	3.047

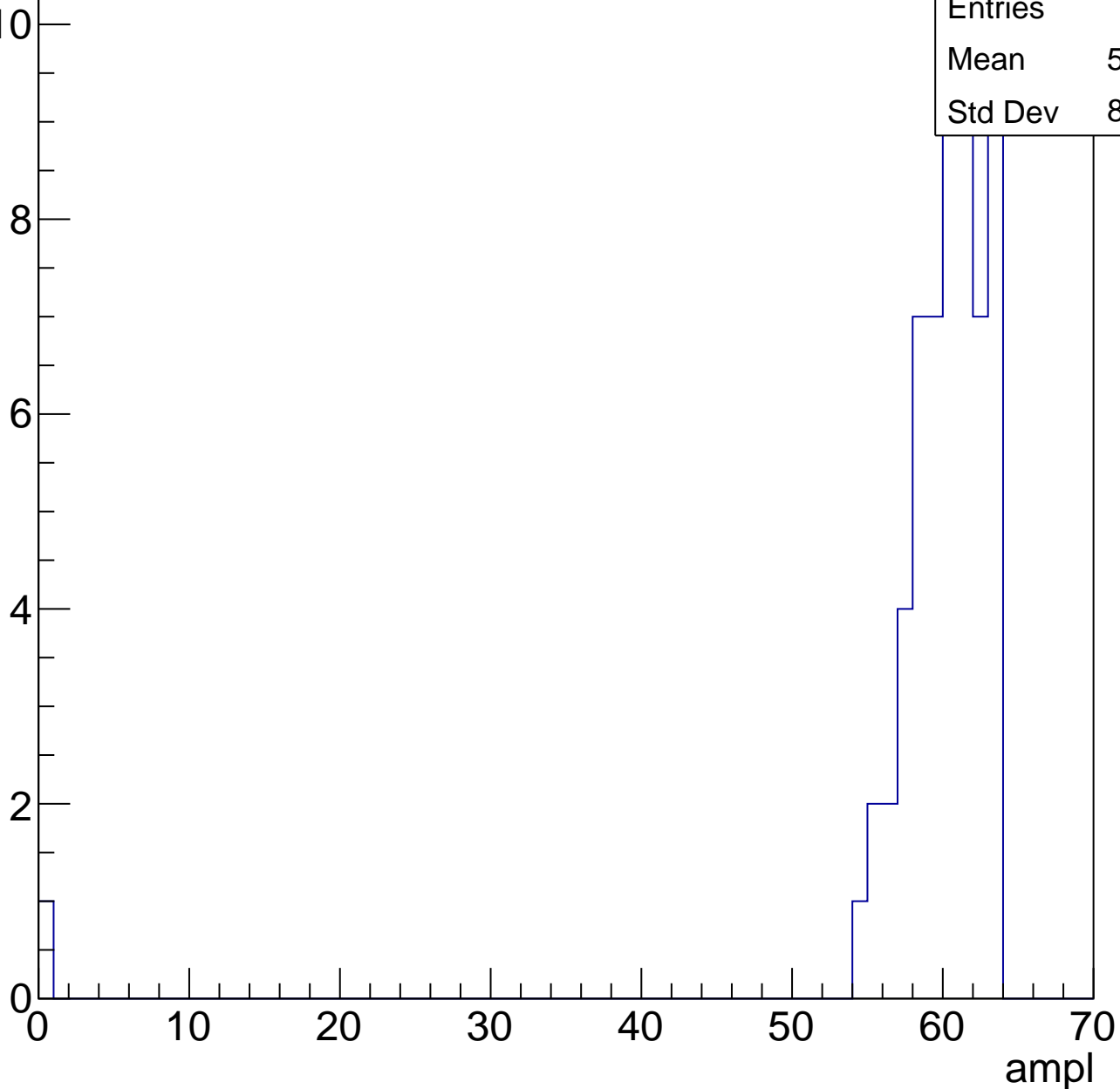


B0L002S, U4-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

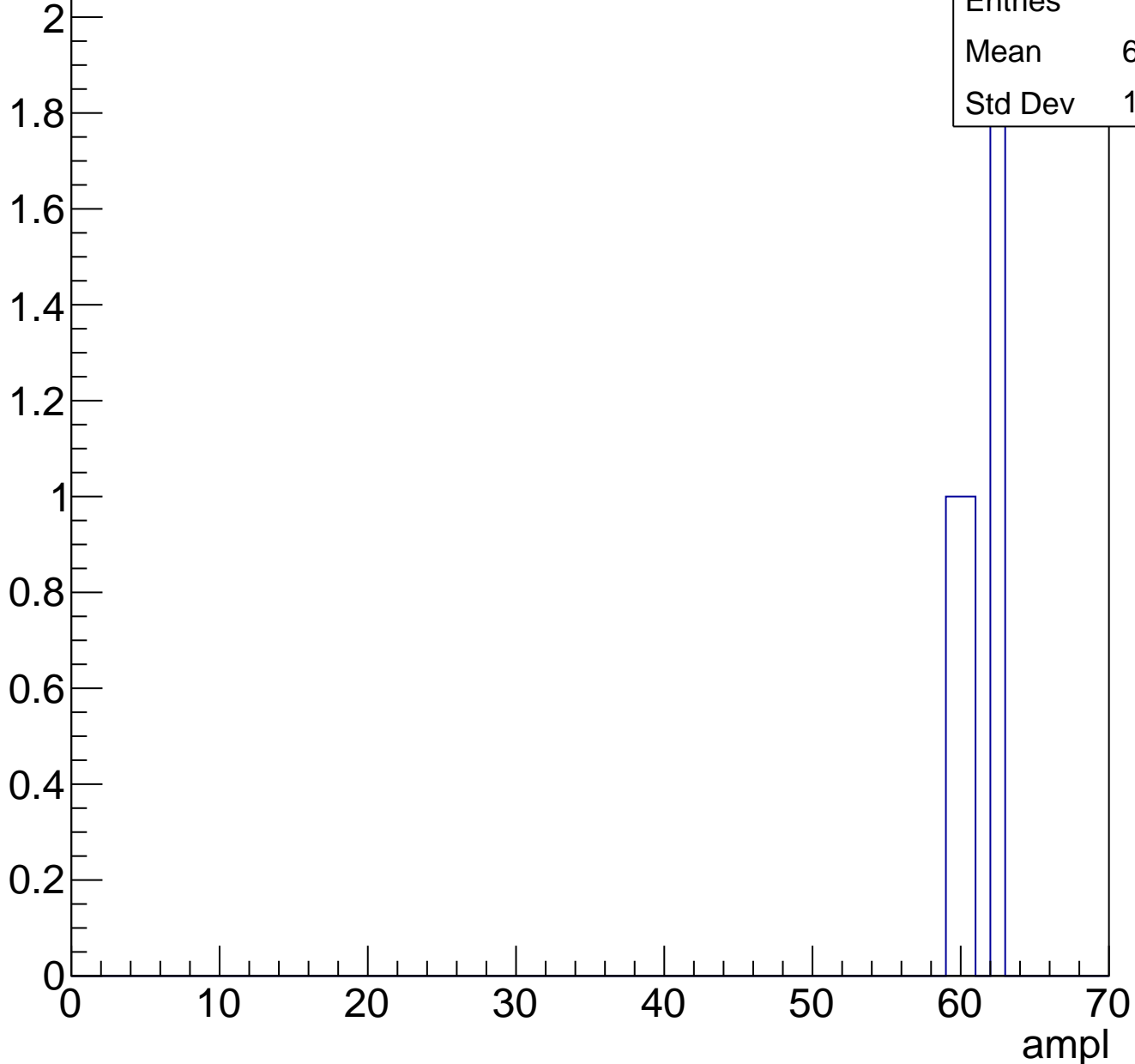
Entries	59
Mean	58.92
Std Dev	8.074



B0L002S, U4-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch82, adc0

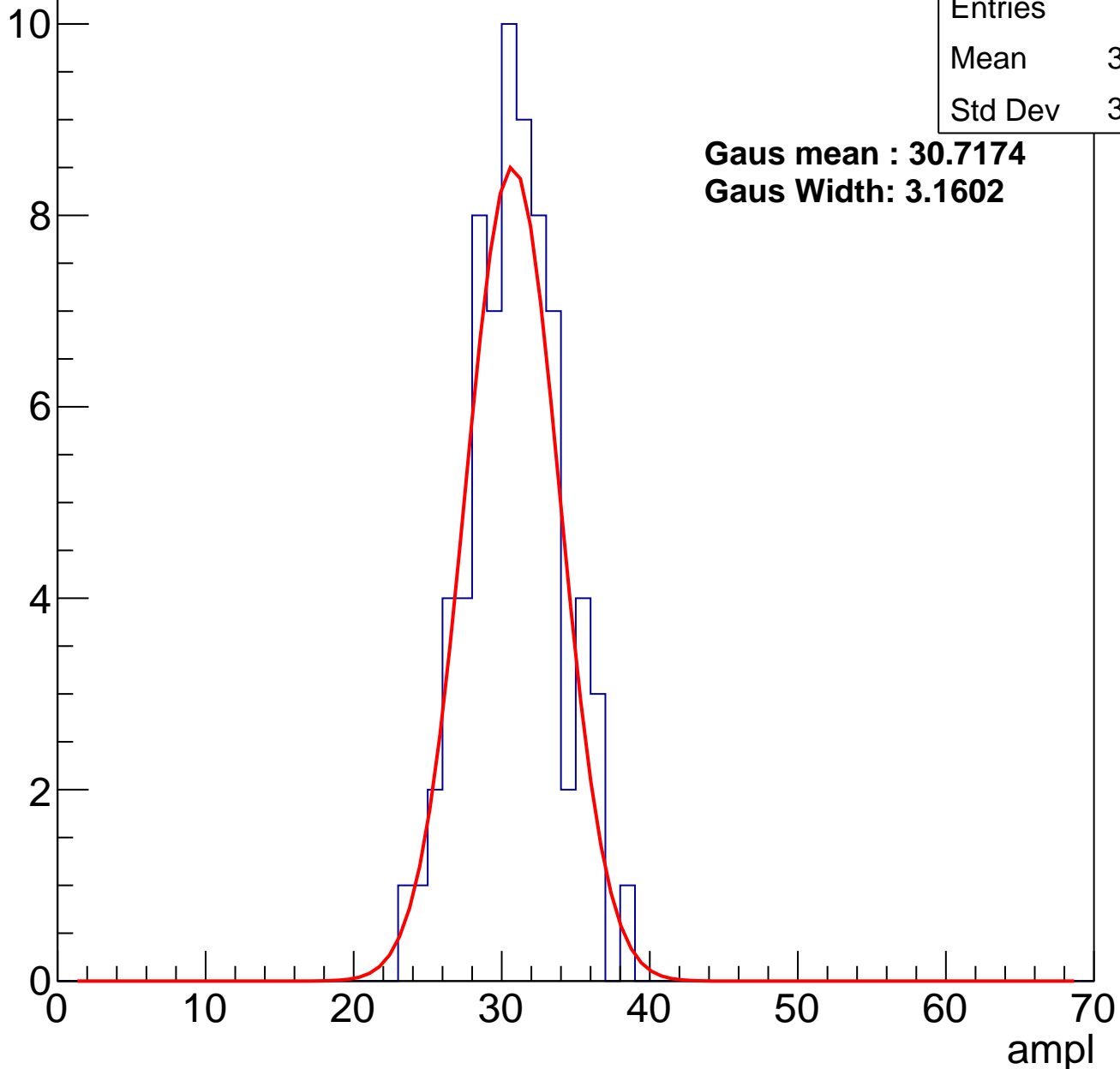
calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	30.37
Std Dev	3.078

Gaus mean : 30.7174

Gaus Width: 3.1602

Entry



B0L002S, U4-ch82, adc1

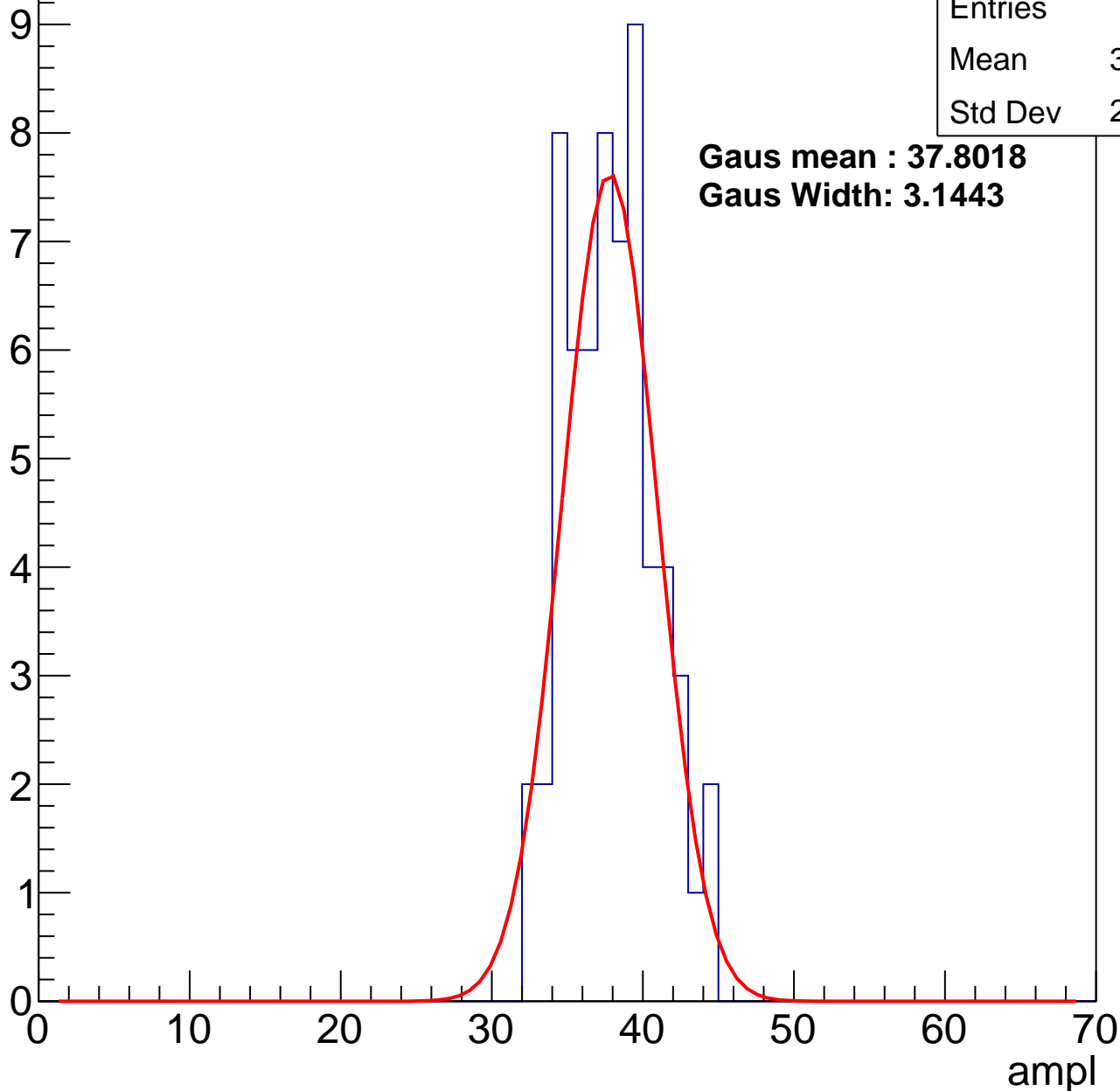
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.45
Std Dev	2.905

Gaus mean : 37.8018

Gaus Width: 3.1443



B0L002S, U4-ch82, adc2

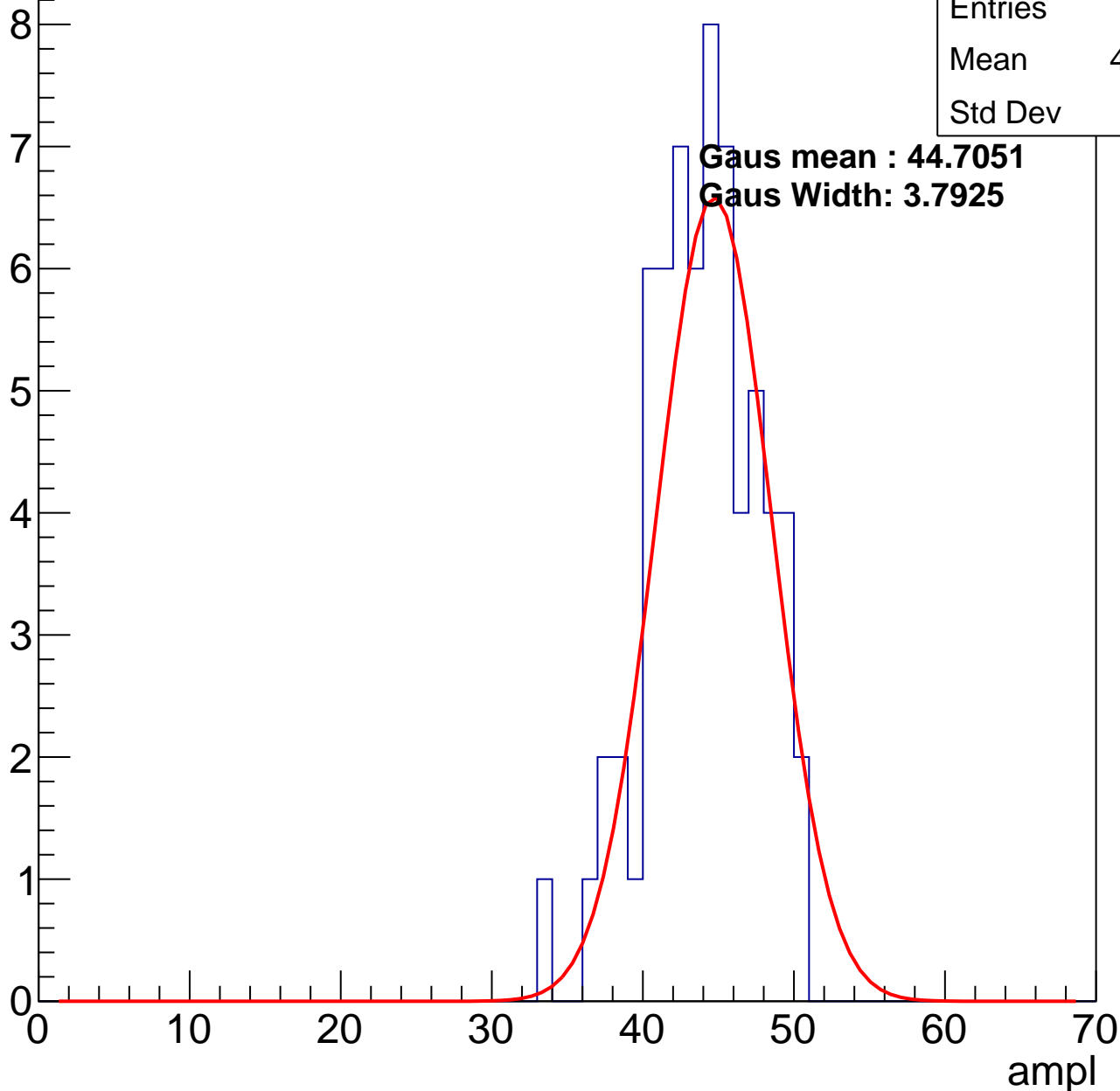
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.48
Std Dev	3.59

Gaus mean : 44.7051

Gaus Width: 3.7925

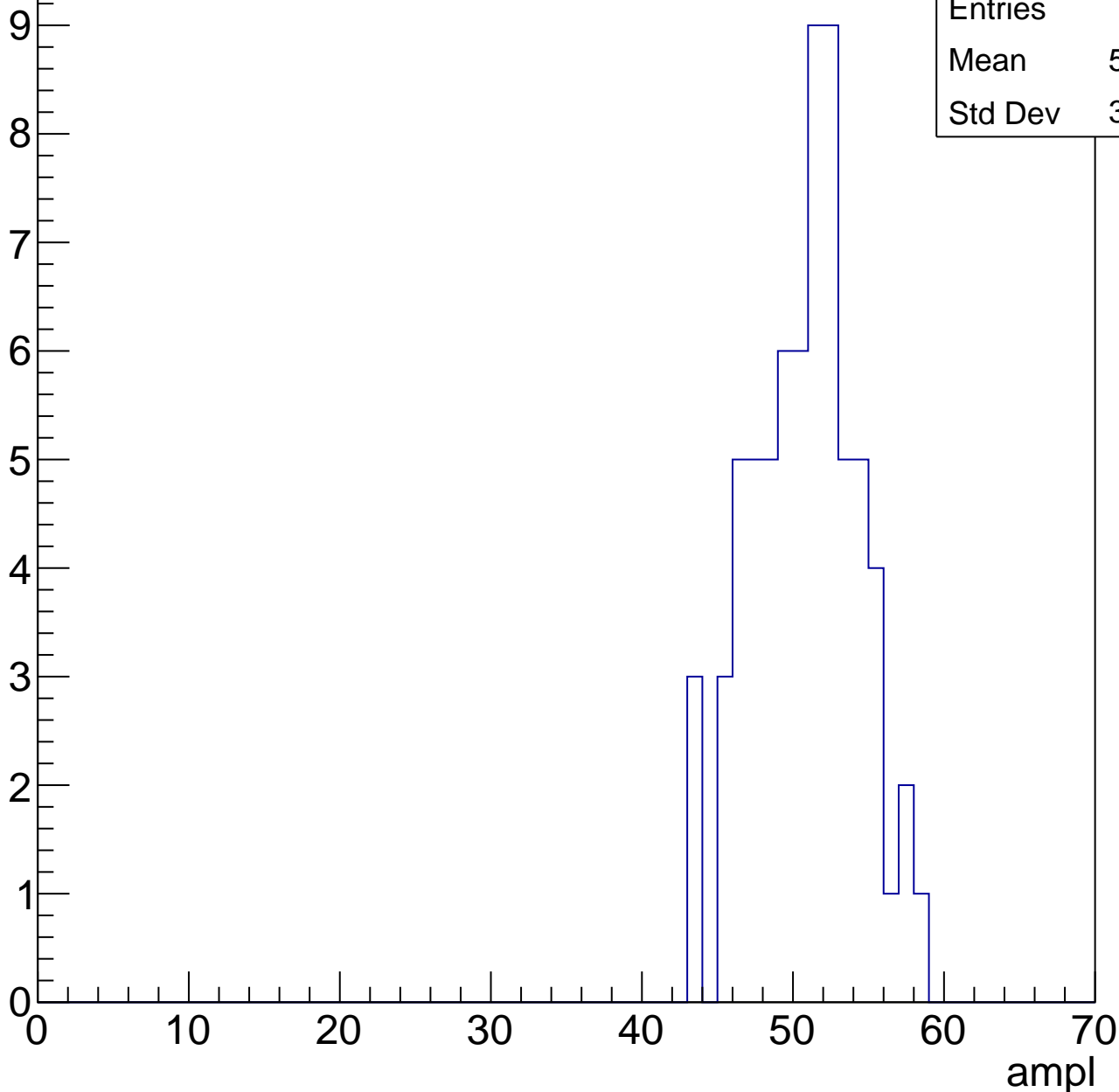


B0L002S, U4-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

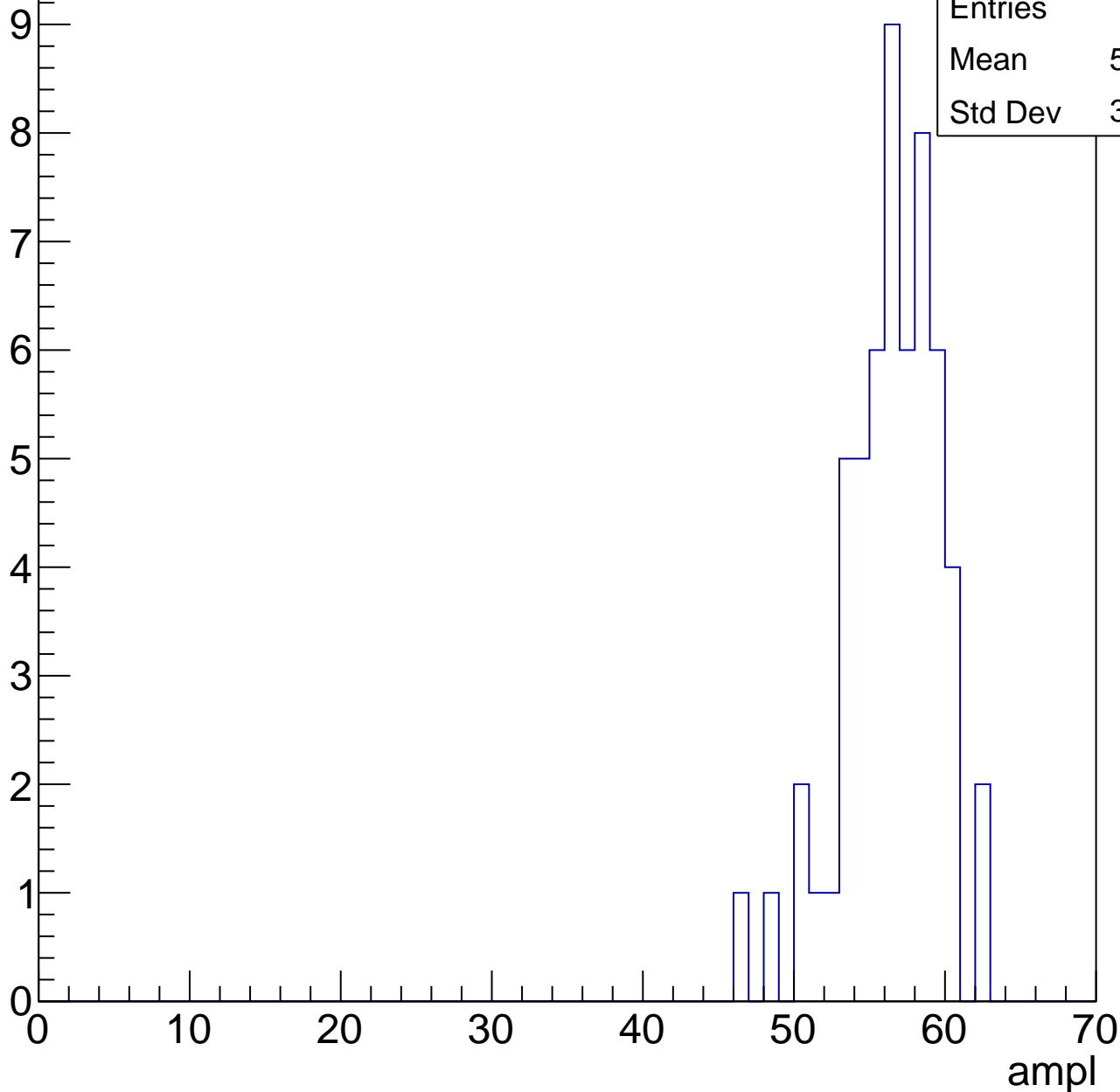
Entries	69
Mean	50.33
Std Dev	3.475



B0L002S, U4-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

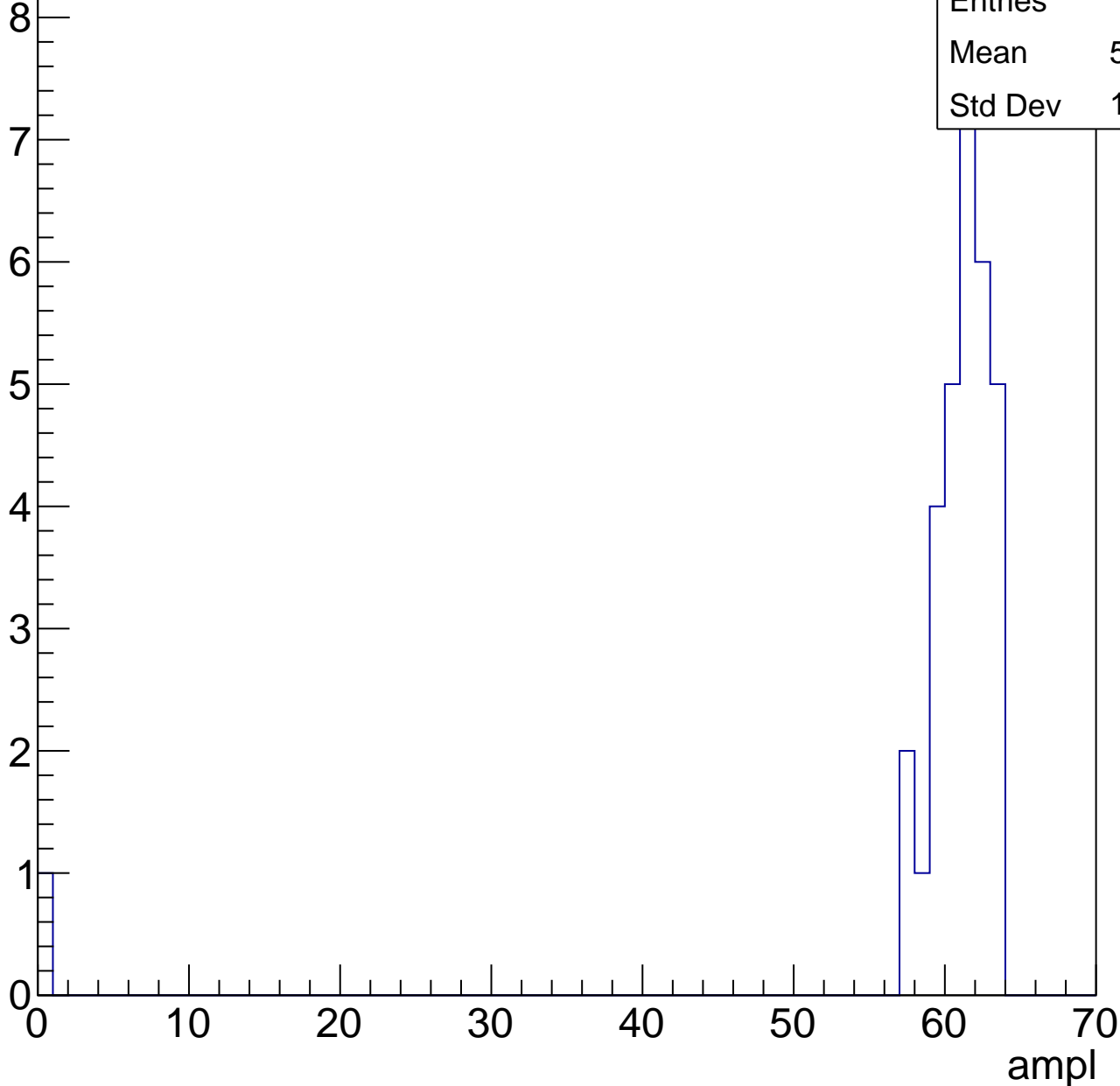


B0L002S, U4-ch82, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	32
Mean	58.84
Std Dev	10.69

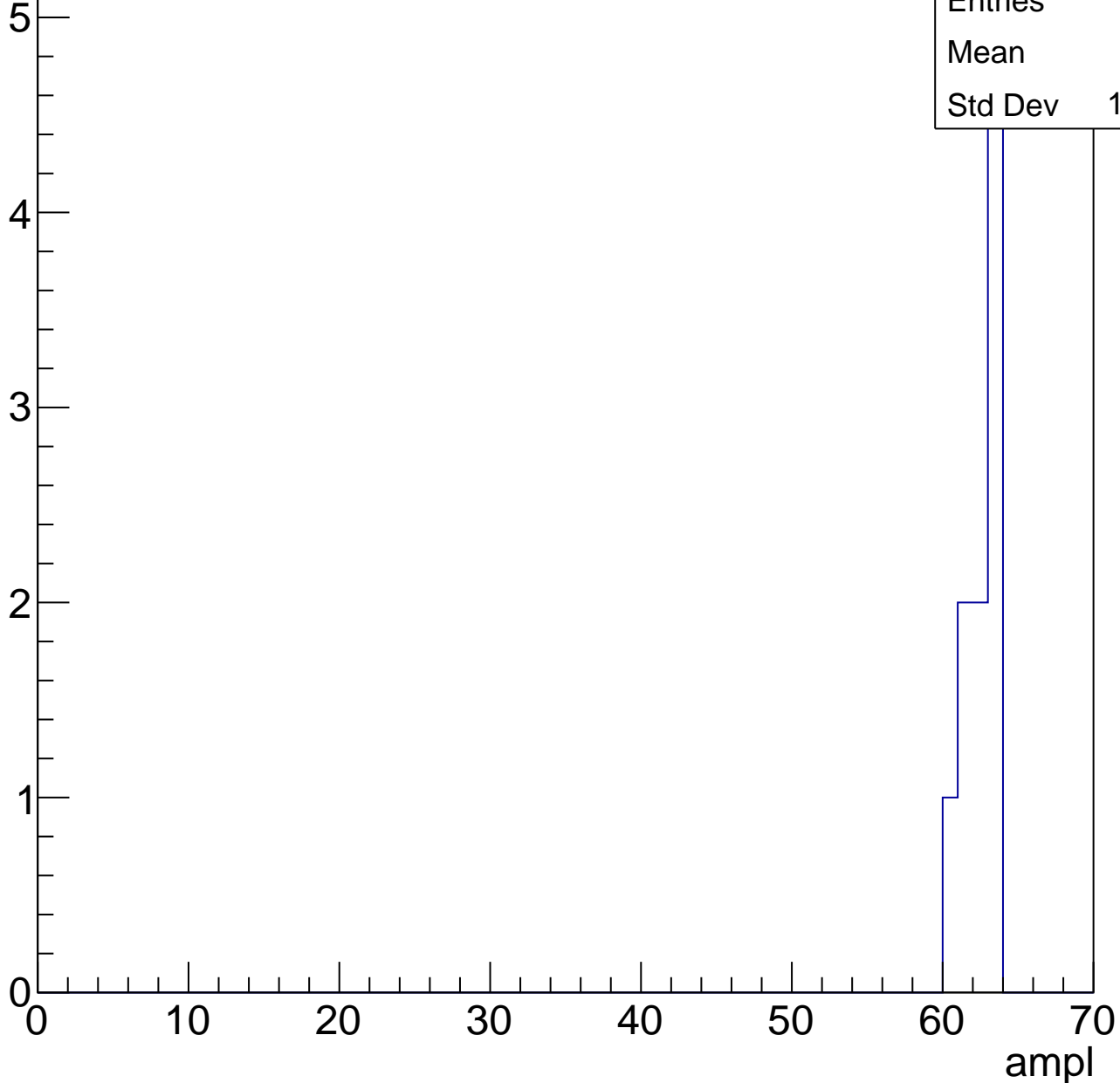


B0L002S, U4-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	62.1
Std Dev	1.044



B0L002S, U4-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch83, adc0

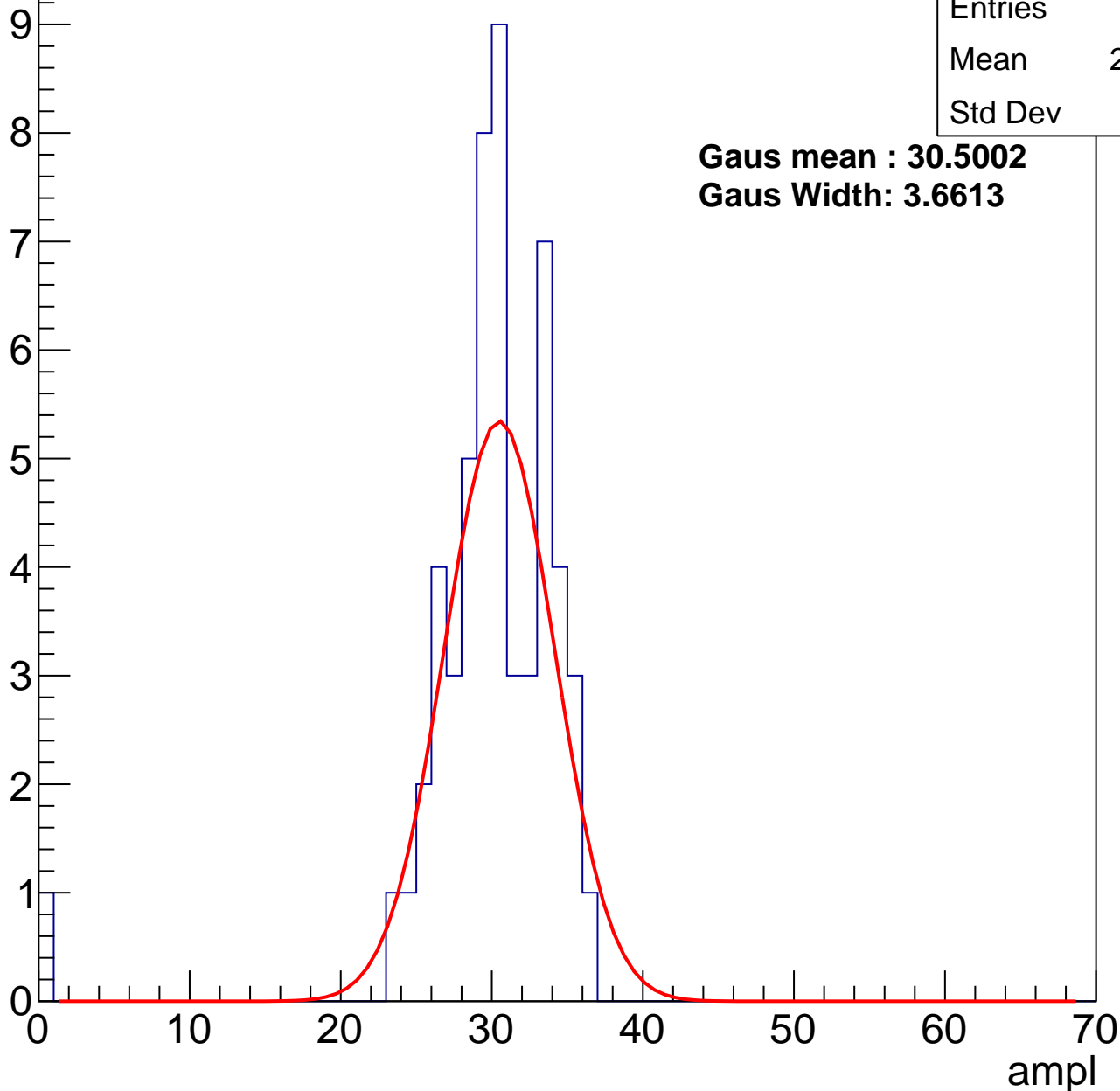
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	29.47
Std Dev	5.03

Gaus mean : 30.5002

Gaus Width: 3.6613



B0L002S, U4-ch83, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	37.04
Std Dev	3.625

Gaus mean : 37.6145

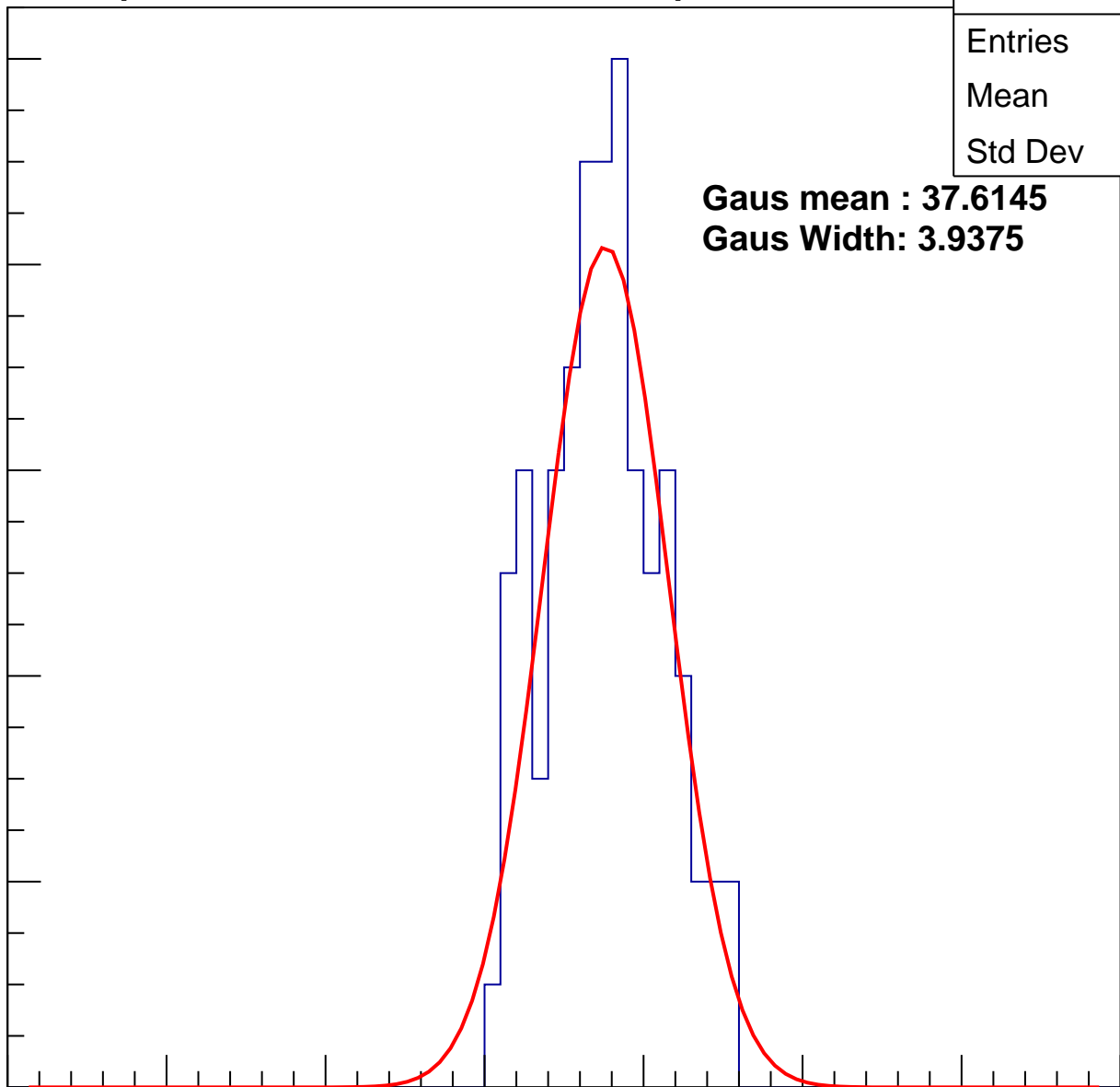
Gaus Width: 3.9375

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

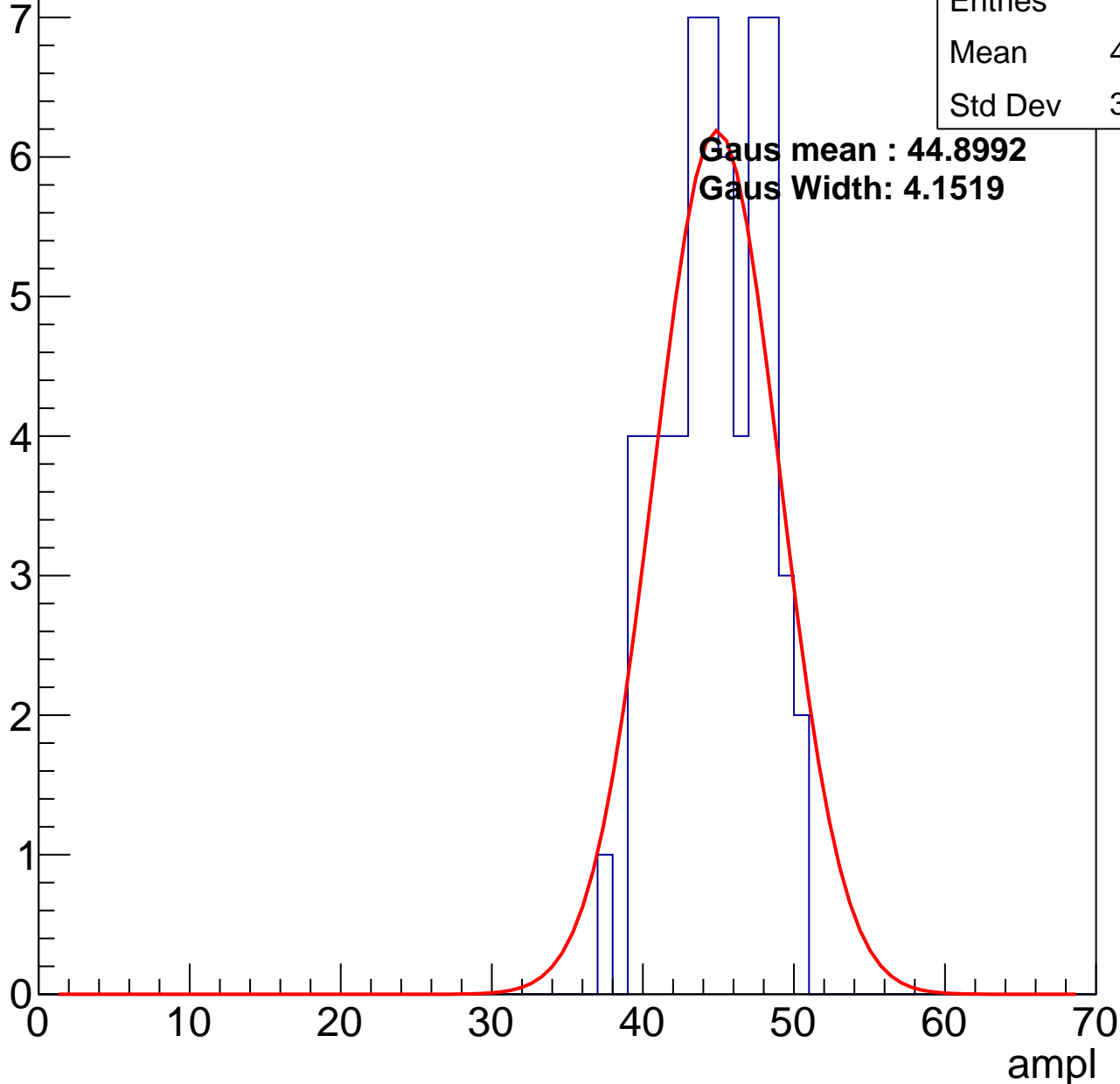


B0L002S, U4-ch83, adc2

calib_packv5_042523_0143.root, FC#8, port C1

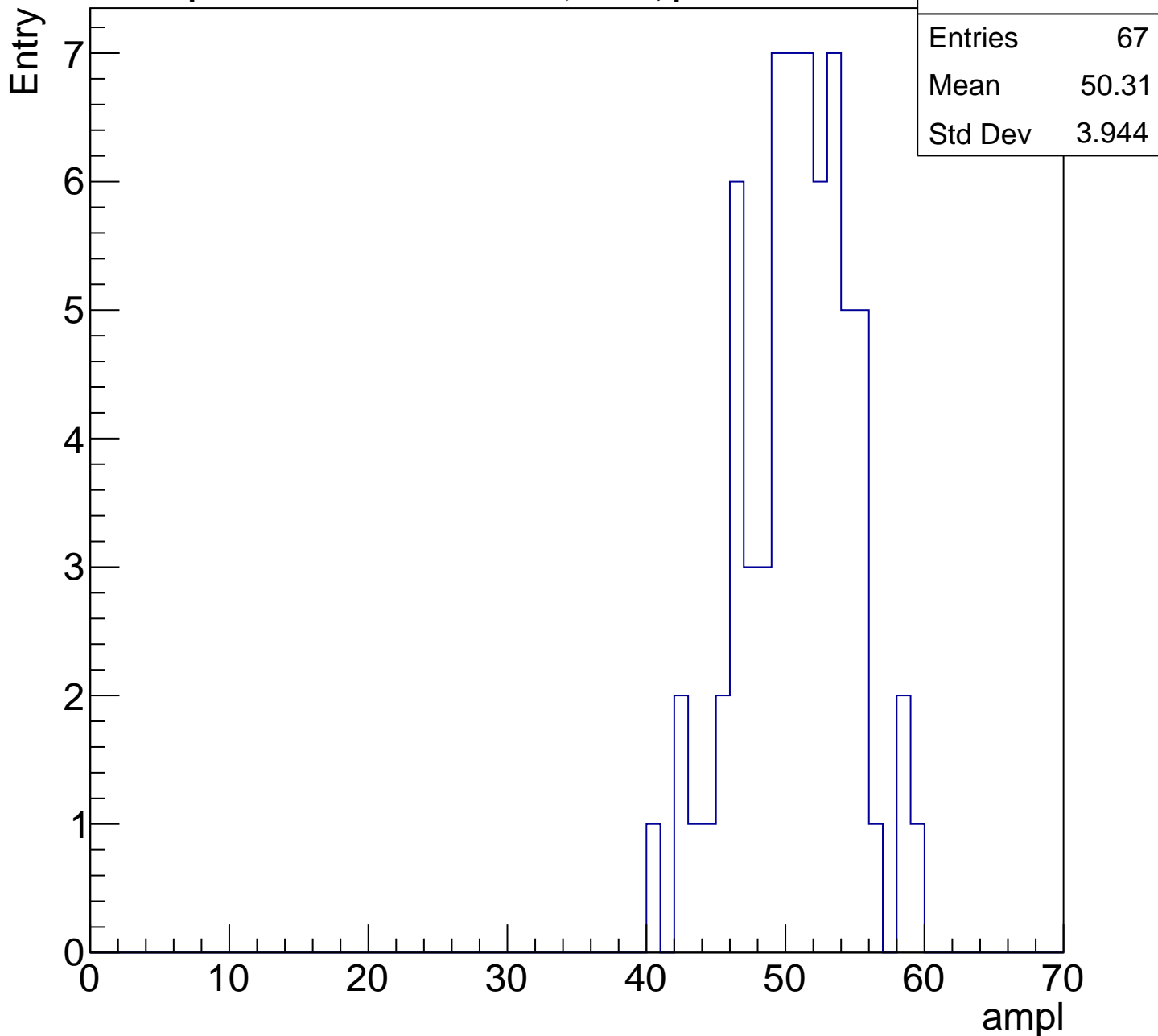
Entry

Entries	60
Mean	44.33
Std Dev	3.187



B0L002S, U4-ch83, adc3

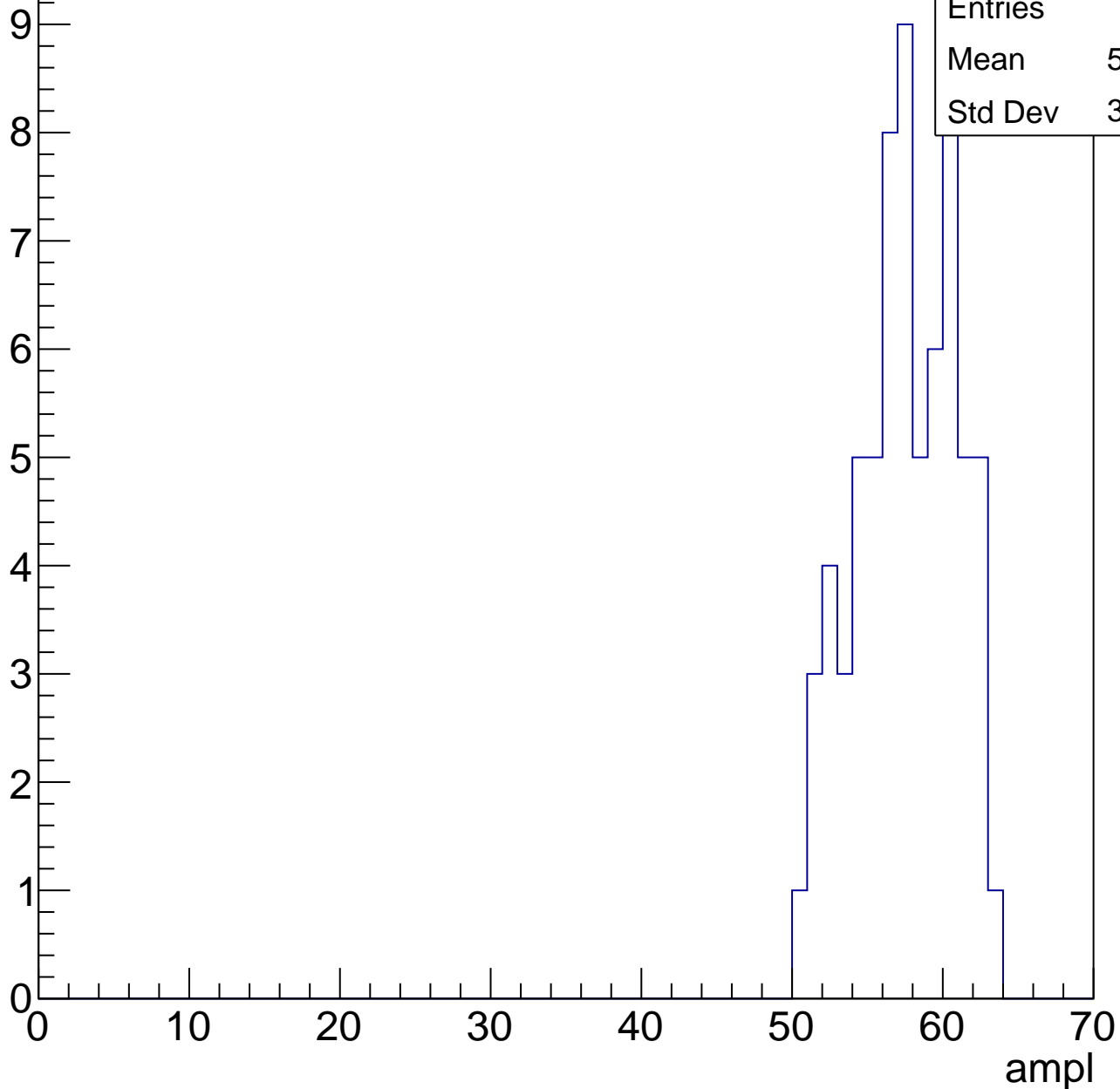
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



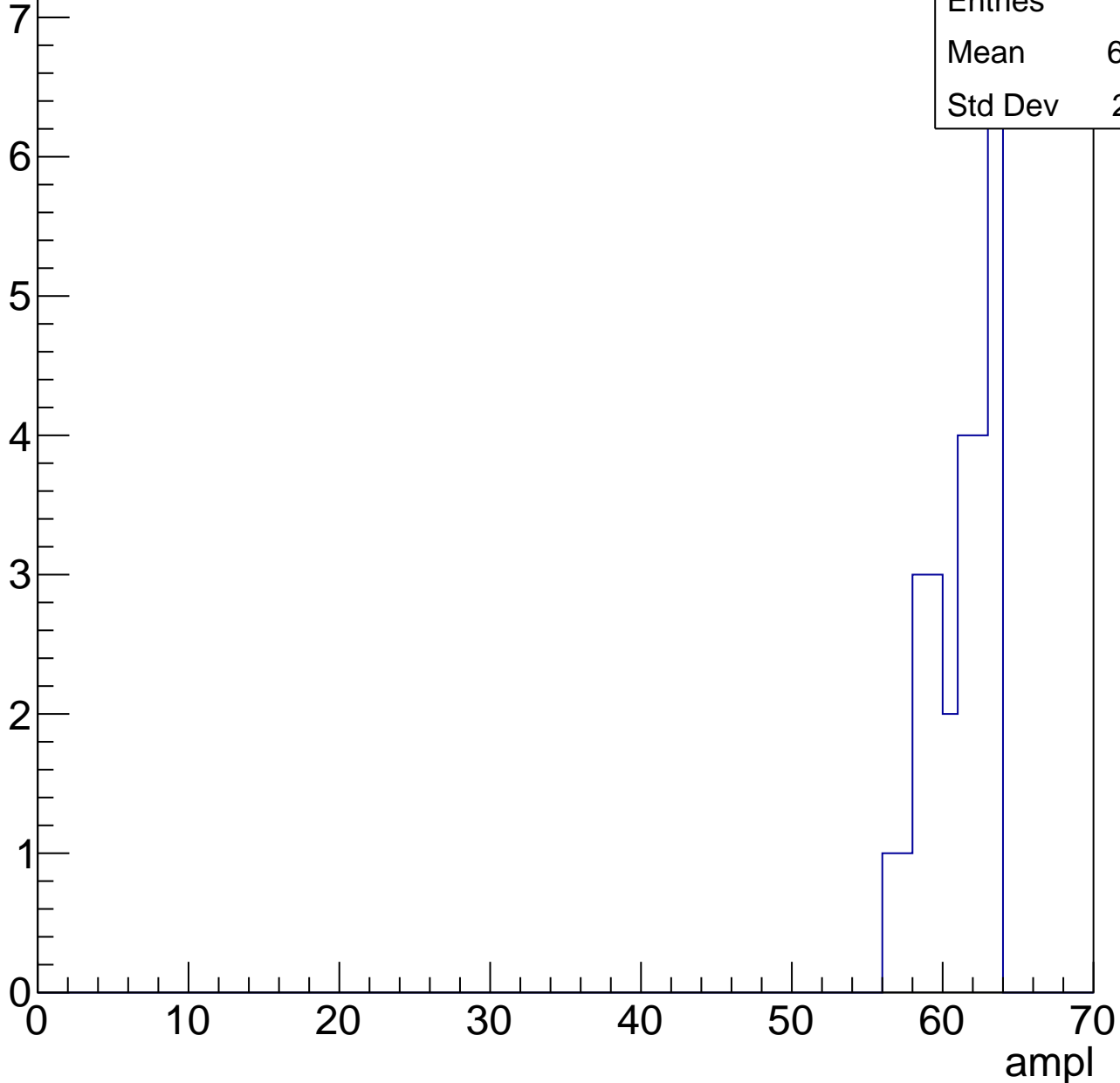
Entries	68
Mean	57.03
Std Dev	3.254

B0L002S, U4-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

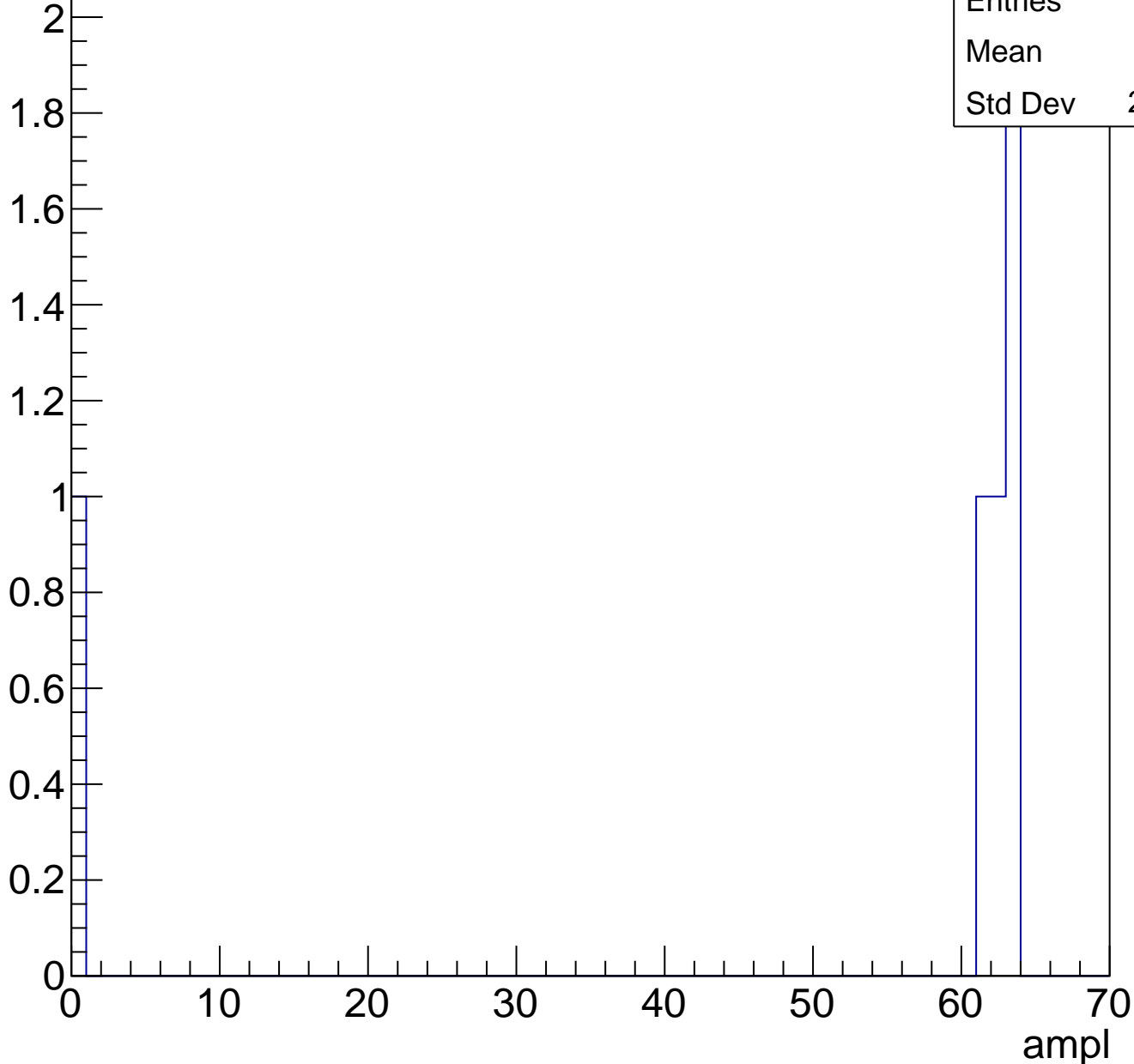
Entries	25
Mean	60.68
Std Dev	2.111



B0L002S, U4-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch84, adc0

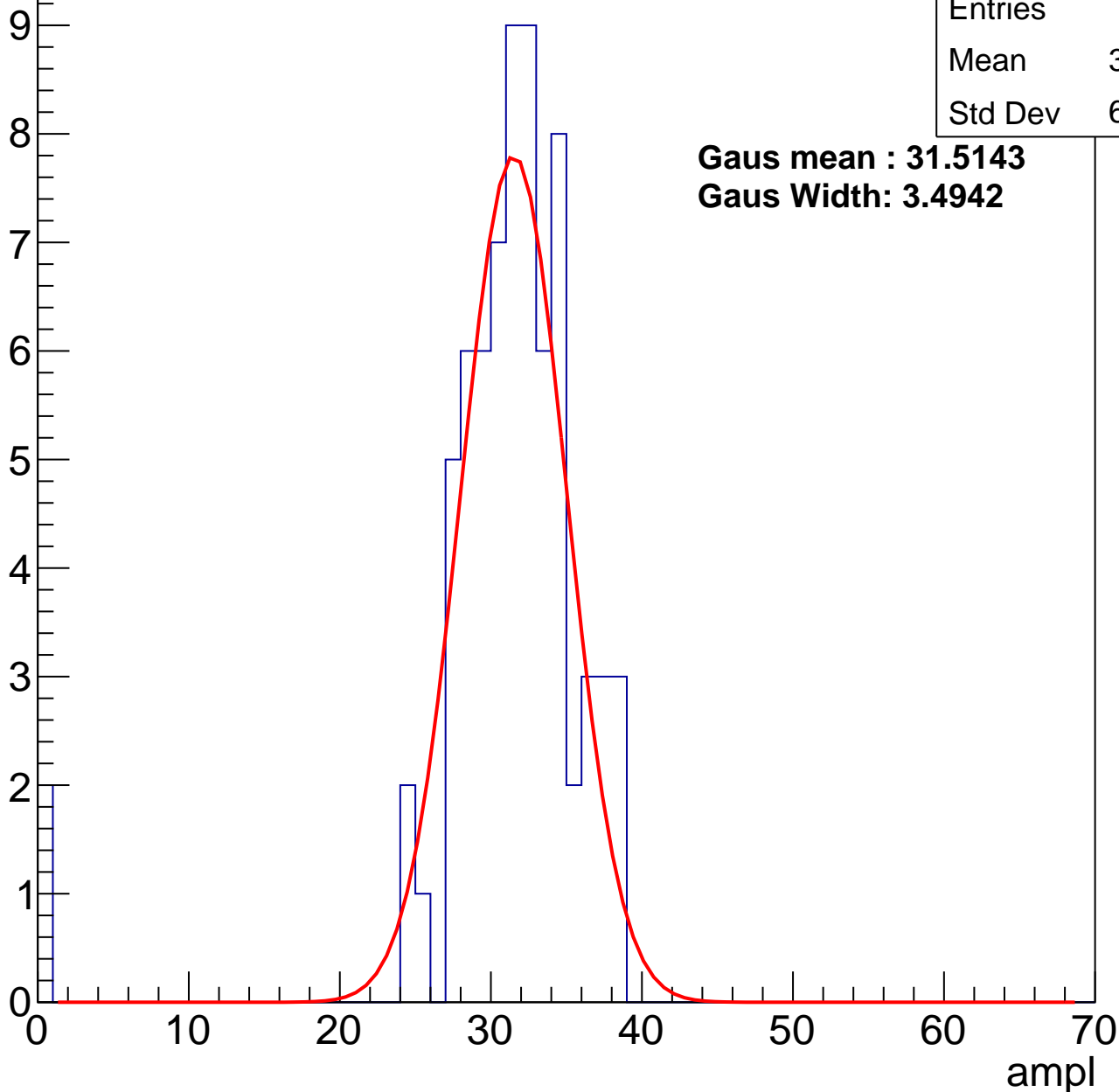
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.56
Std Dev	6.094

Gaus mean : 31.5143

Gaus Width: 3.4942



B0L002S, U4-ch84, adc1

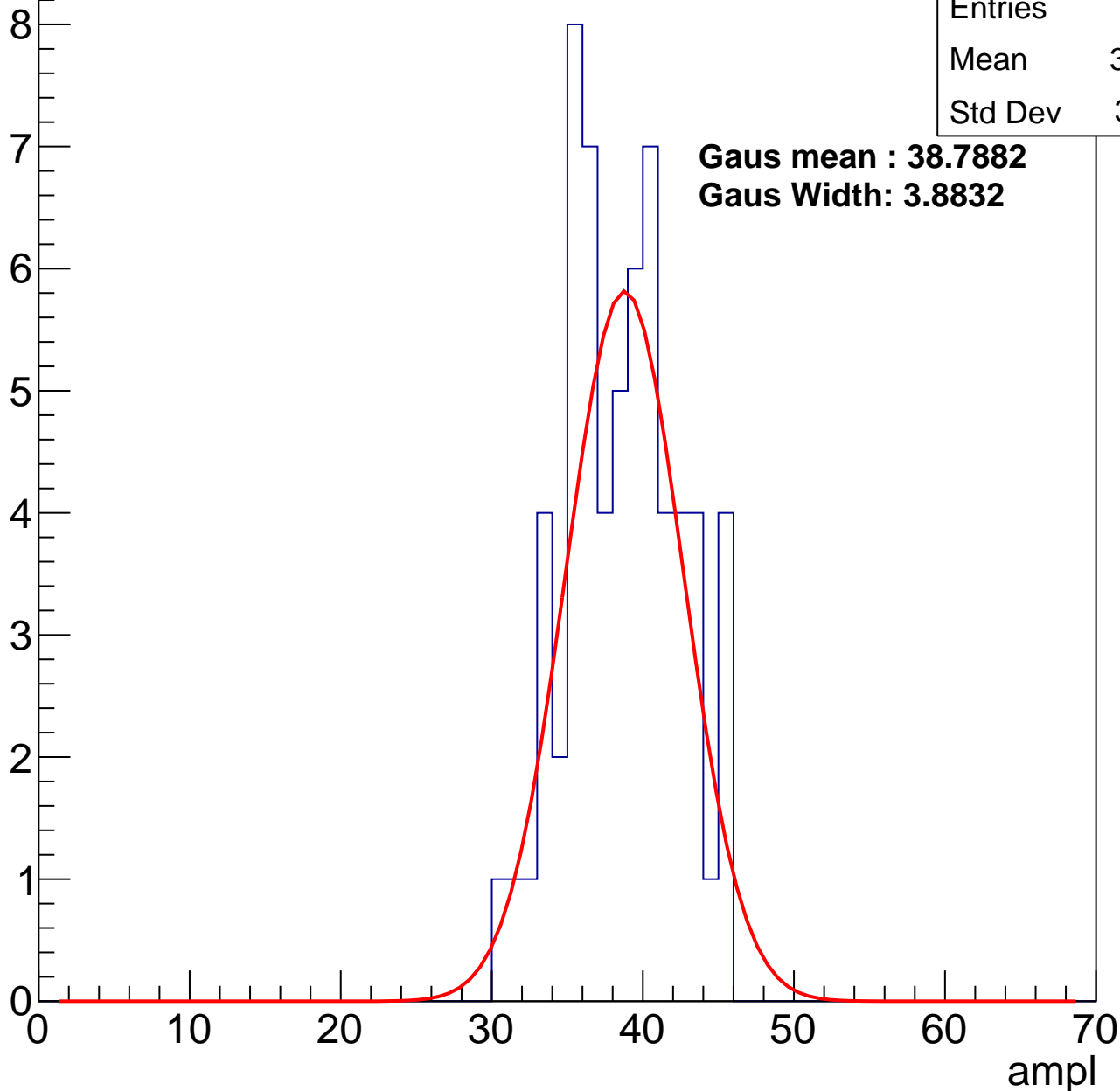
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.17
Std Dev	3.671

Gaus mean : 38.7882

Gaus Width: 3.8832



B0L002S, U4-ch84, adc2

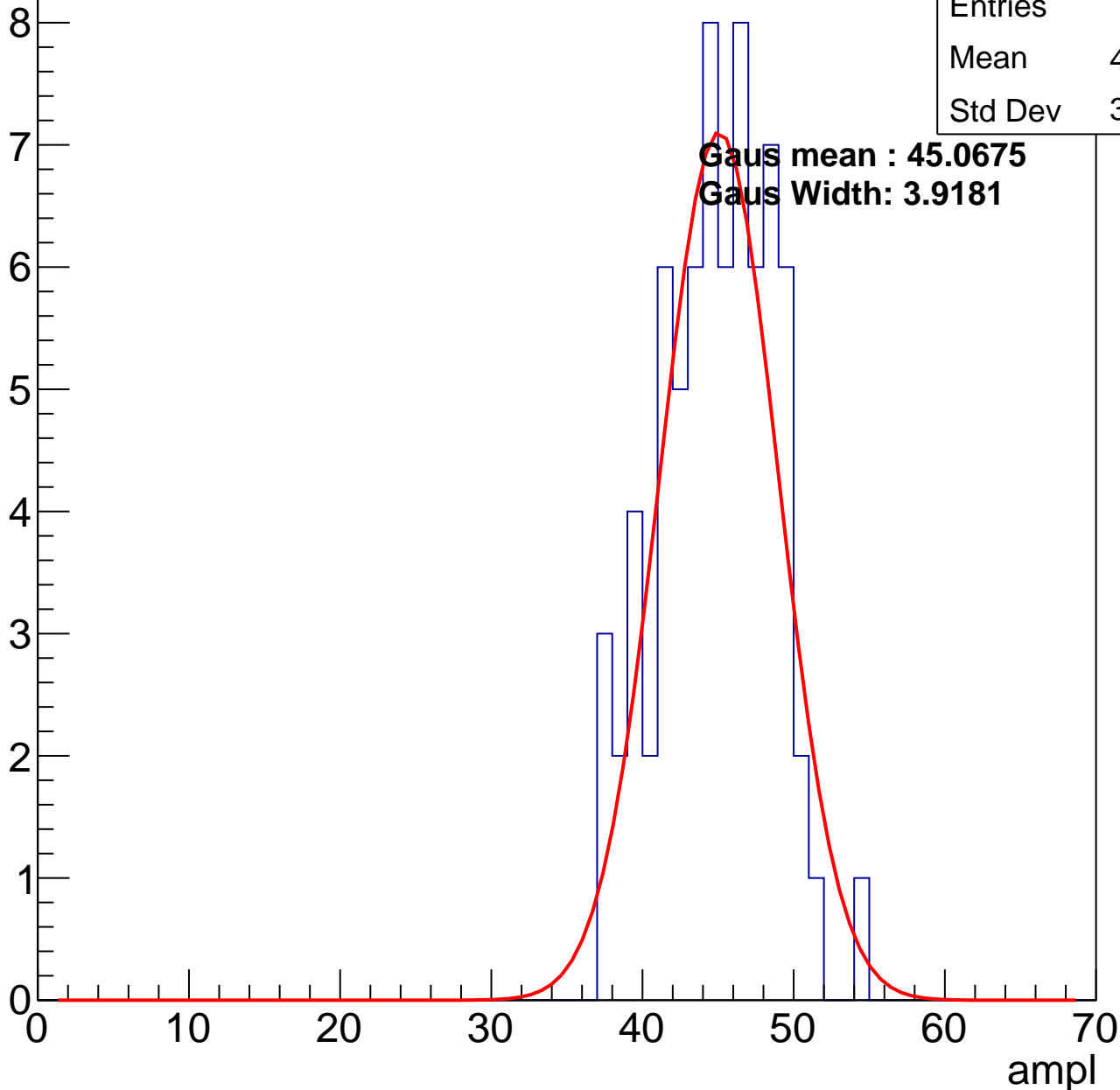
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.44
Std Dev	3.686

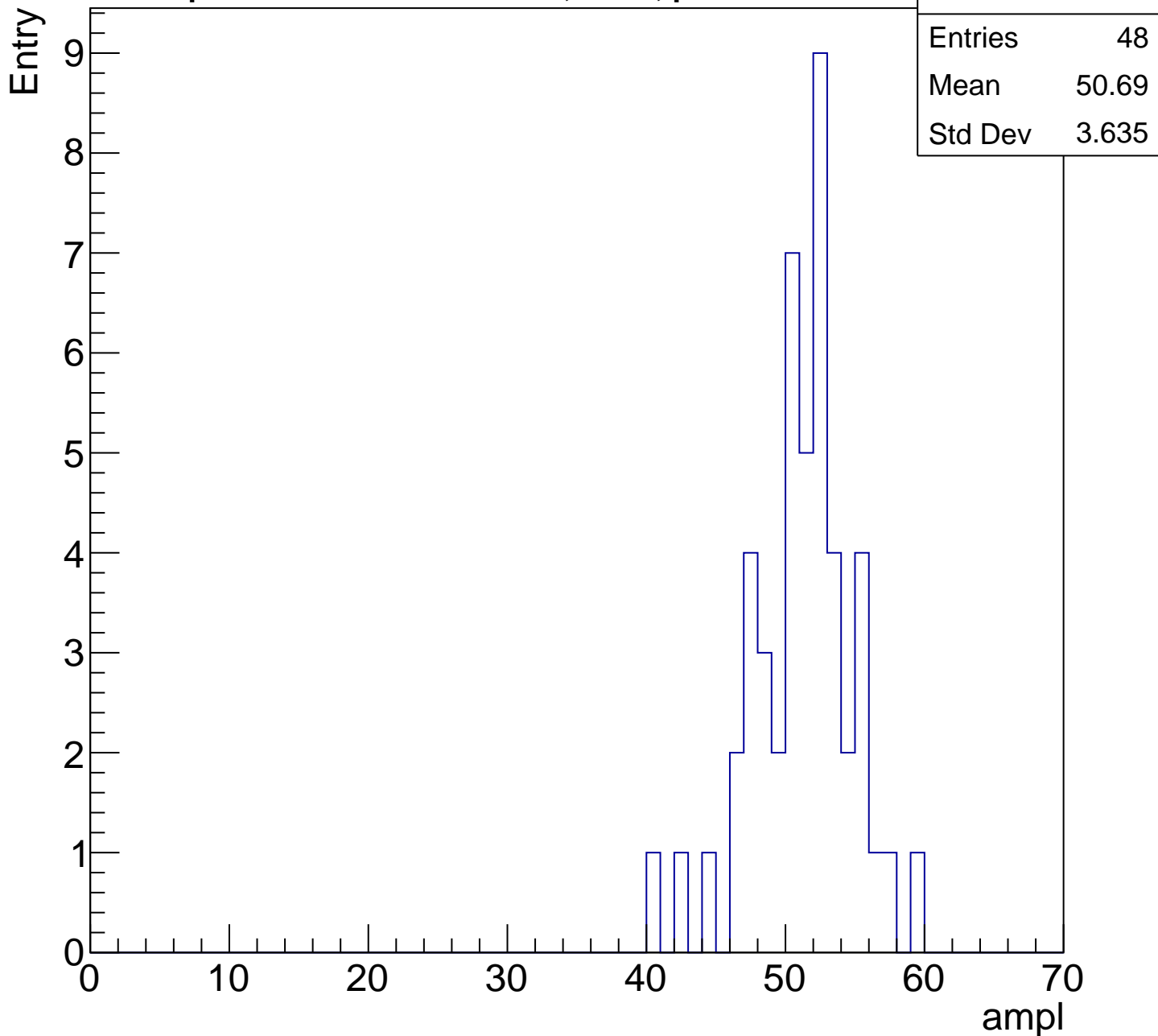
Gaus mean : 45.0675

Gaus Width: 3.9181



B0L002S, U4-ch84, adc3

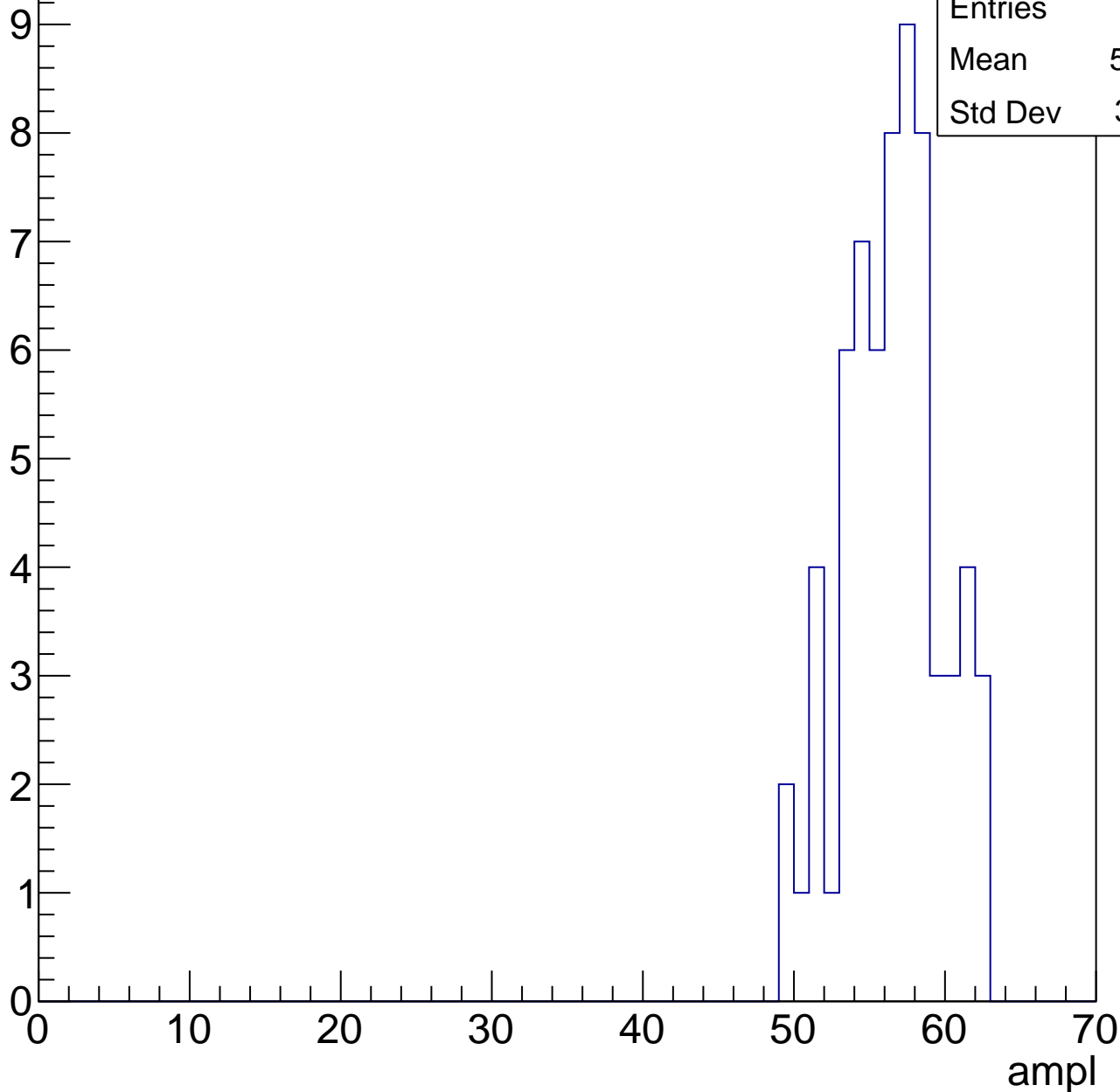
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

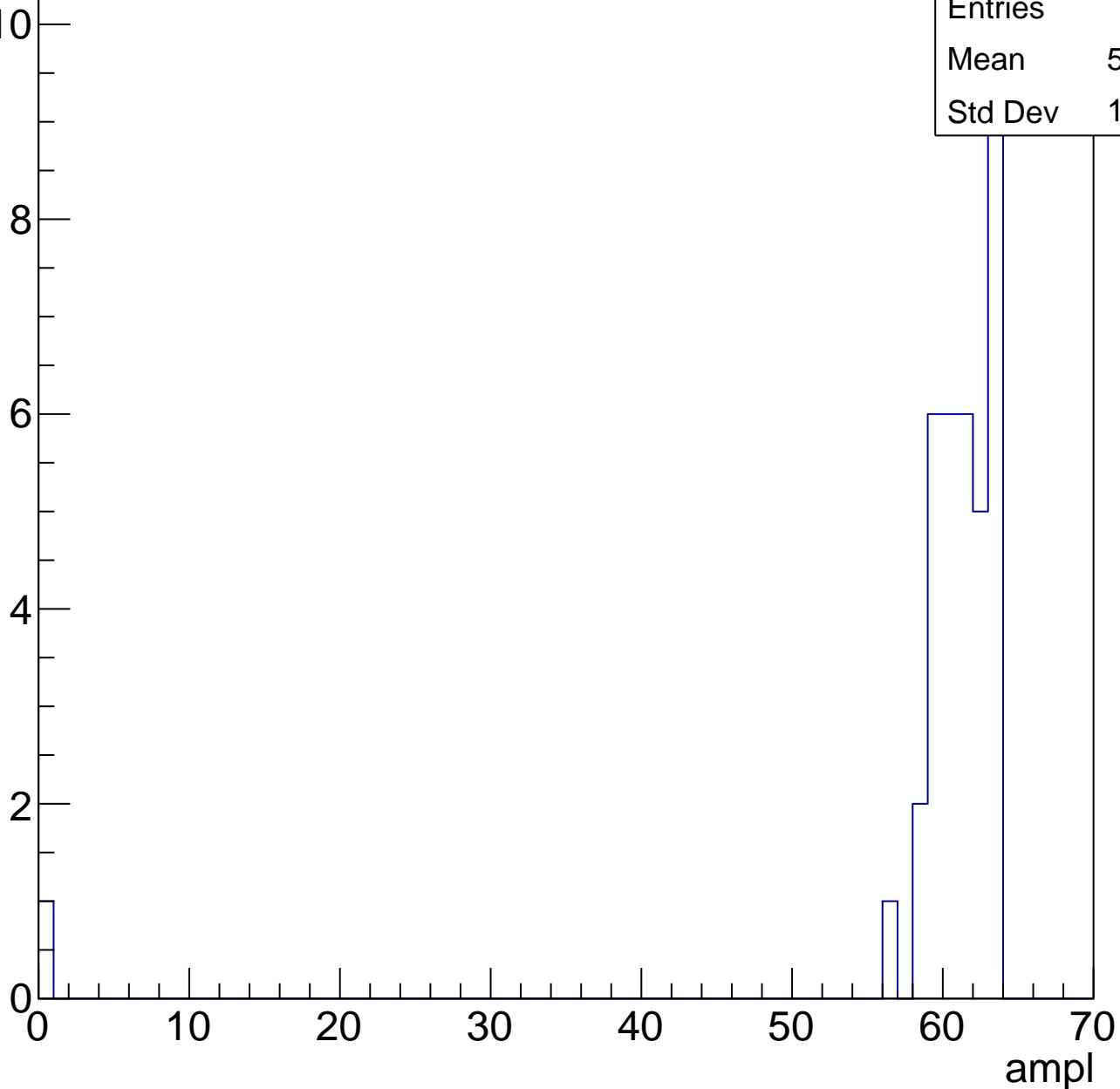


B0L002S, U4-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

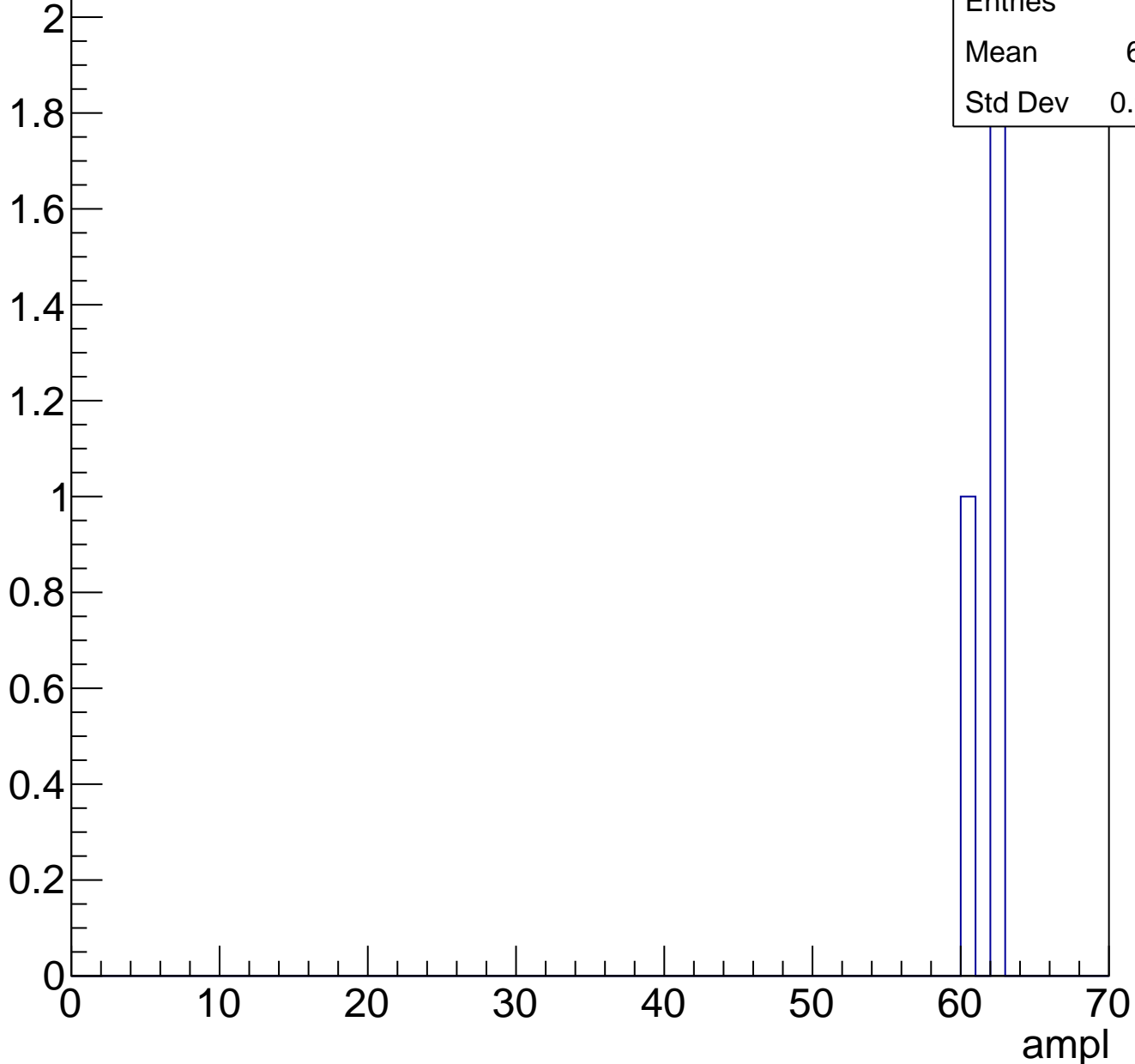
Entries	37
Mean	59.24
Std Dev	10.03



B0L002S, U4-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch85, adc0

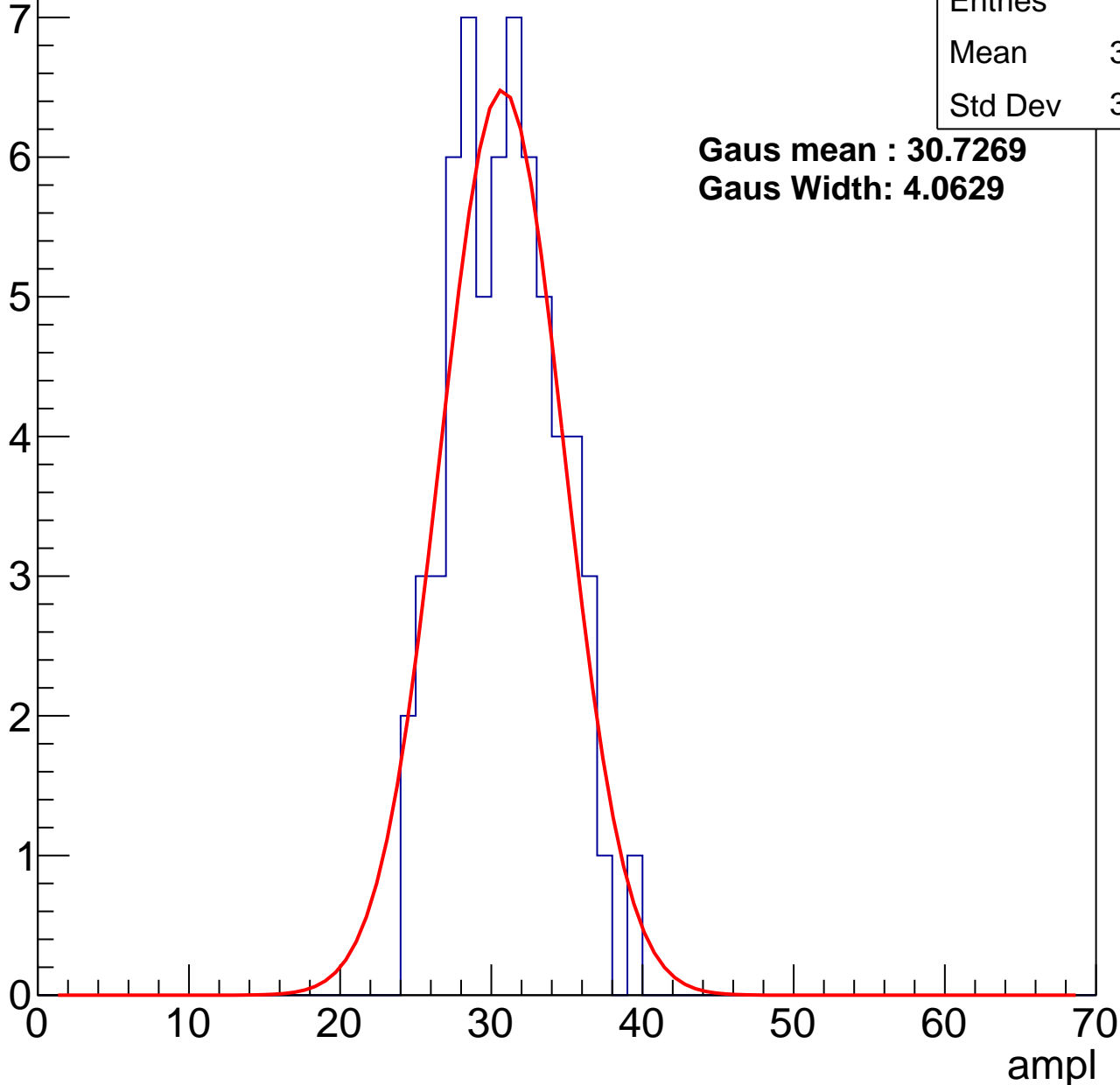
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.44
Std Dev	3.445

Gaus mean : 30.7269

Gaus Width: 4.0629



B0L002S, U4-ch85, adc1

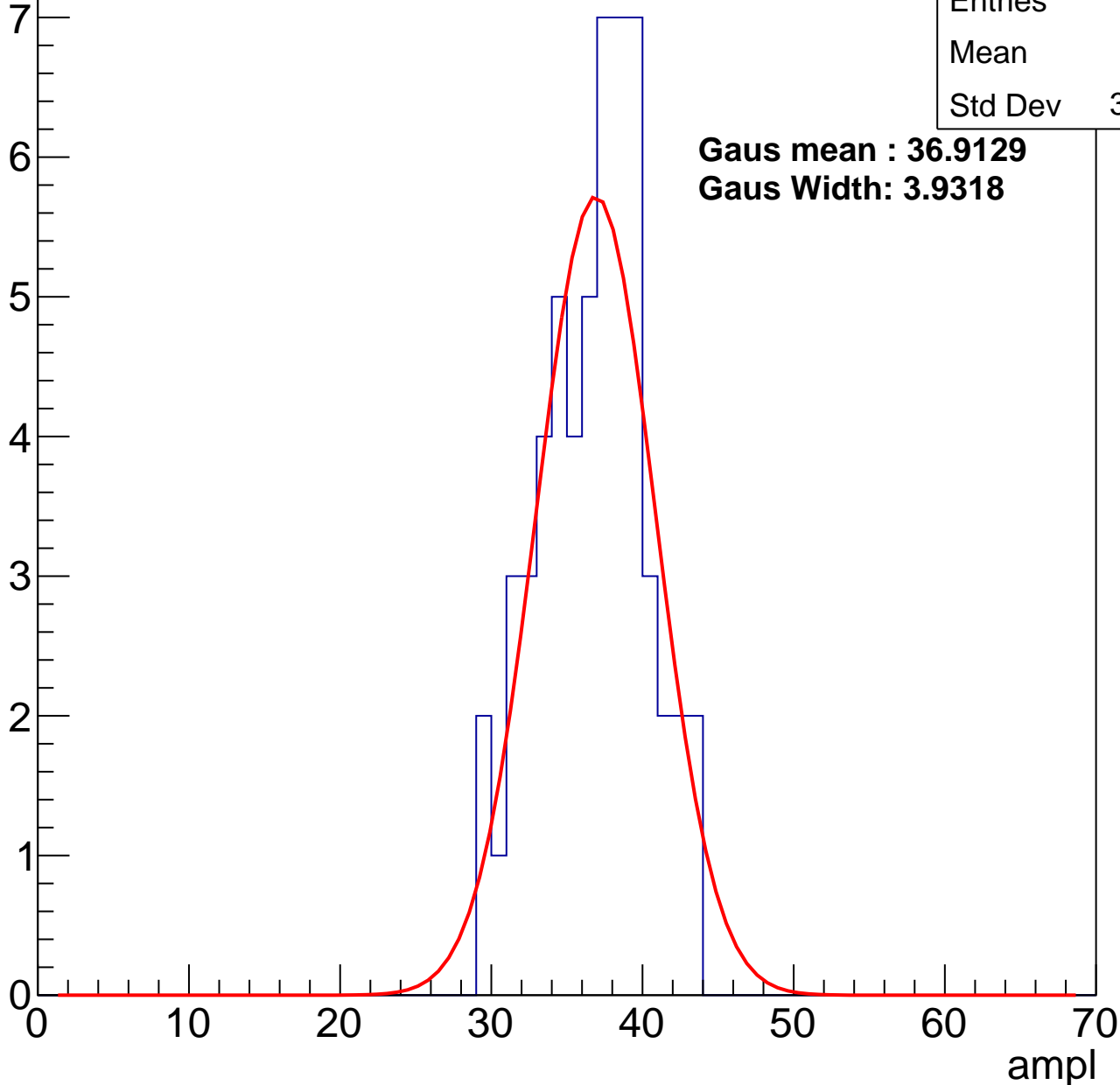
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.3
Std Dev	3.449

Gaus mean : 36.9129

Gaus Width: 3.9318



B0L002S, U4-ch85, adc2

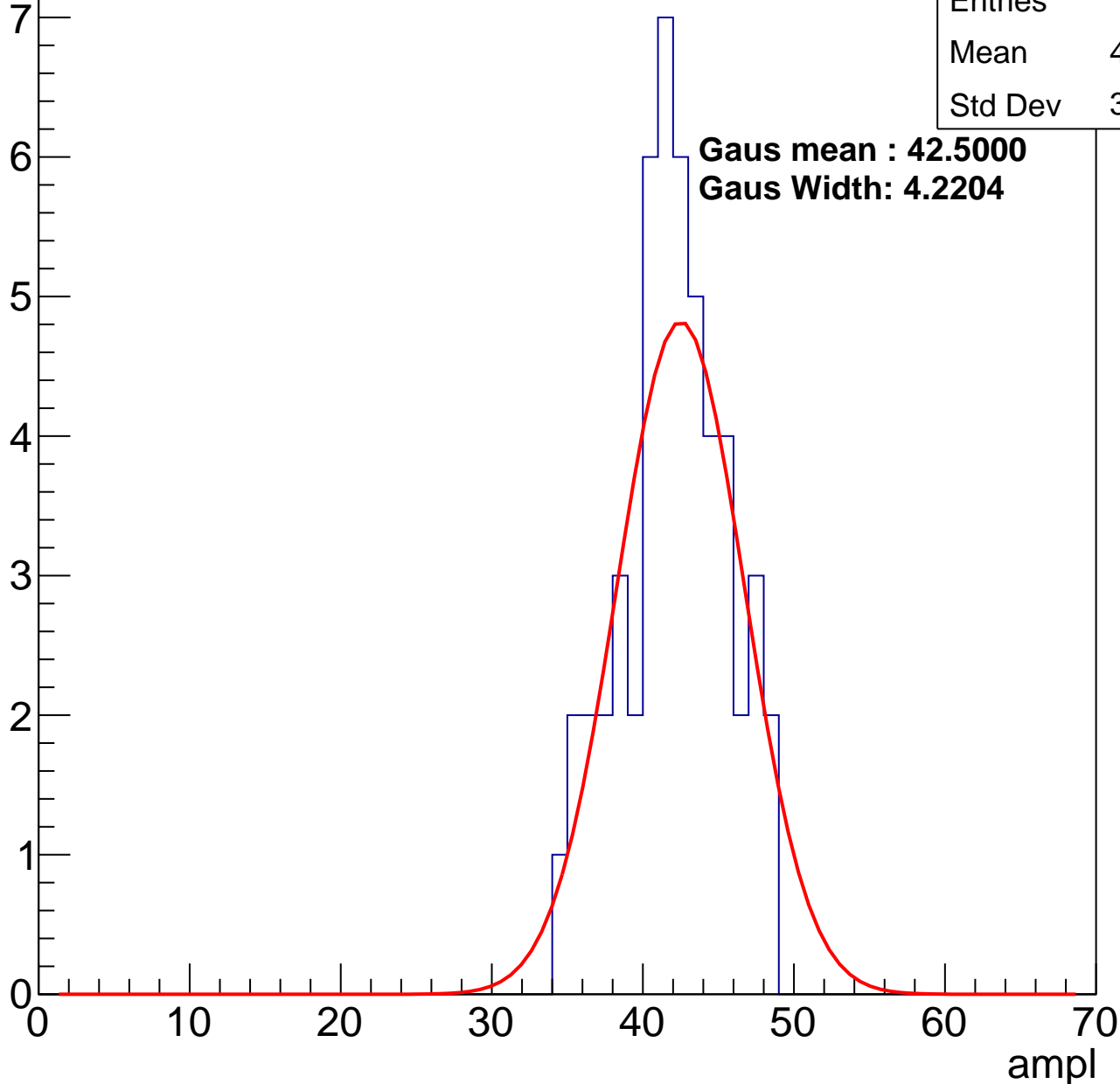
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	41.59
Std Dev	3.454

Gaus mean : 42.5000

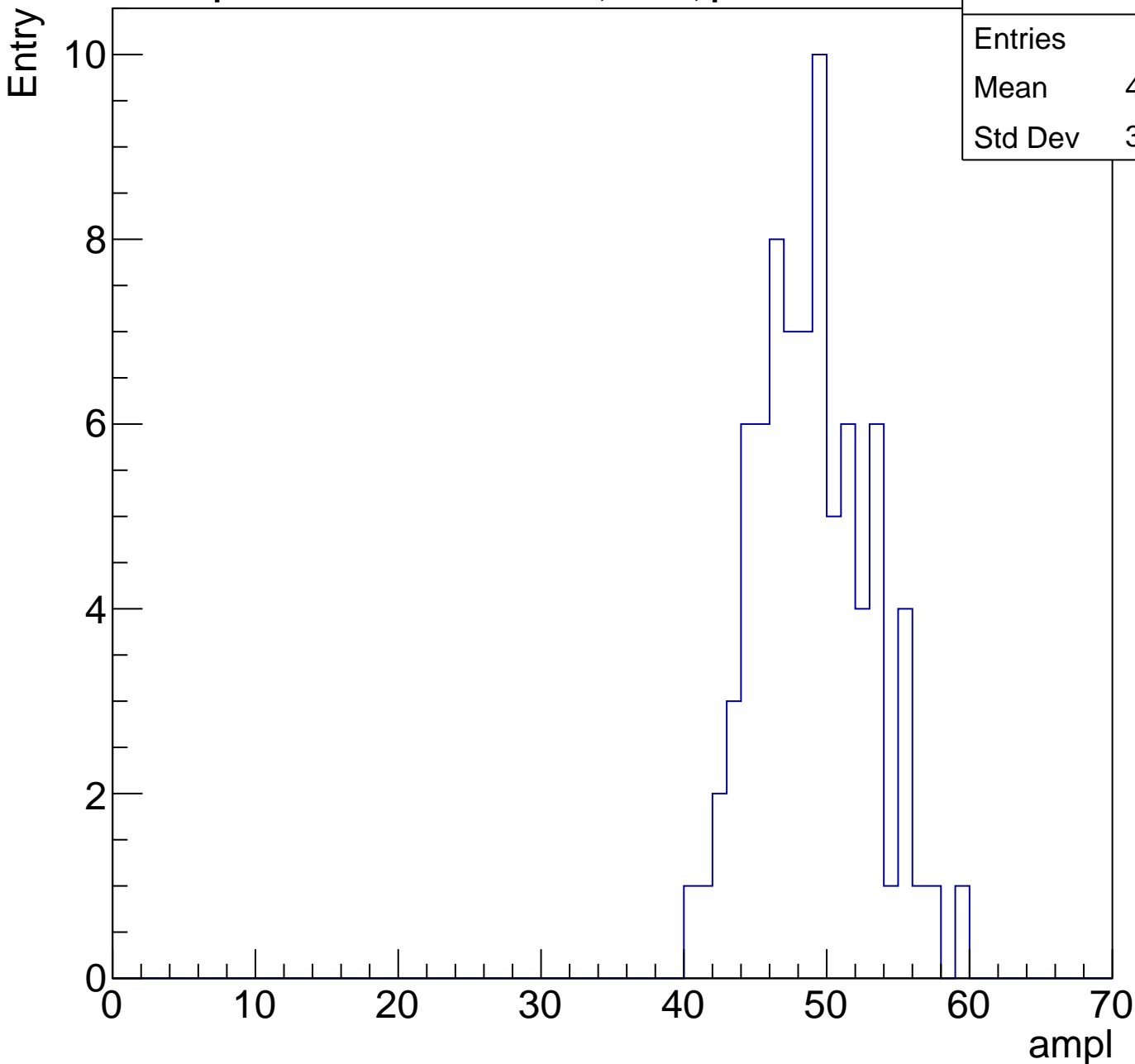
Gaus Width: 4.2204



B0L002S, U4-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	48.49
Std Dev	3.915

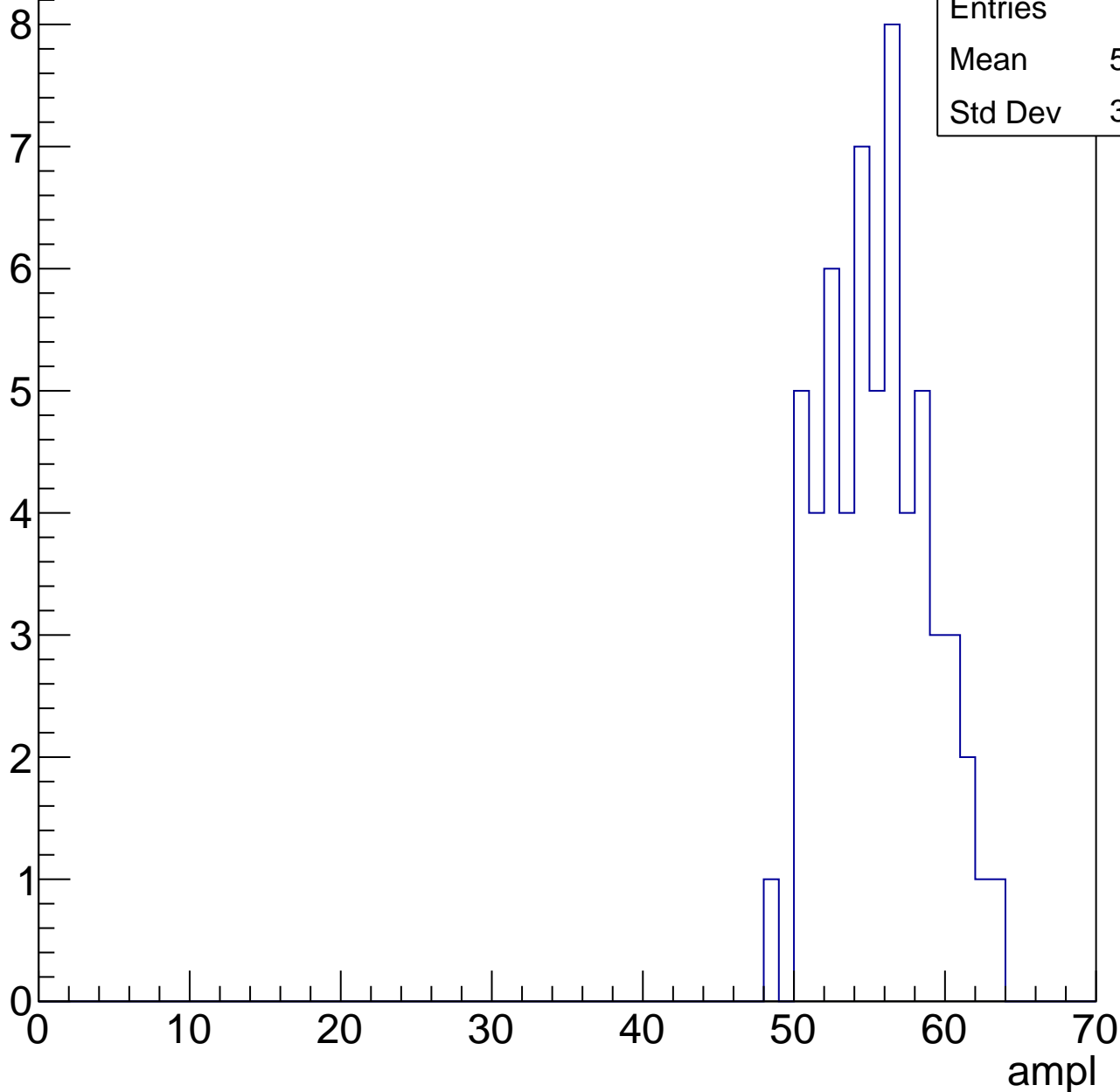


B0L002S, U4-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.07
Std Dev	3.424



B0L002S, U4-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

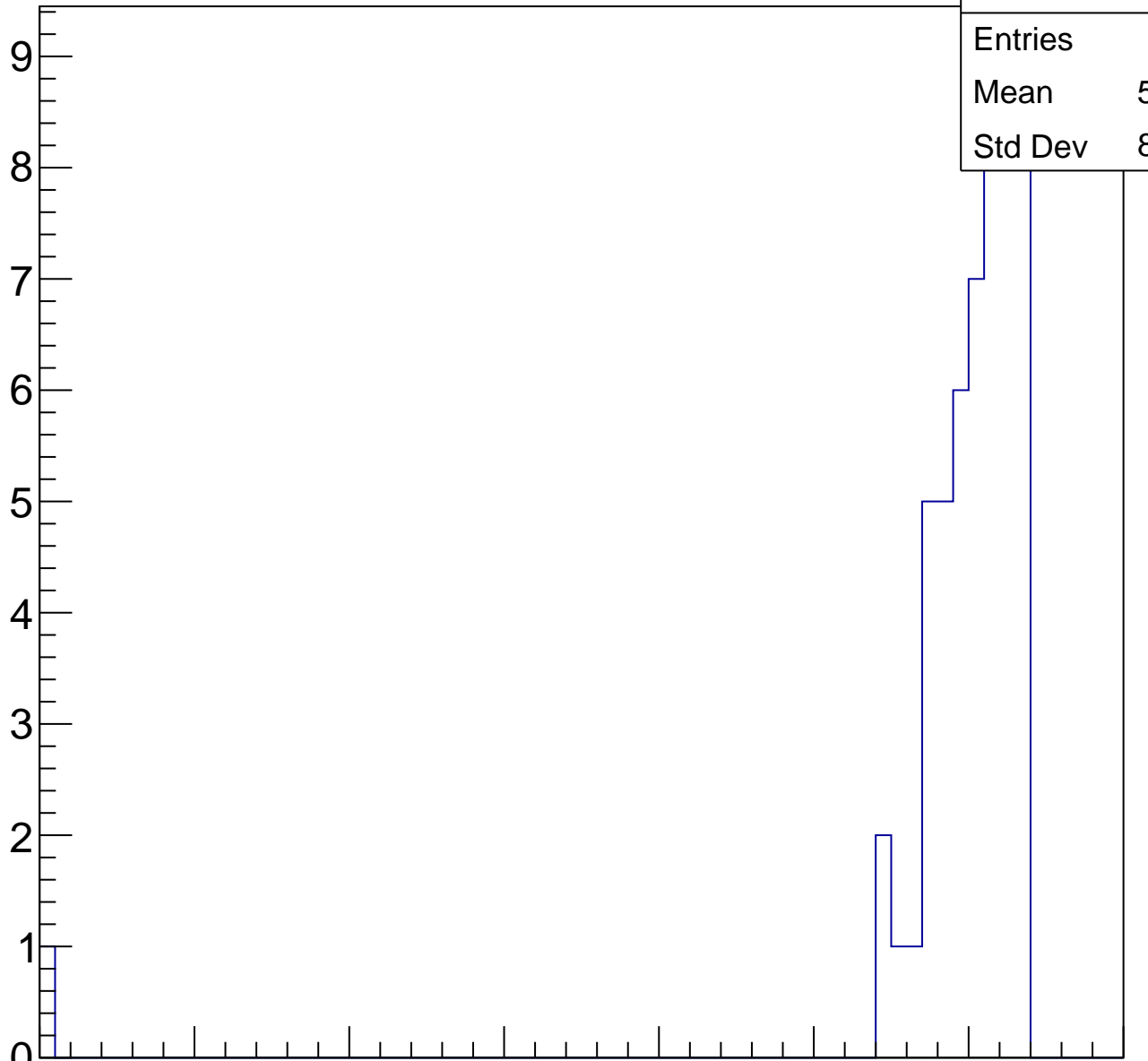
Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.83
Std Dev	8.498

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	21
Std Dev	0

B0L002S, U4-ch86, adc0

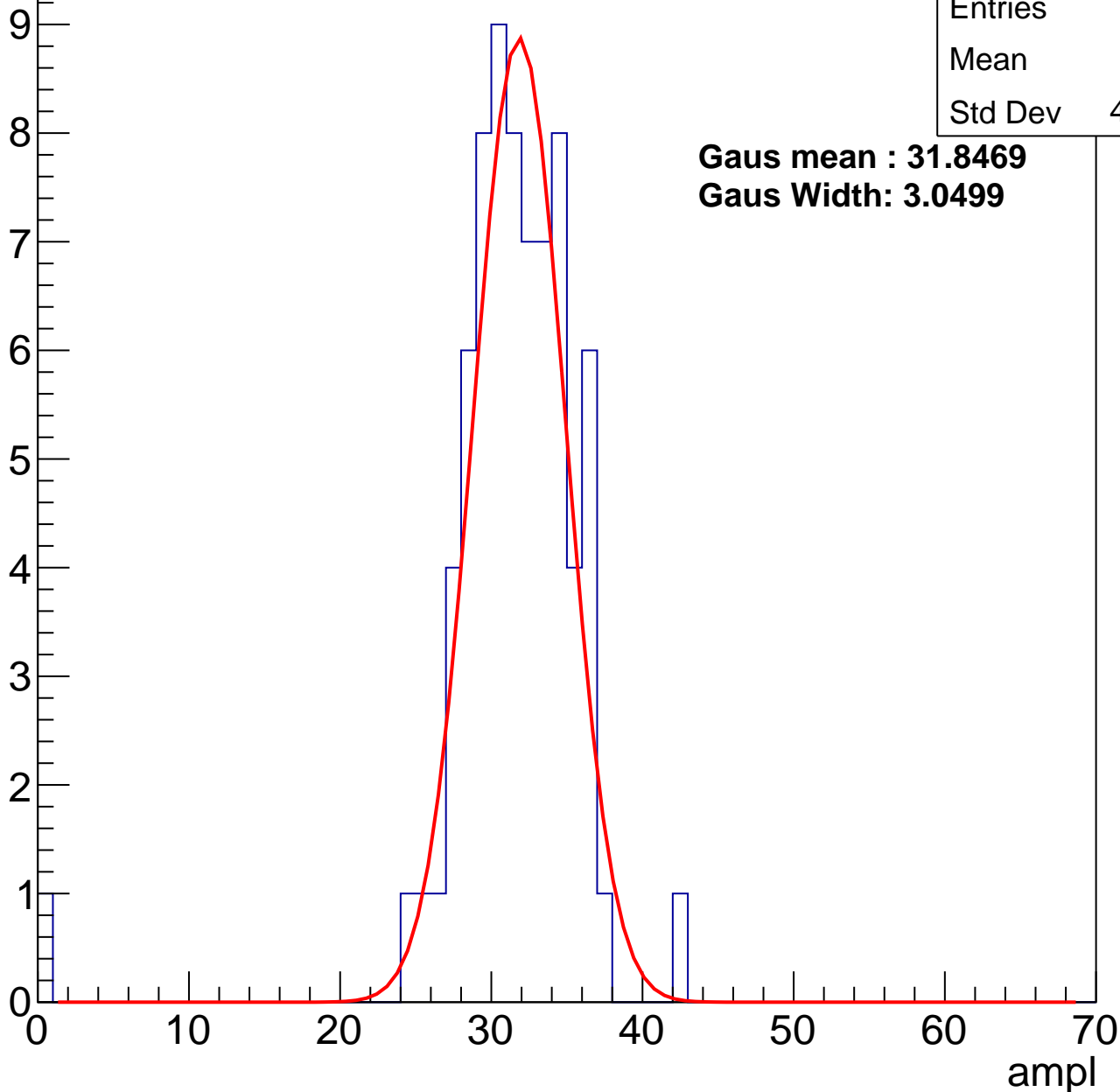
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	31
Std Dev	4.837

Gaus mean : 31.8469

Gaus Width: 3.0499



B0L002S, U4-ch86, adc1

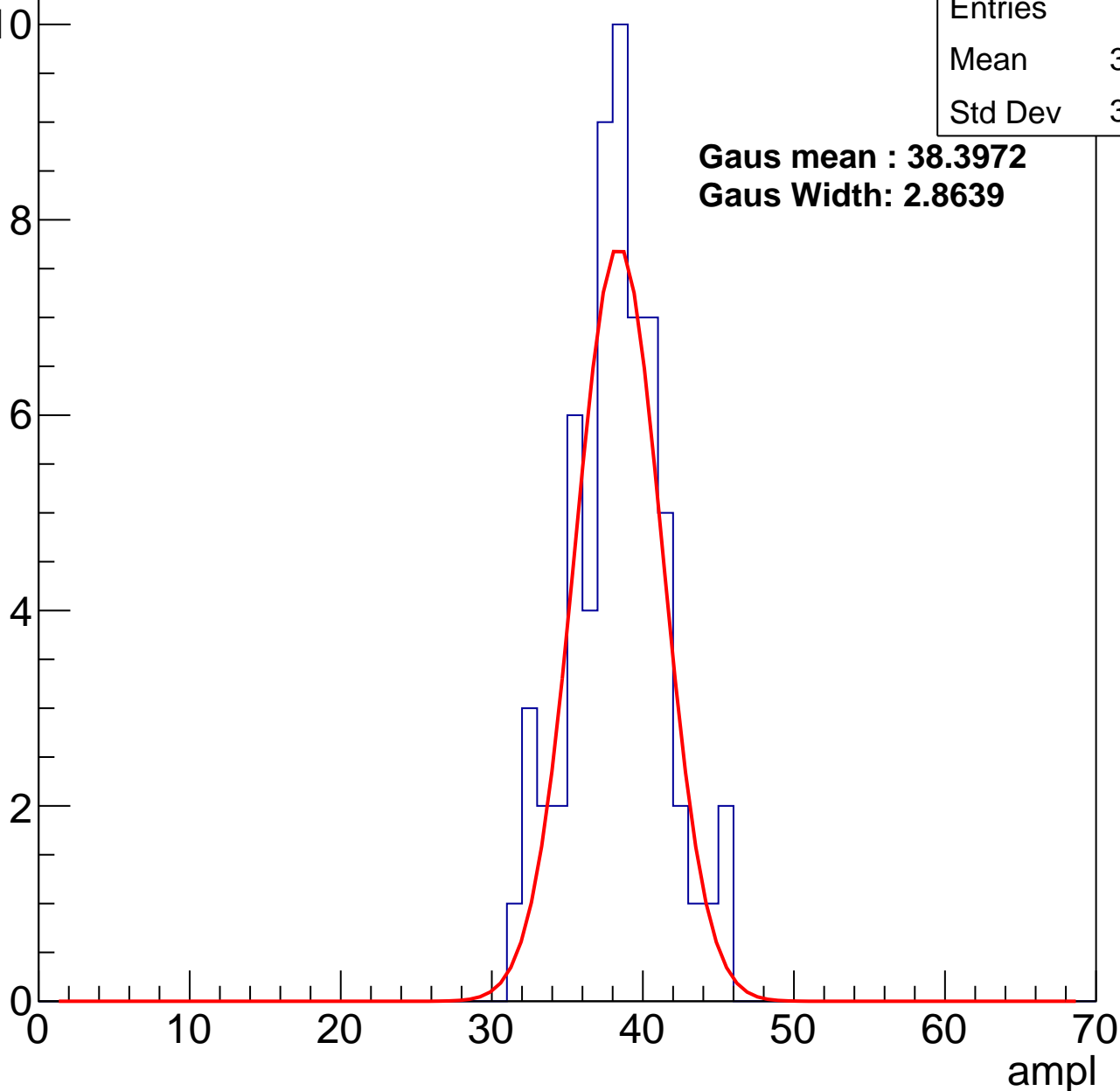
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	37.85
Std Dev	3.079

Gaus mean : 38.3972

Gaus Width: 2.8639



B0L002S, U4-ch86, adc2

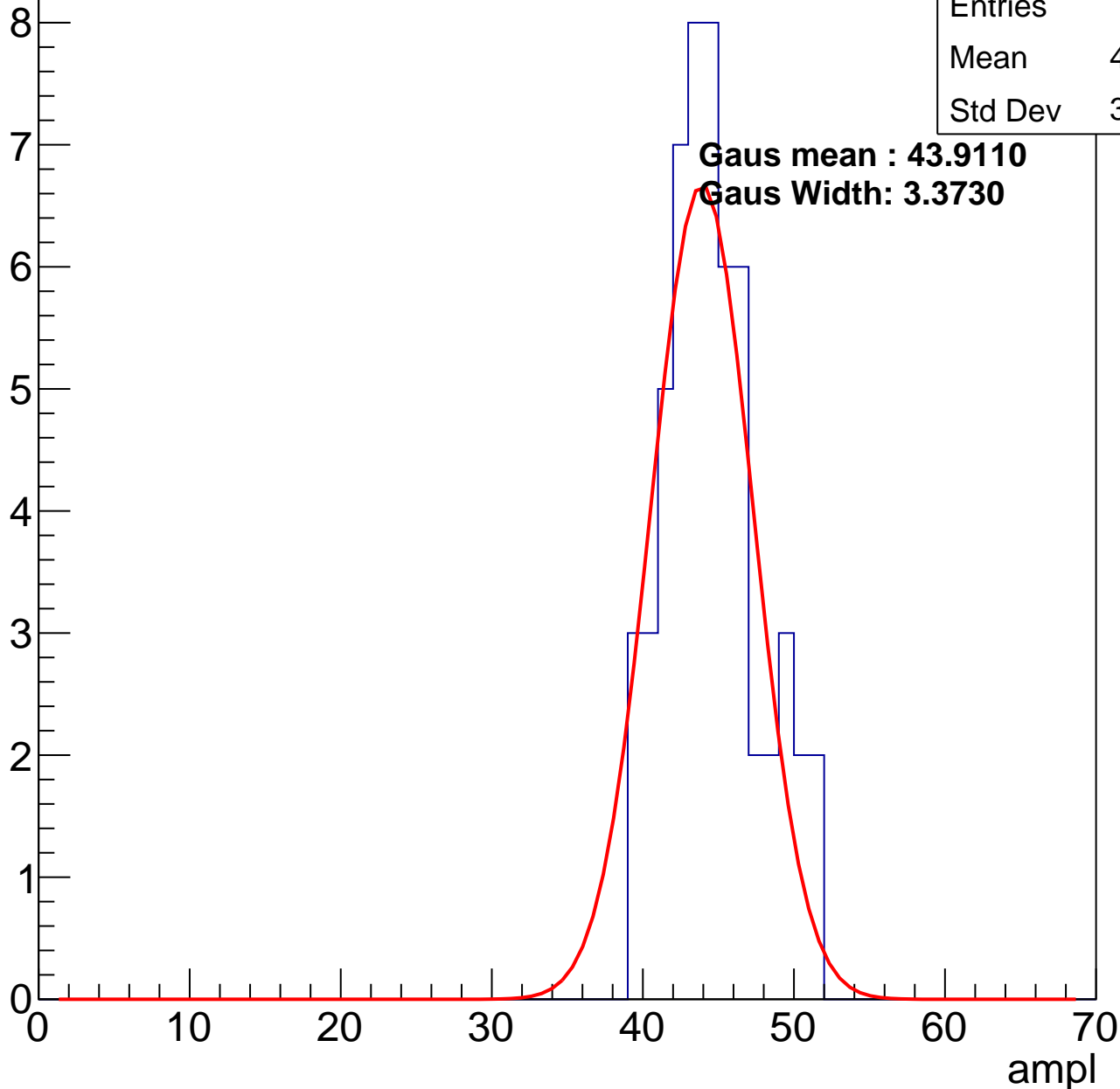
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.16
Std Dev	3.042

Gaus mean : 43.9110

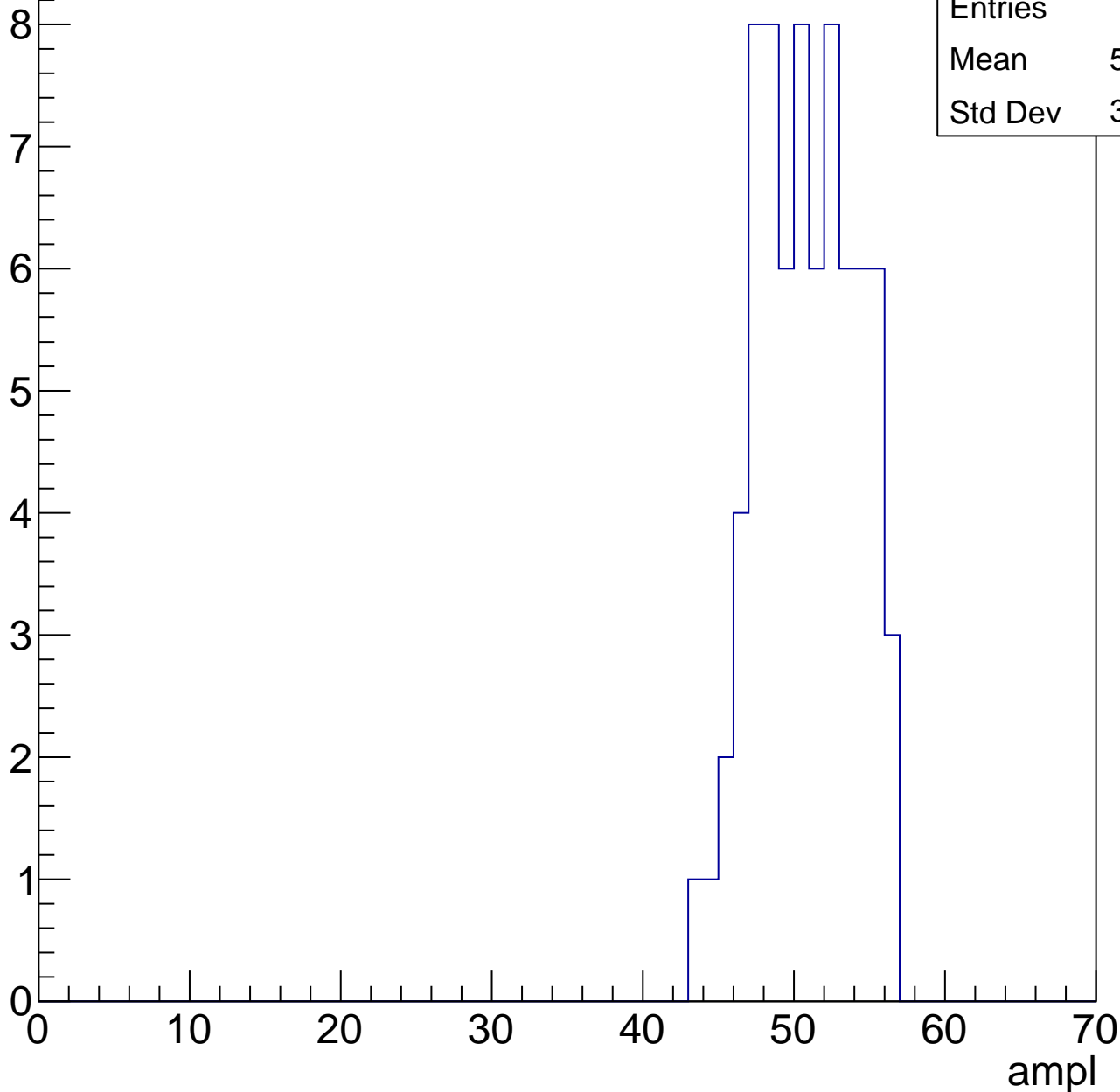
Gaus Width: 3.3730



B0L002S, U4-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

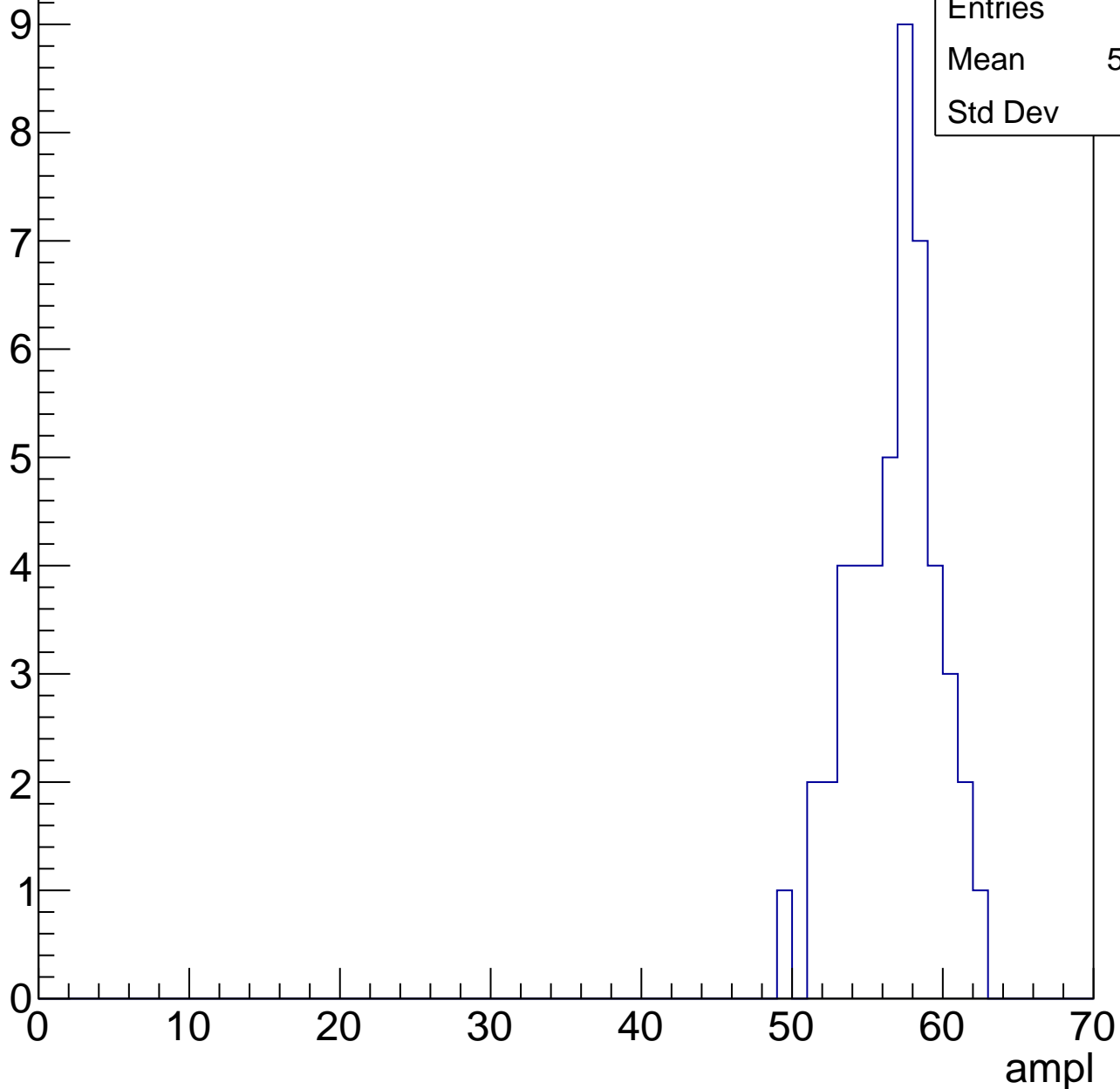
Entry



B0L002S, U4-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



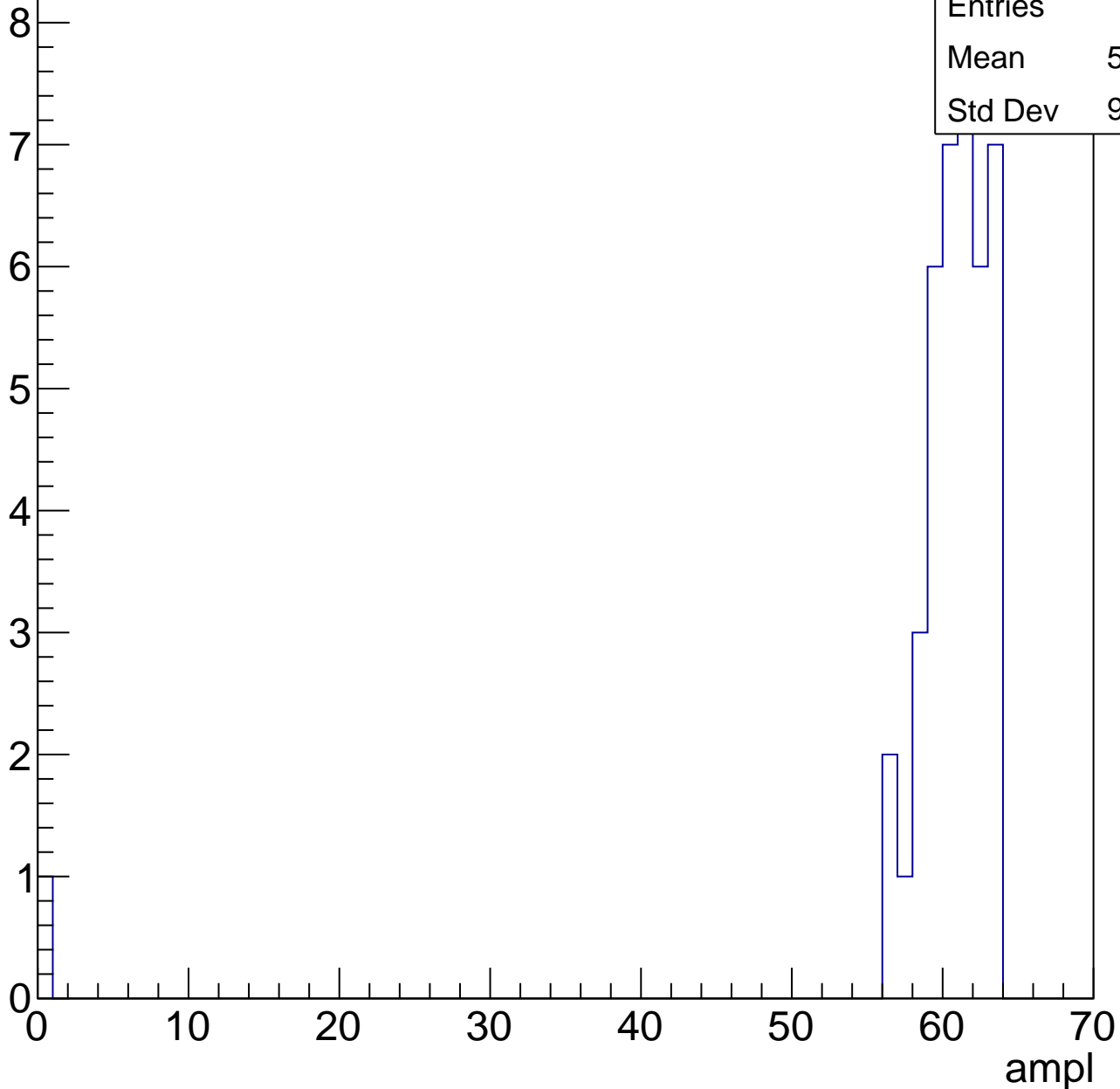
Entries	48
Mean	56.29
Std Dev	2.85

B0L002S, U4-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

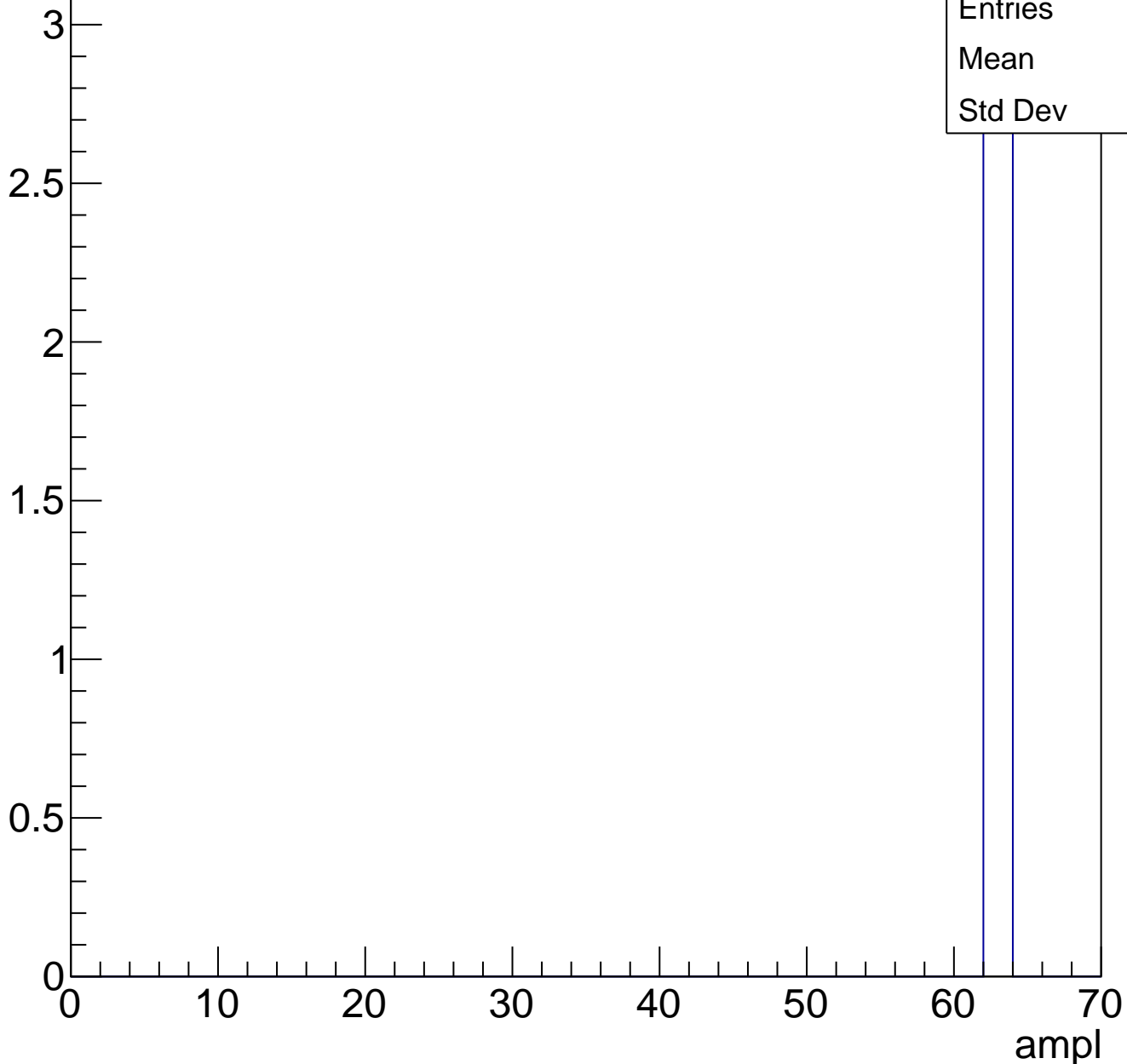
Entries	41
Mean	58.98
Std Dev	9.514



B0L002S, U4-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	62.5
Std Dev	0.5

B0L002S, U4-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch87, adc0

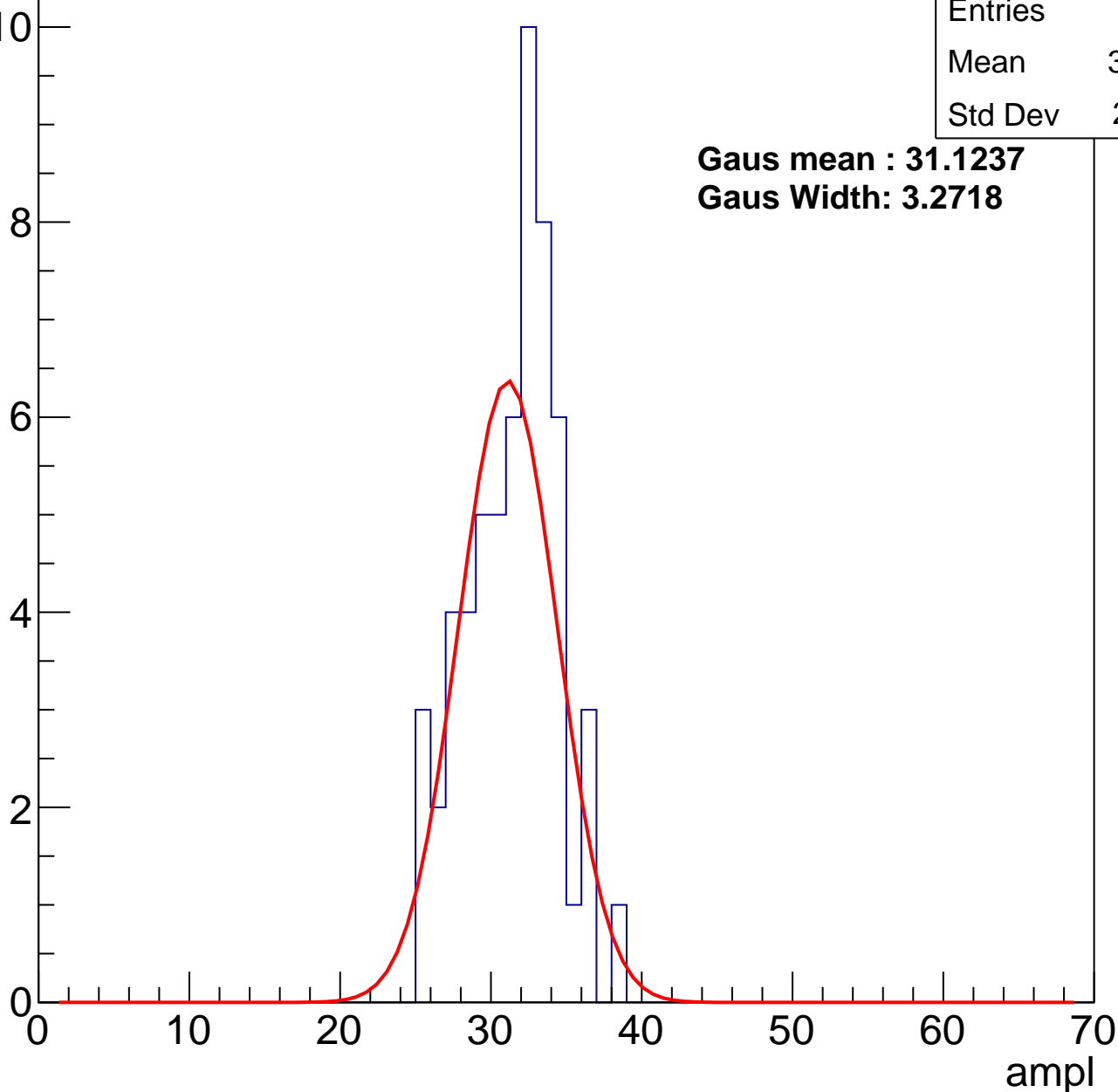
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.98
Std Dev	2.991

Gaus mean : 31.1237

Gaus Width: 3.2718



B0L002S, U4-ch87, adc1

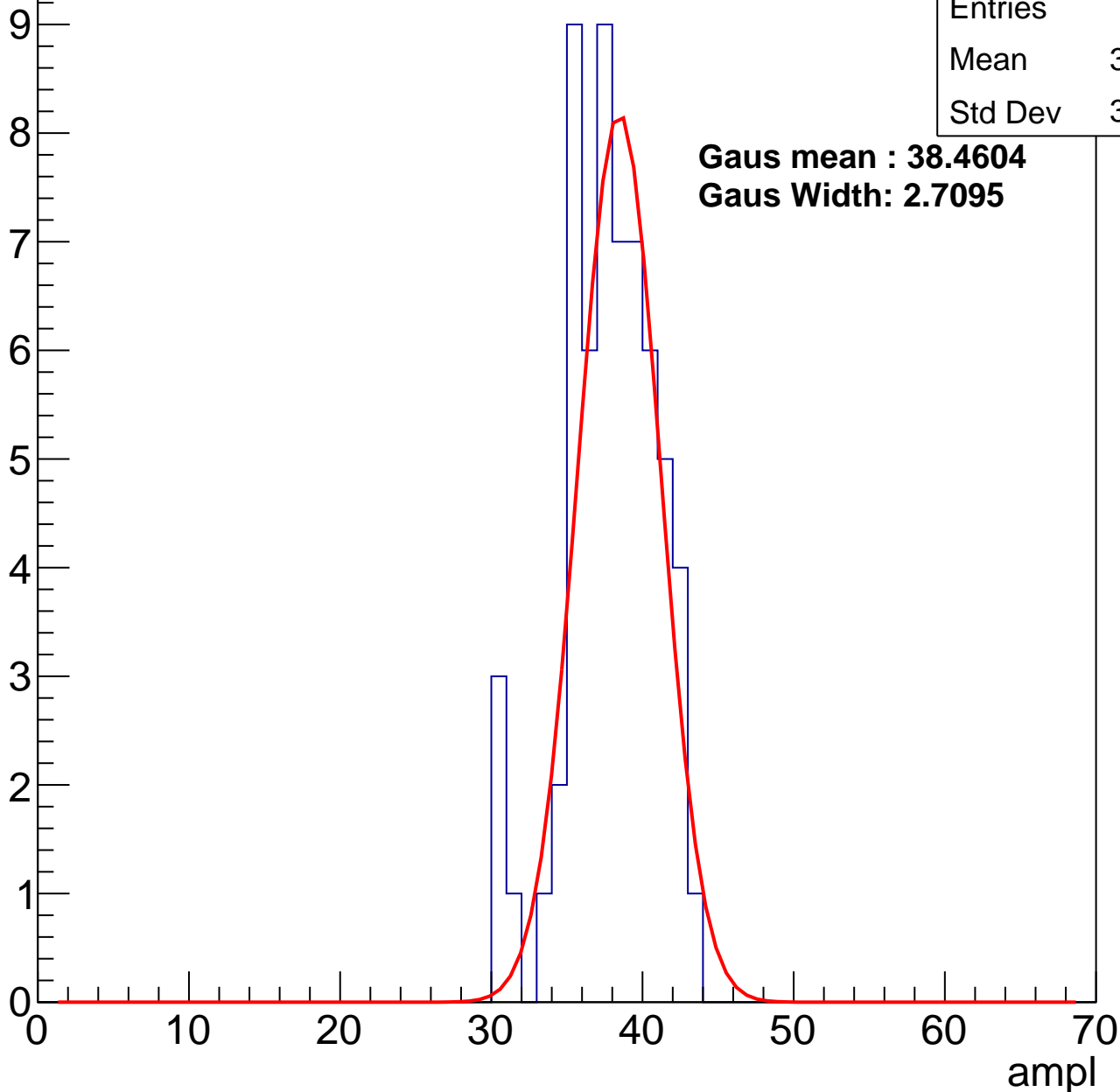
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.39
Std Dev	3.015

Gaus mean : 38.4604

Gaus Width: 2.7095



B0L002S, U4-ch87, adc2

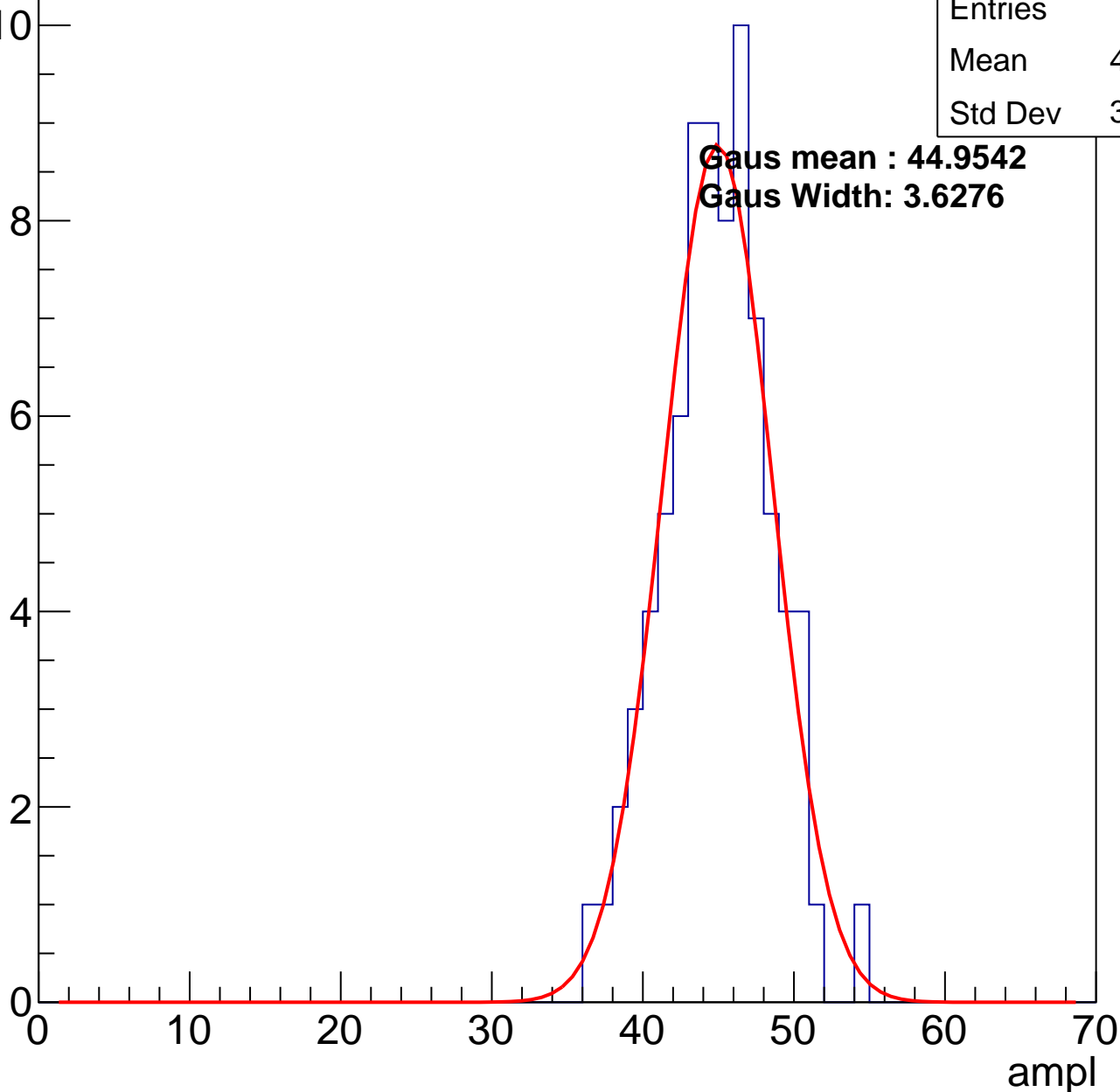
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	44.45
Std Dev	3.482

Gaus mean : 44.9542

Gaus Width: 3.6276



B0L002S, U4-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

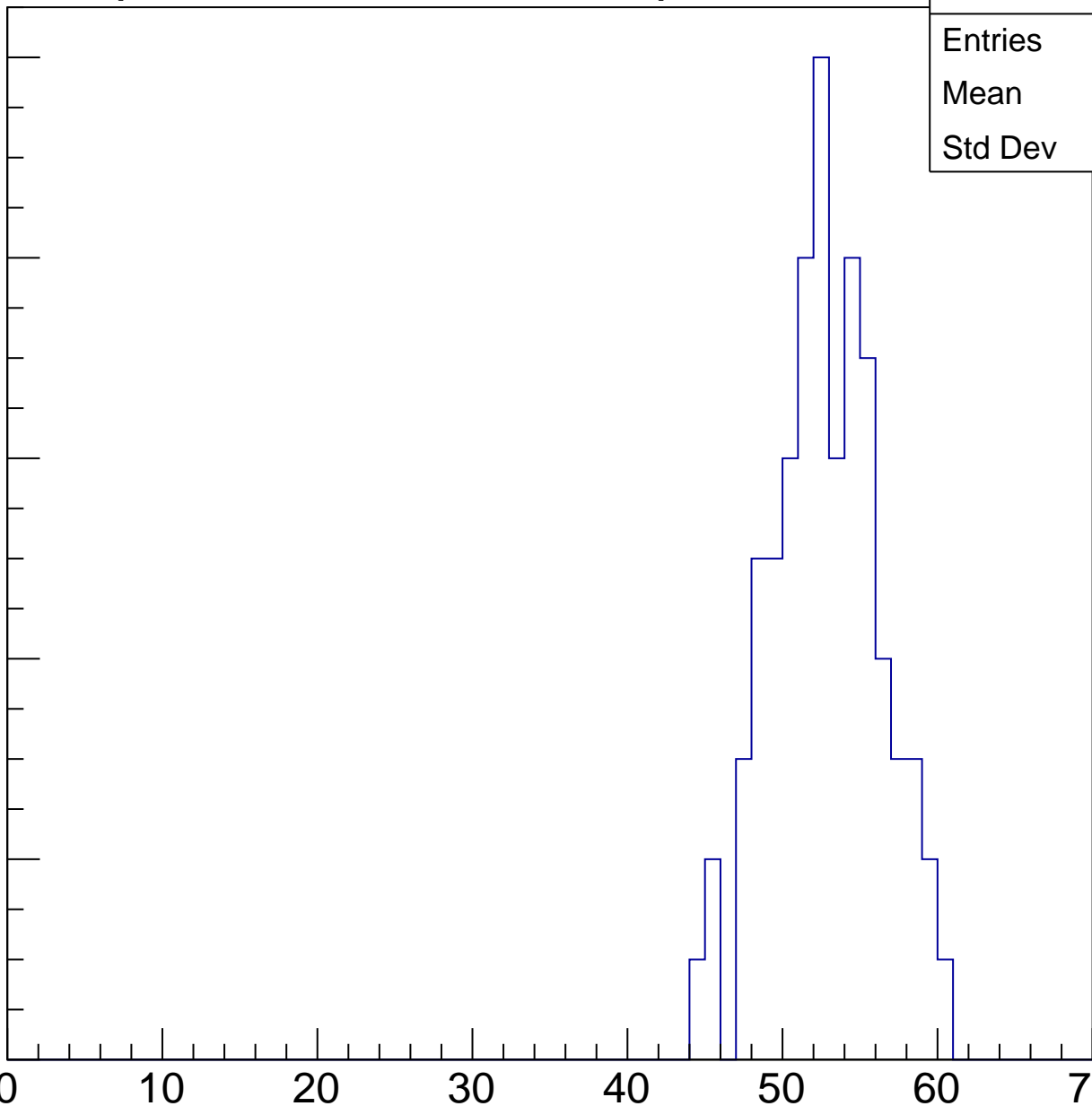
Entries	74
Mean	52.3
Std Dev	3.483

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

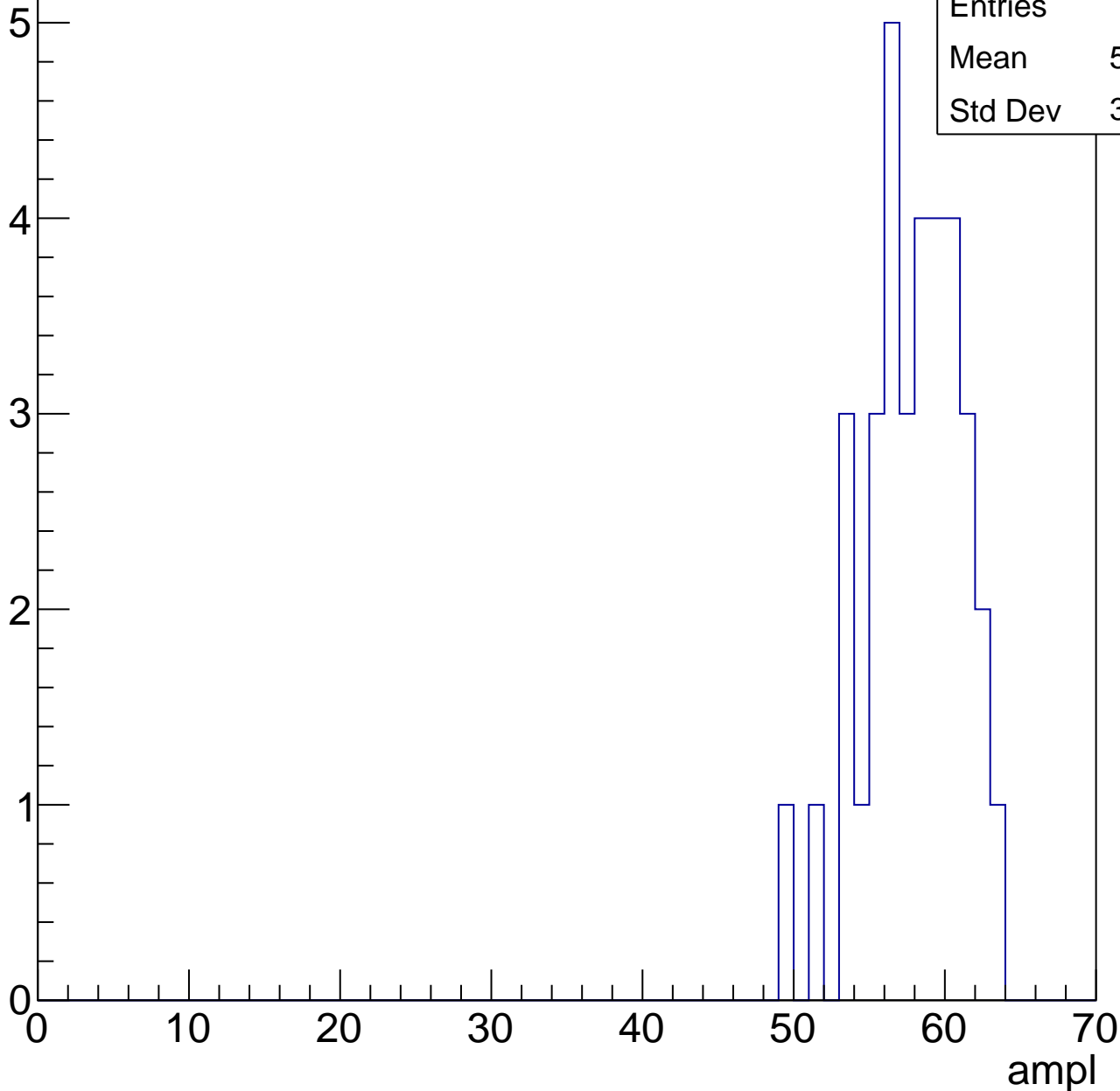


B0L002S, U4-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	57.34
Std Dev	3.207

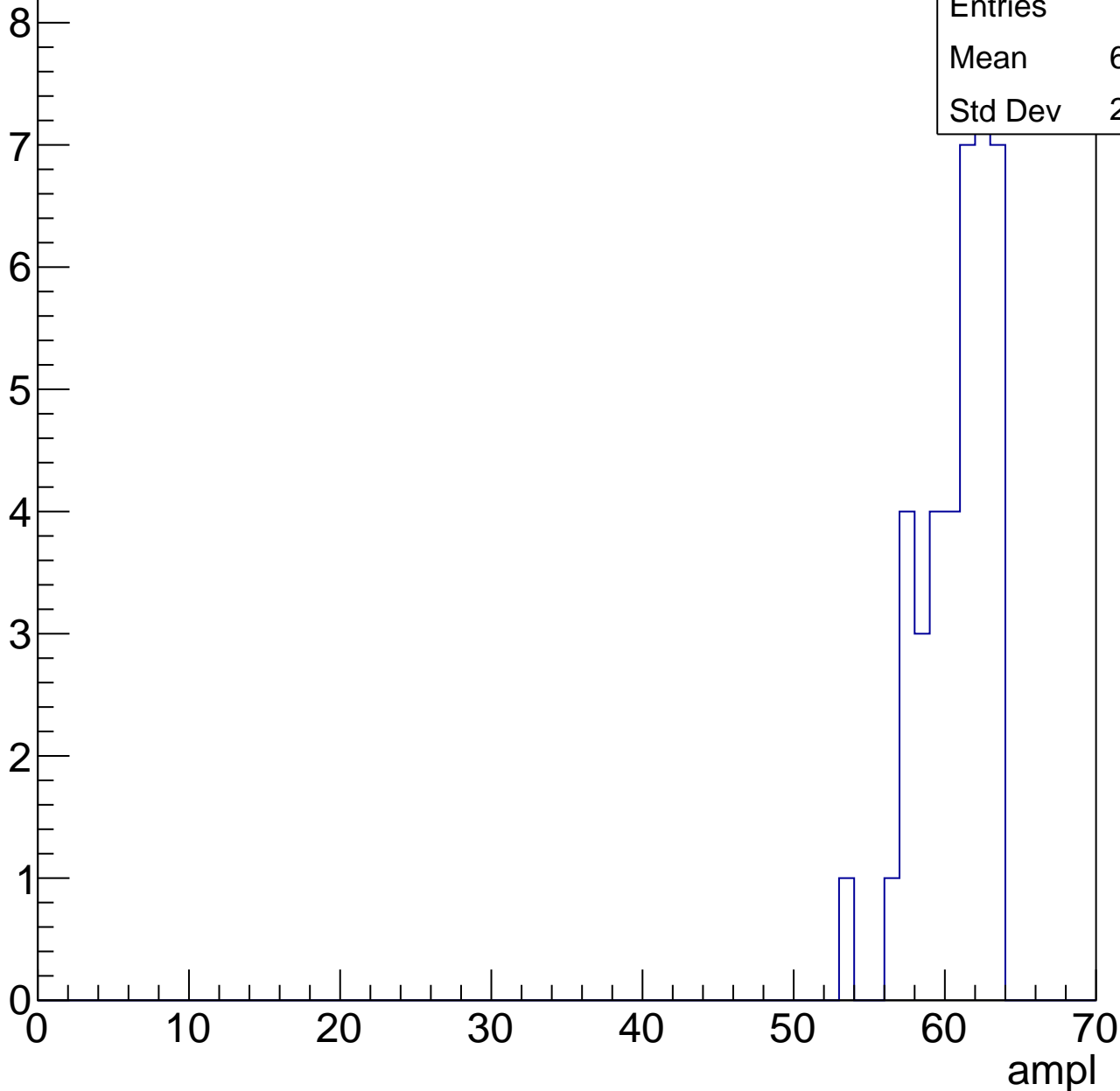


B0L002S, U4-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

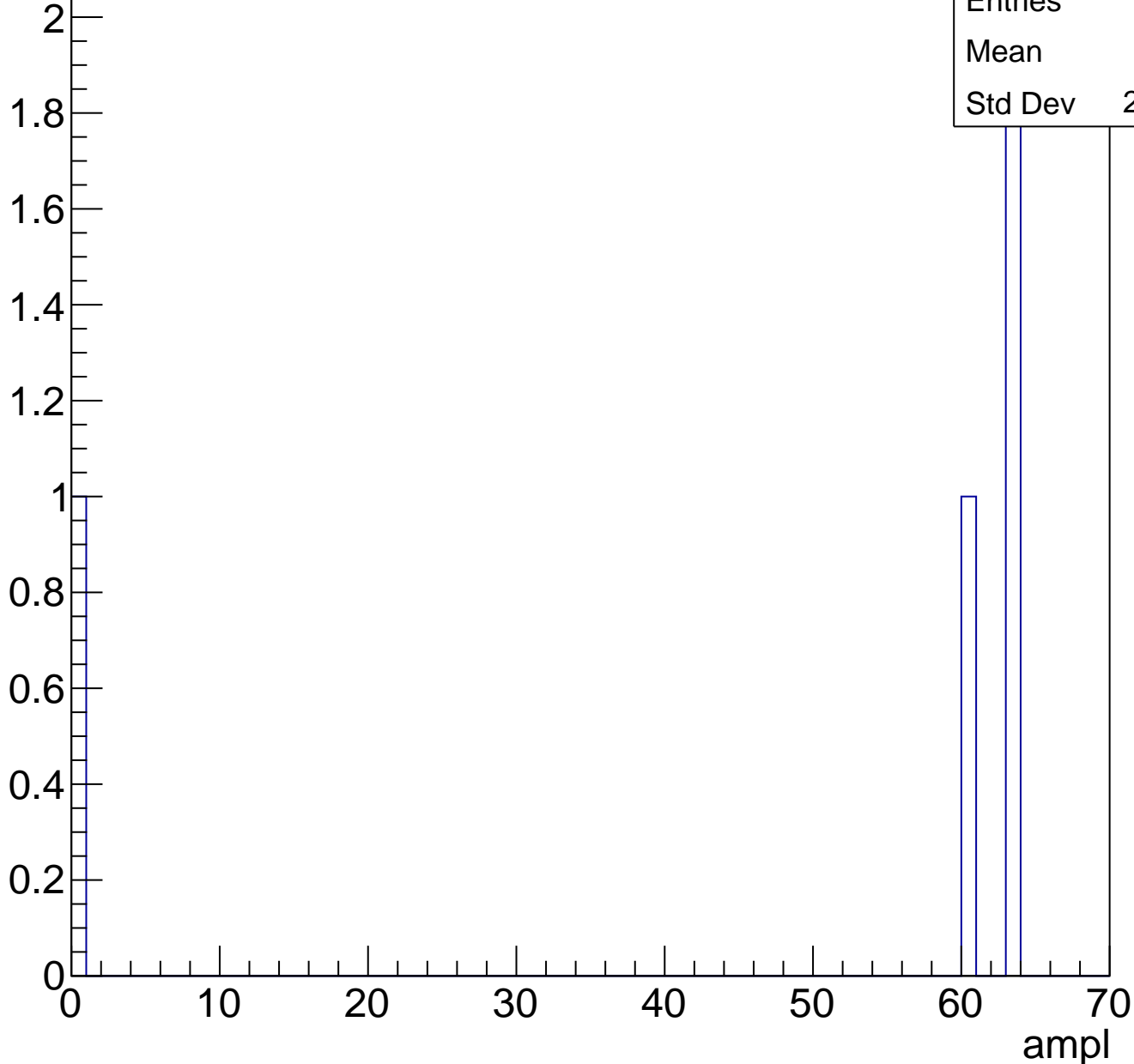
Entries	39
Mean	60.28
Std Dev	2.353



B0L002S, U4-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B0L002S, U4-ch88, adc0

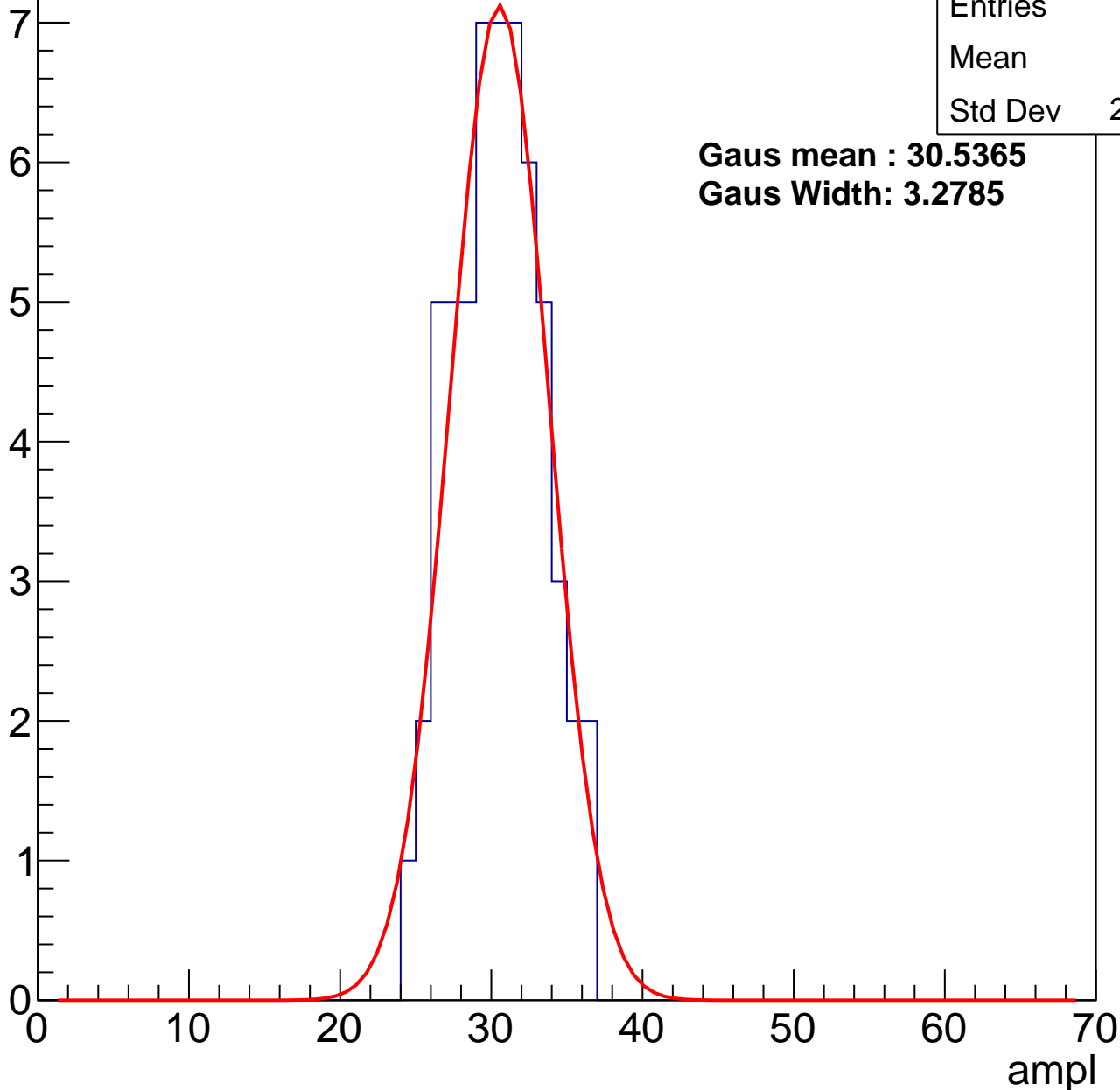
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	30
Std Dev	2.914

Gaus mean : 30.5365

Gaus Width: 3.2785



B0L002S, U4-ch88, adc1

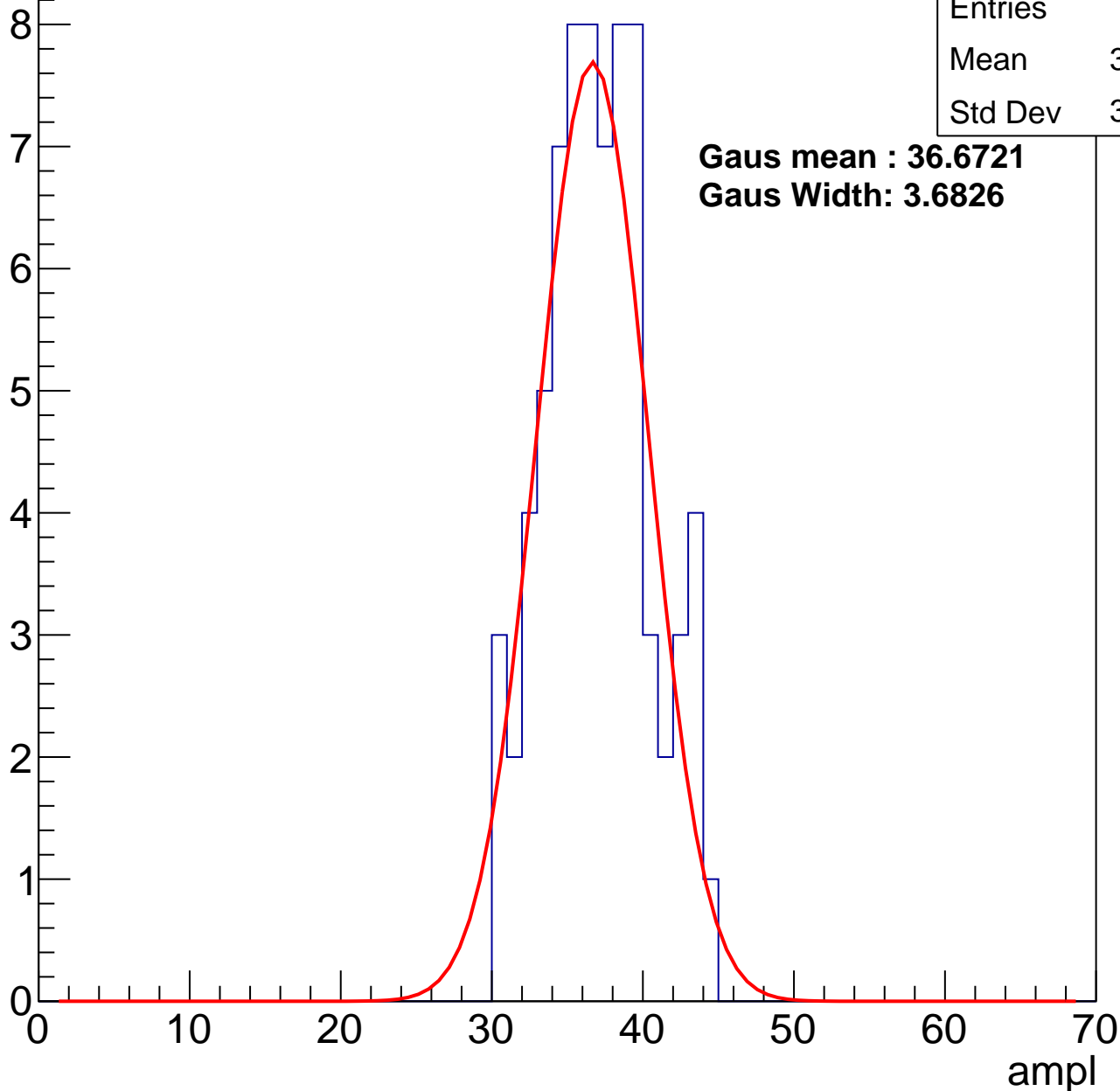
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	36.58
Std Dev	3.424

Gaus mean : 36.6721

Gaus Width: 3.6826



B0L002S, U4-ch88, adc2

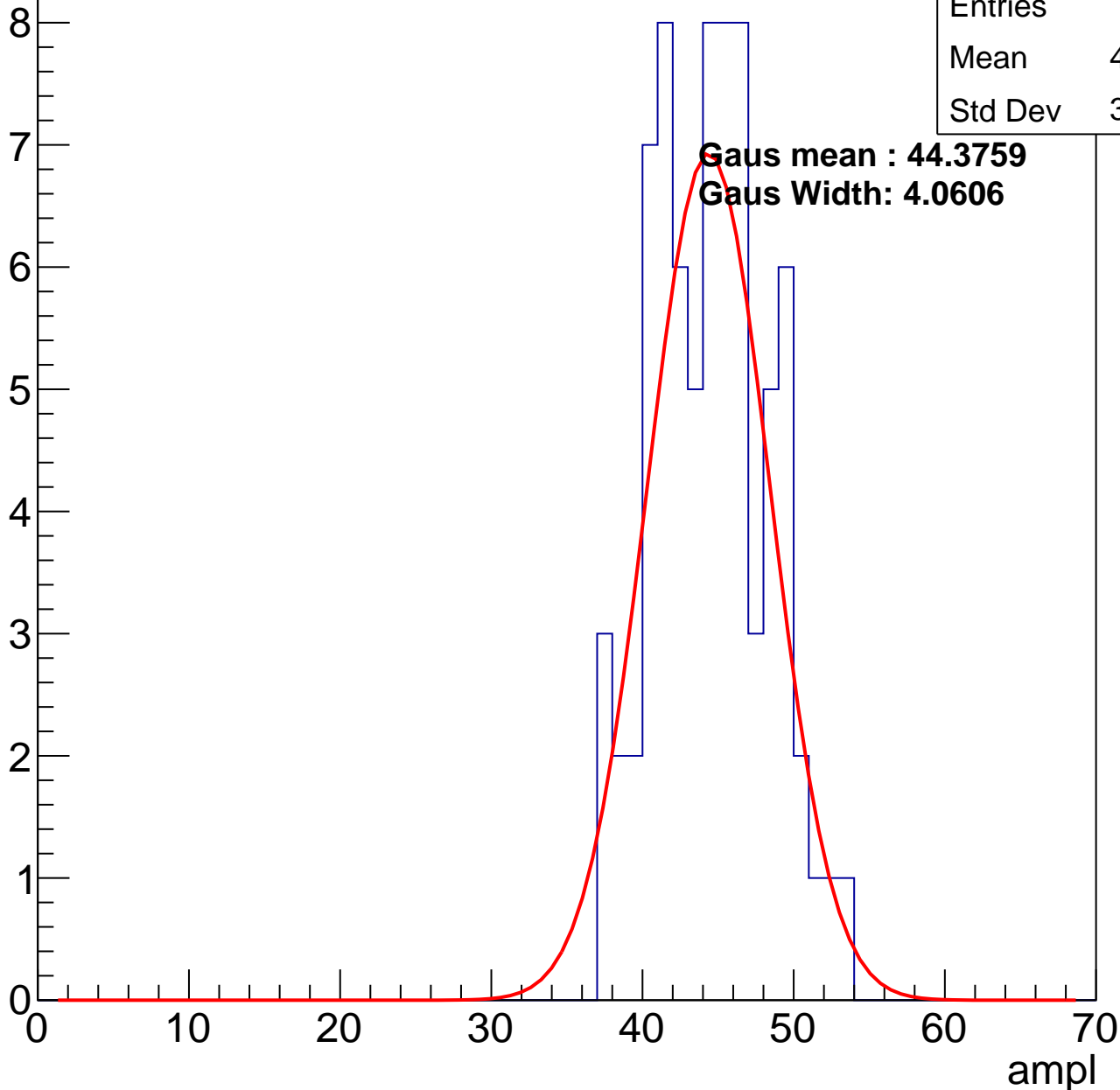
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	44.09
Std Dev	3.707

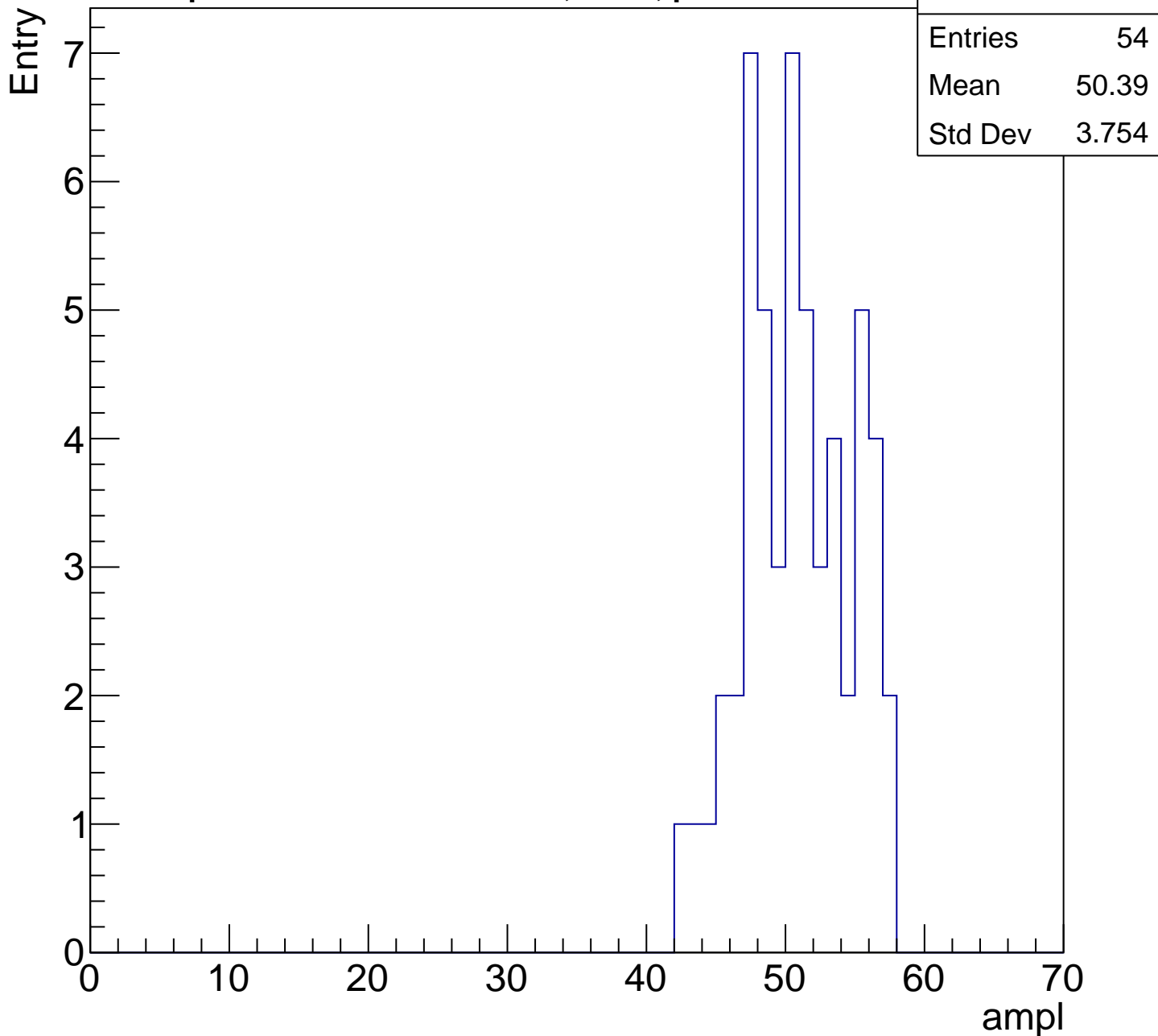
Gaus mean : 44.3759

Gaus Width: 4.0606



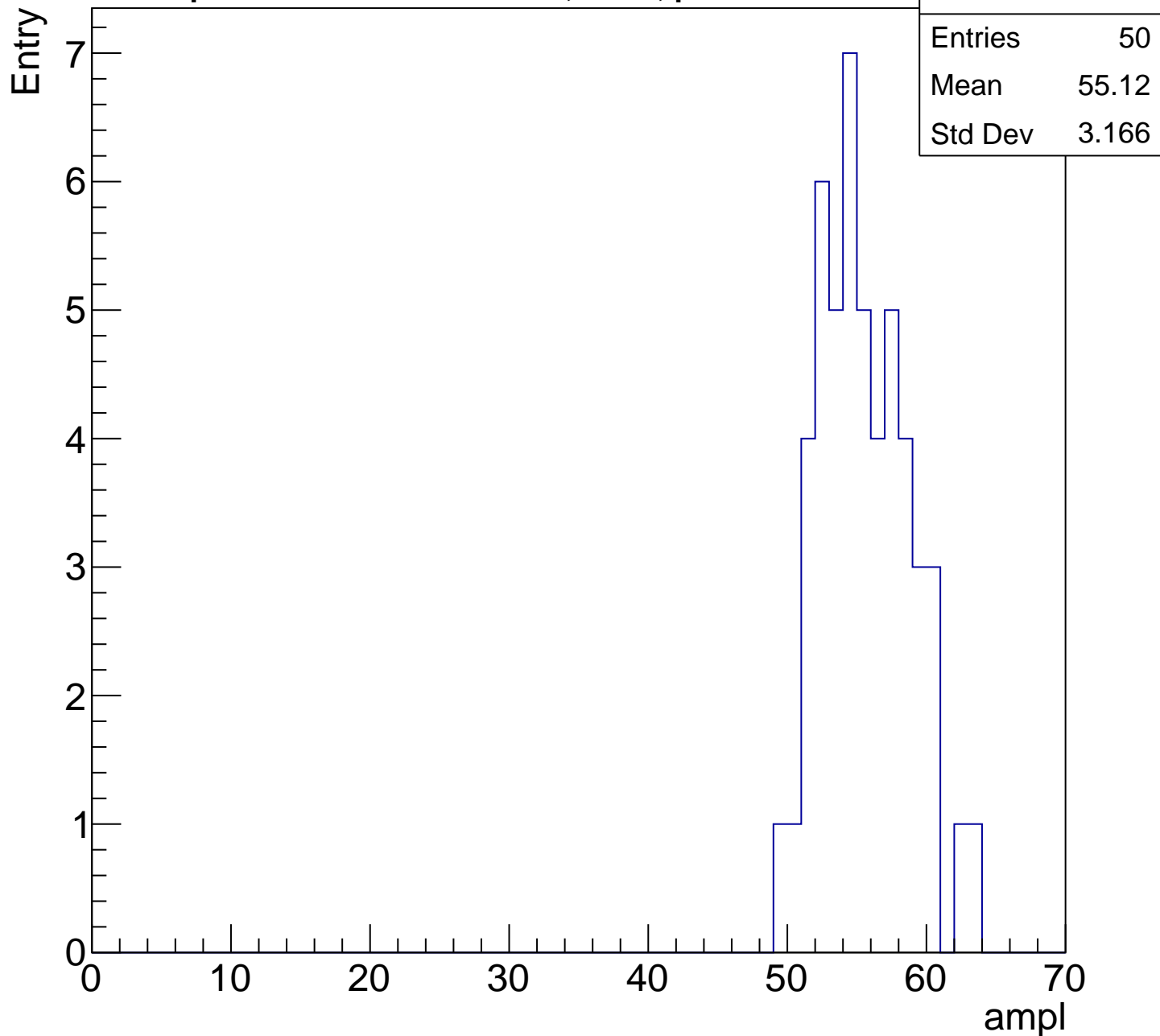
B0L002S, U4-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

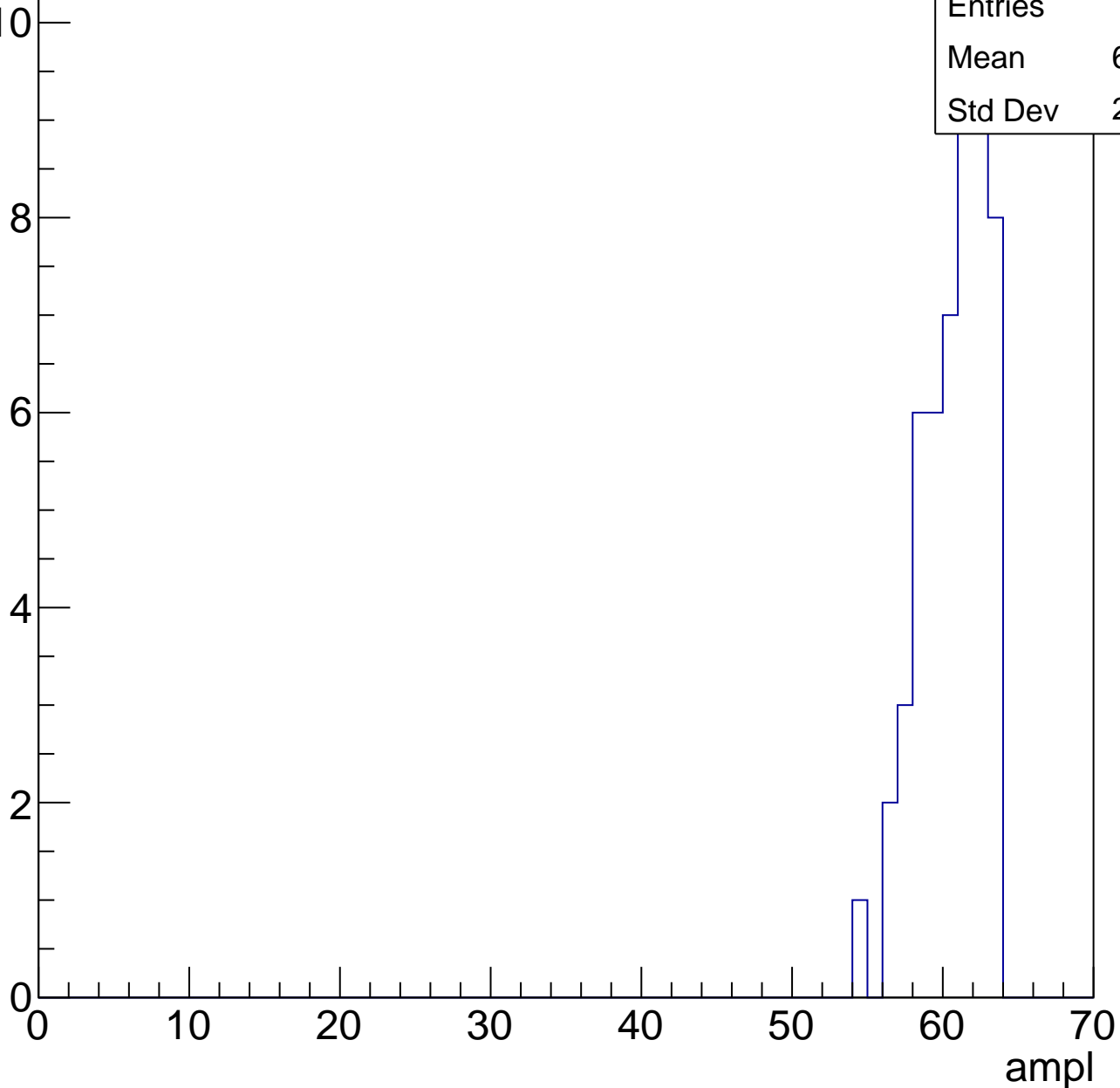


B0L002S, U4-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

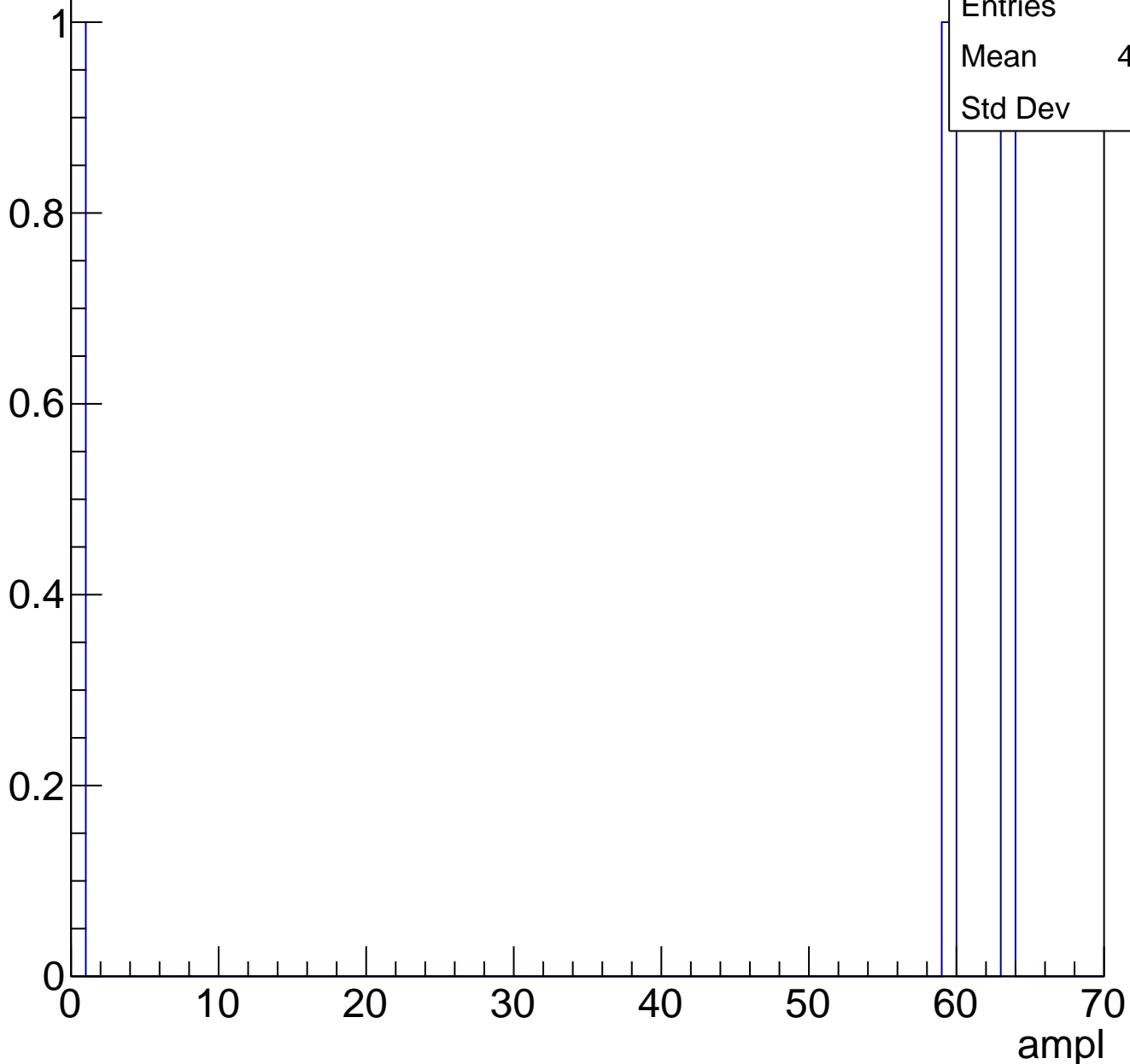
Entries	52
Mean	60.21
Std Dev	2.151



B0L002S, U4-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch89, adc0

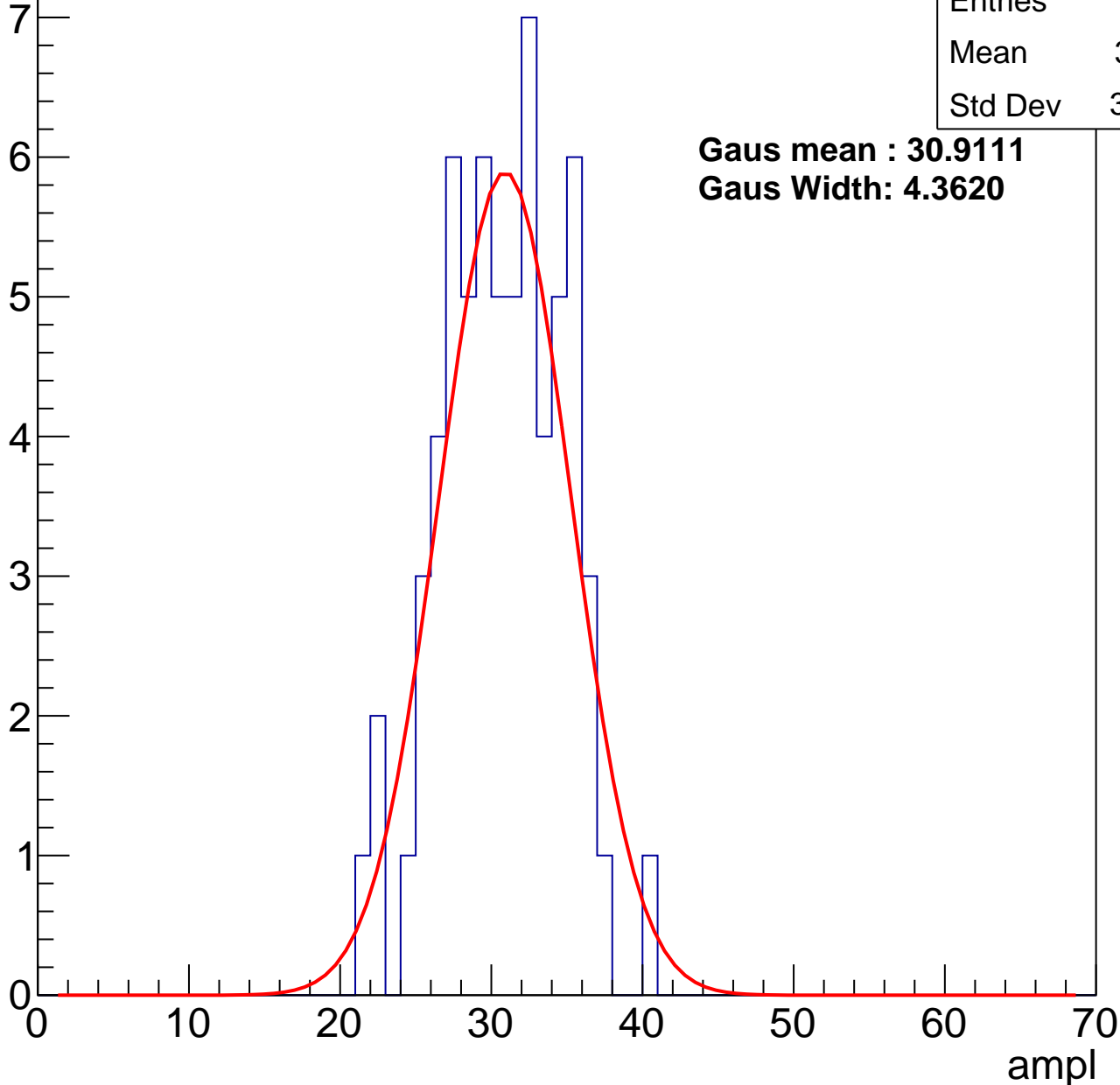
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.31
Std Dev	3.949

Gaus mean : 30.9111

Gaus Width: 4.3620



B0L002S, U4-ch89, adc1

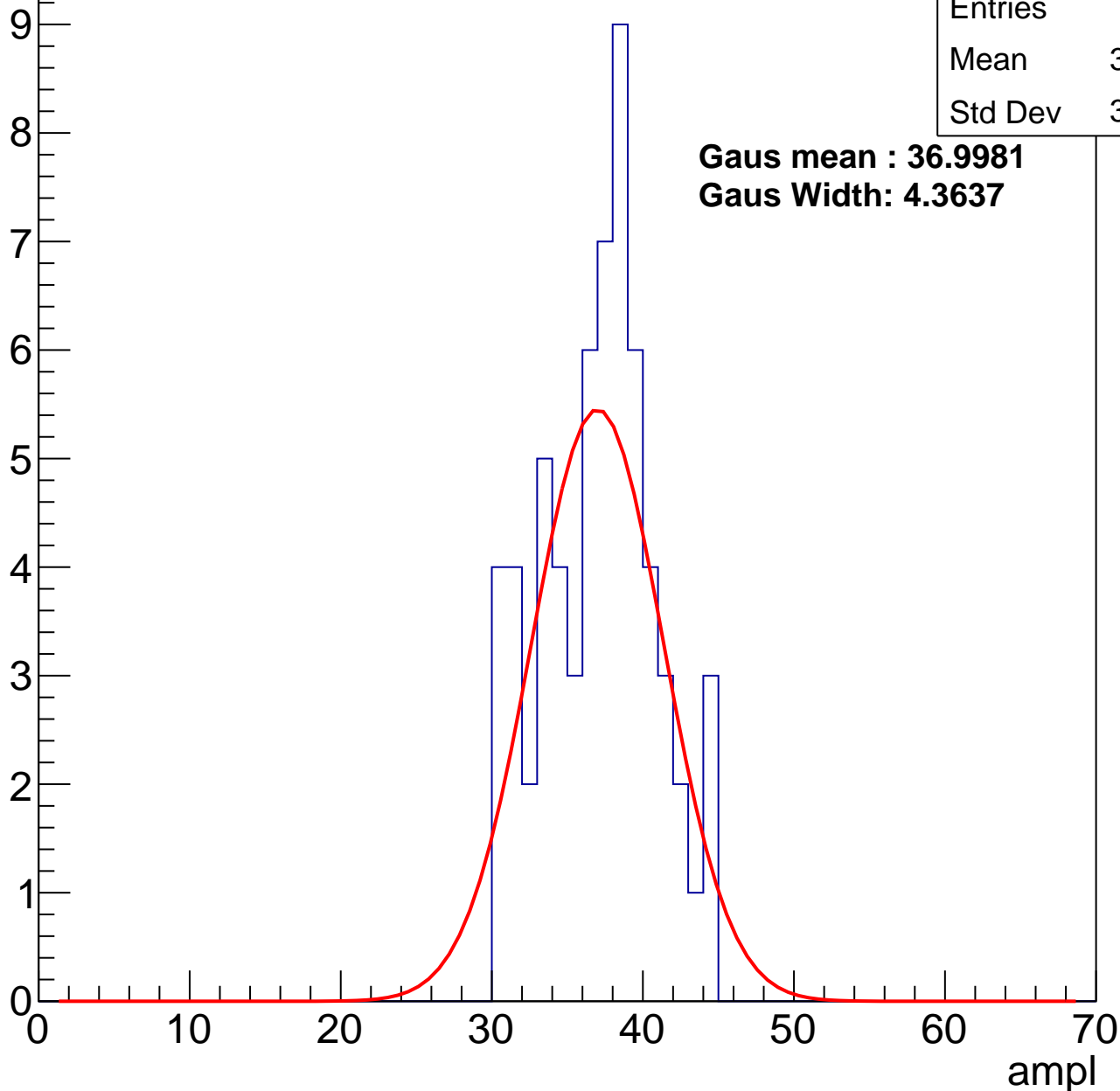
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.62
Std Dev	3.705

Gaus mean : 36.9981

Gaus Width: 4.3637



B0L002S, U4-ch89, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.22
Std Dev	3.378

Gaus mean : 43.9373

Gaus Width: 3.2867

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

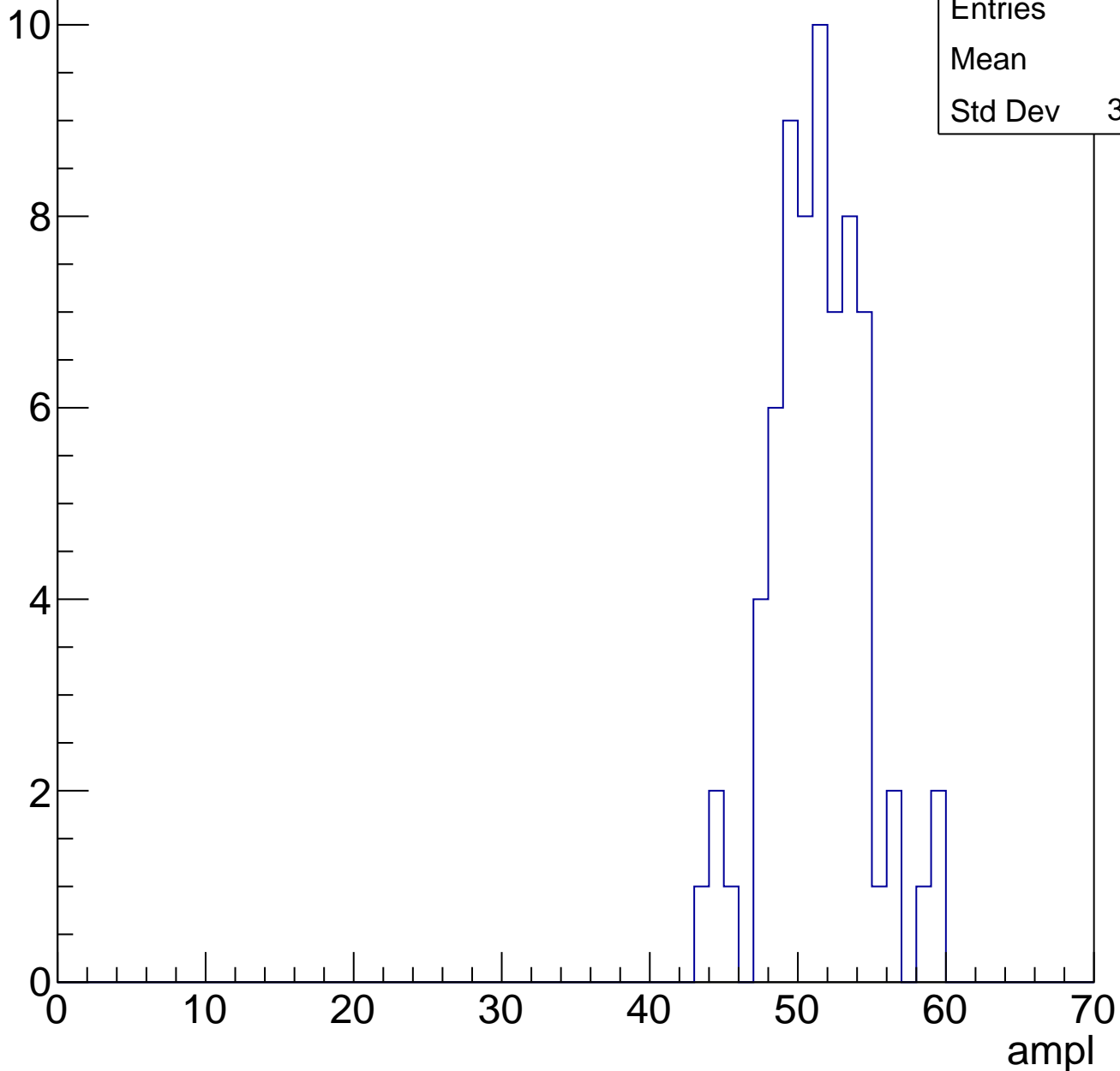
0 10 20 30 40 50 60 70

B0L002S, U4-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	50.9
Std Dev	3.195

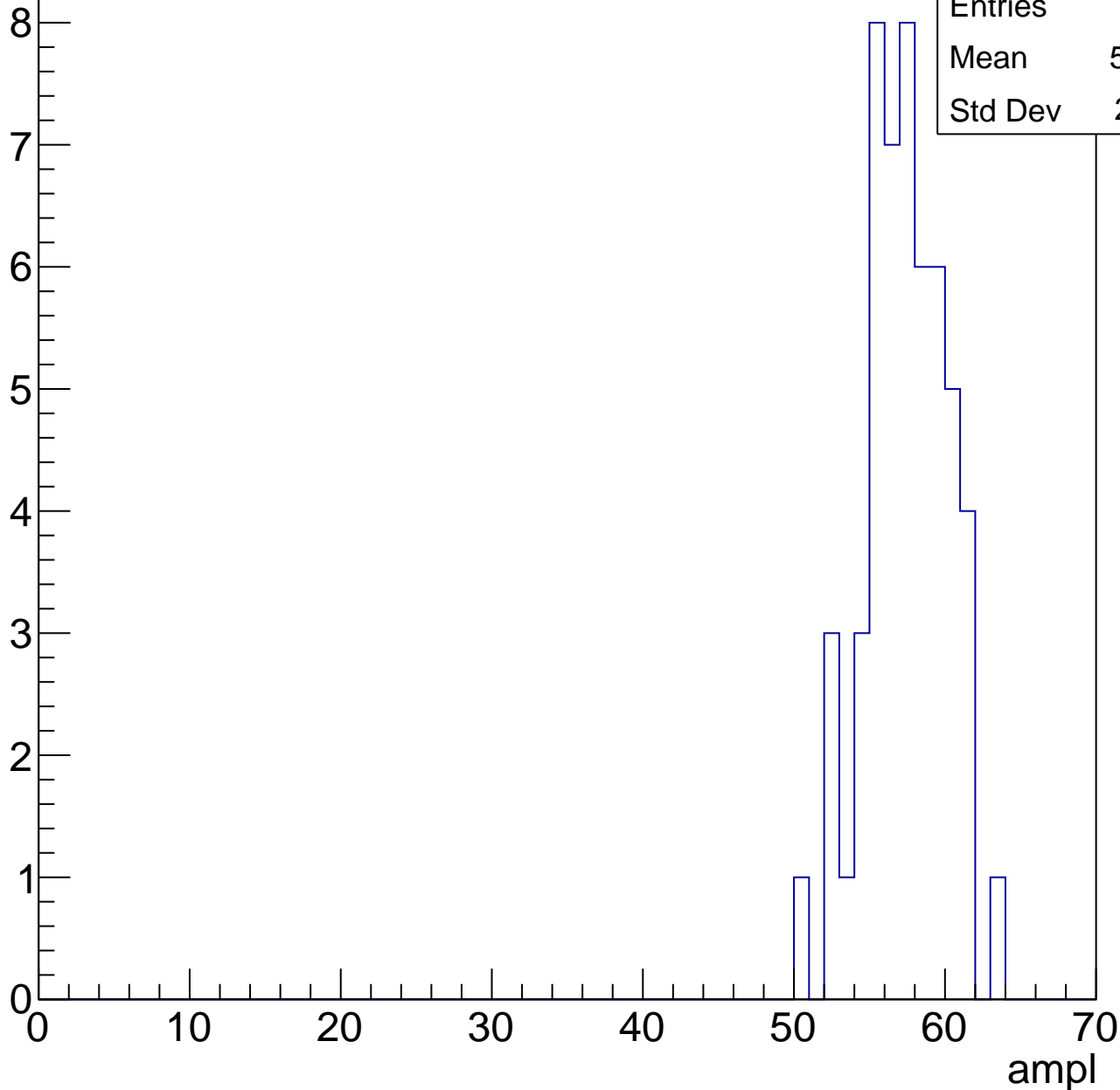
Entry



B0L002S, U4-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

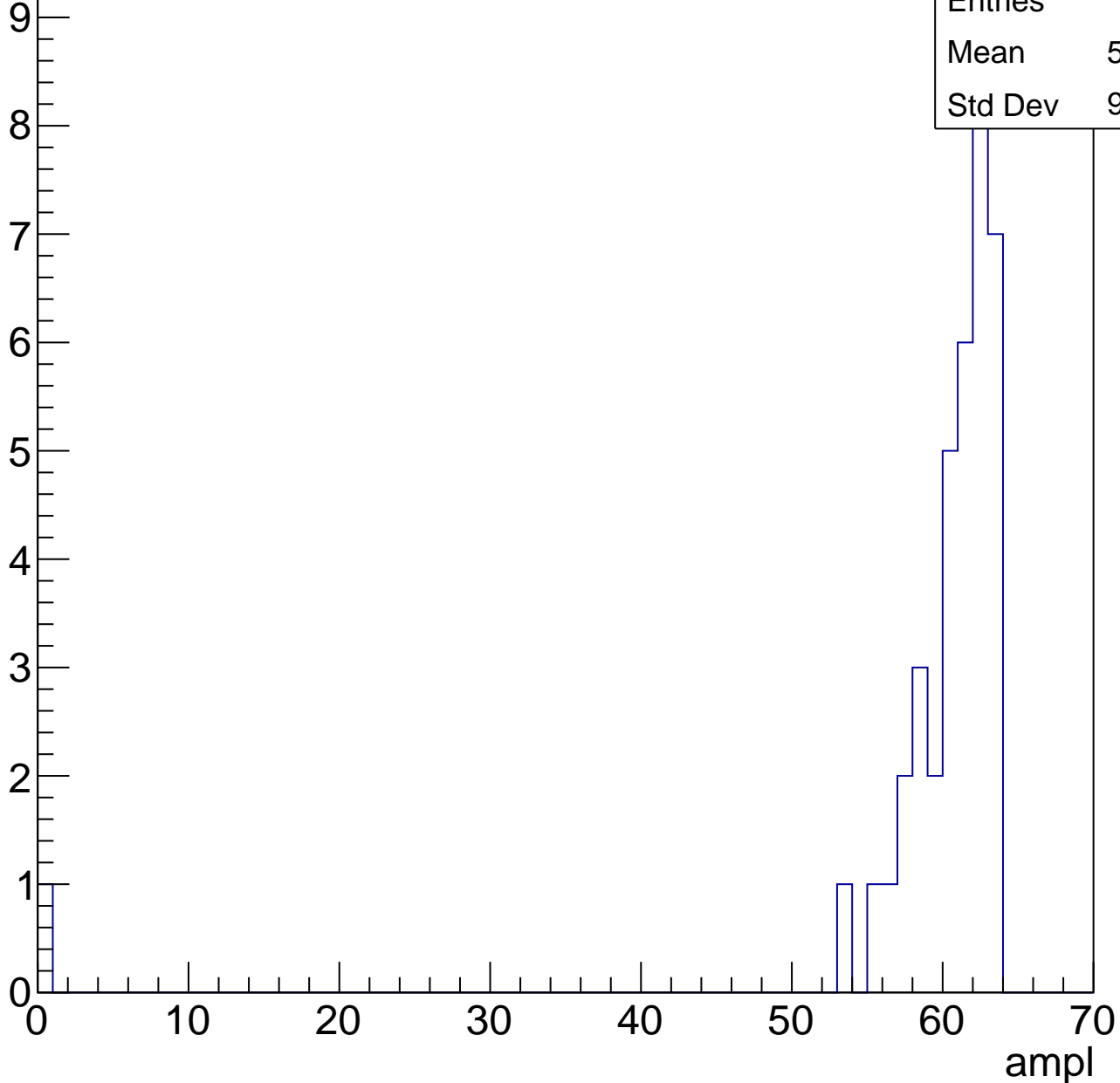


B0L002S, U4-ch89, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.82
Std Dev	9.965



B0L002S, U4-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch90, adc0

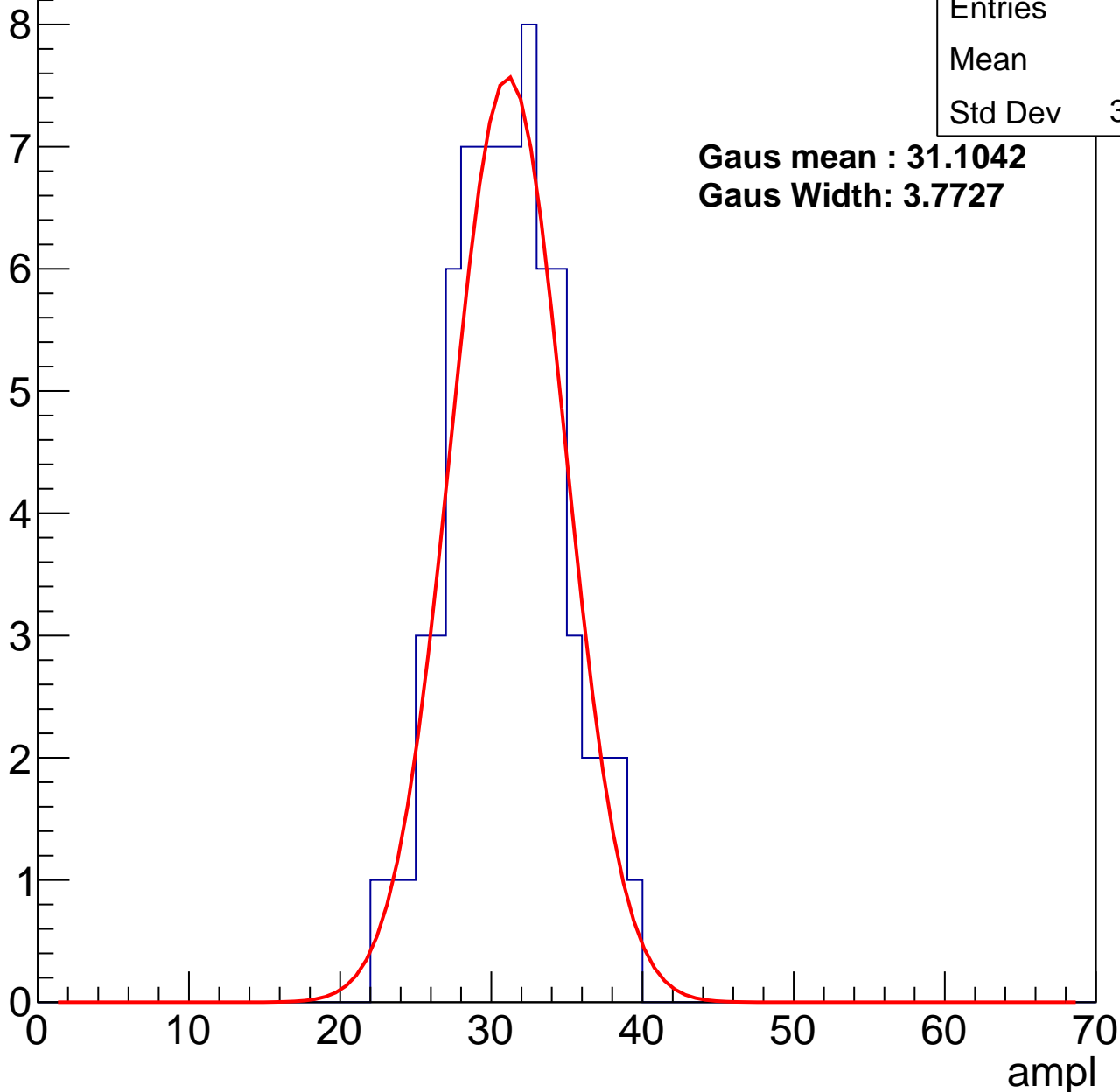
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.6
Std Dev	3.659

Gaus mean : 31.1042

Gaus Width: 3.7727



B0L002S, U4-ch90, adc1

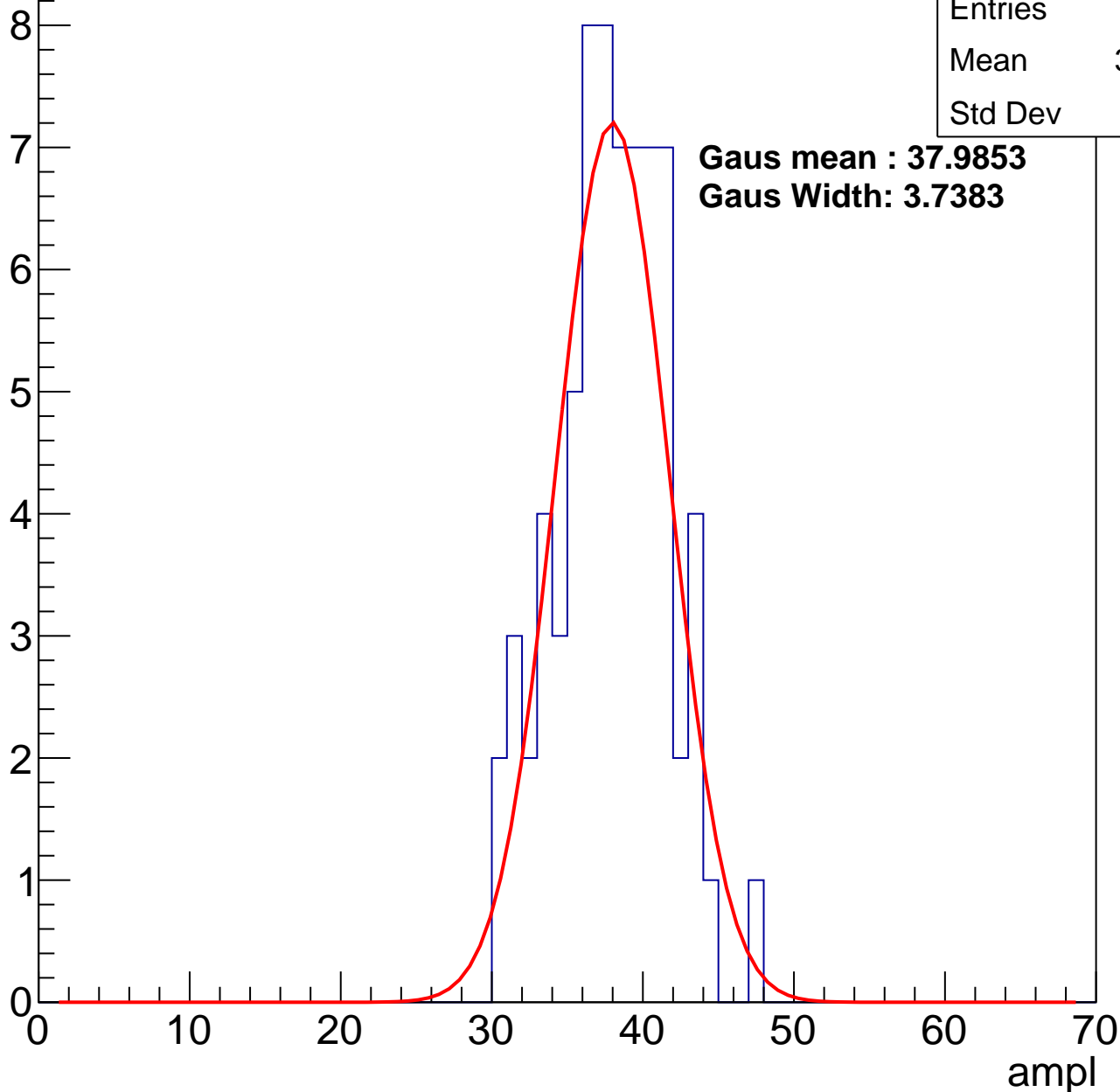
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.51
Std Dev	3.58

Gaus mean : 37.9853

Gaus Width: 3.7383



B0L002S, U4-ch90, adc2

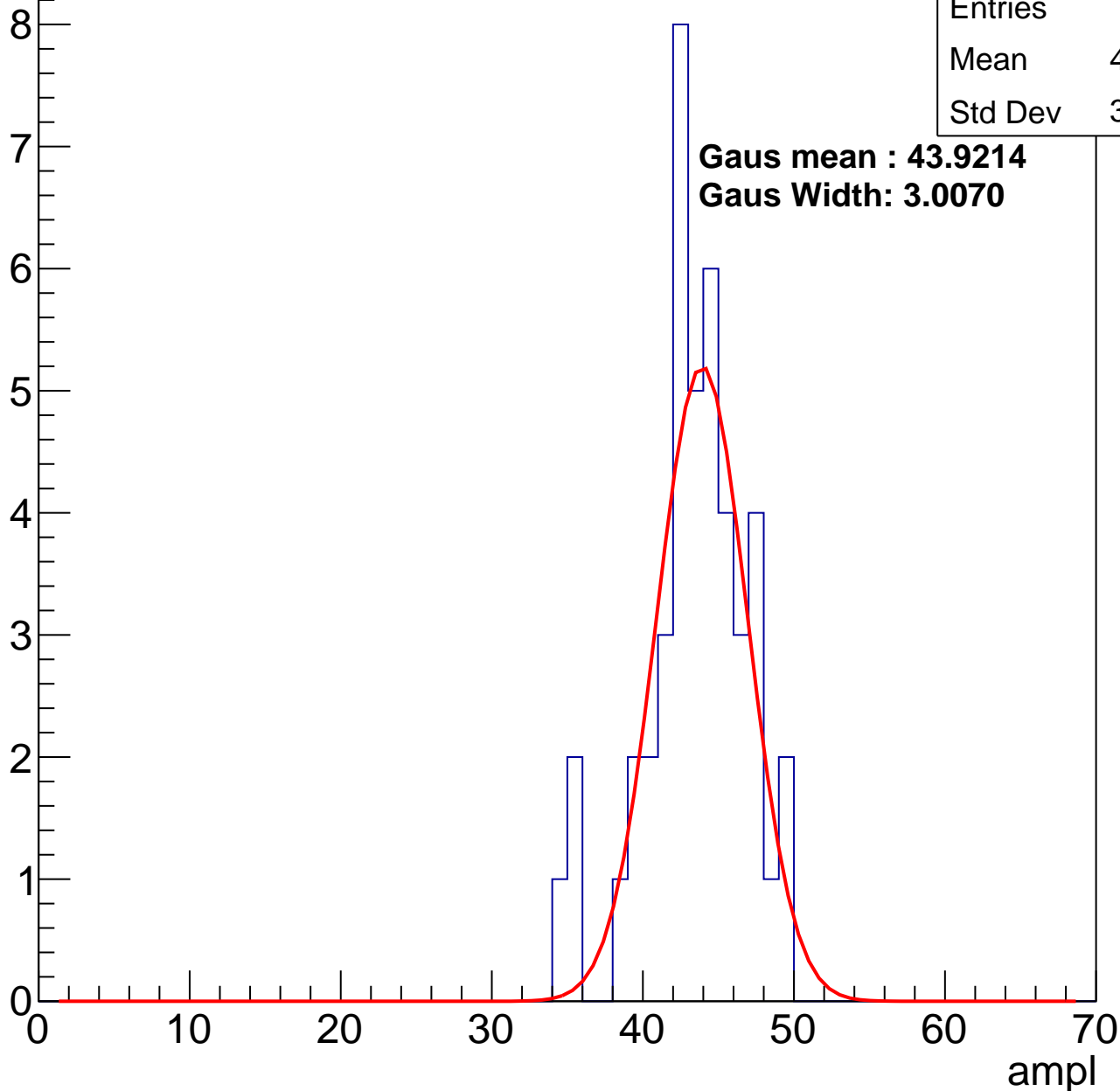
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	42.95
Std Dev	3.418

Gaus mean : 43.9214

Gaus Width: 3.0070



B0L002S, U4-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

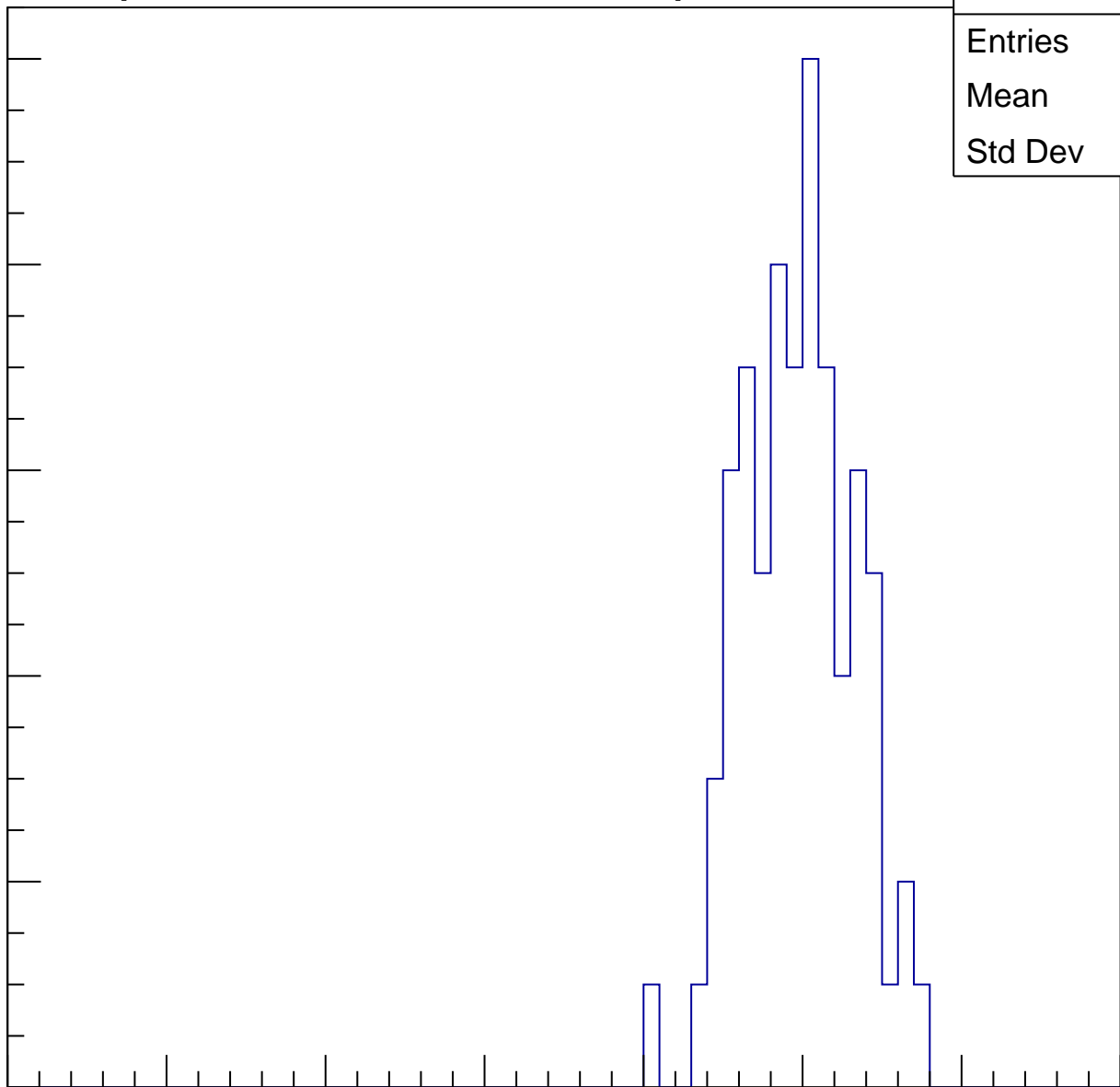
Entries	74
Mean	49.27
Std Dev	3.422

Entry

10
8
6
4
2
0

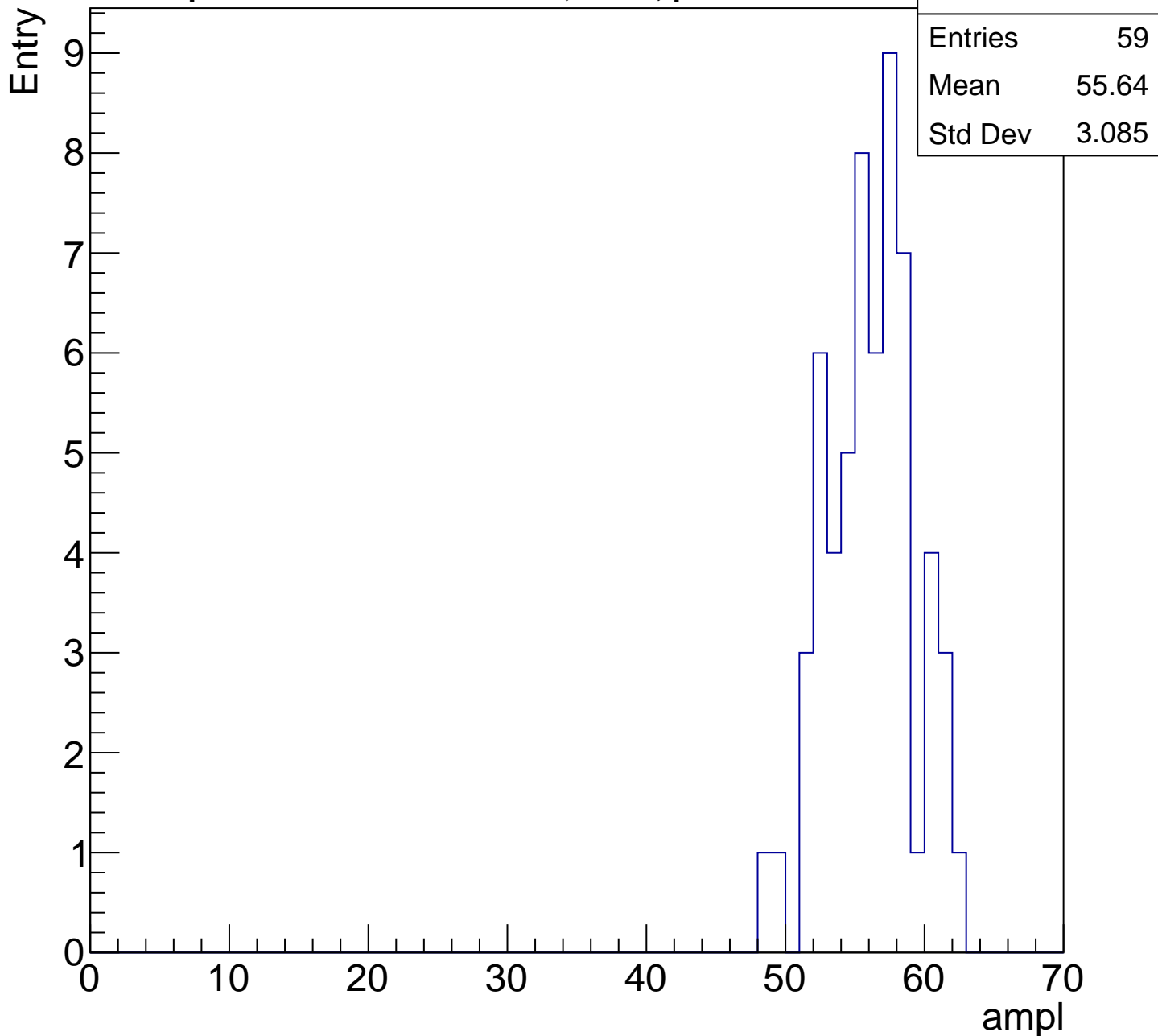
0 10 20 30 40 50 60 70

ampl



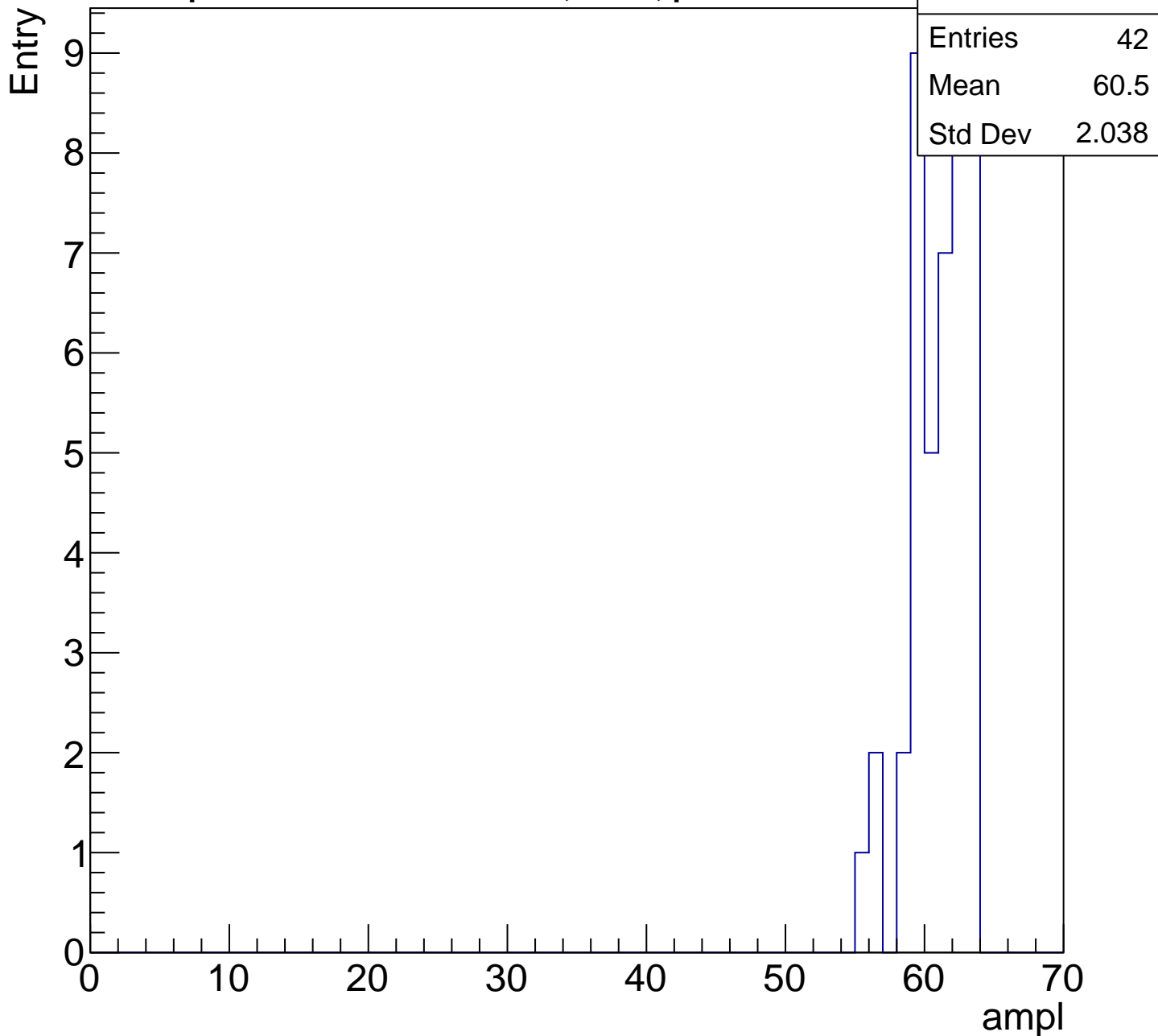
B0L002S, U4-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch90, adc5

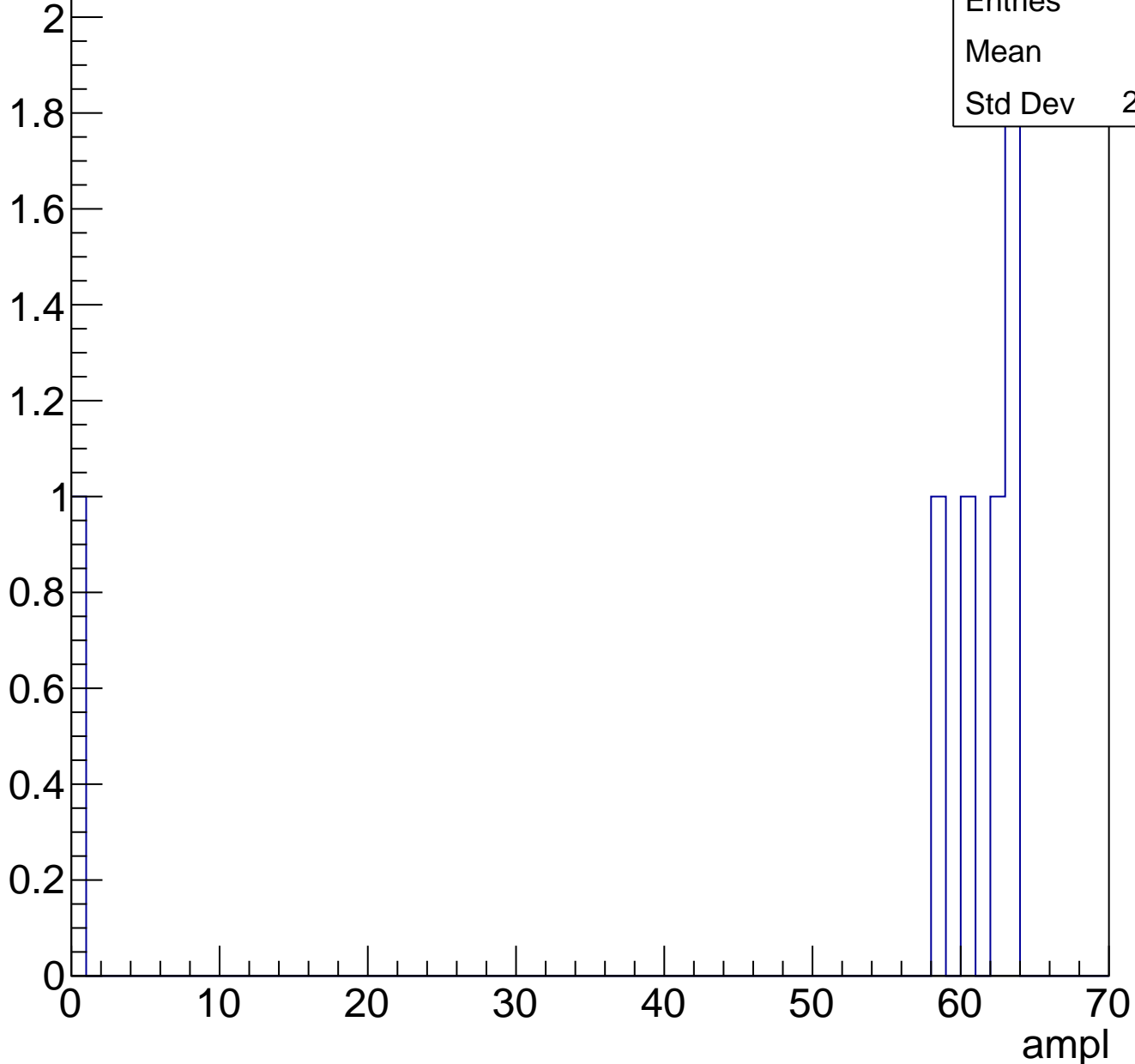
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	51
Std Dev	22.88

B0L002S, U4-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch91, adc0

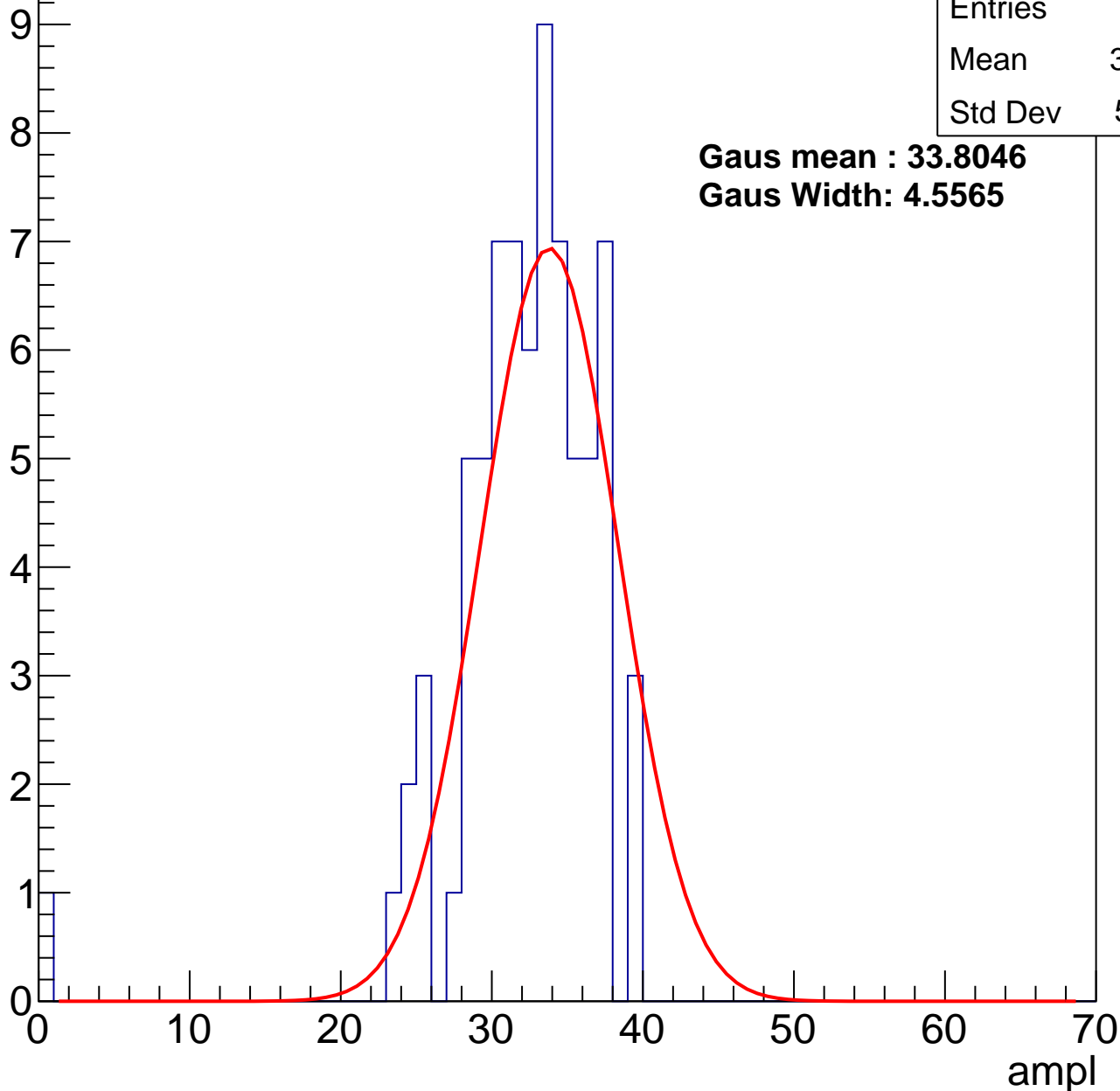
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	31.66
Std Dev	5.251

Gaus mean : 33.8046

Gaus Width: 4.5565



B0L002S, U4-ch91, adc1

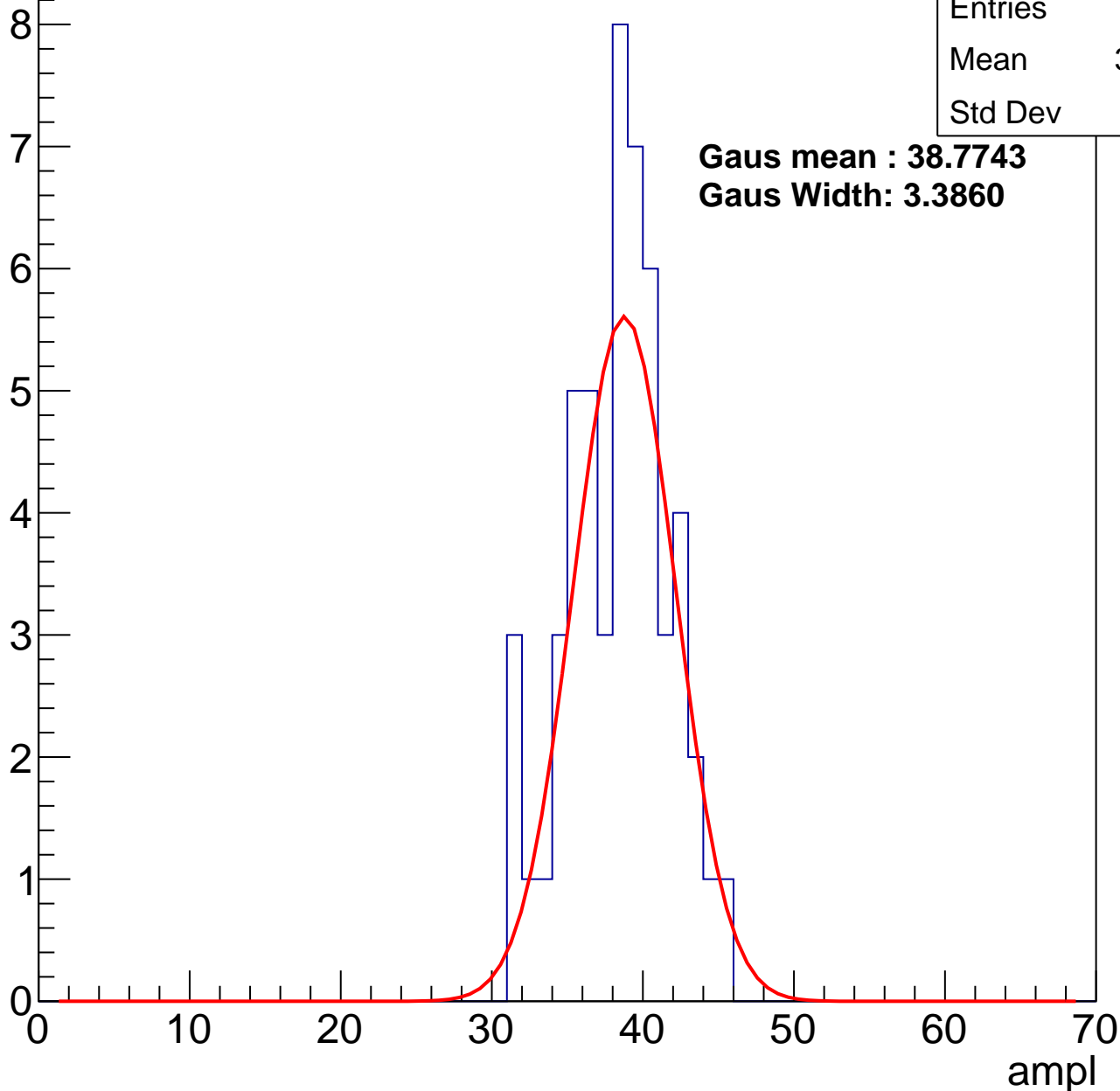
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	37.91
Std Dev	3.31

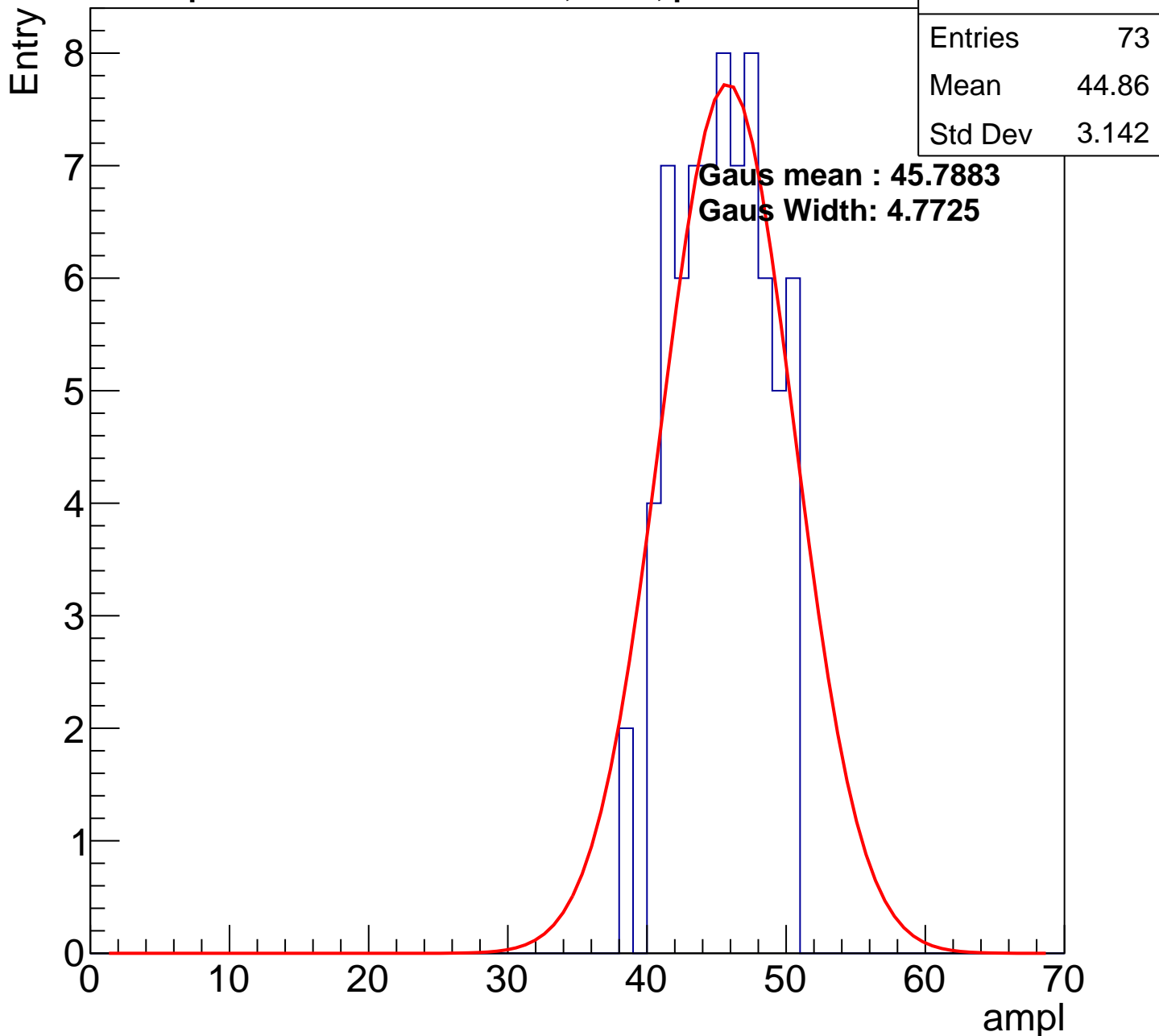
Gaus mean : 38.7743

Gaus Width: 3.3860



B0L002S, U4-ch91, adc2

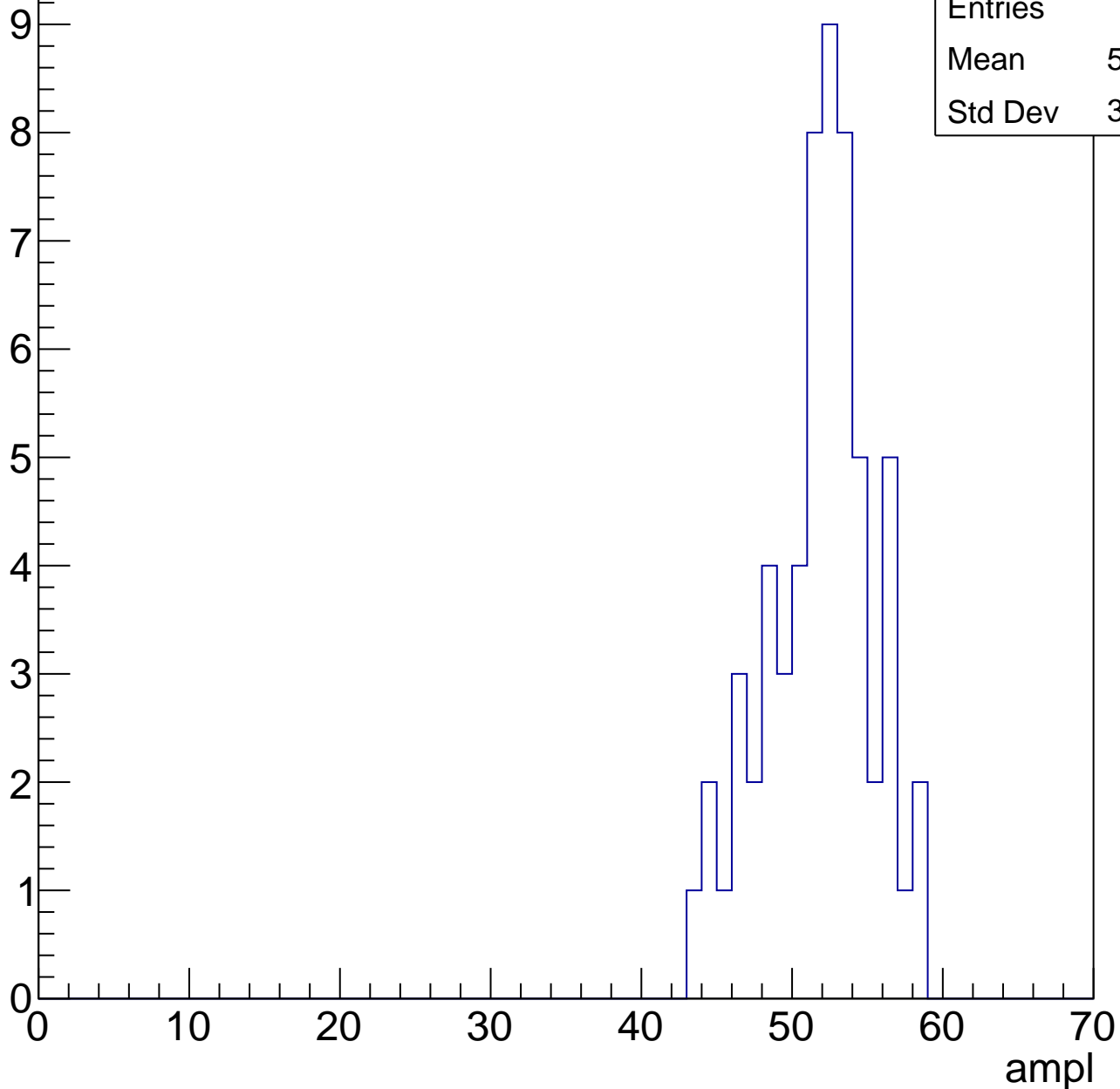
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



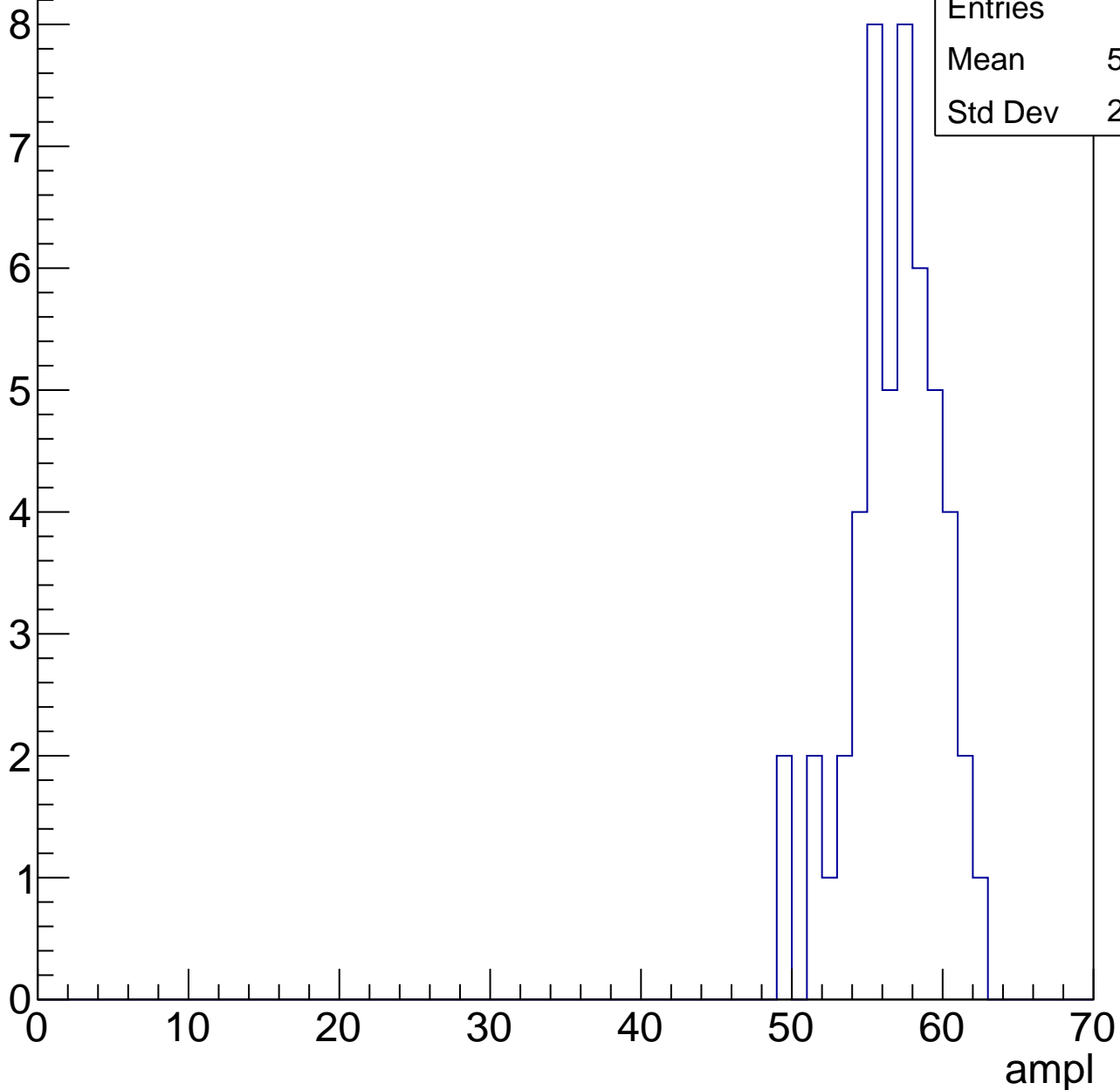
Entries	60
Mean	51.33
Std Dev	3.482

B0L002S, U4-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	56.34
Std Dev	2.923

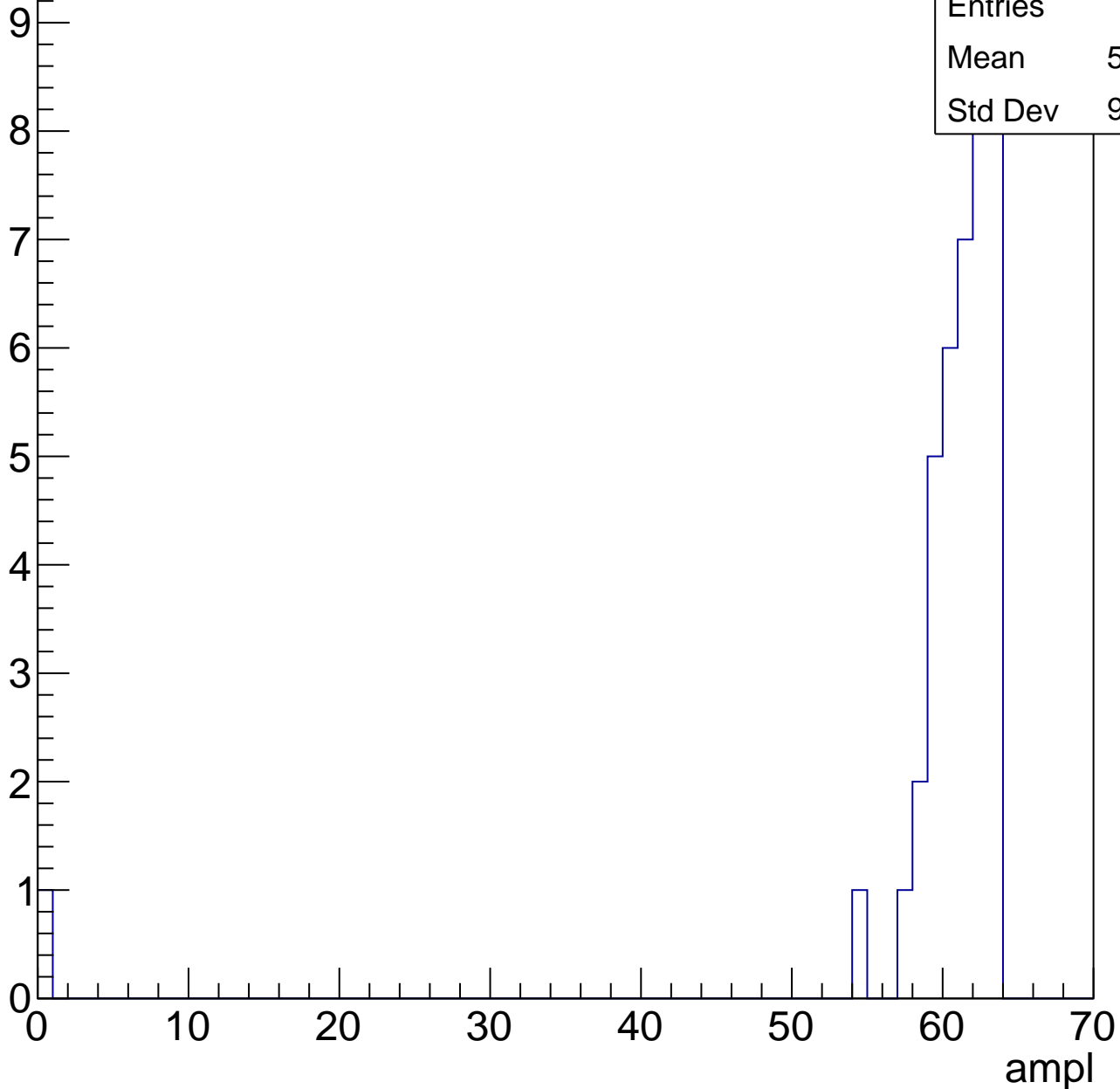


B0L002S, U4-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.27
Std Dev	9.685



B0L002S, U4-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch92, adc0

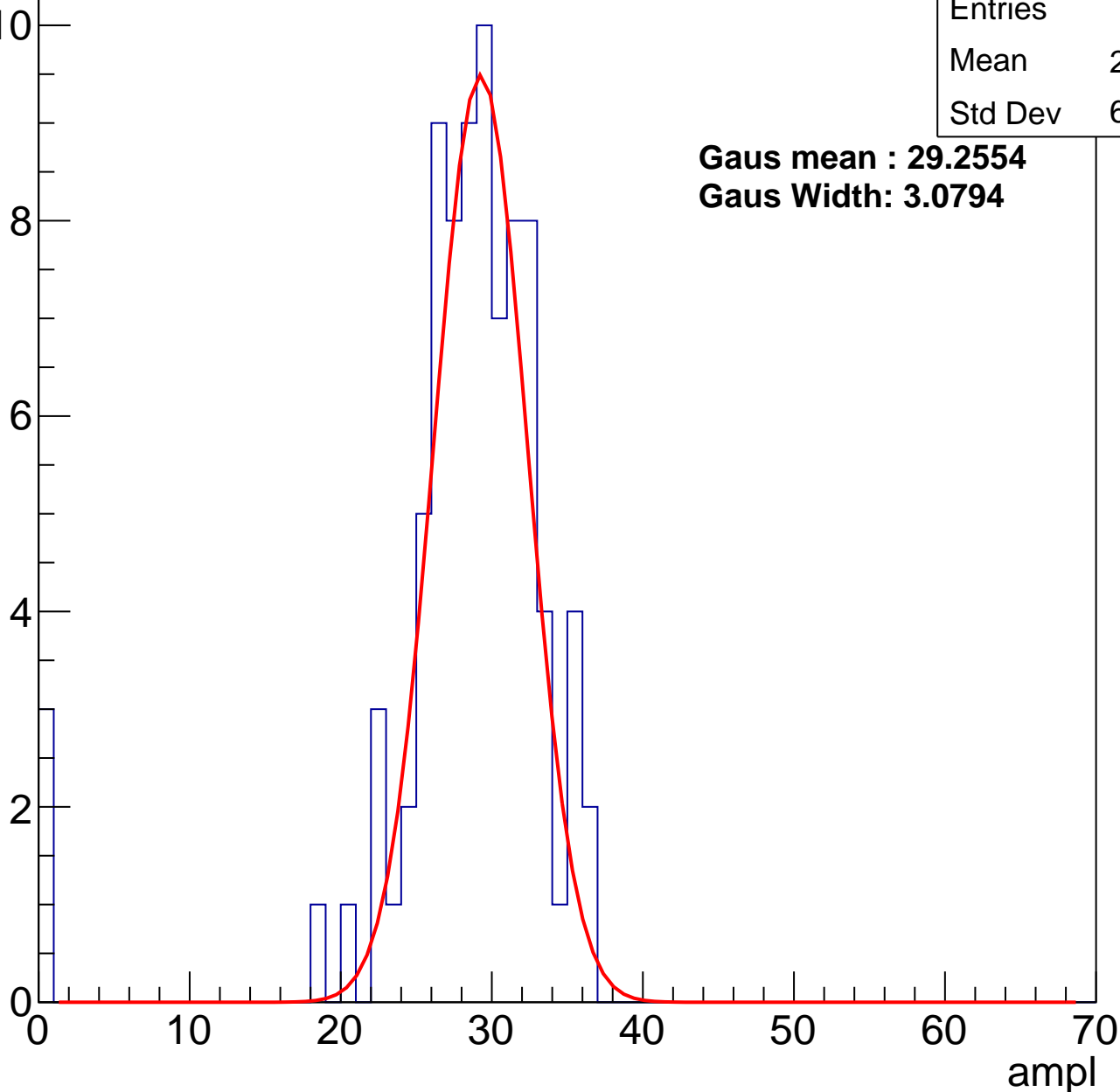
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	27.72
Std Dev	6.344

Gaus mean : 29.2554

Gaus Width: 3.0794



B0L002S, U4-ch92, adc1

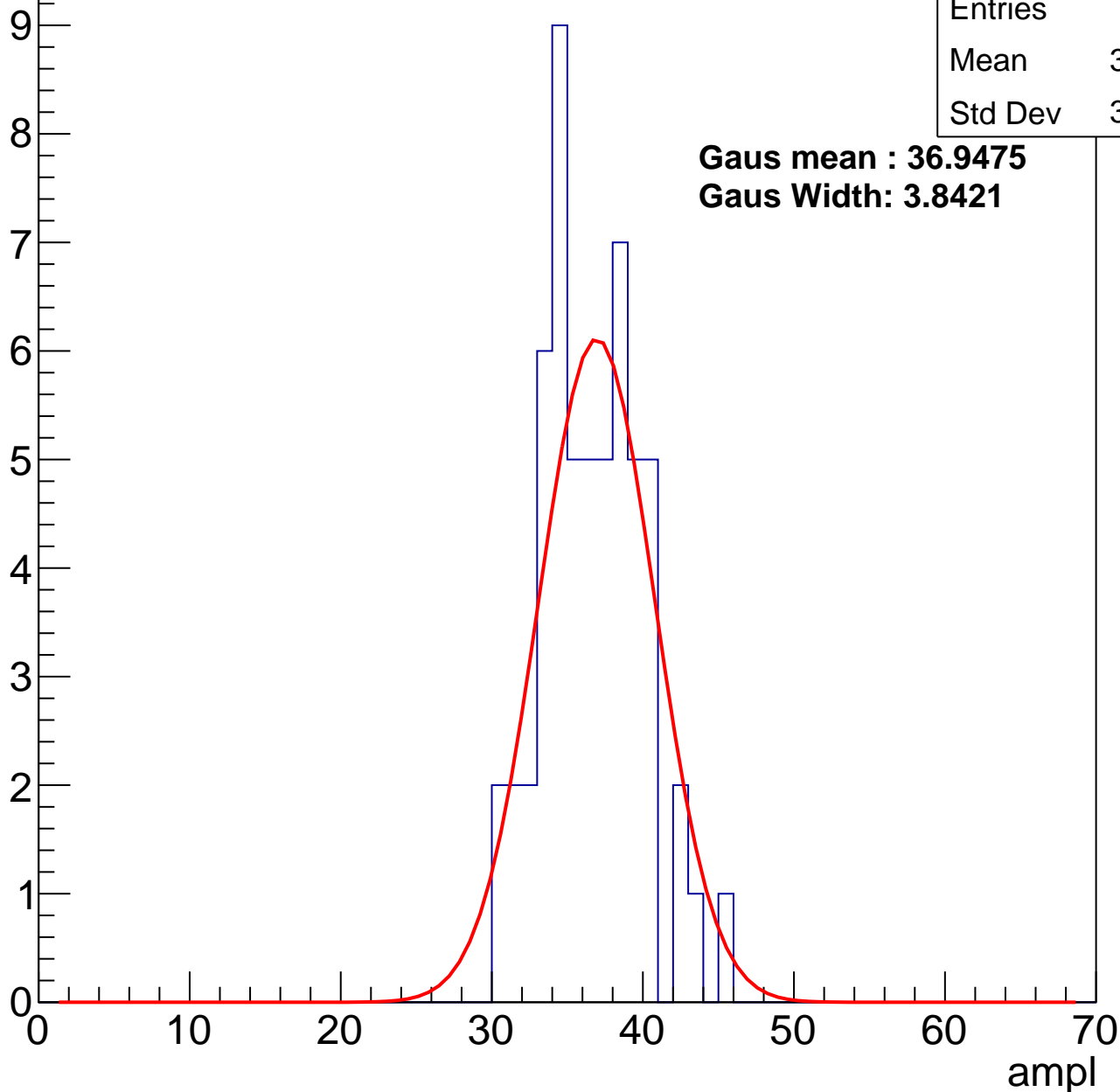
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.19
Std Dev	3.263

Gaus mean : 36.9475

Gaus Width: 3.8421



B0L002S, U4-ch92, adc2

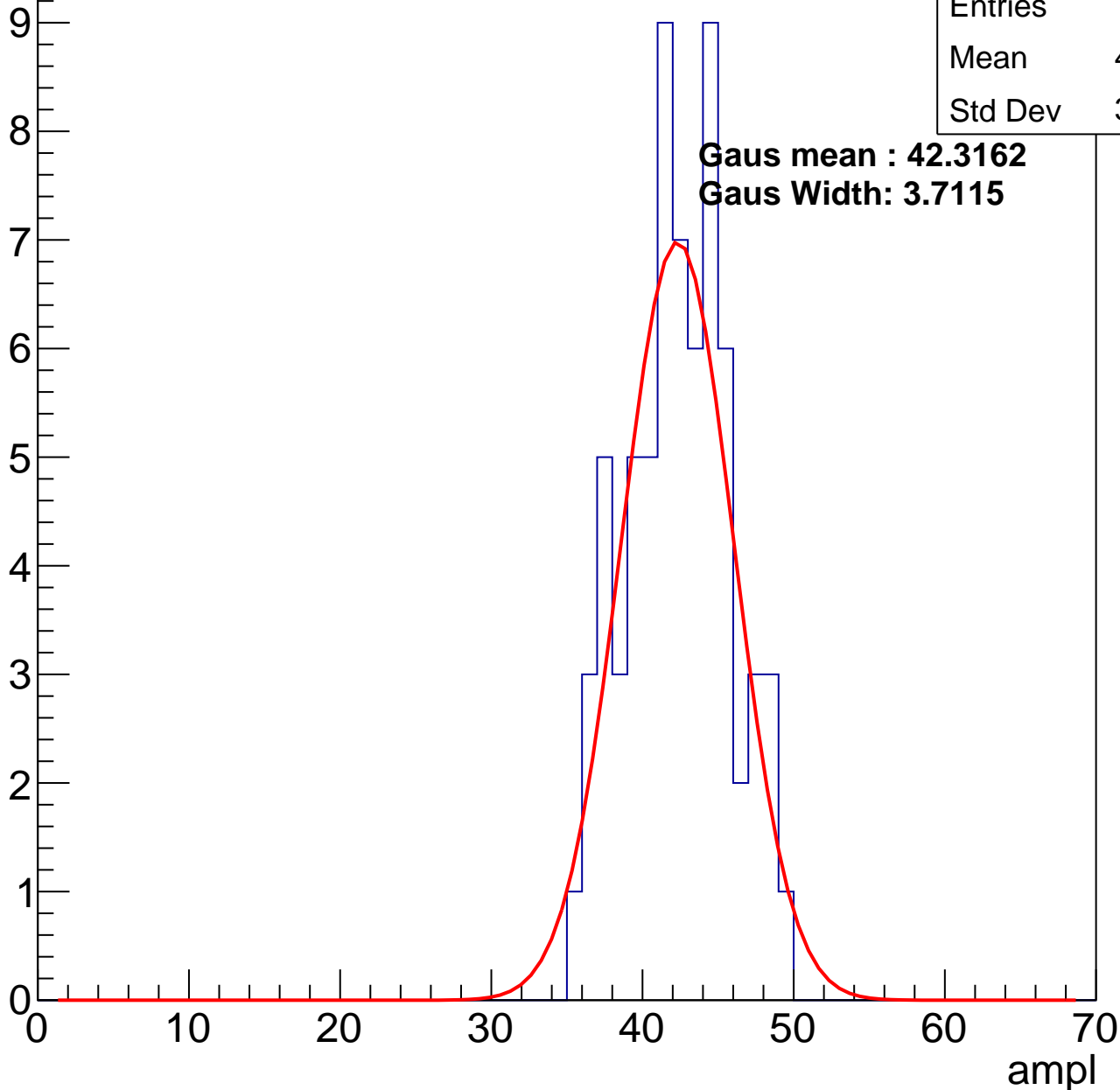
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	41.91
Std Dev	3.351

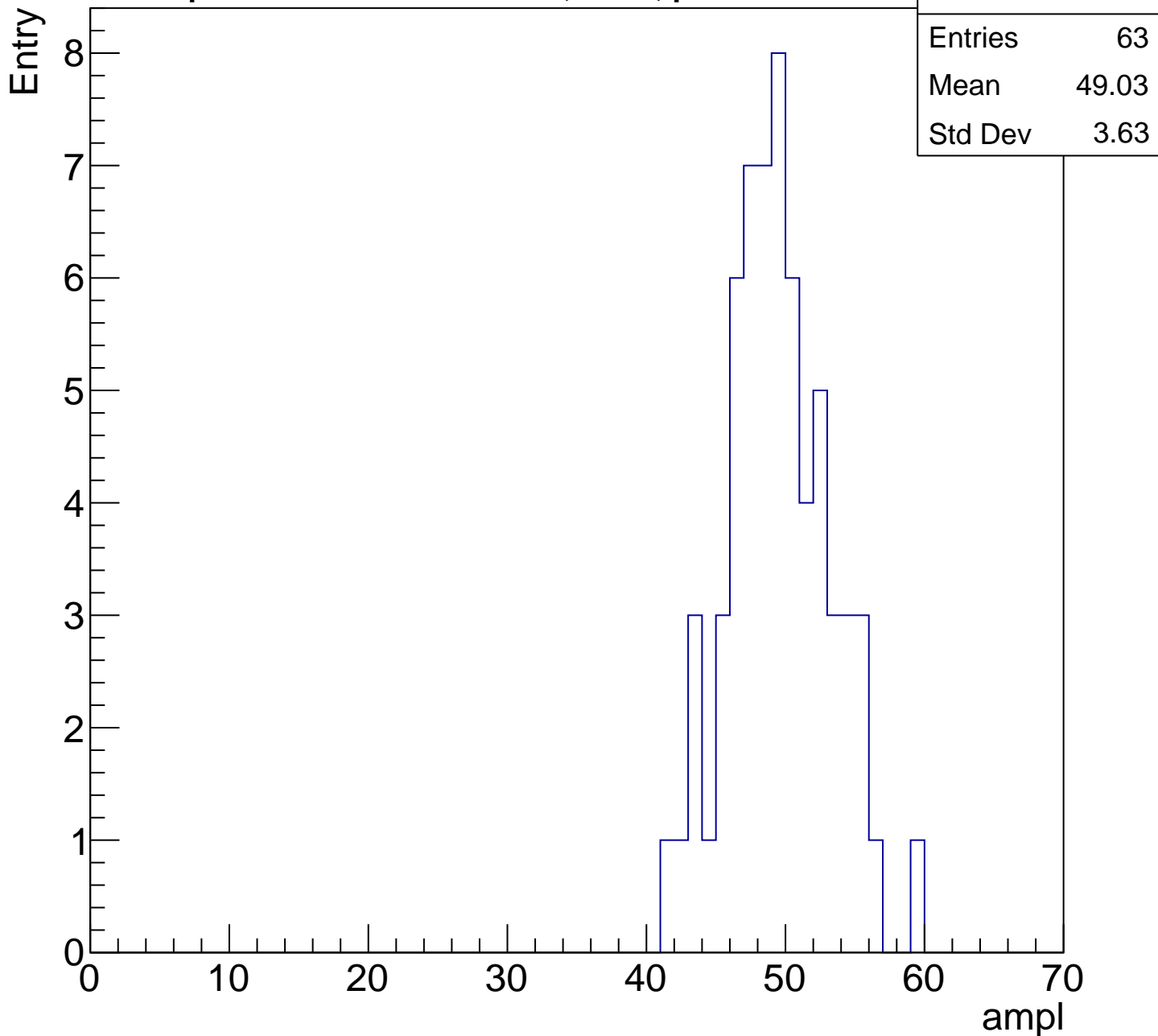
Gaus mean : 42.3162

Gaus Width: 3.7115



B0L002S, U4-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

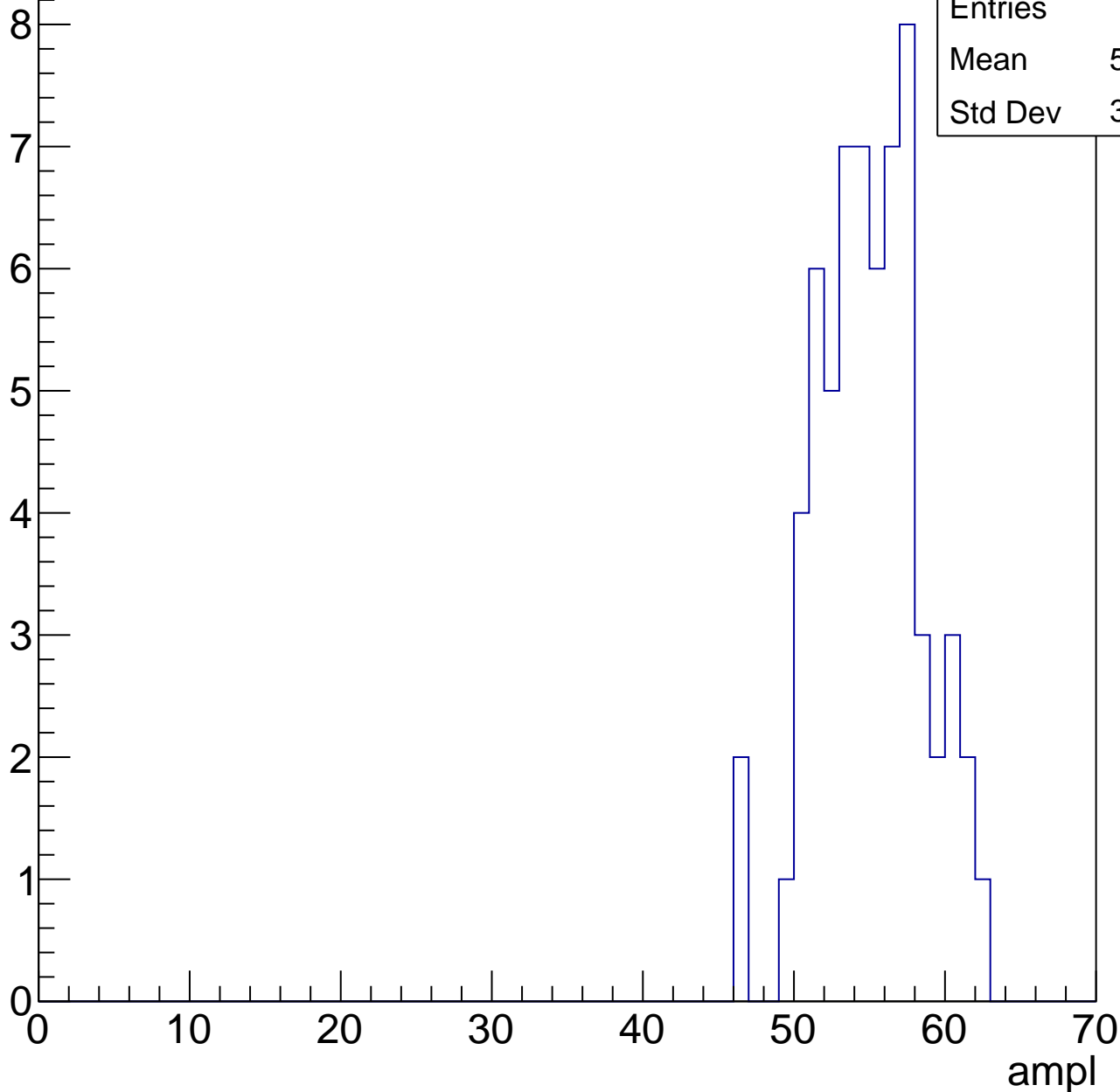


B0L002S, U4-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	54.53
Std Dev	3.432

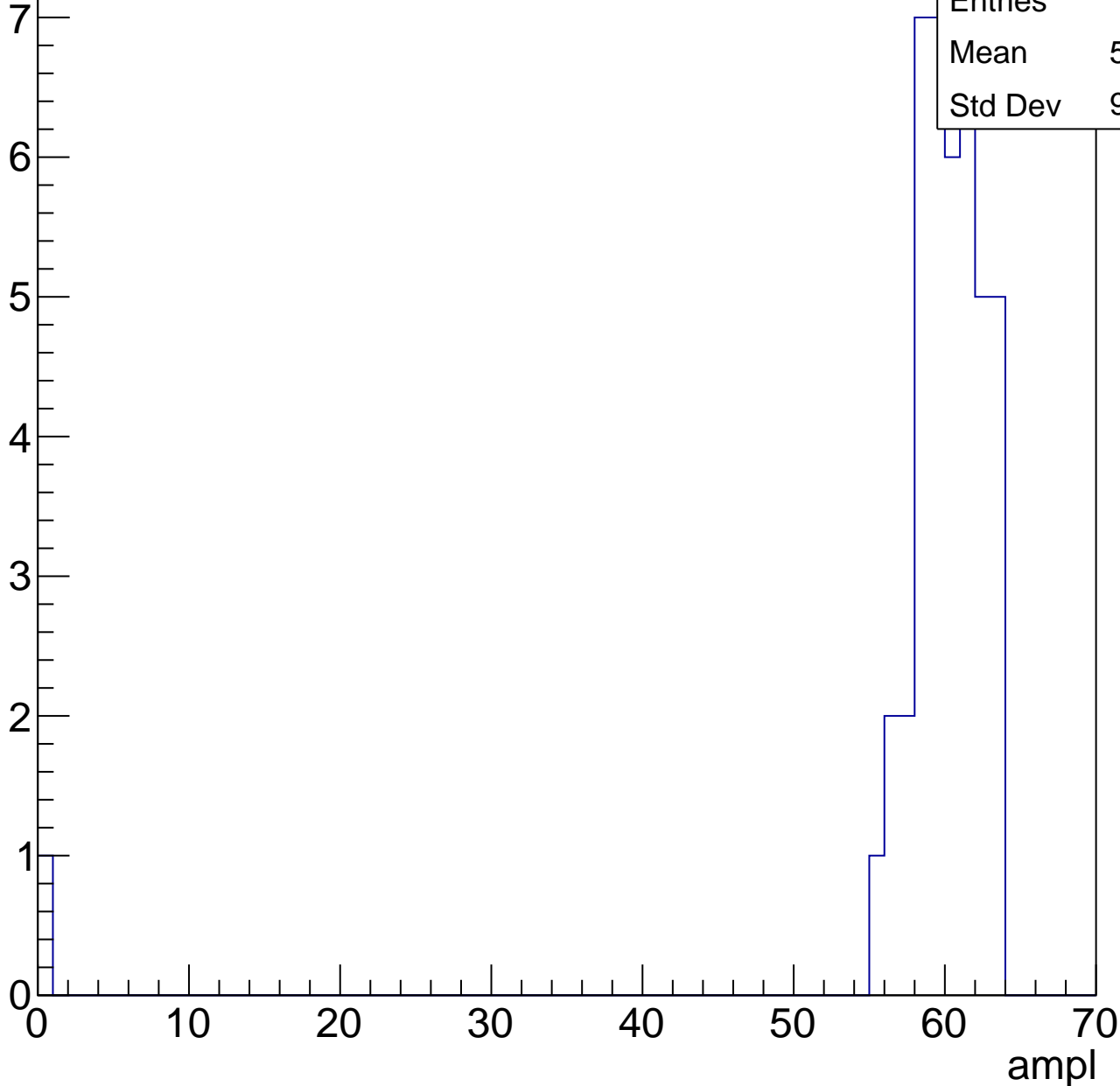


B0L002S, U4-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

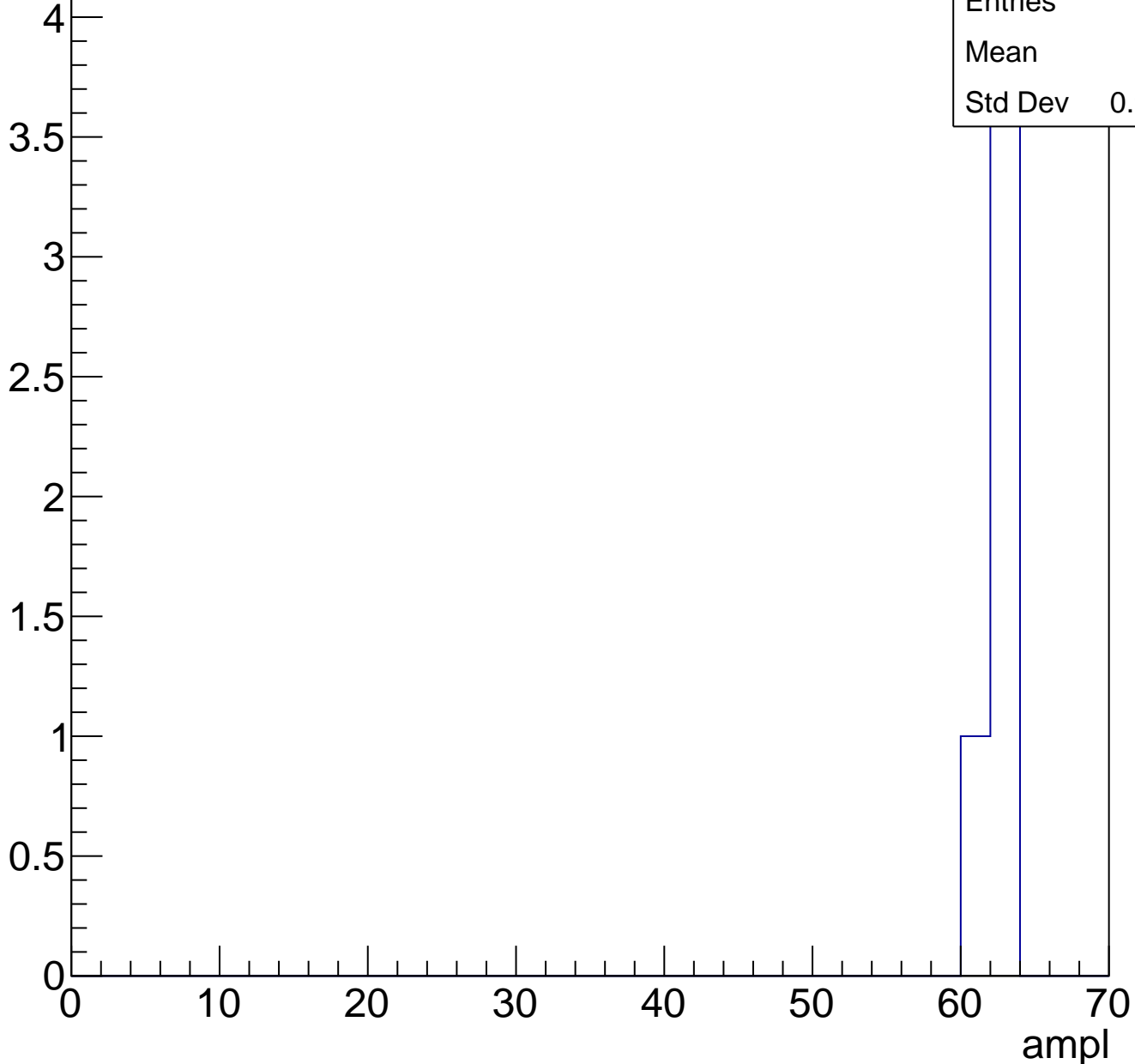
Entries	43
Mean	58.42
Std Dev	9.244



B0L002S, U4-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	10
Mean	62.1
Std Dev	0.9434

B0L002S, U4-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L002S, U4-ch93, adc0

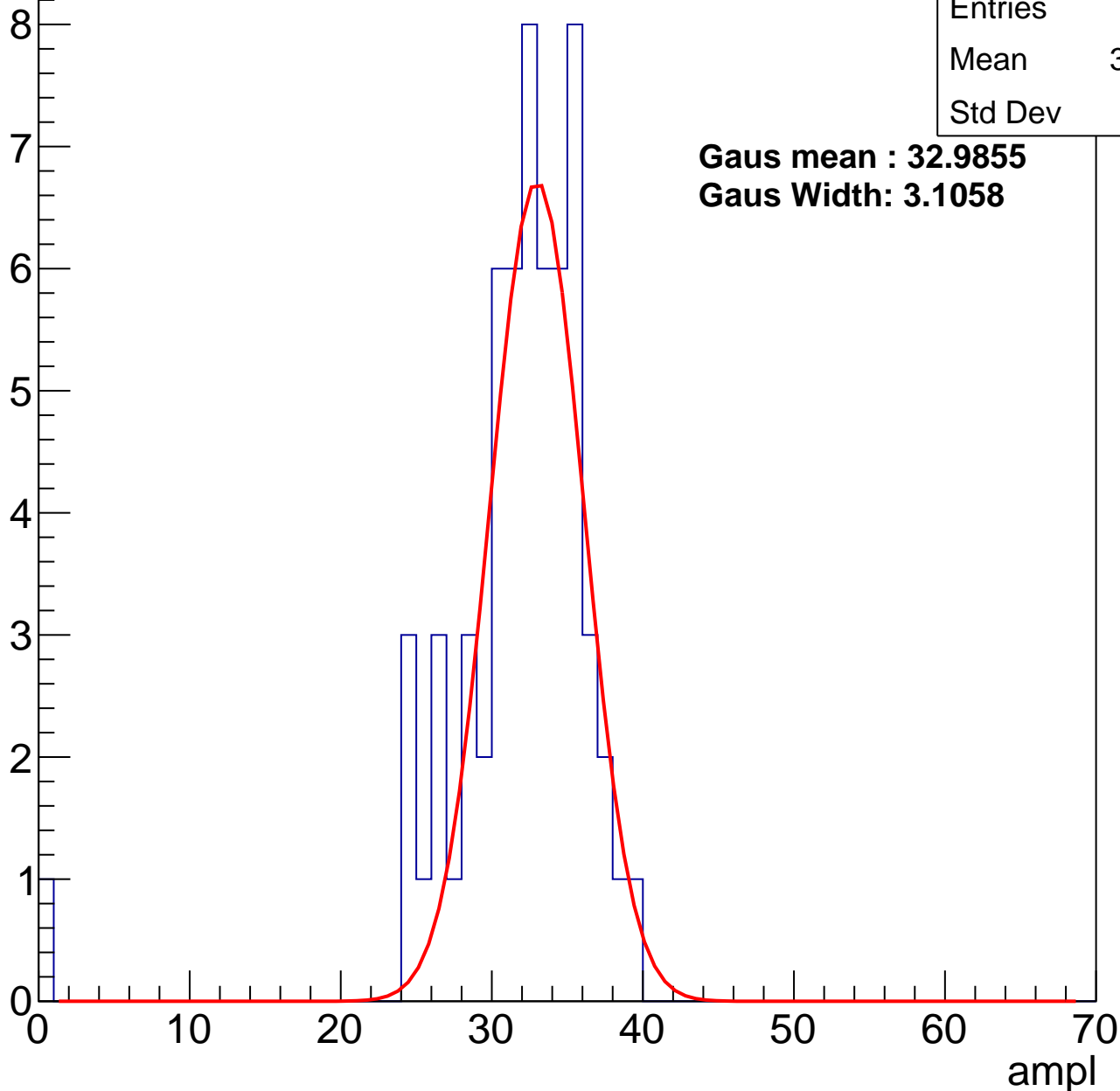
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	31.26
Std Dev	5.35

Gaus mean : 32.9855

Gaus Width: 3.1058



B0L002S, U4-ch93, adc1

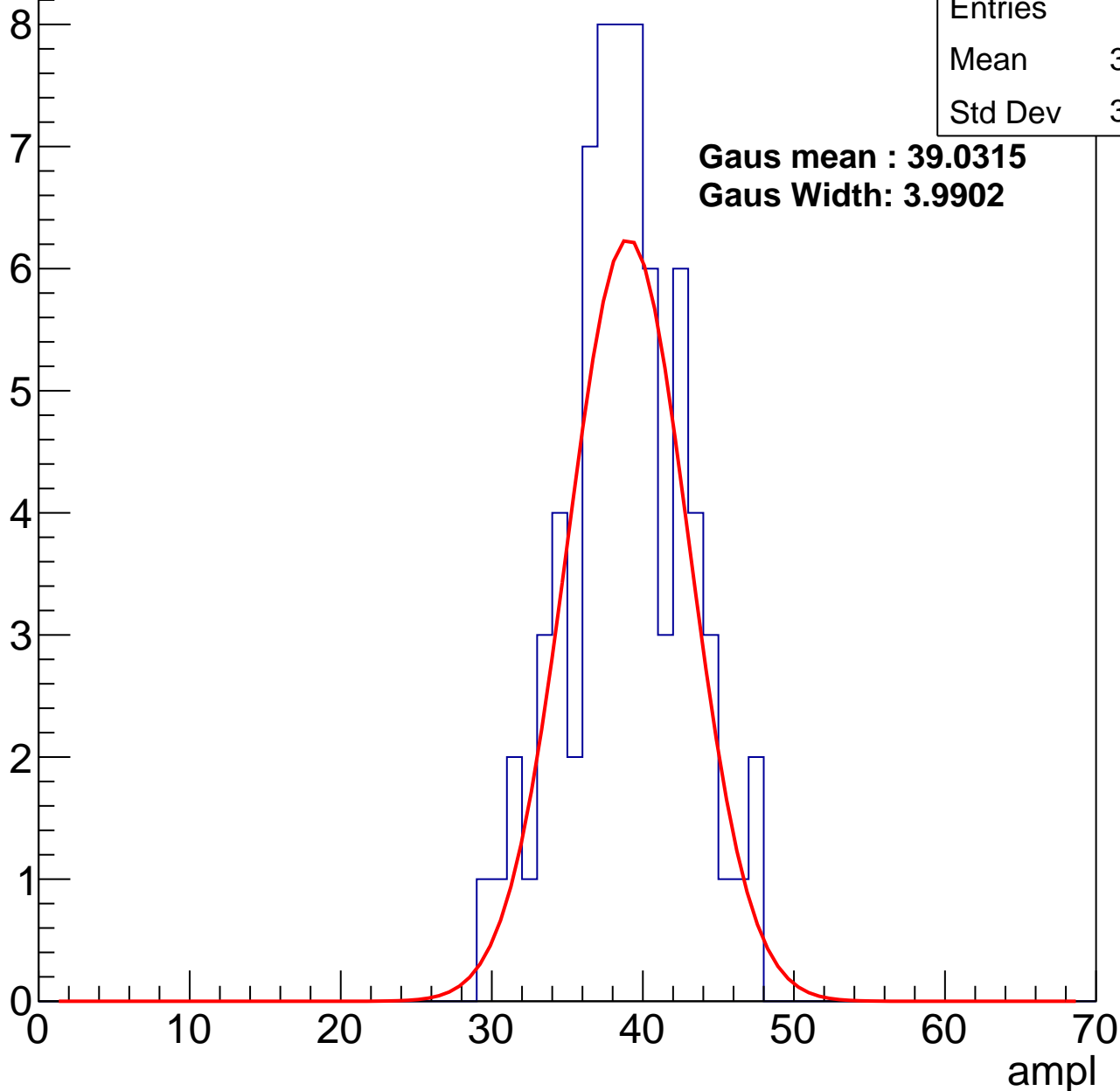
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.39
Std Dev	3.934

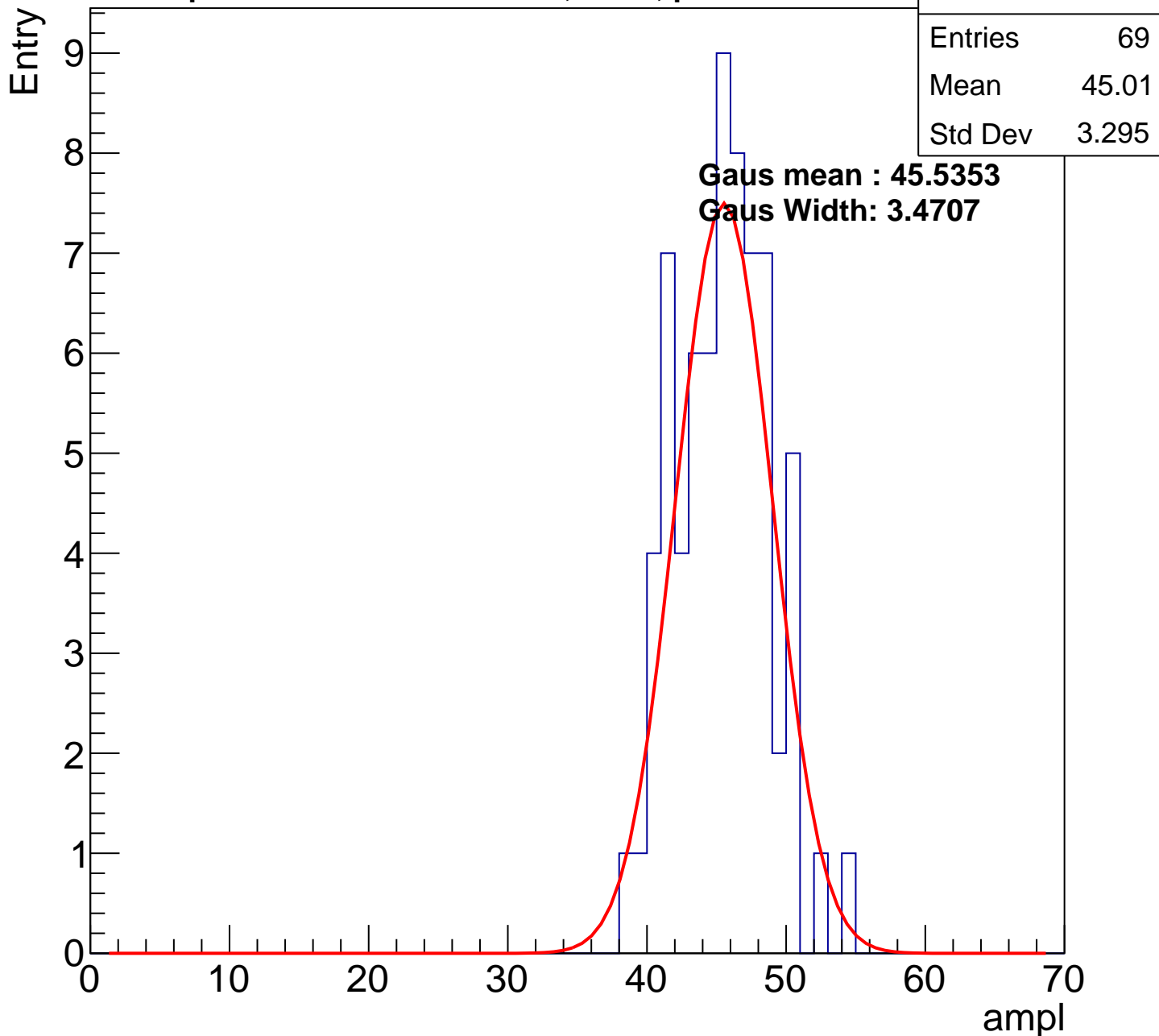
Gaus mean : 39.0315

Gaus Width: 3.9902



B0L002S, U4-ch93, adc2

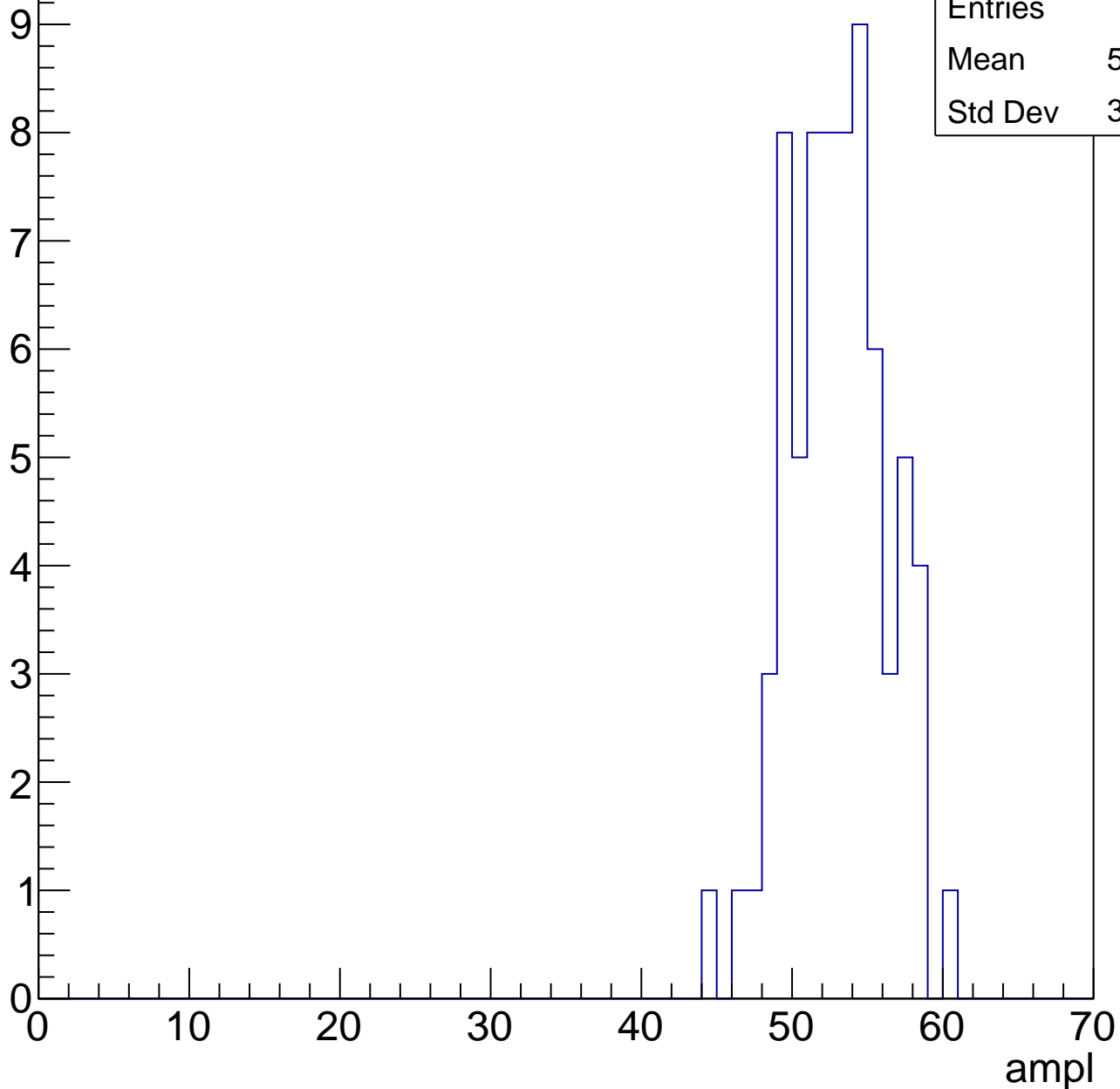
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch93, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



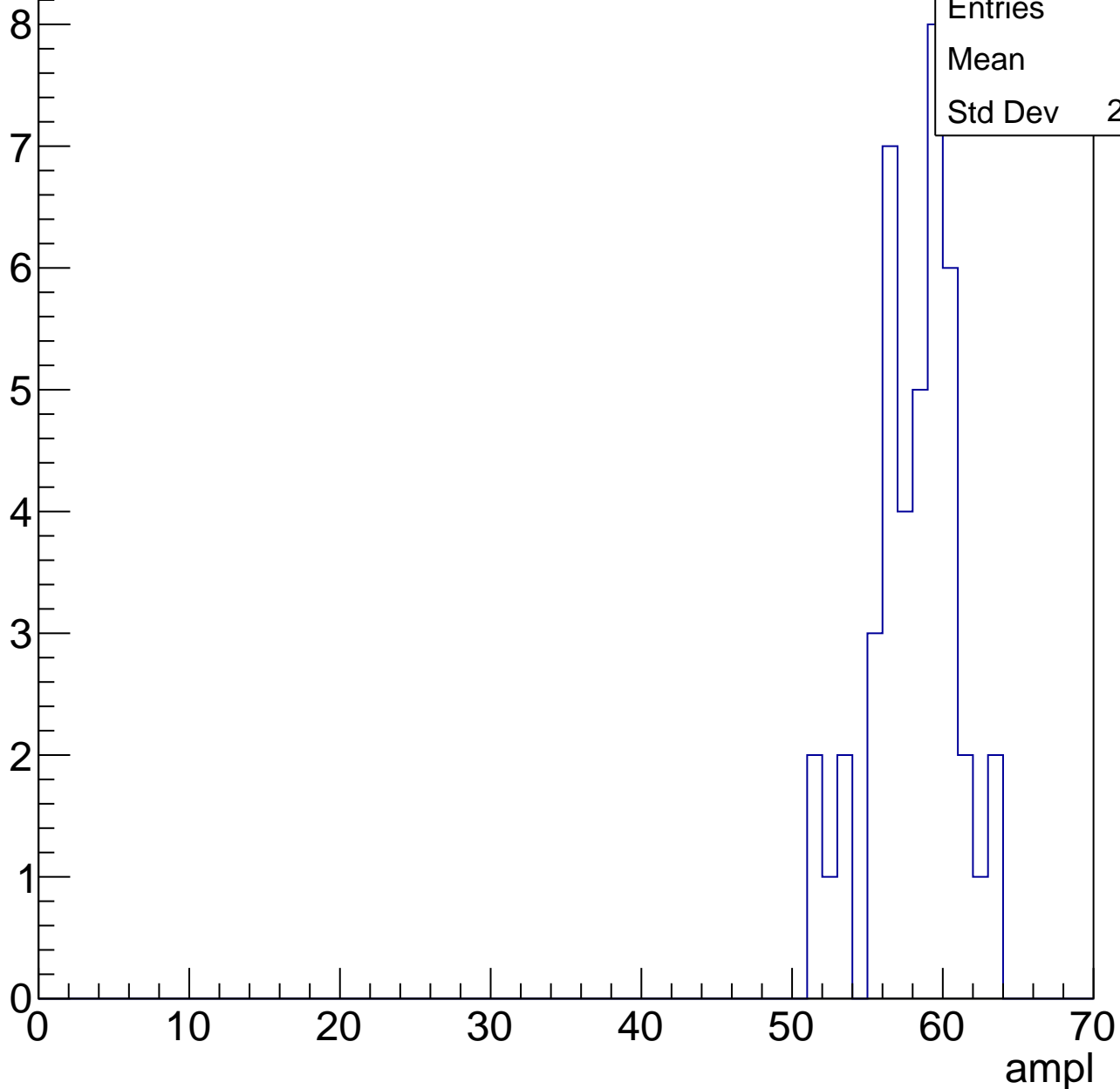
Entries	71
Mean	52.56
Std Dev	3.205

B0L002S, U4-ch93, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	57.6
Std Dev	2.862



B0L002S, U4-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	59.16
Std Dev	10.78

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

B0L002S, U4-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch94, adc0

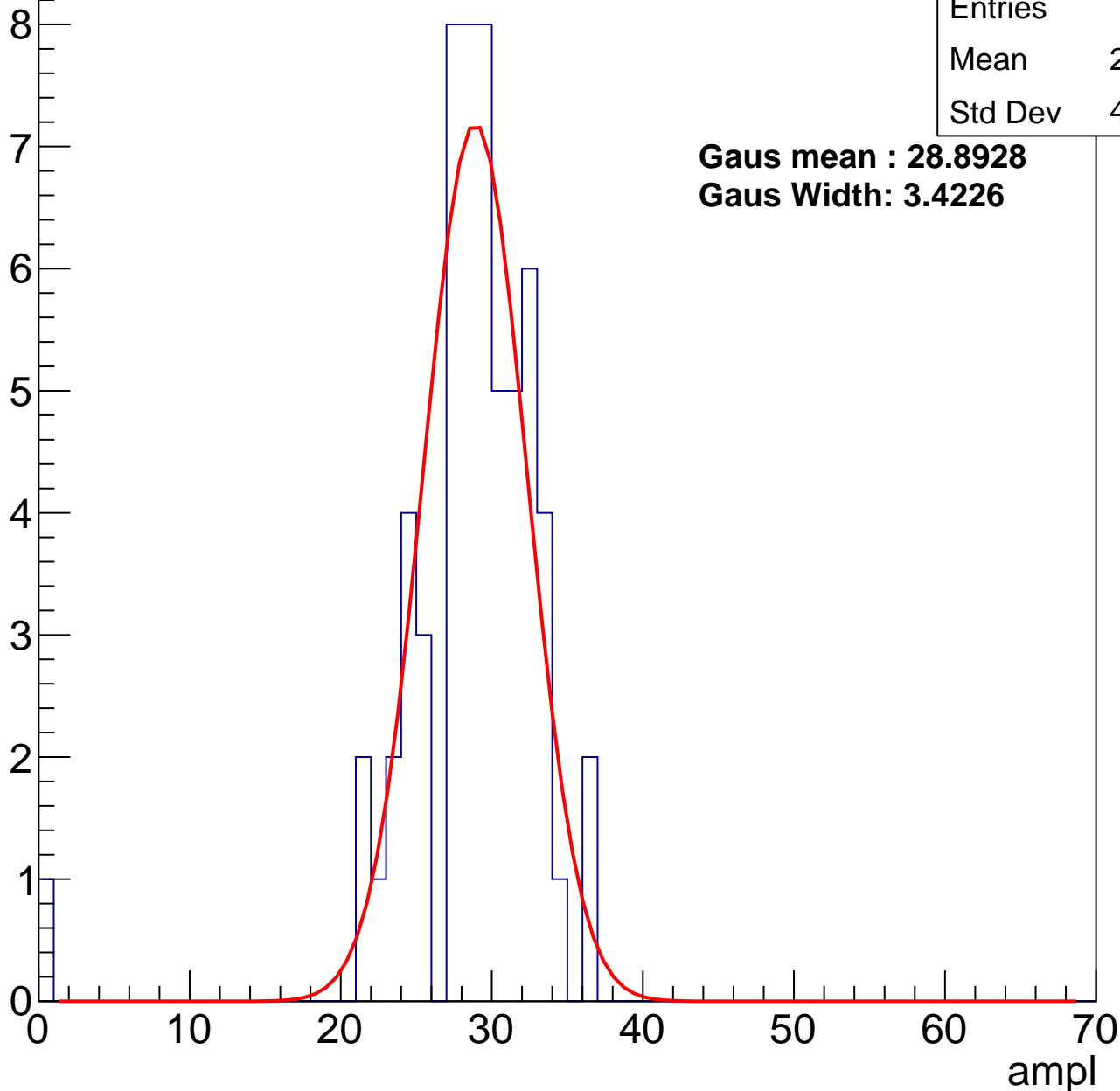
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28.13
Std Dev	4.998

Gaus mean : 28.8928

Gaus Width: 3.4226



B0L002S, U4-ch94, adc1

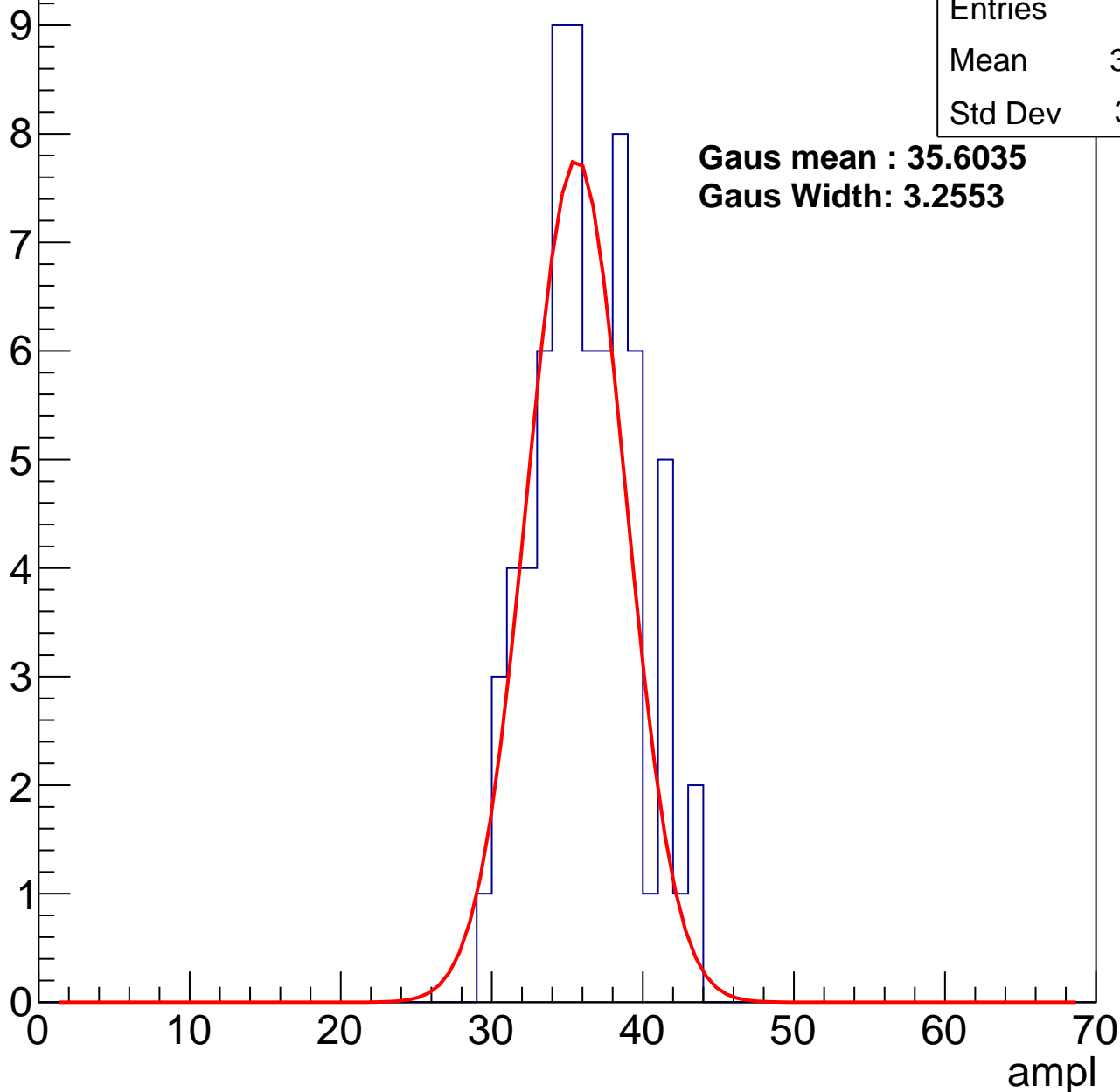
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	35.76
Std Dev	3.321

Gaus mean : 35.6035

Gaus Width: 3.2553



B0L002S, U4-ch94, adc2

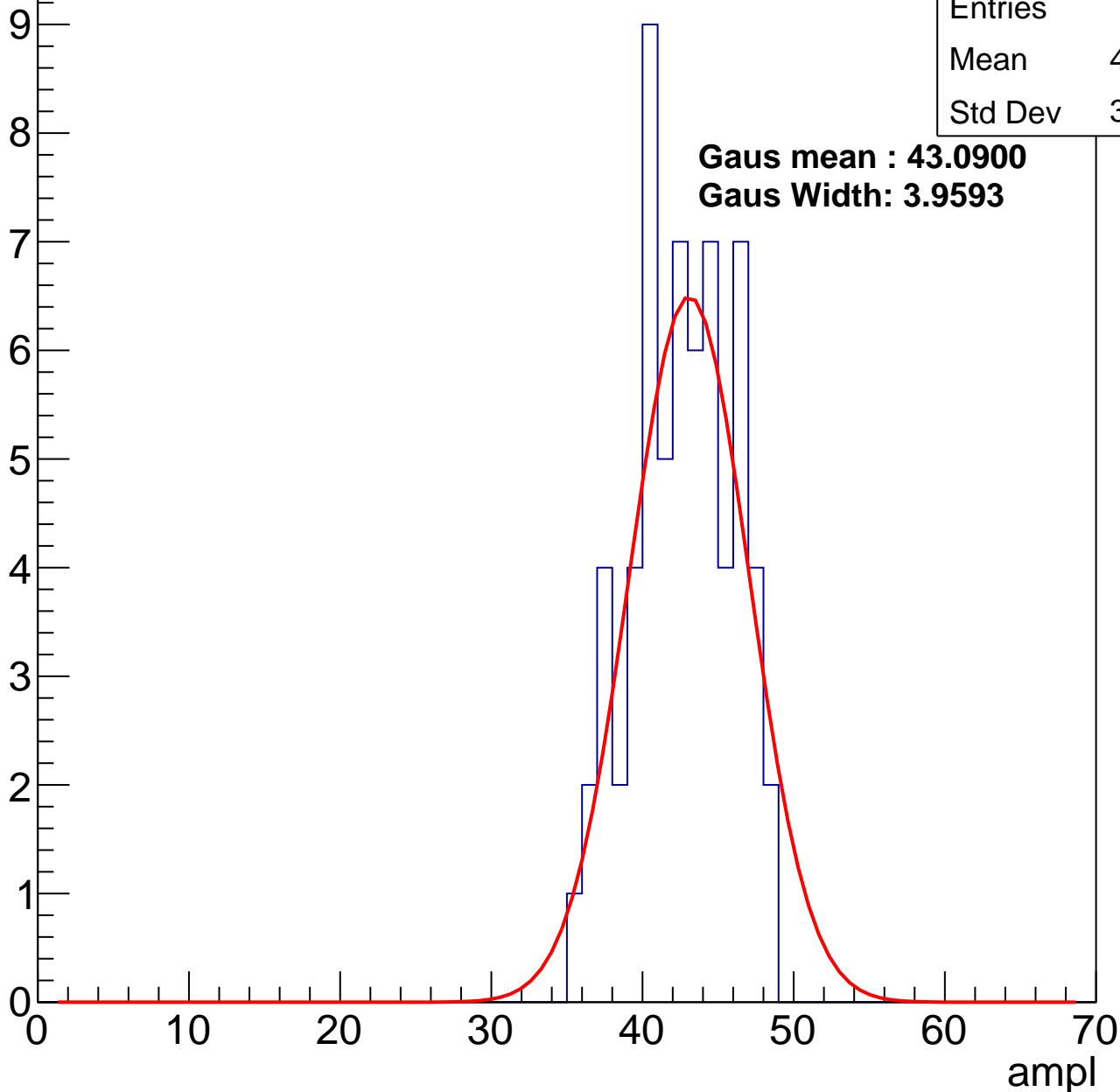
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.16
Std Dev	3.265

Gaus mean : 43.0900

Gaus Width: 3.9593



B0L002S, U4-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	48.97
Std Dev	3.239

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

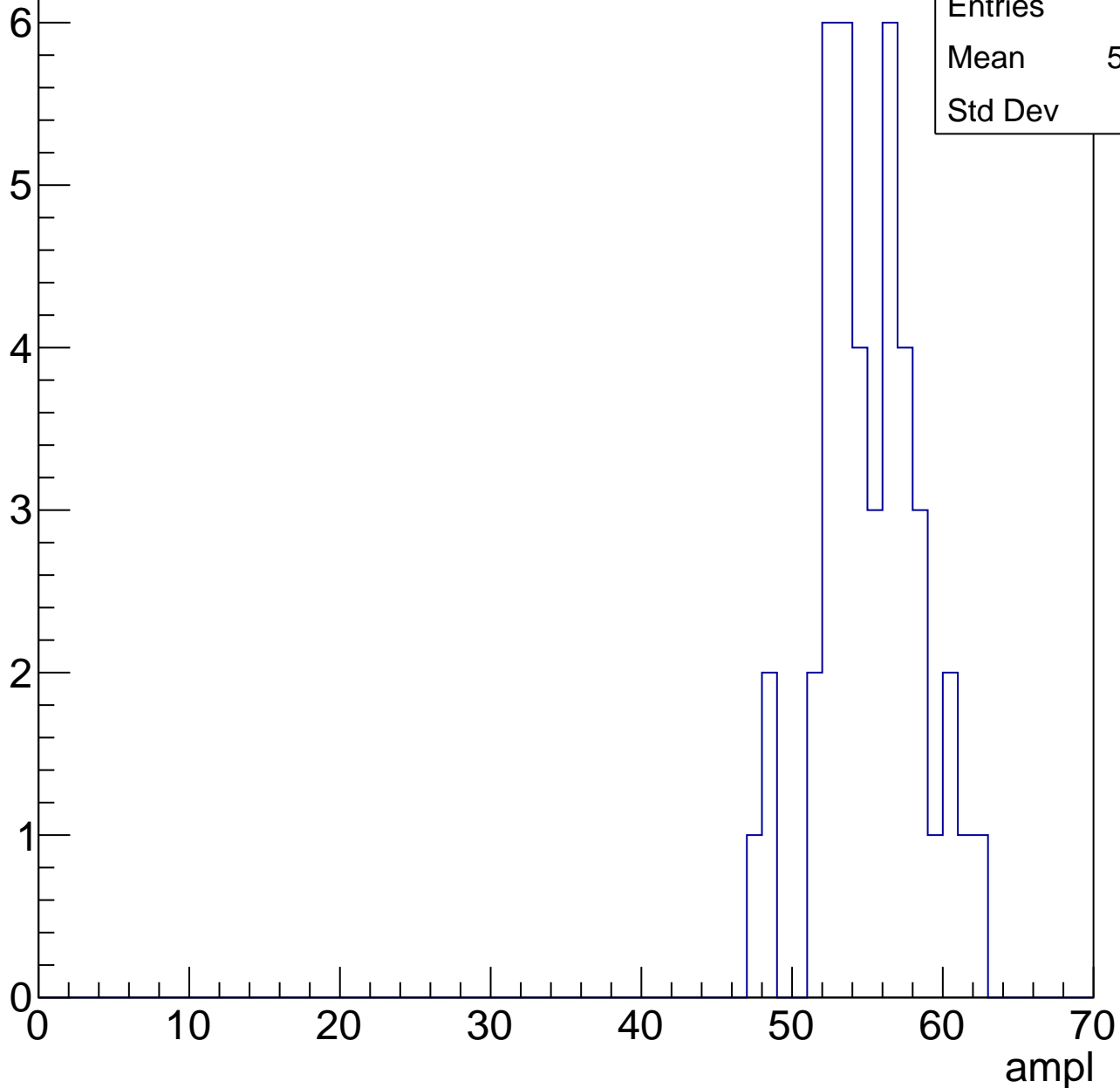
ampl



B0L002S, U4-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	42
Mean	54.67
Std Dev	3.35

B0L002S, U4-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 58

Mean 57.98

Std Dev 8.123

ampl

0

10

20

30

40

50

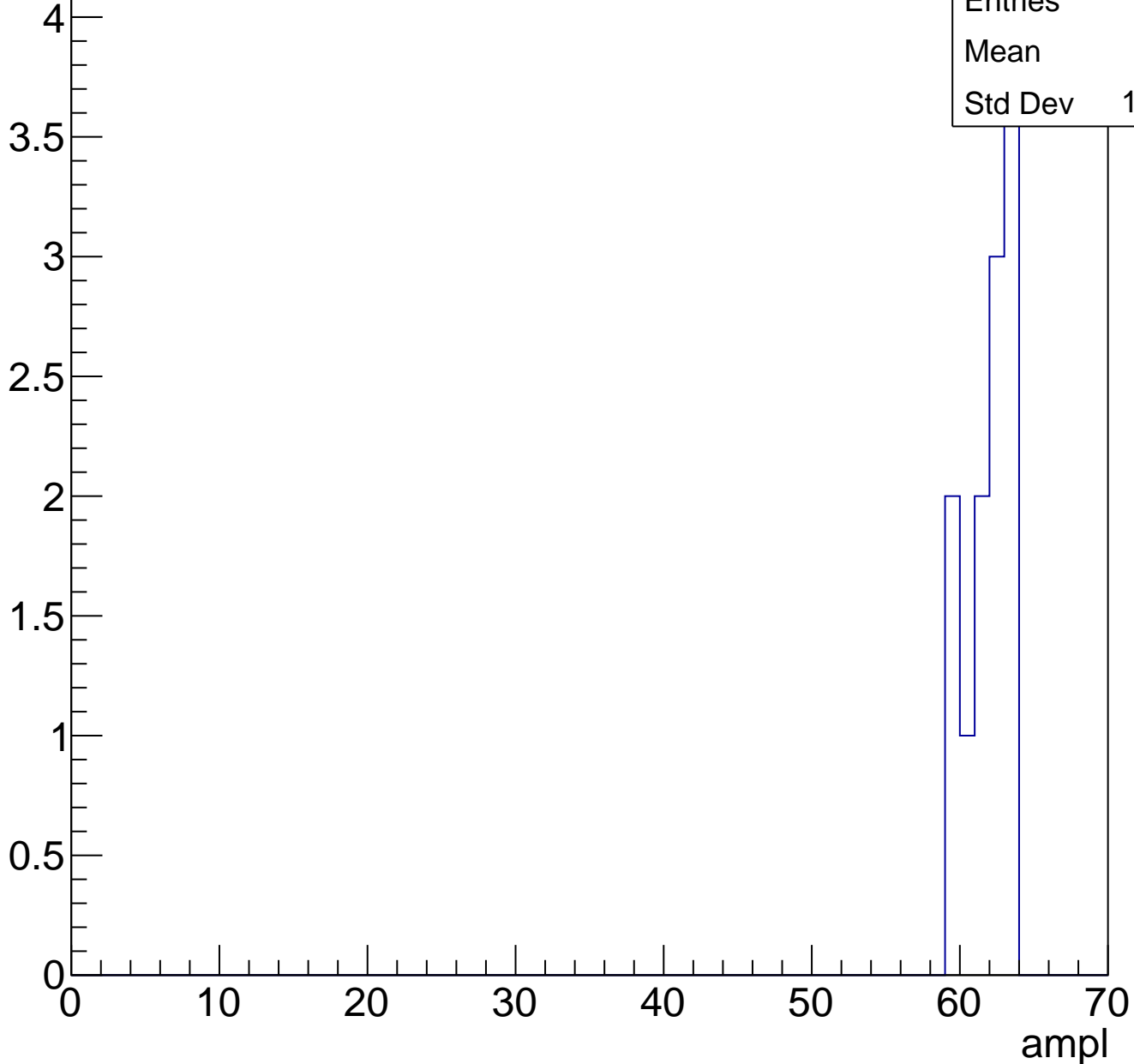
60

70

B0L002S, U4-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch95, adc0

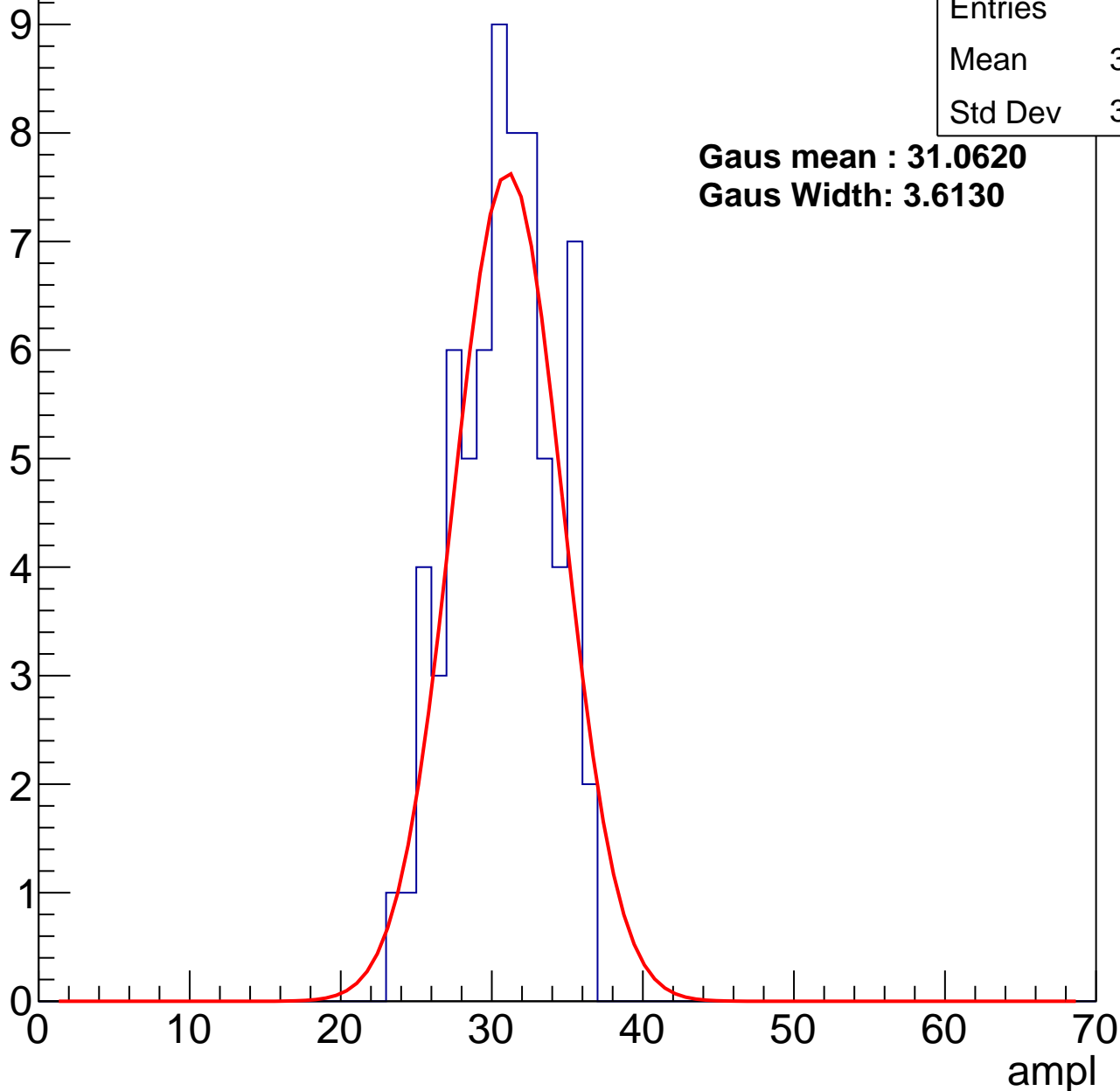
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.33
Std Dev	3.188

Gaus mean : 31.0620

Gaus Width: 3.6130



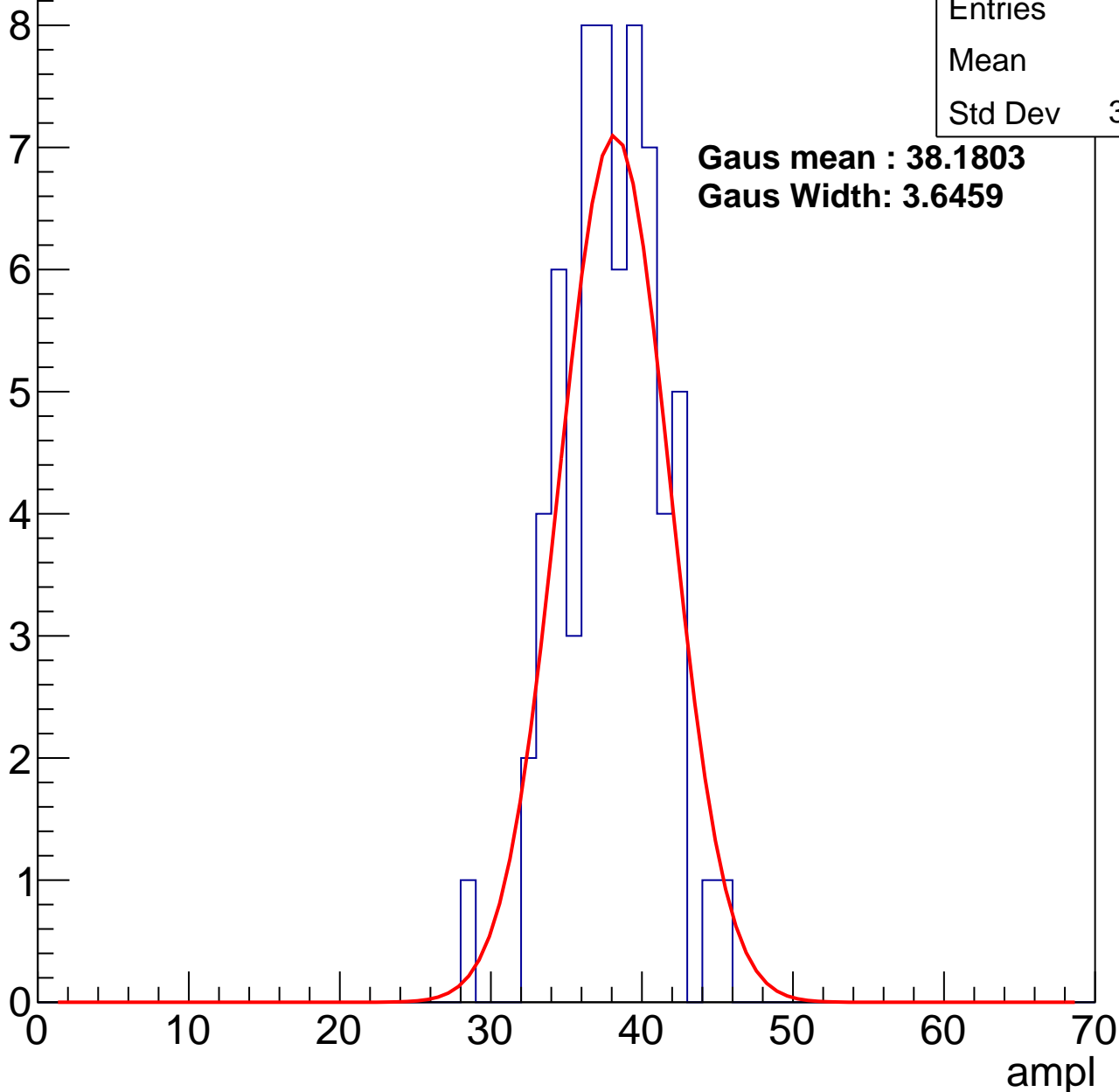
B0L002S, U4-ch95, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.5
Std Dev	3.202

Gaus mean : 38.1803
Gaus Width: 3.6459



B0L002S, U4-ch95, adc2

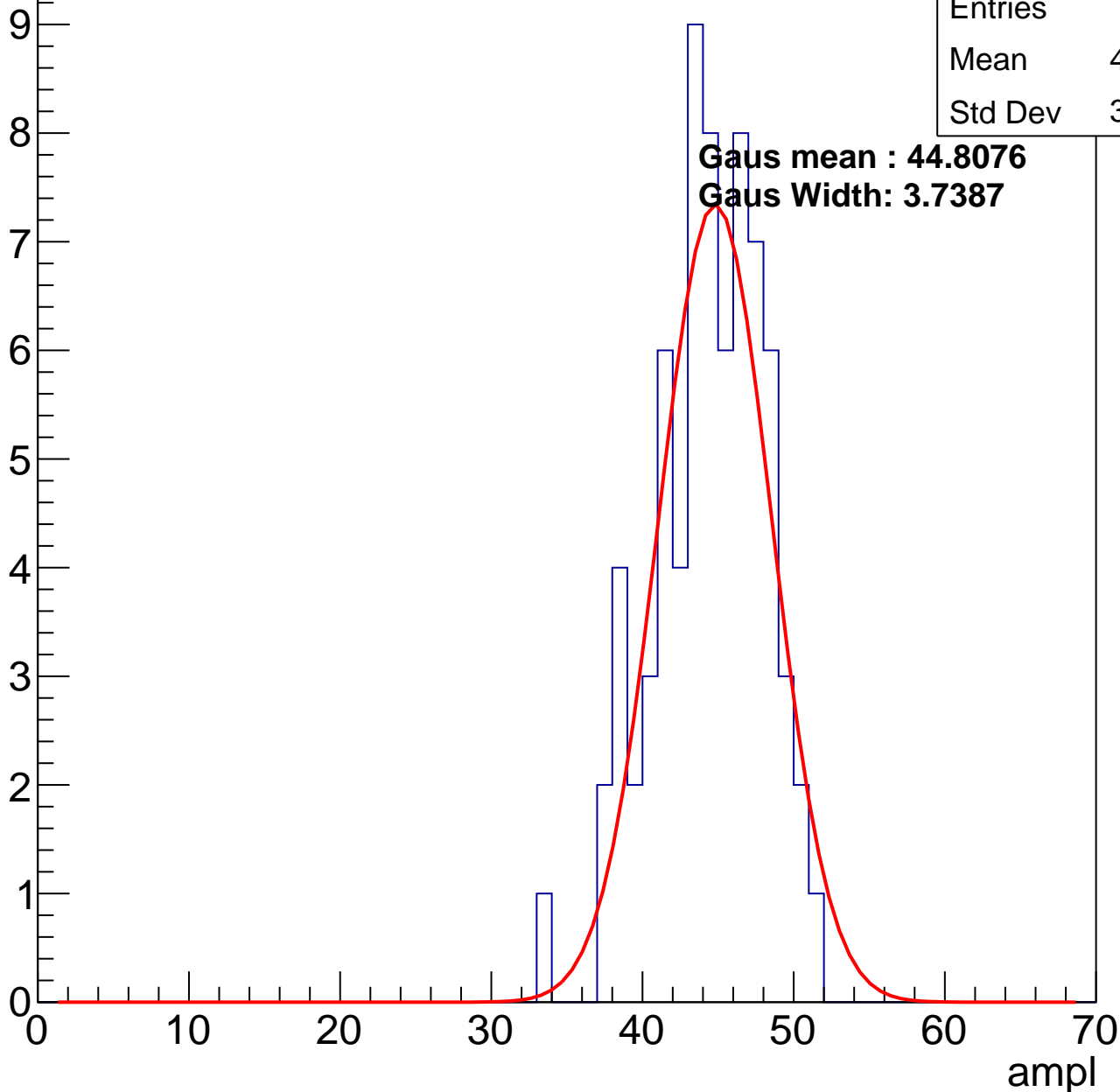
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.93
Std Dev	3.599

Gaus mean : 44.8076

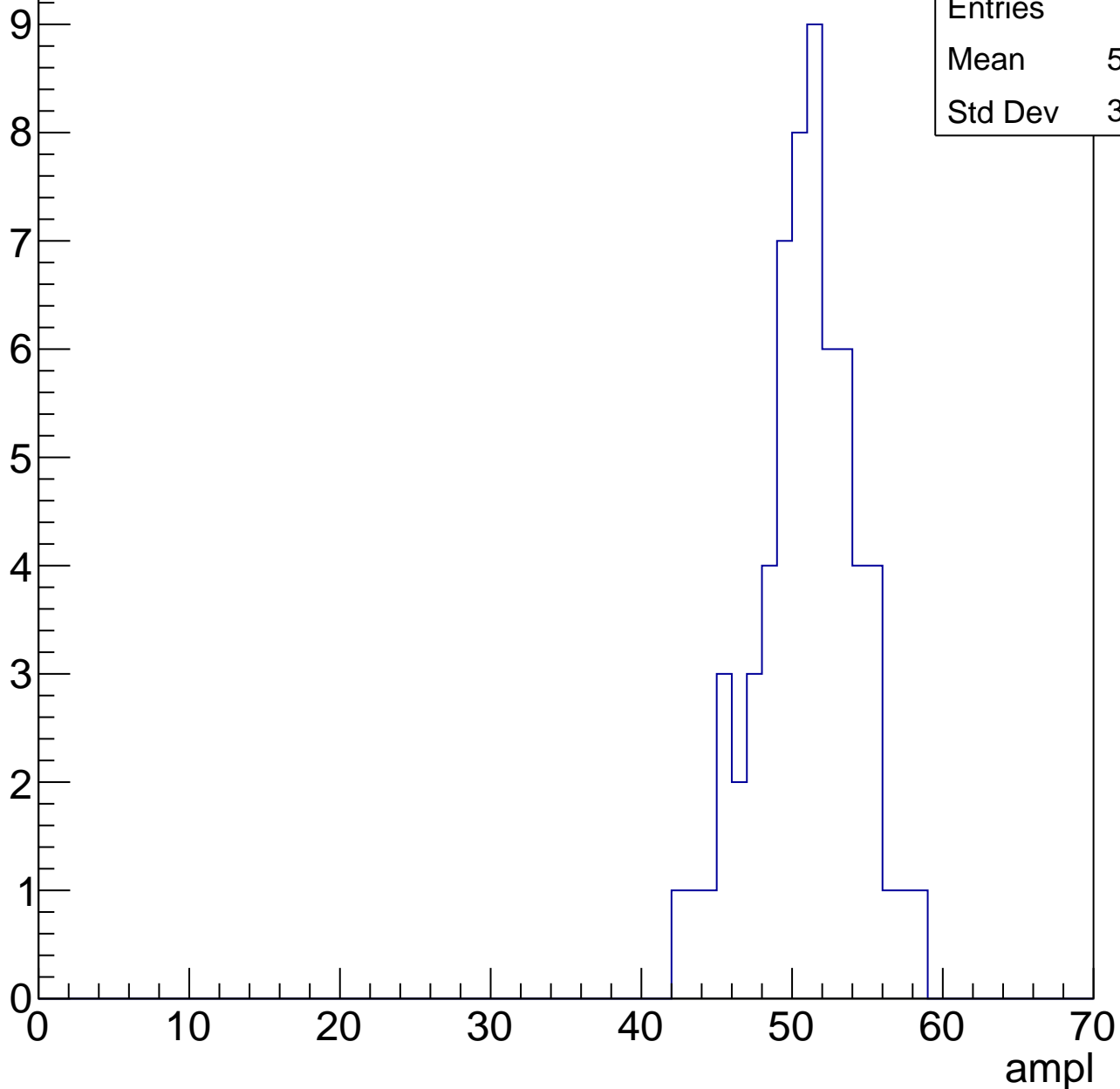
Gaus Width: 3.7387



B0L002S, U4-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



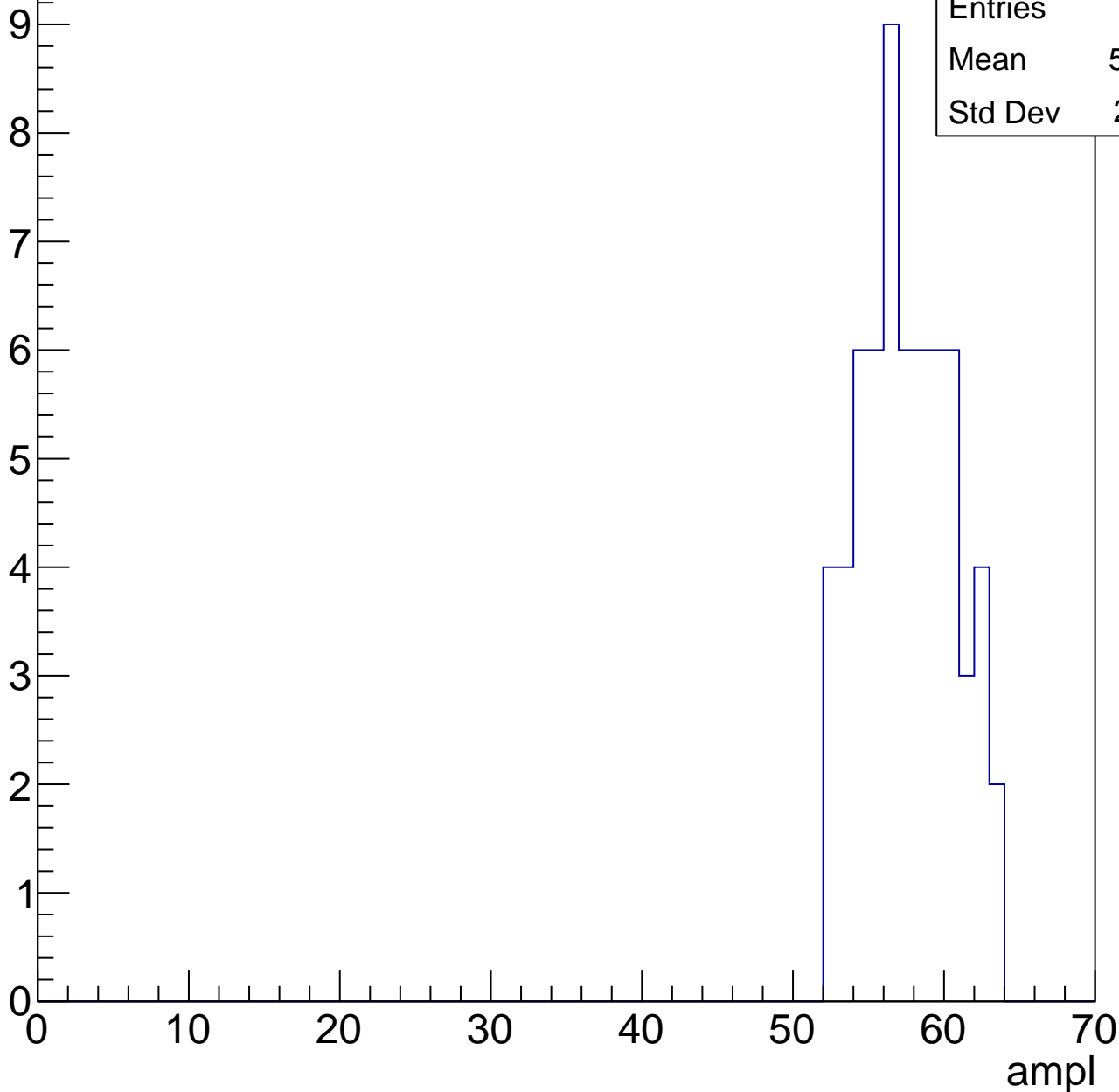
Entries	62
Mean	50.45
Std Dev	3.344

B0L002S, U4-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	57.08
Std Dev	2.991

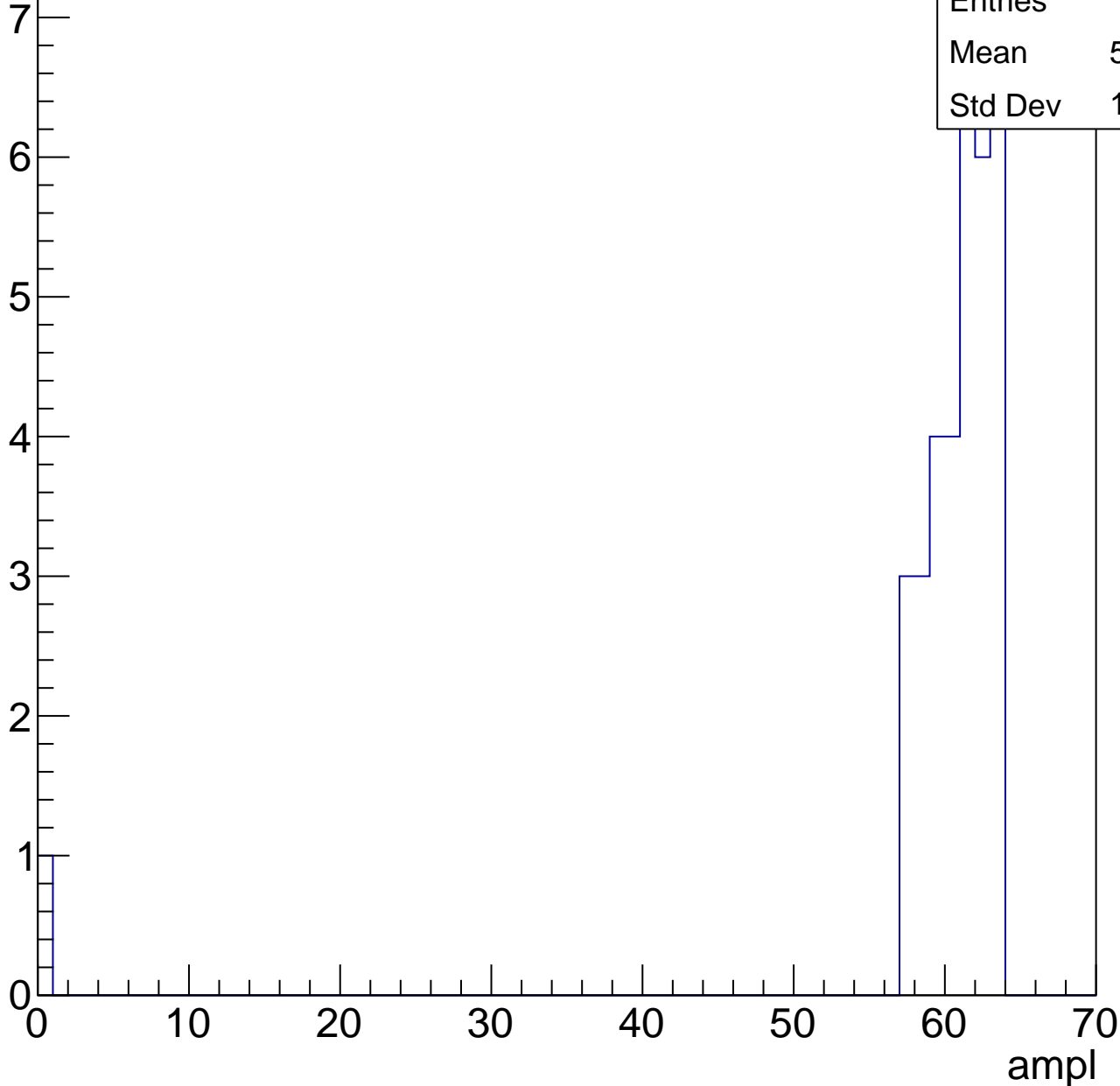


B0L002S, U4-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

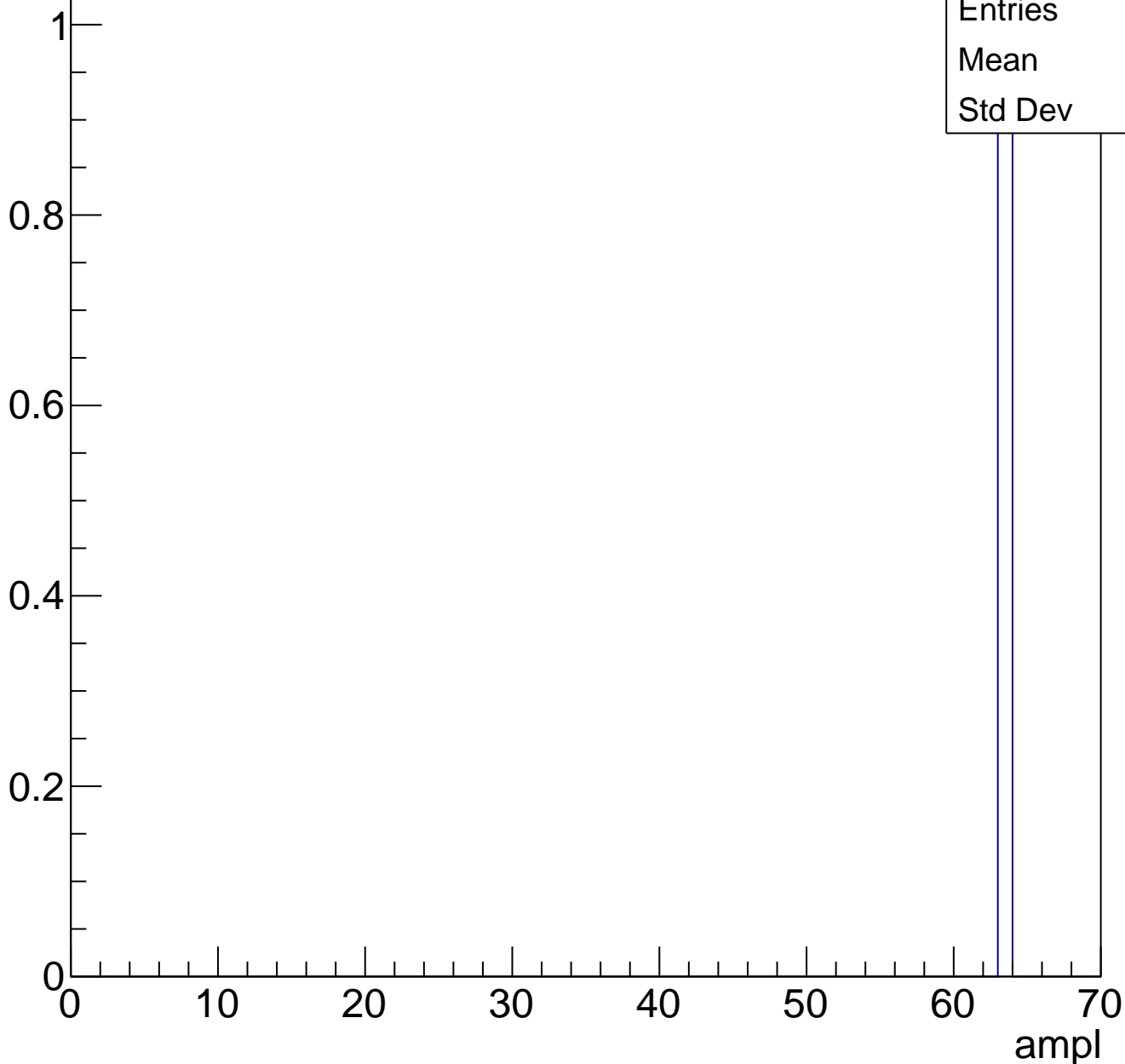
Entries	35
Mean	58.89
Std Dev	10.27



B0L002S, U4-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U4-ch96, adc0

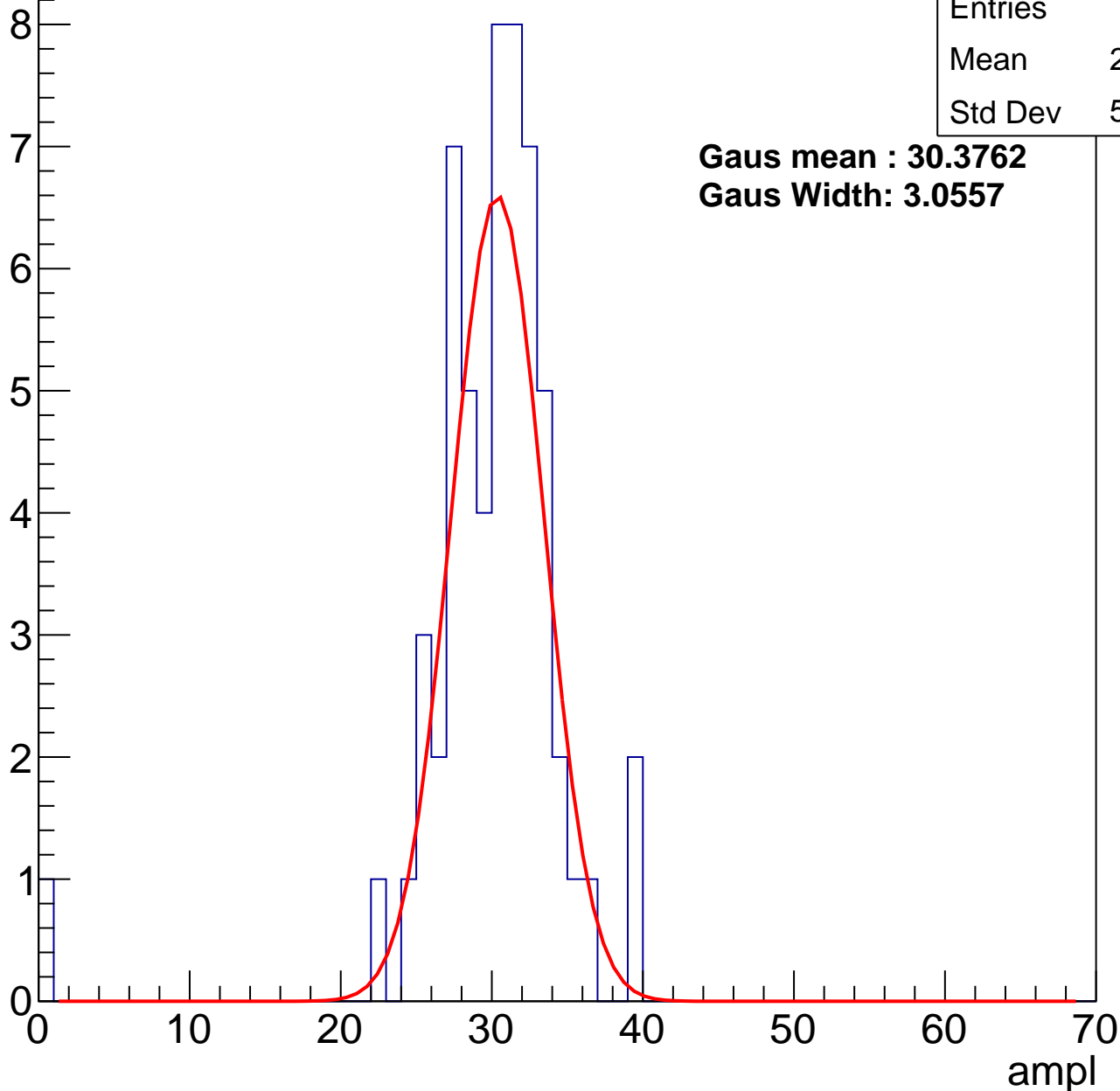
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.52
Std Dev	5.113

Gaus mean : 30.3762

Gaus Width: 3.0557

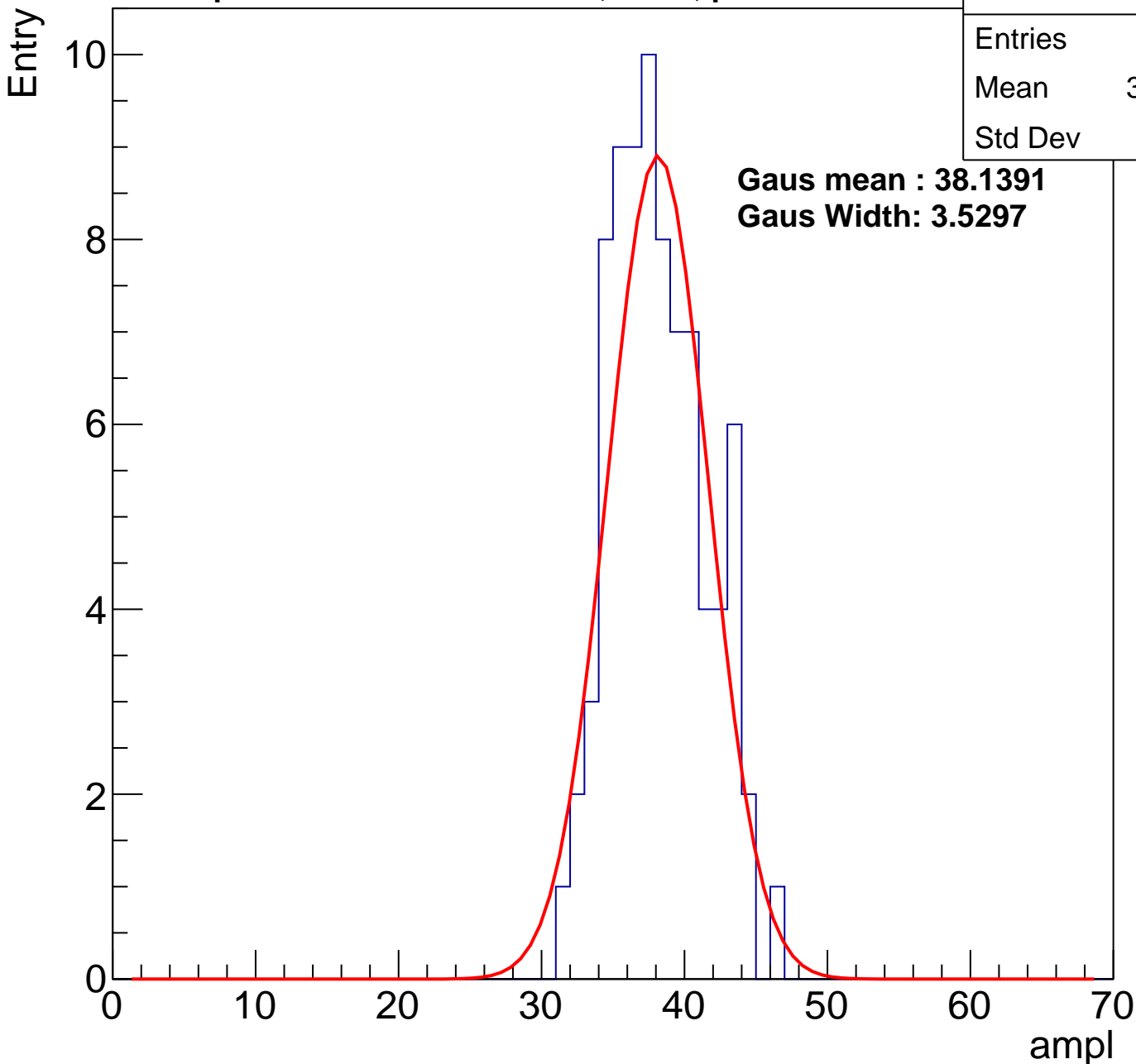


B0L002S, U4-ch96, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	81
Mean	37.73
Std Dev	3.27

Gaus mean : 38.1391
Gaus Width: 3.5297



B0L002S, U4-ch96, adc2

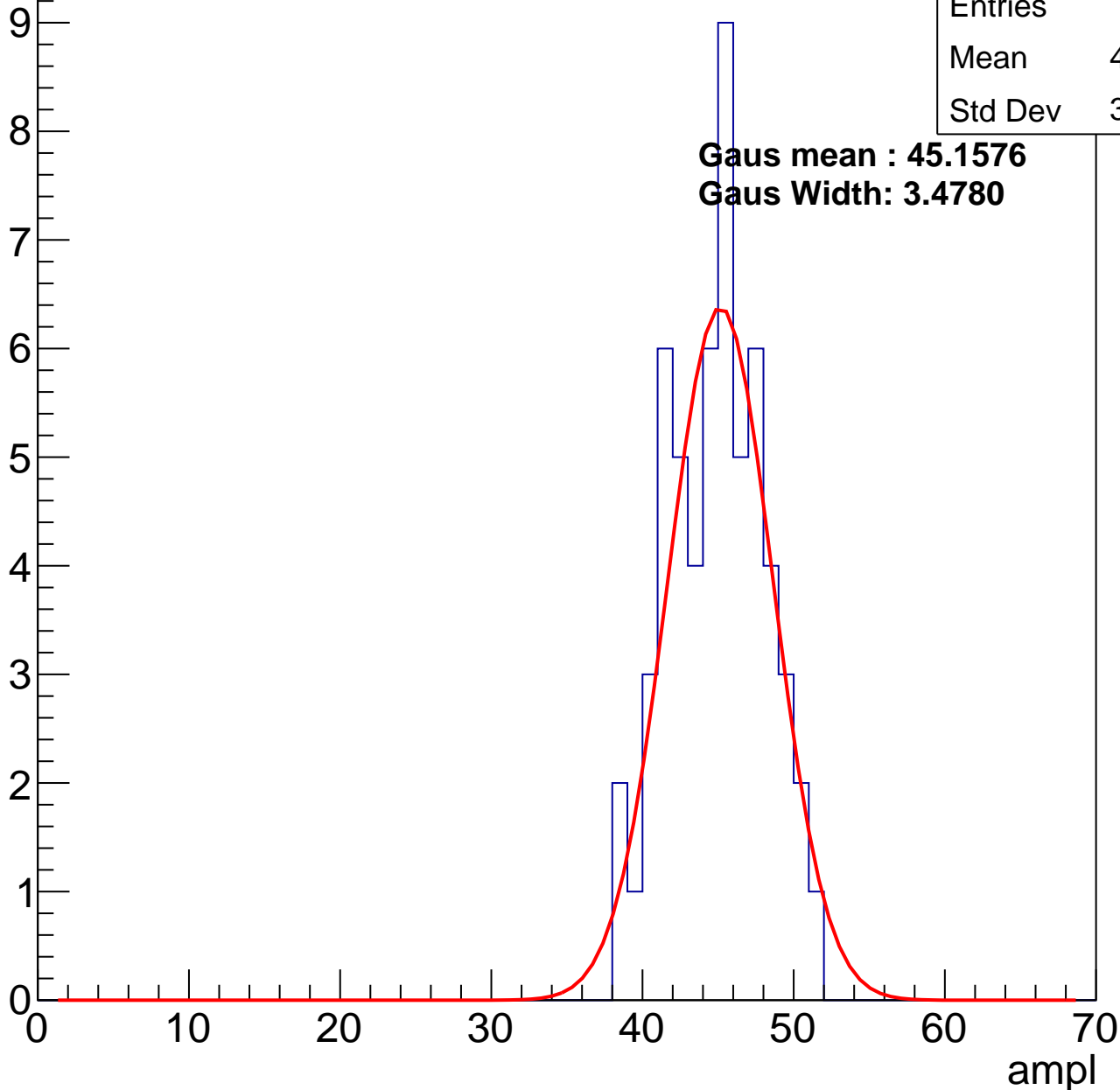
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.46
Std Dev	3.118

Gaus mean : 45.1576

Gaus Width: 3.4780

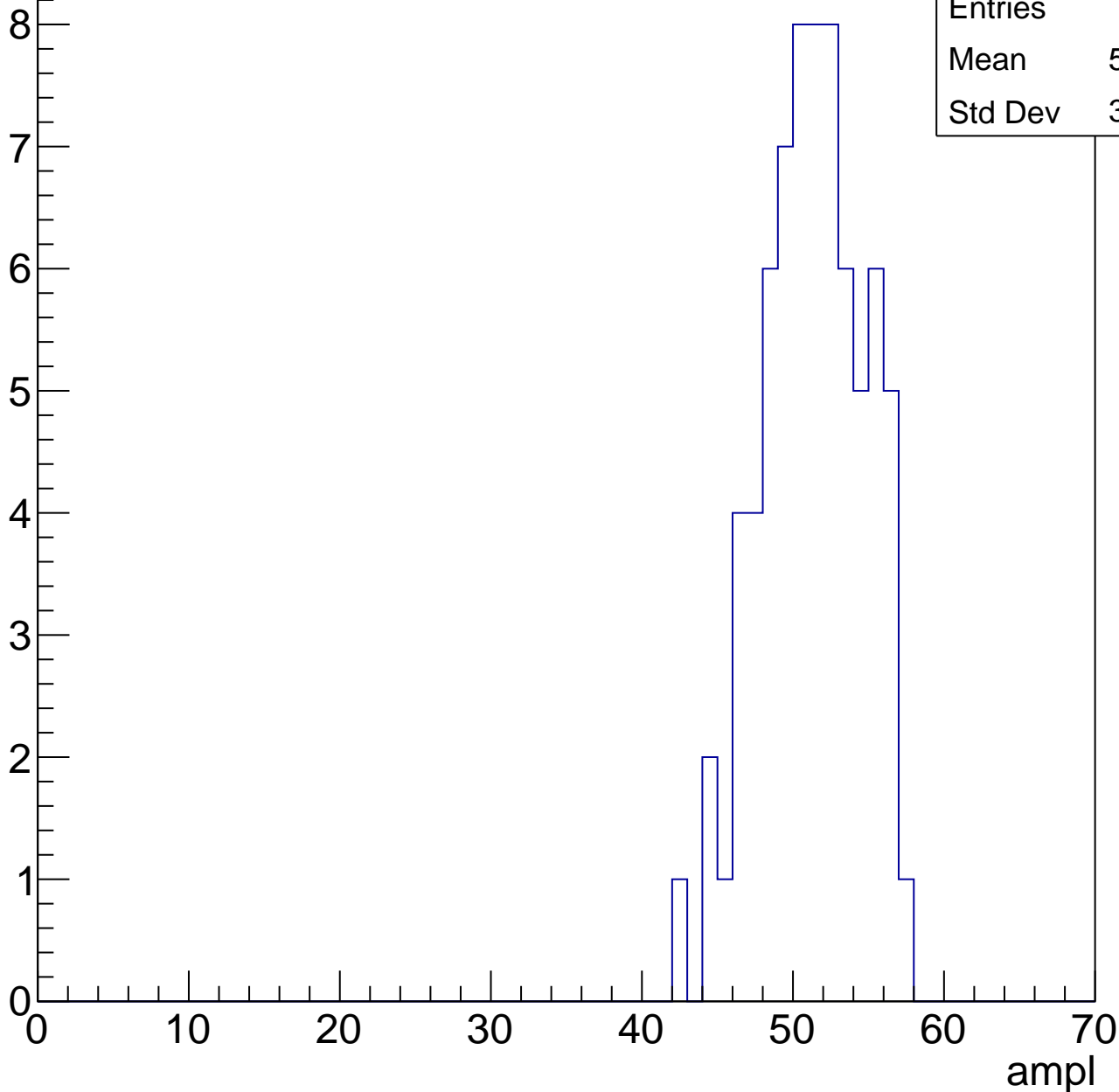


B0L002S, U4-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	50.79
Std Dev	3.333

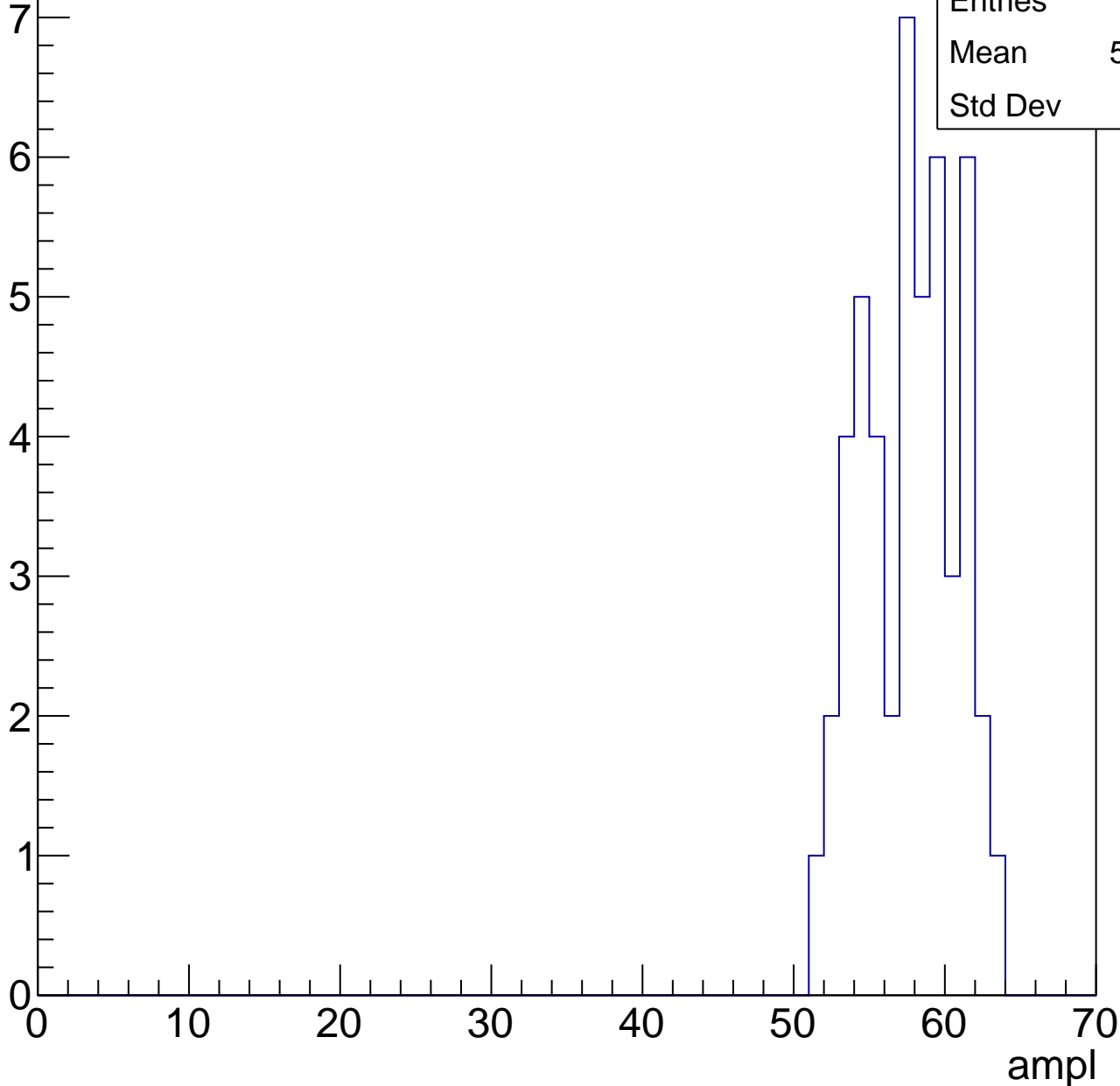


B0L002S, U4-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

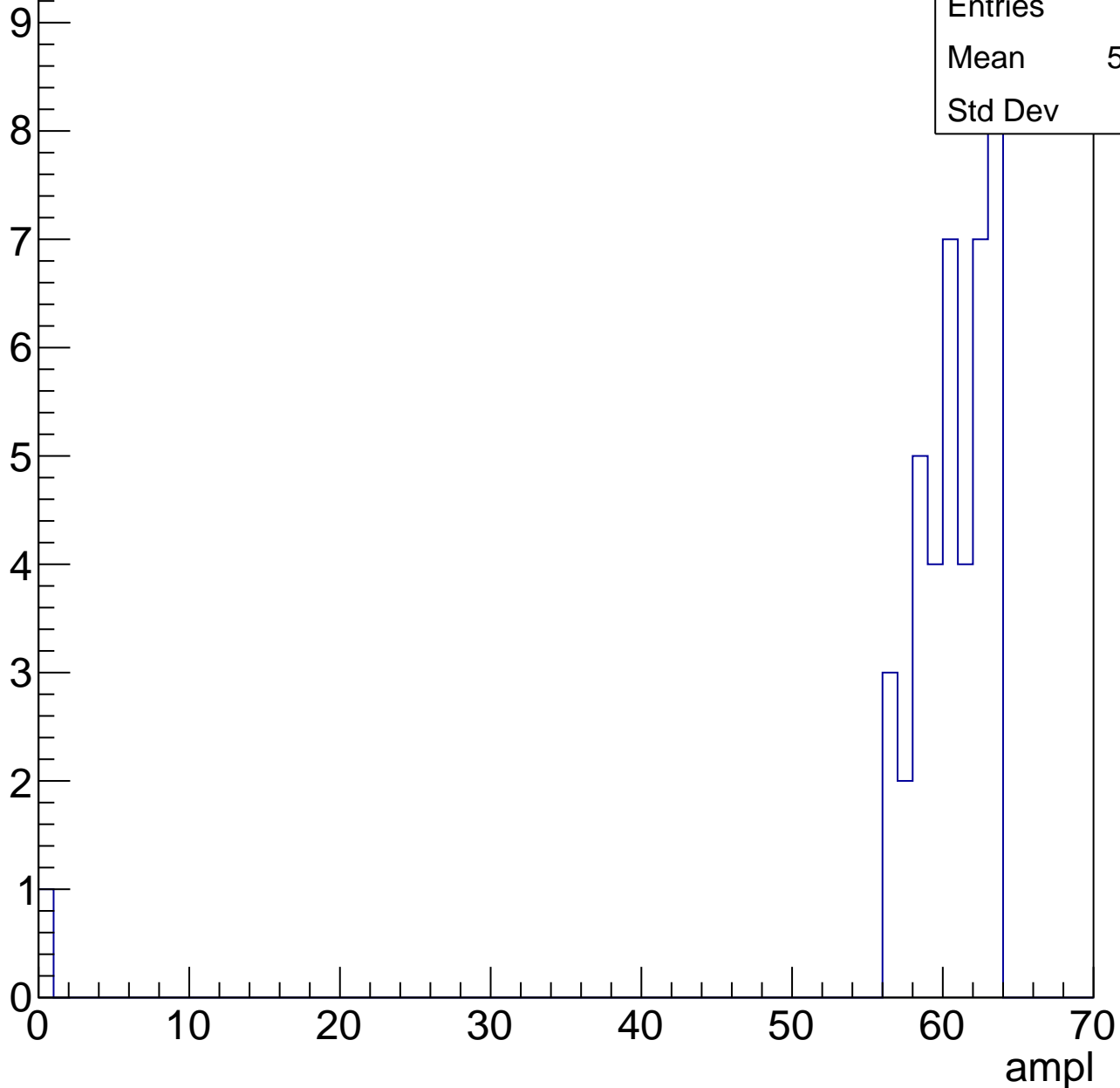
Entries	48
Mean	57.19
Std Dev	3.06



B0L002S, U4-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

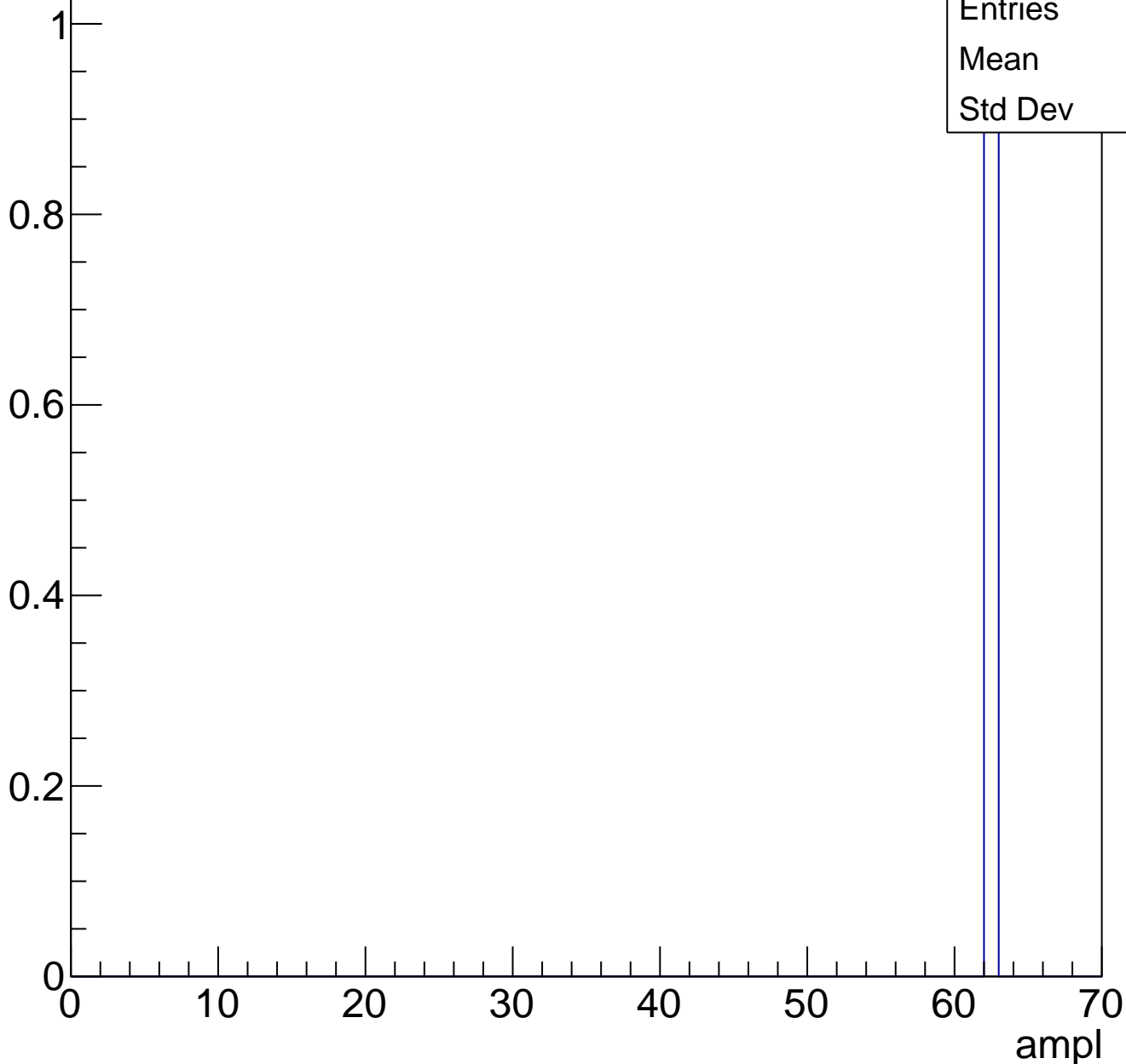
Entry



B0L002S, U4-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L002S, U4-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch97, adc0

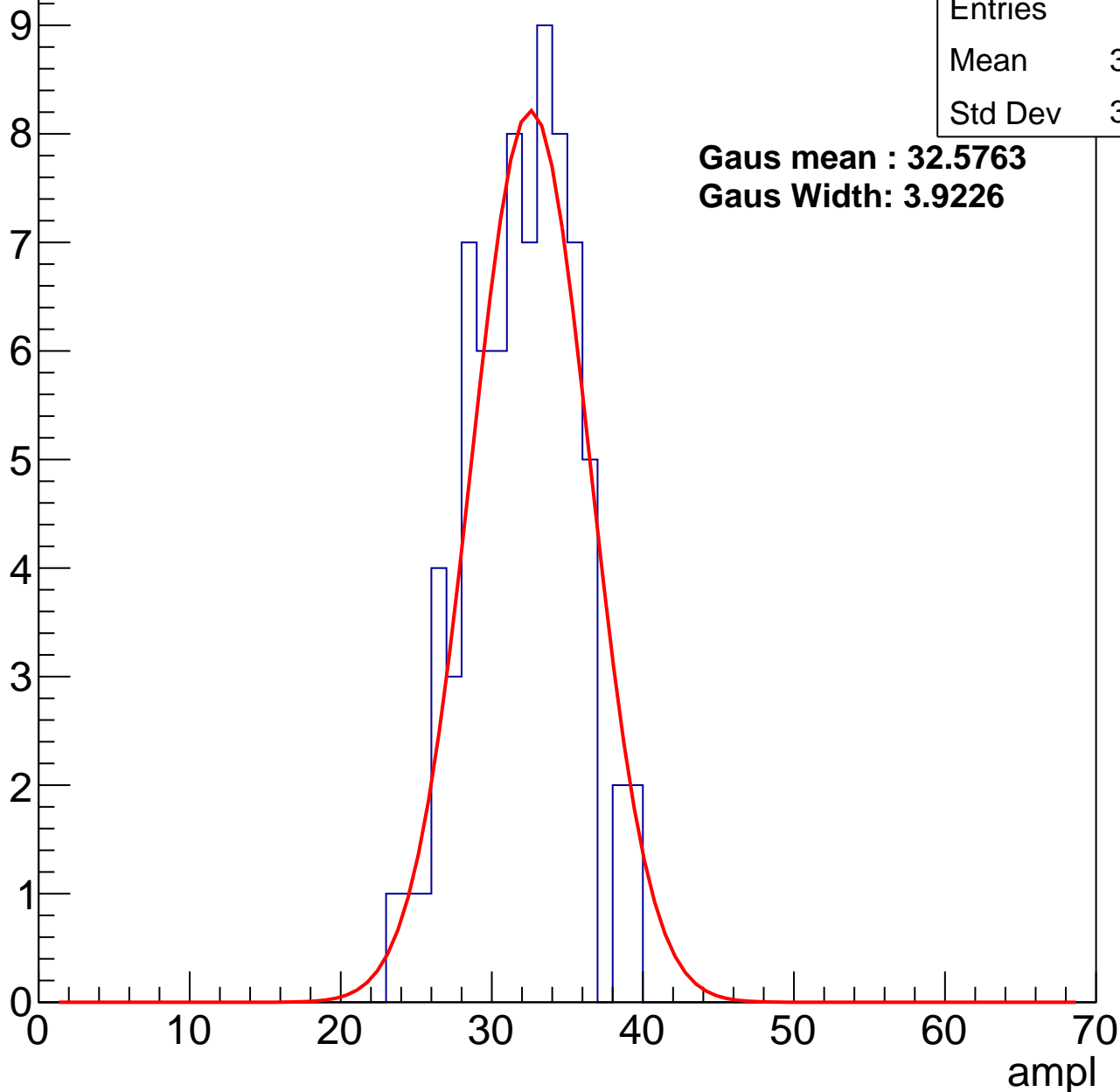
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.52
Std Dev	3.507

Gaus mean : 32.5763

Gaus Width: 3.9226



B0L002S, U4-ch97, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	61
Mean	38.85
Std Dev	2.896

Gaus mean : 39.7679

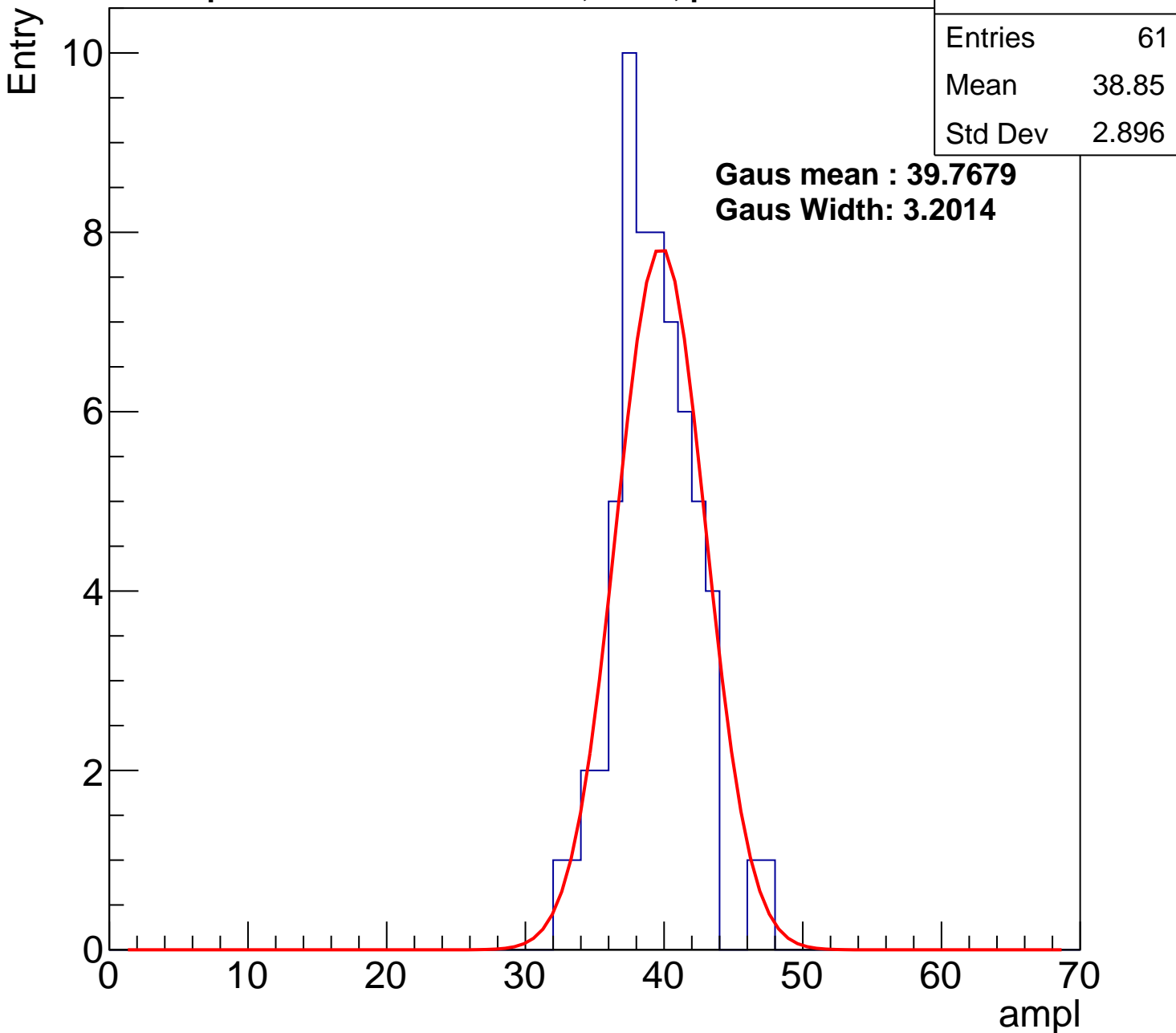
Gaus Width: 3.2014

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch97, adc2

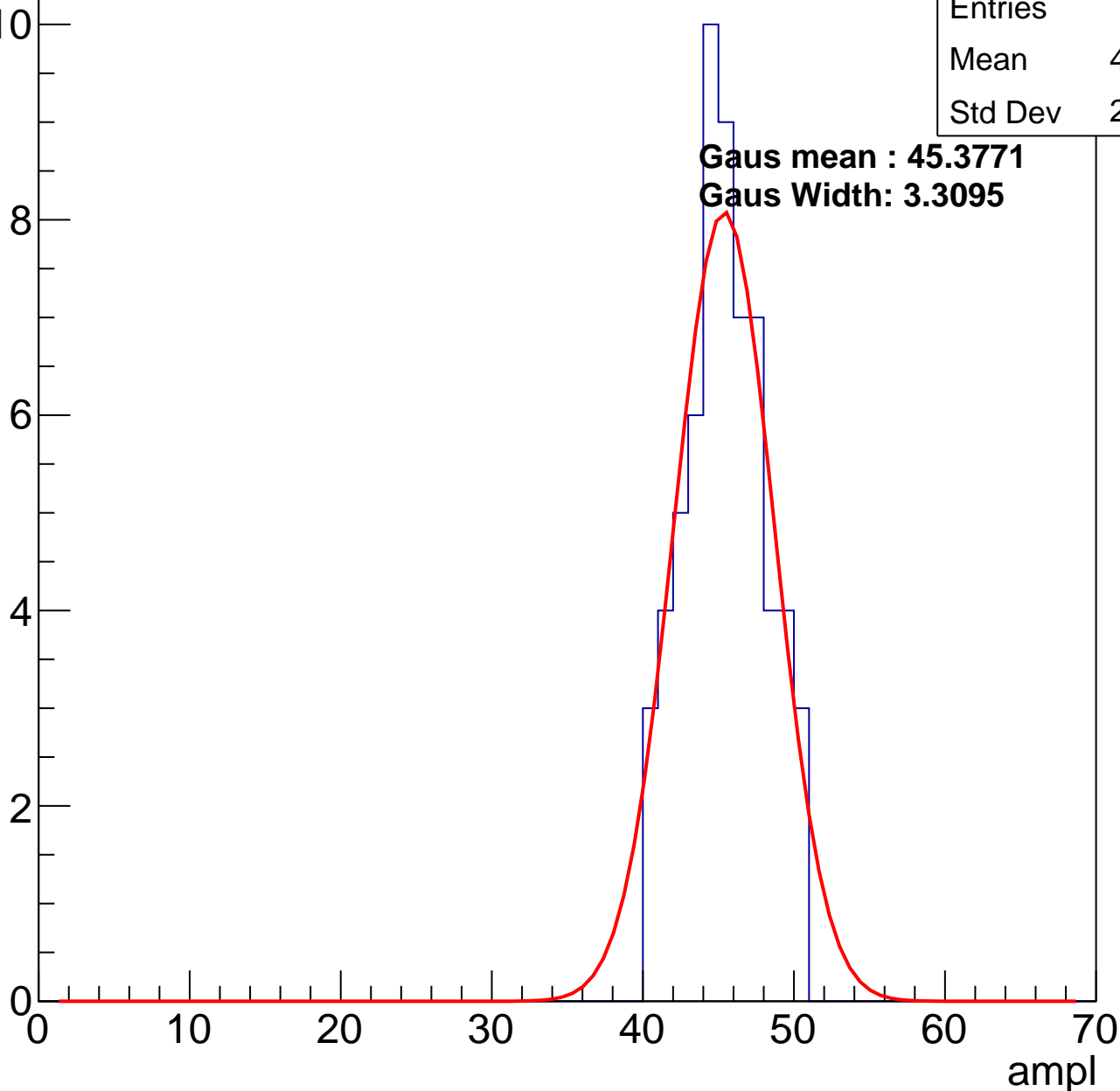
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.94
Std Dev	2.627

Gaus mean : 45.3771

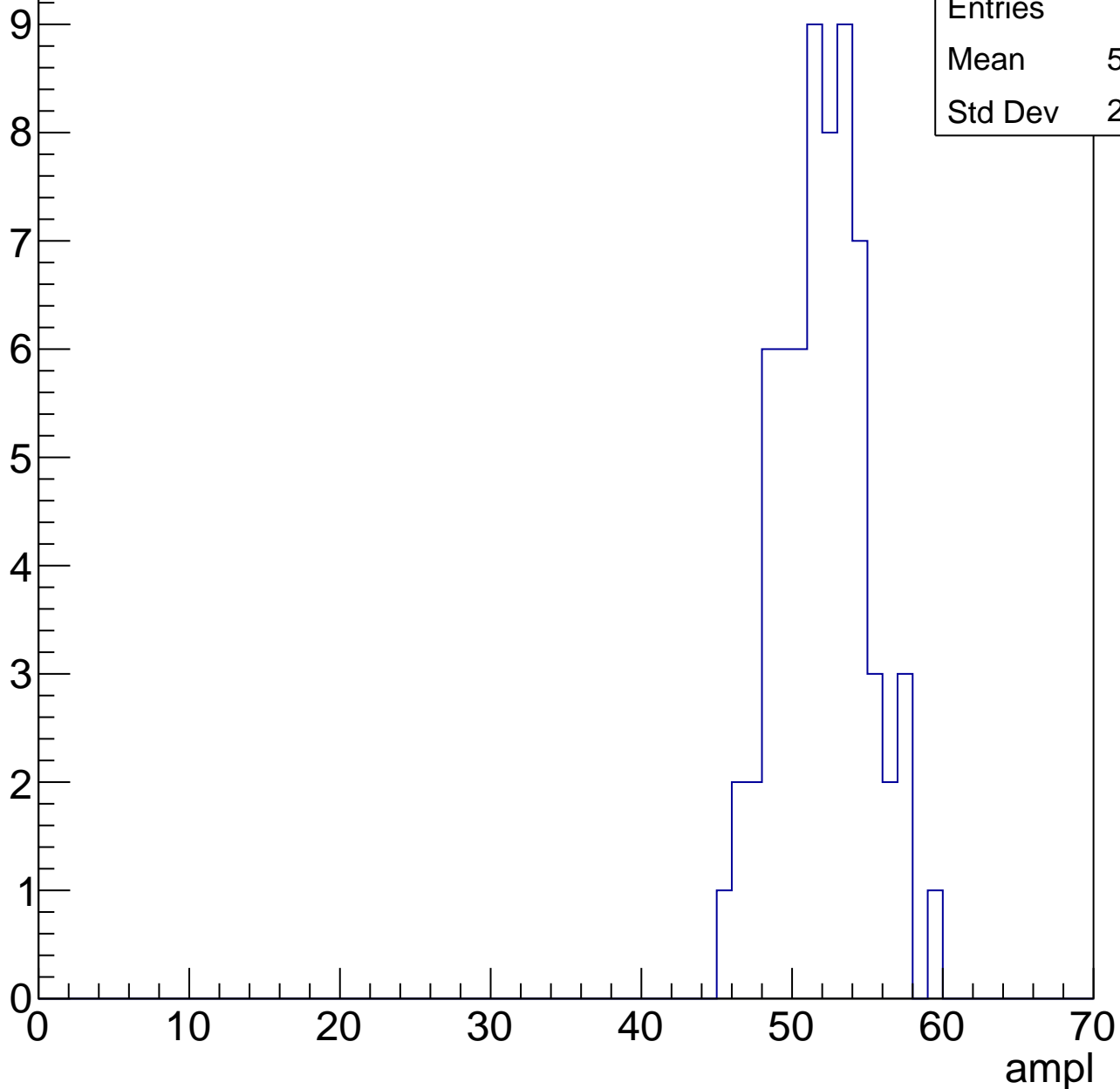
Gaus Width: 3.3095



B0L002S, U4-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

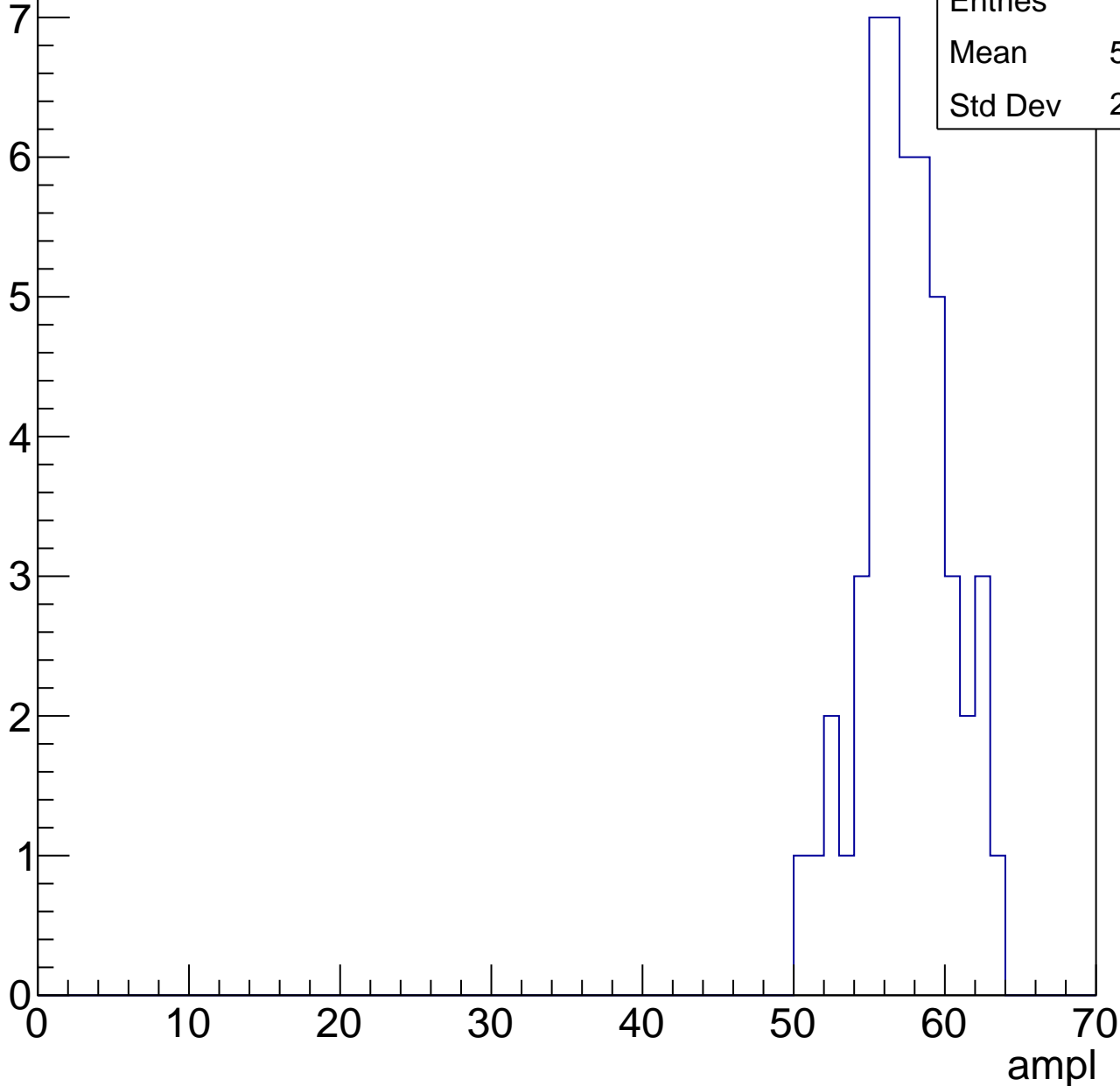


B0L002S, U4-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

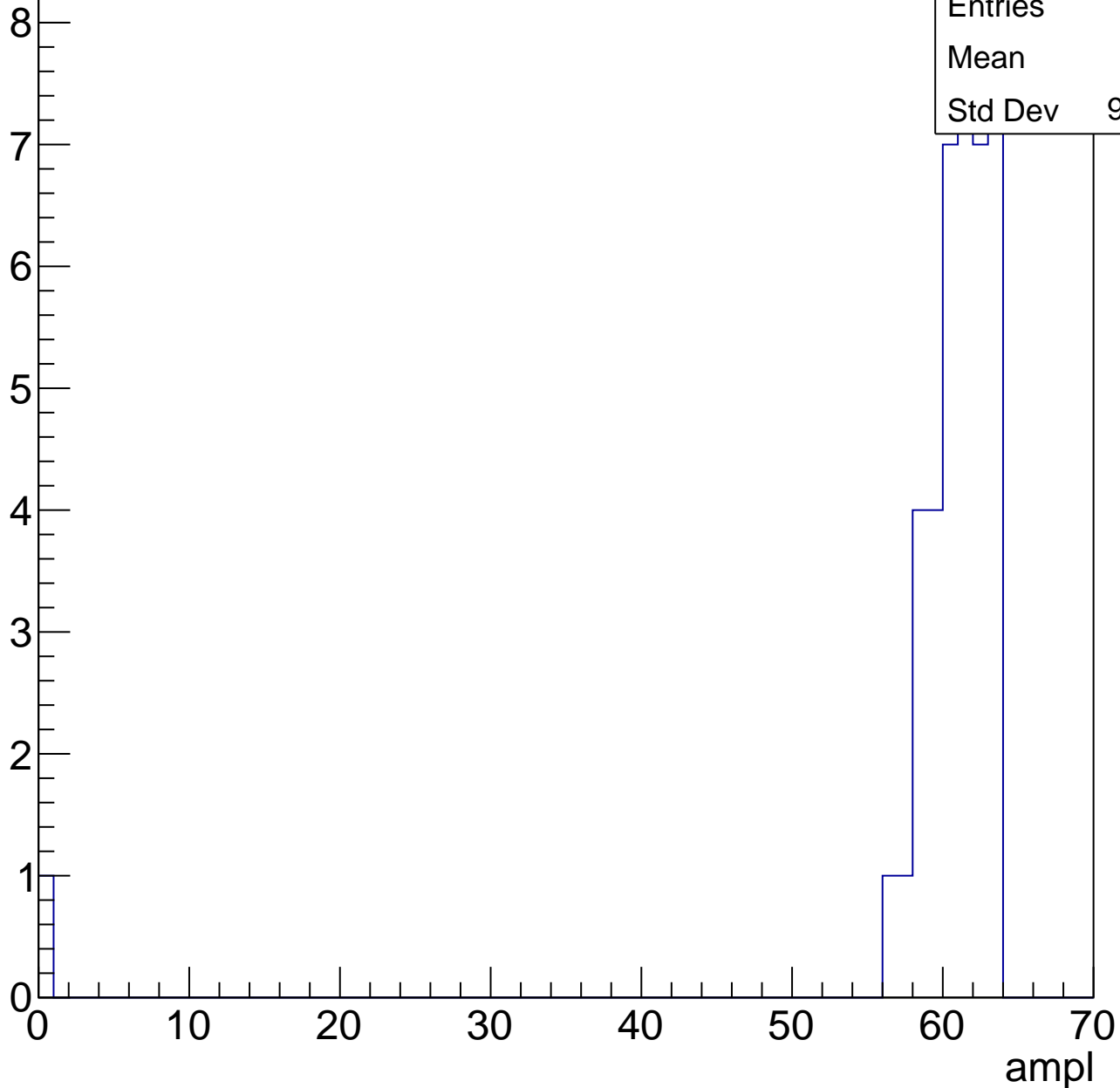
Entries	48
Mean	56.94
Std Dev	2.918



B0L002S, U4-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

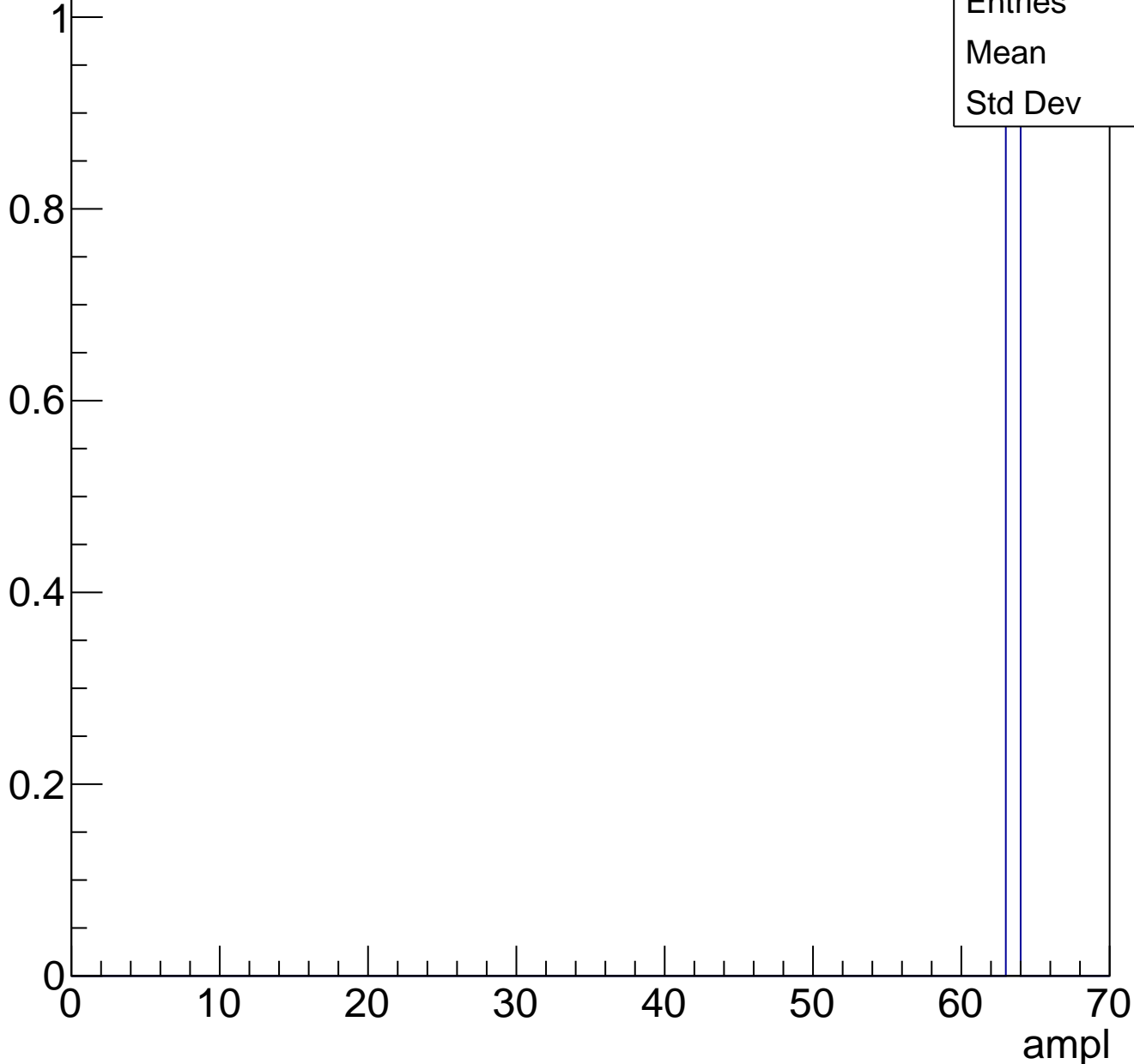


Entries	41
Mean	59.2
Std Dev	9.534

B0L002S, U4-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch98, adc0

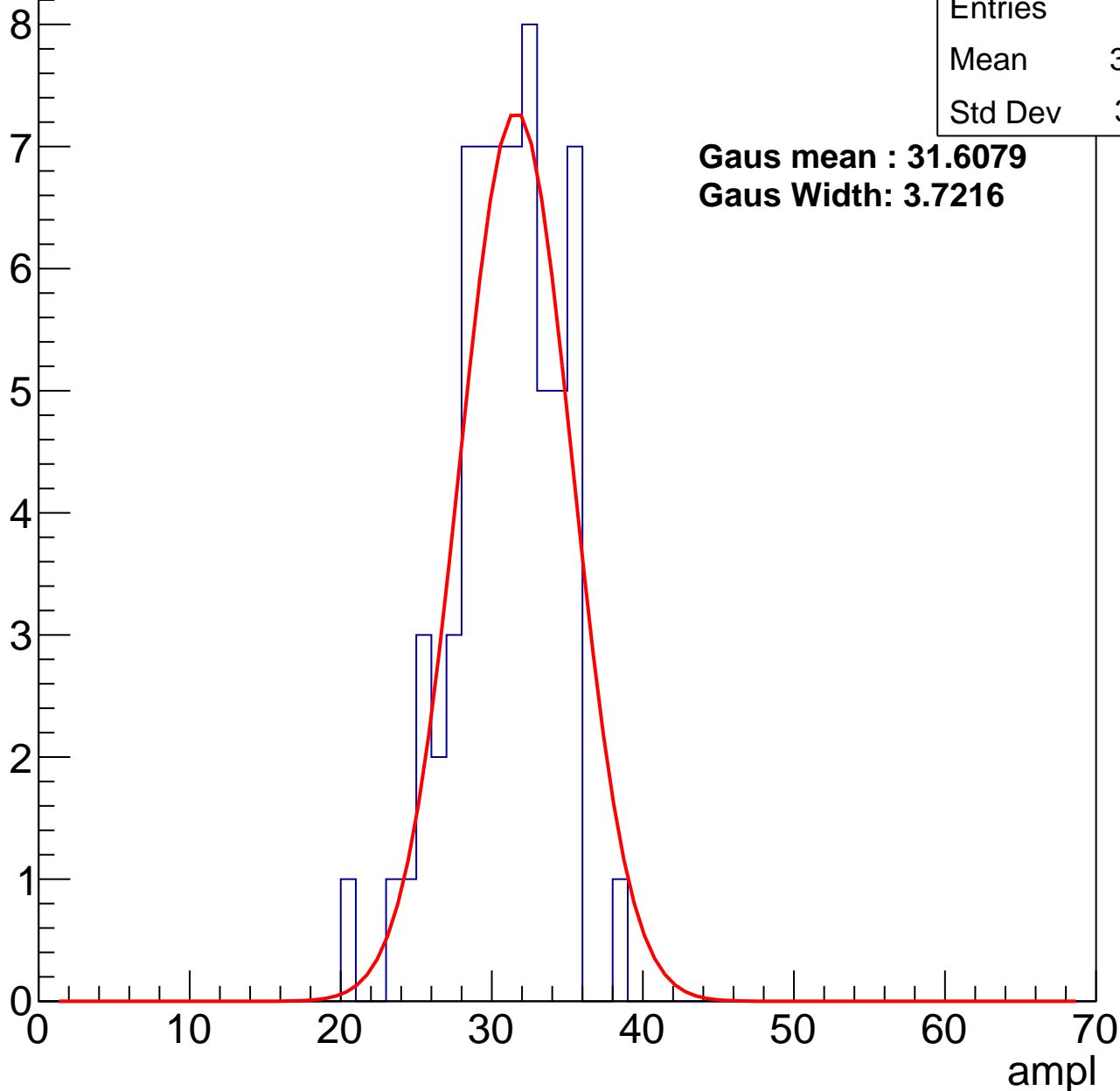
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.38
Std Dev	3.391

Gaus mean : 31.6079

Gaus Width: 3.7216



B0L002S, U4-ch98, adc1

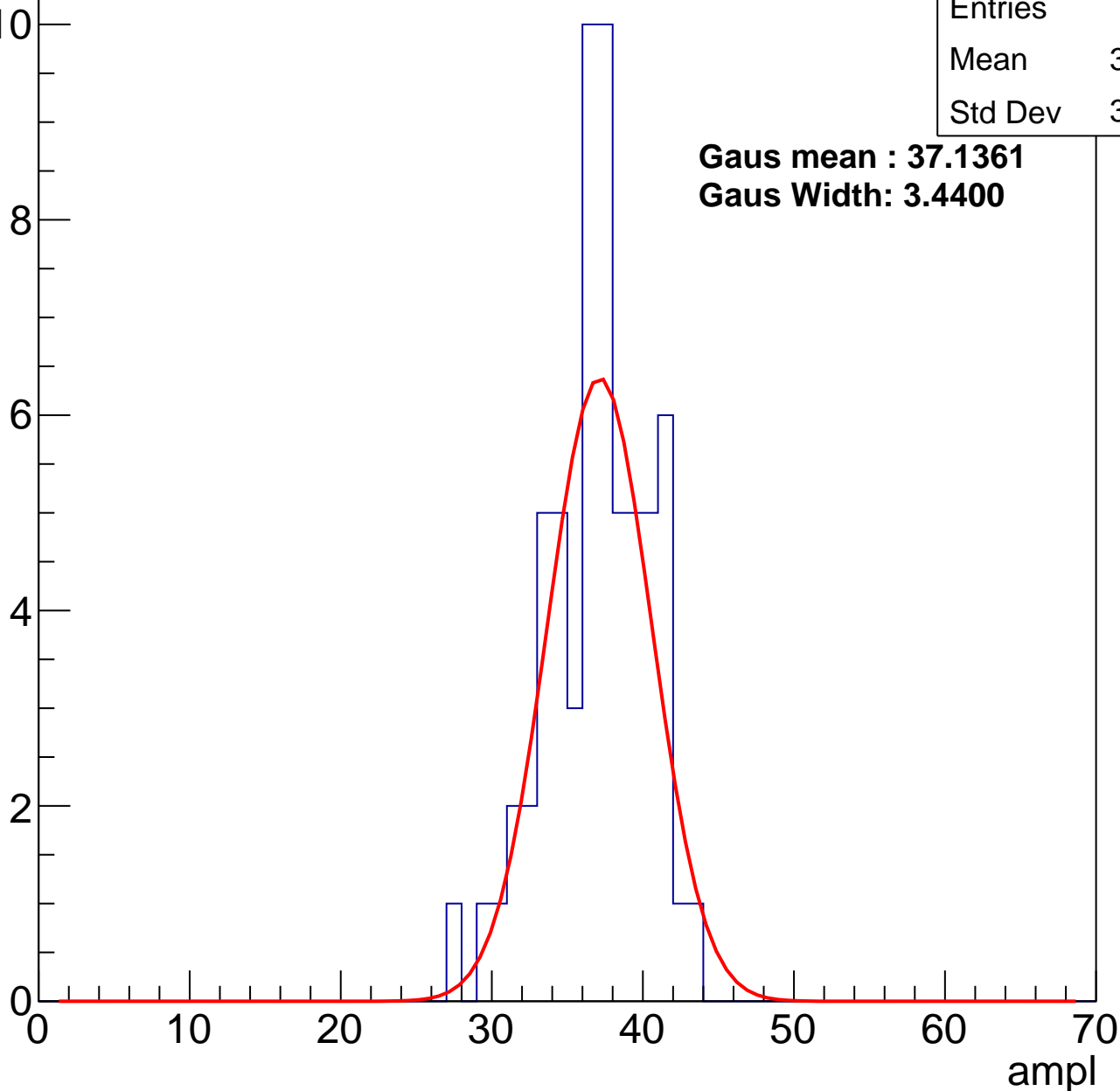
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.48
Std Dev	3.314

Gaus mean : 37.1361

Gaus Width: 3.4400



B0L002S, U4-ch98, adc2

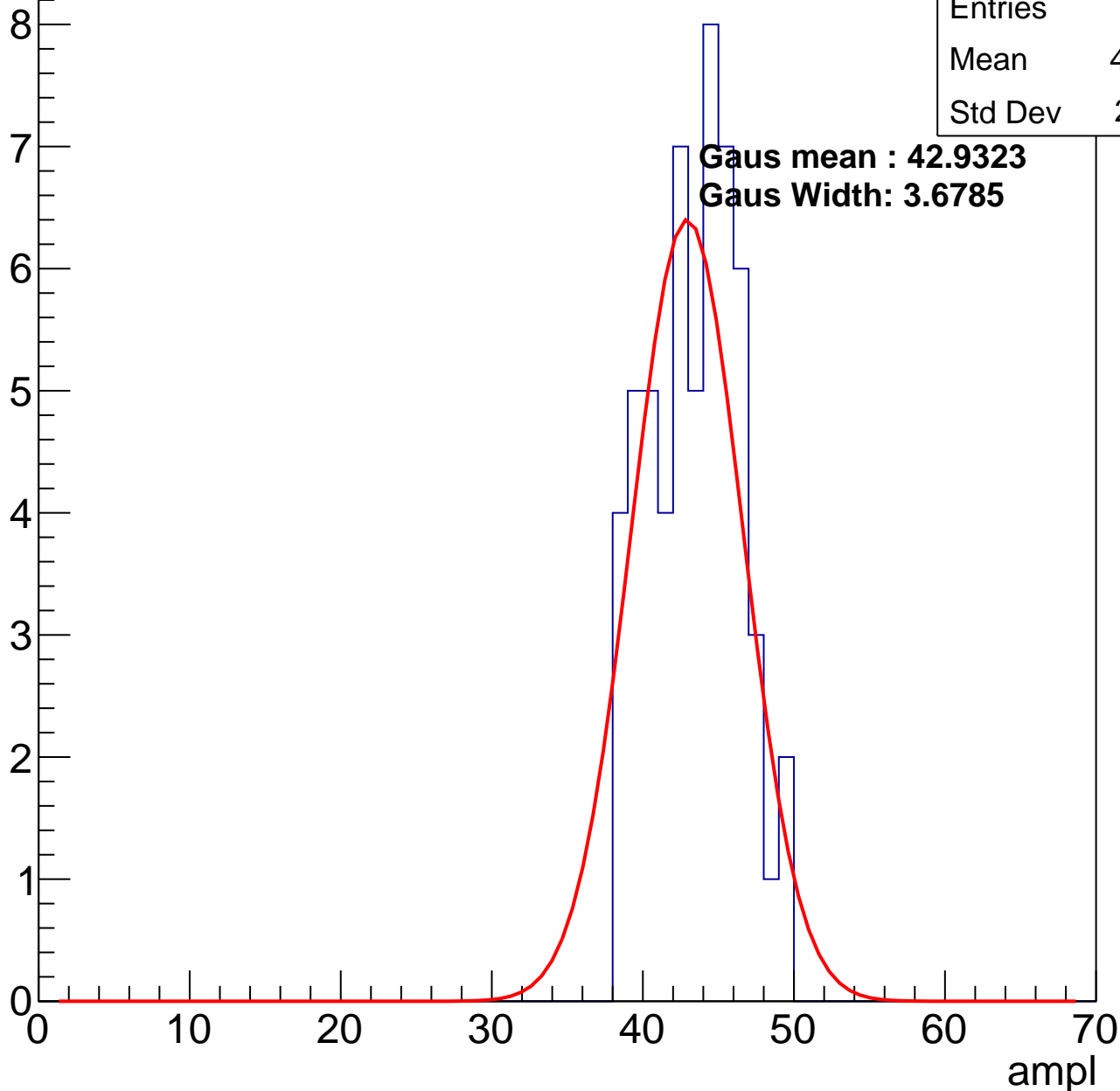
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.98
Std Dev	2.911

Gaus mean : 42.9323

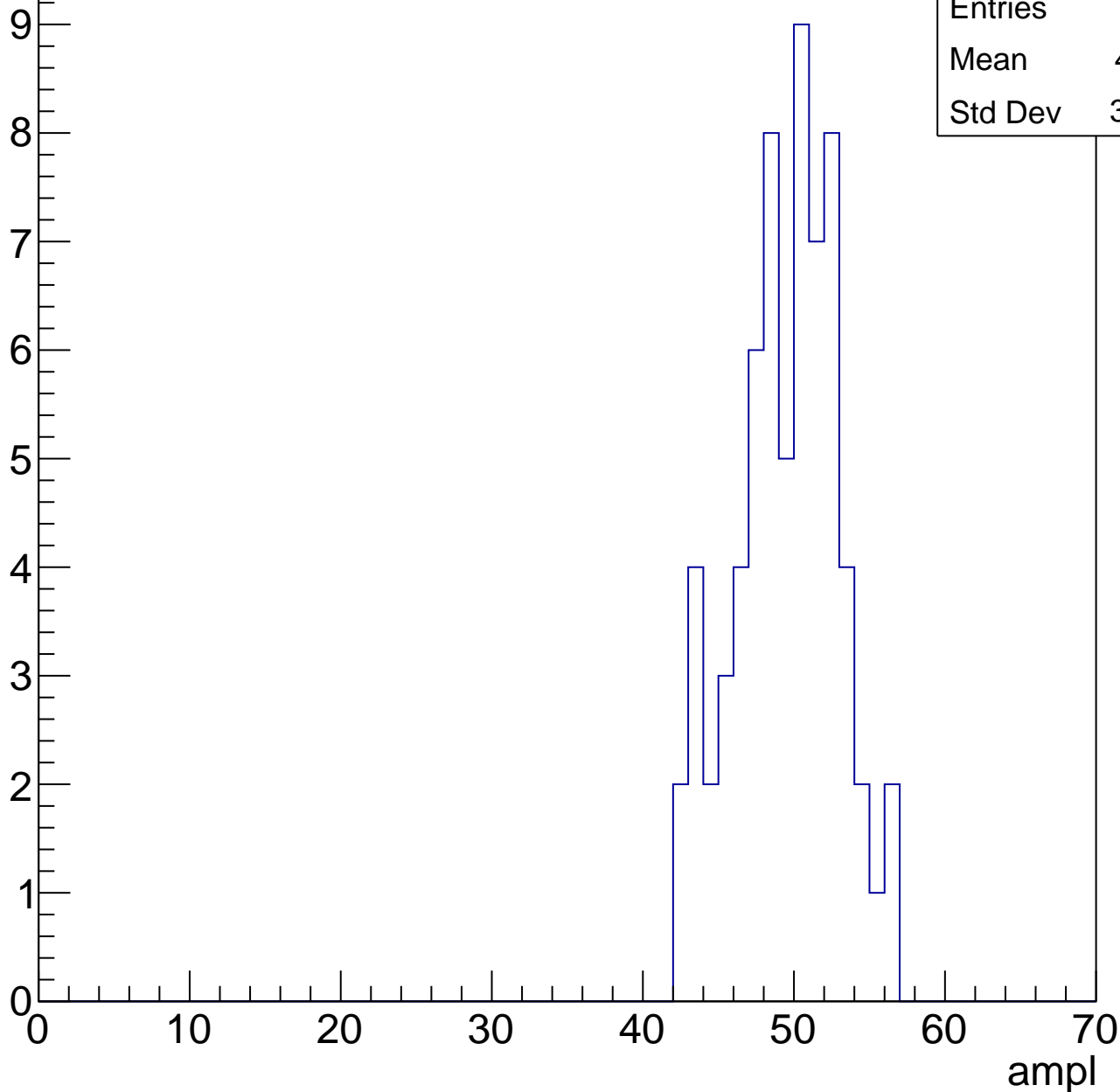
Gaus Width: 3.6785



B0L002S, U4-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	67
Mean	49.01
Std Dev	3.379

B0L002S, U4-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 54.65

Std Dev 3.357

0

0

10

20

30

40

50

60

70

ampl

0

1

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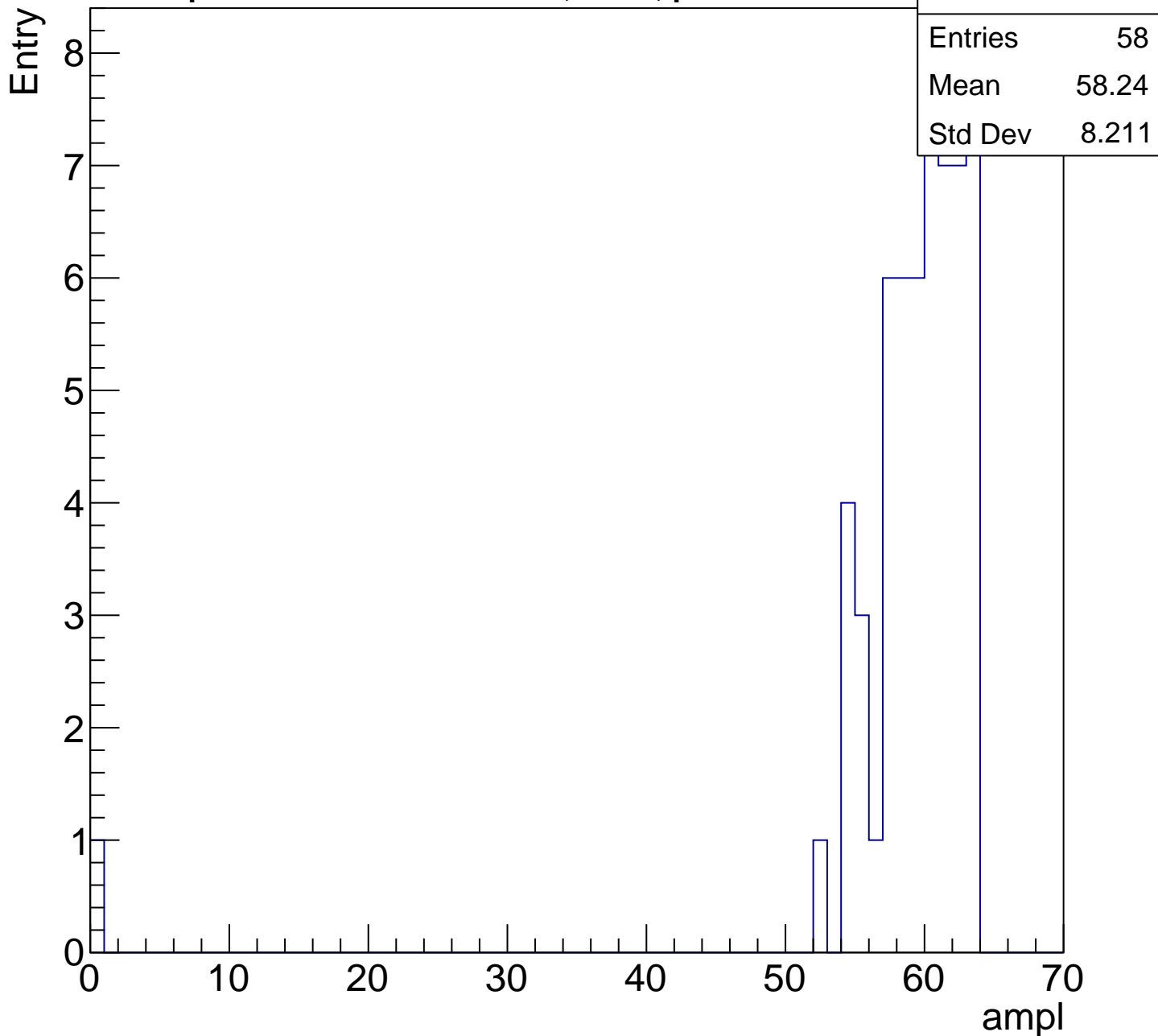
320

321

322

B0L002S, U4-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

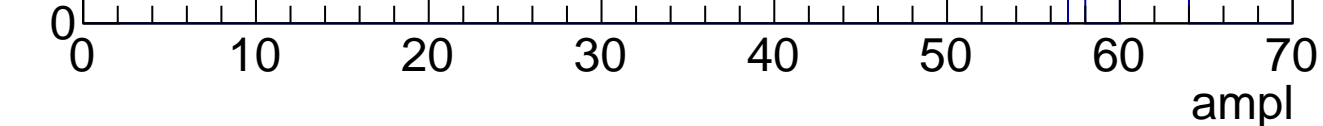
9

Mean

61.11

Std Dev

1.728



B0L002S, U4-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch99, adc0

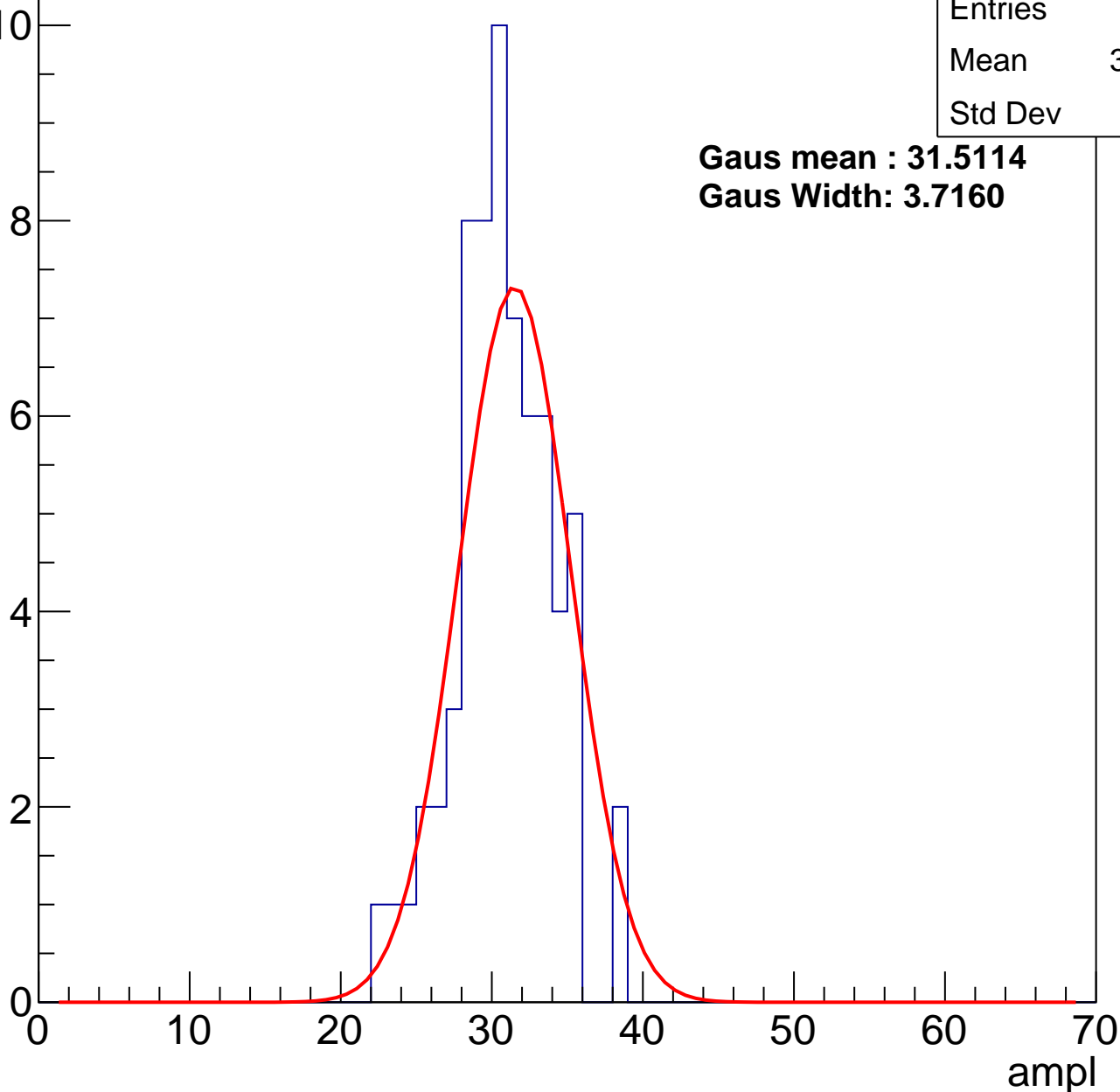
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	30.33
Std Dev	3.24

Gaus mean : 31.5114

Gaus Width: 3.7160



B0L002S, U4-ch99, adc1

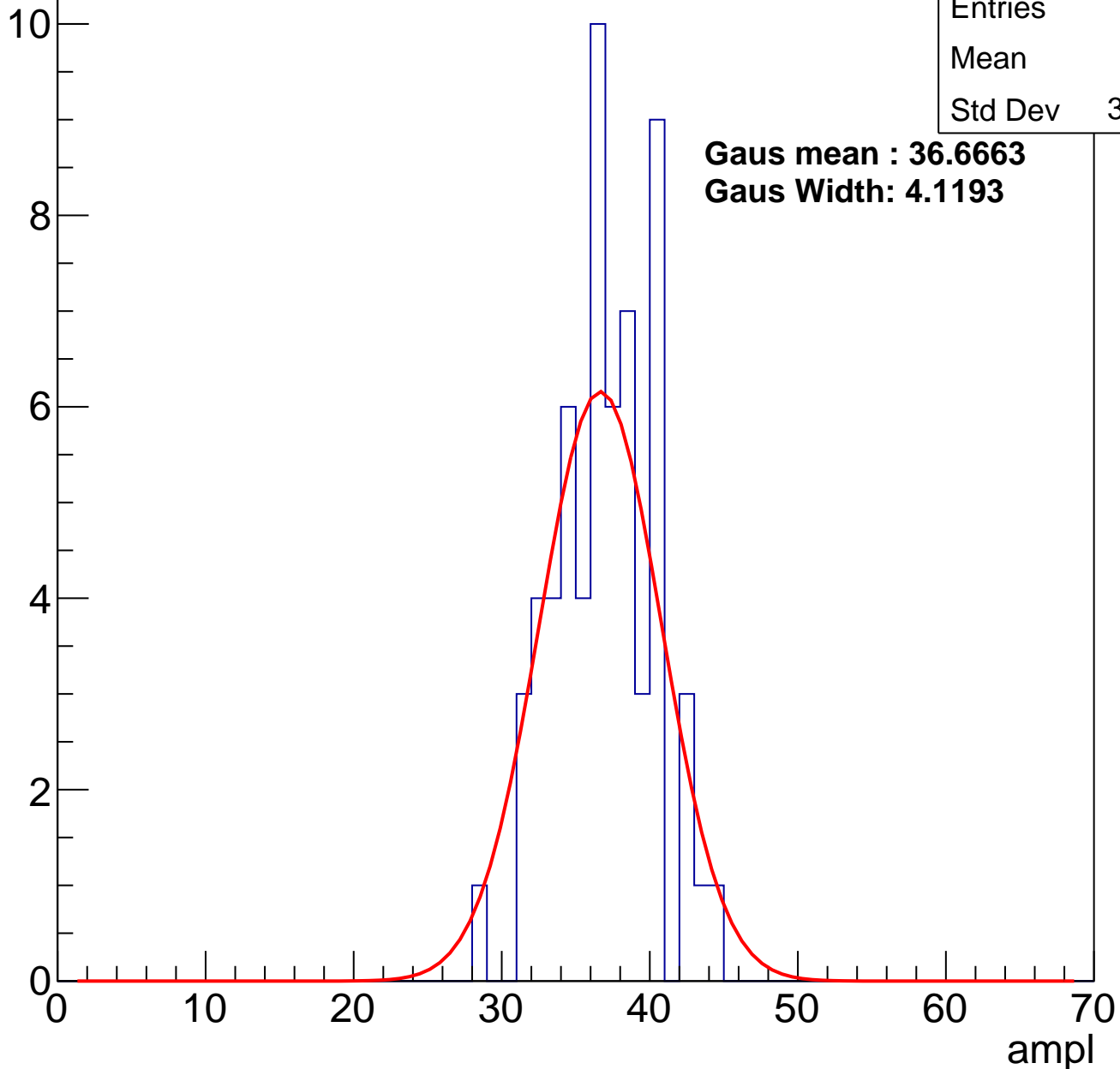
calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	36.5
Std Dev	3.306

Gaus mean : 36.6663

Gaus Width: 4.1193

Entry



B0L002S, U4-ch99, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	43.12
Std Dev	3.42

Gaus mean : 44.7175

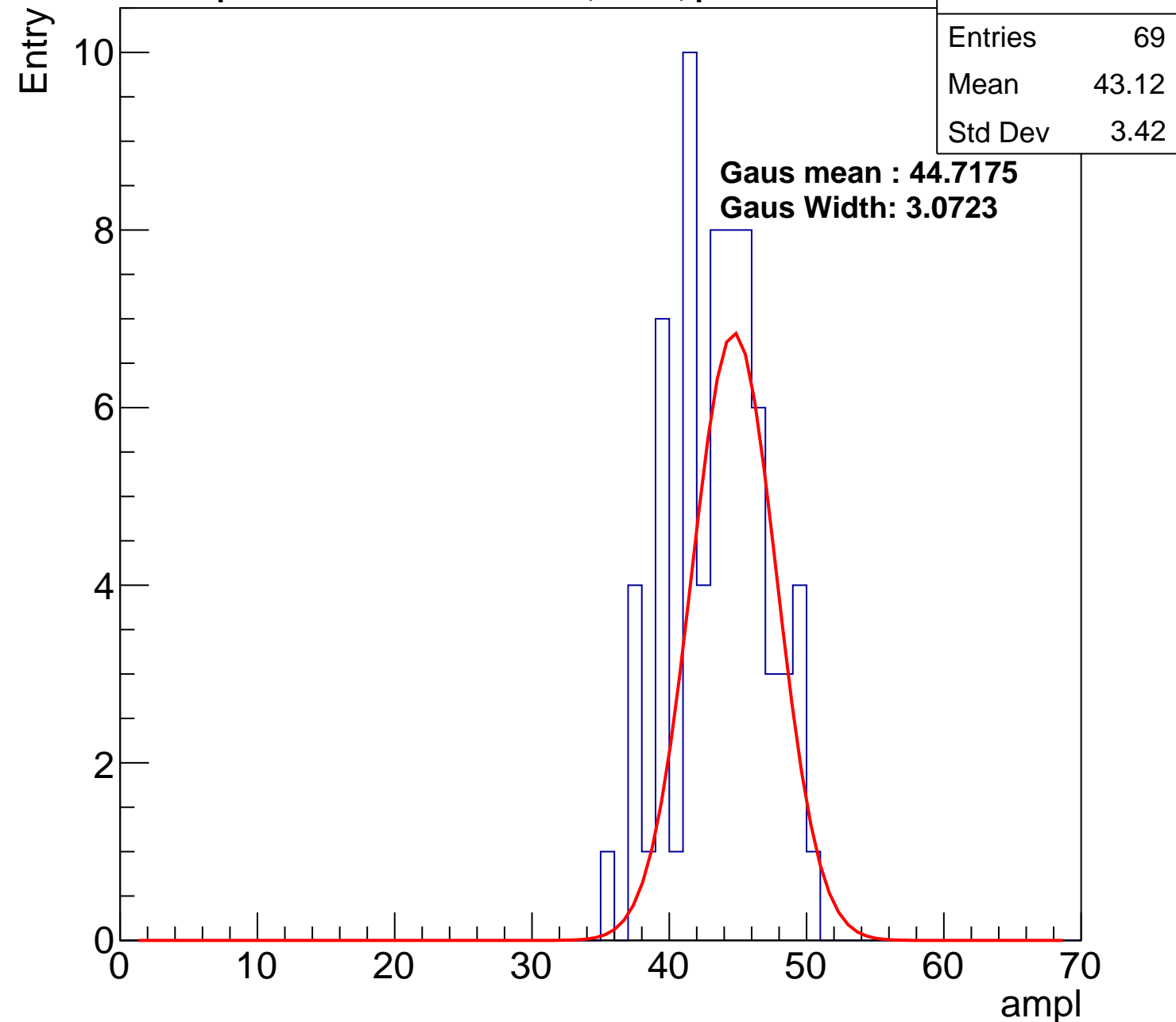
Gaus Width: 3.0723

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

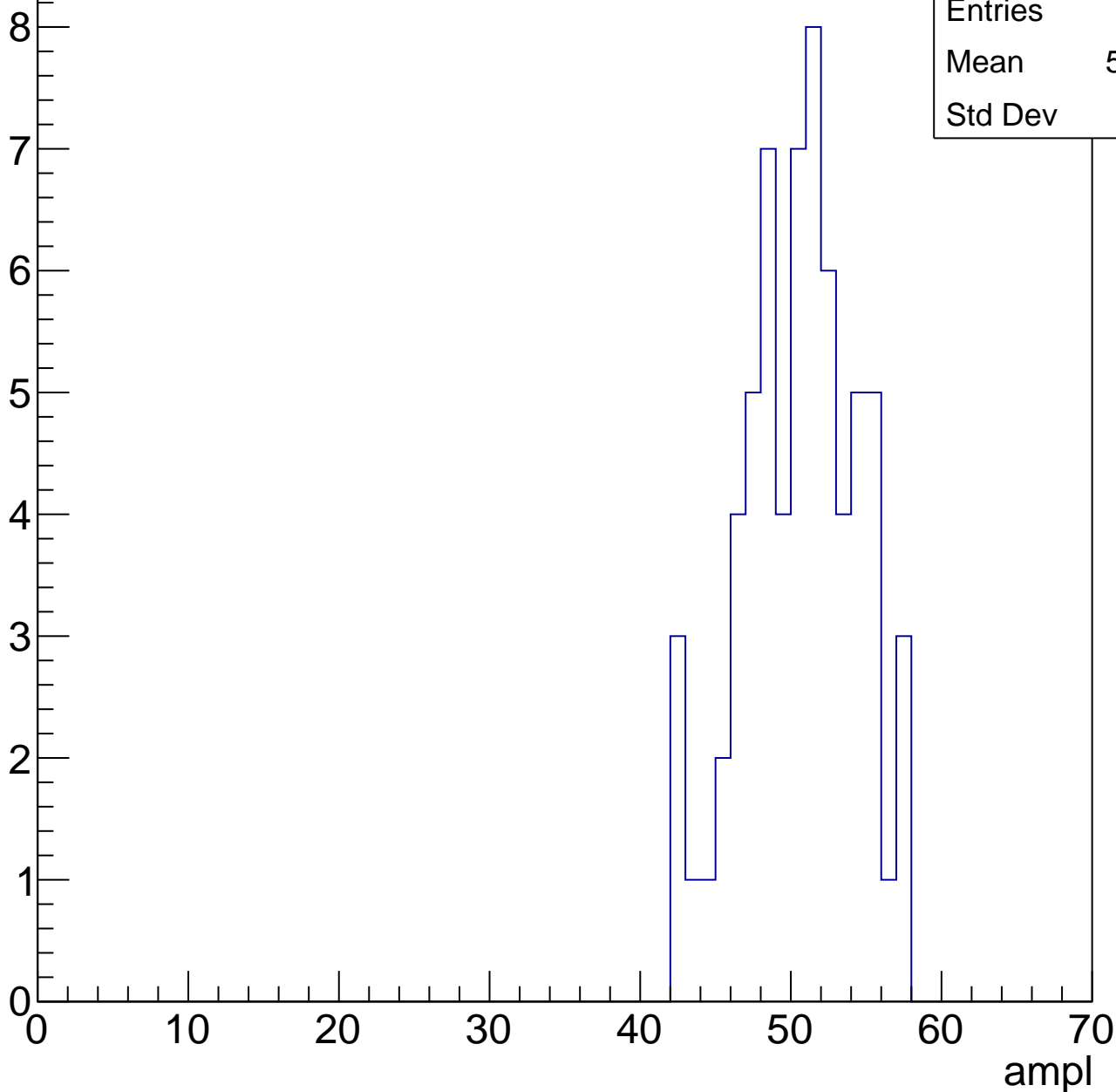


B0L002S, U4-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.12
Std Dev	3.74

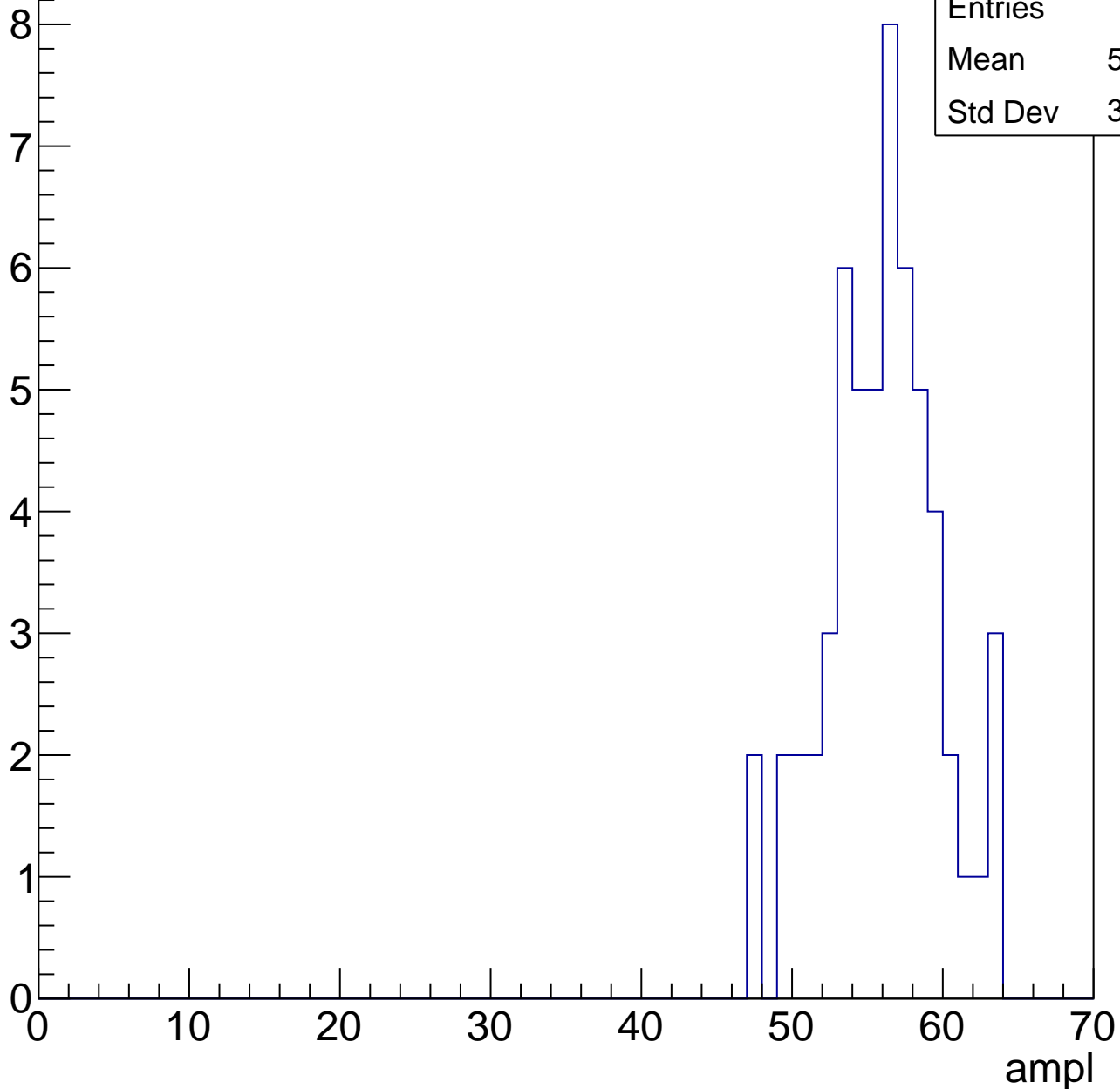


B0L002S, U4-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	55.46
Std Dev	3.733

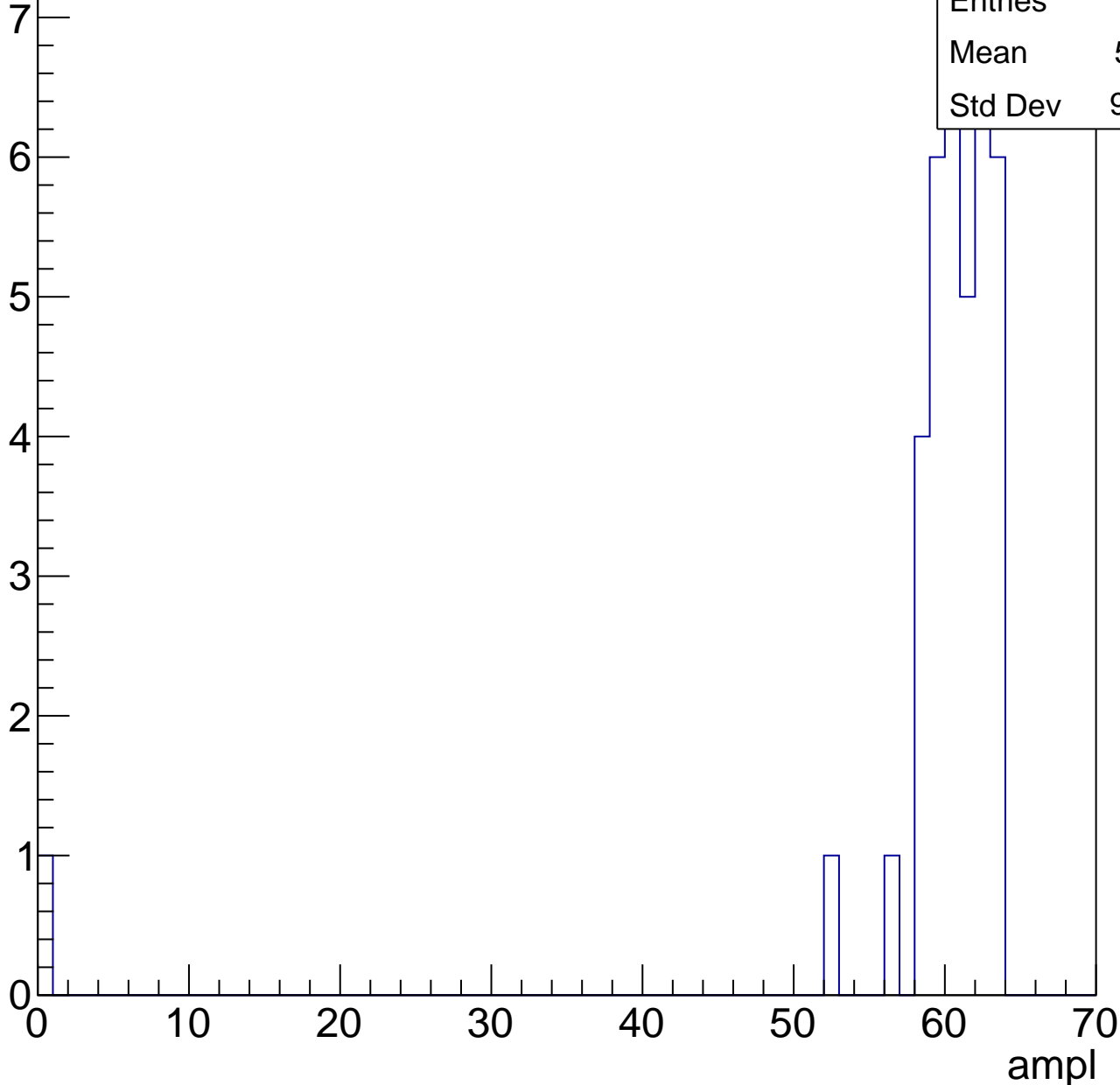


B0L002S, U4-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

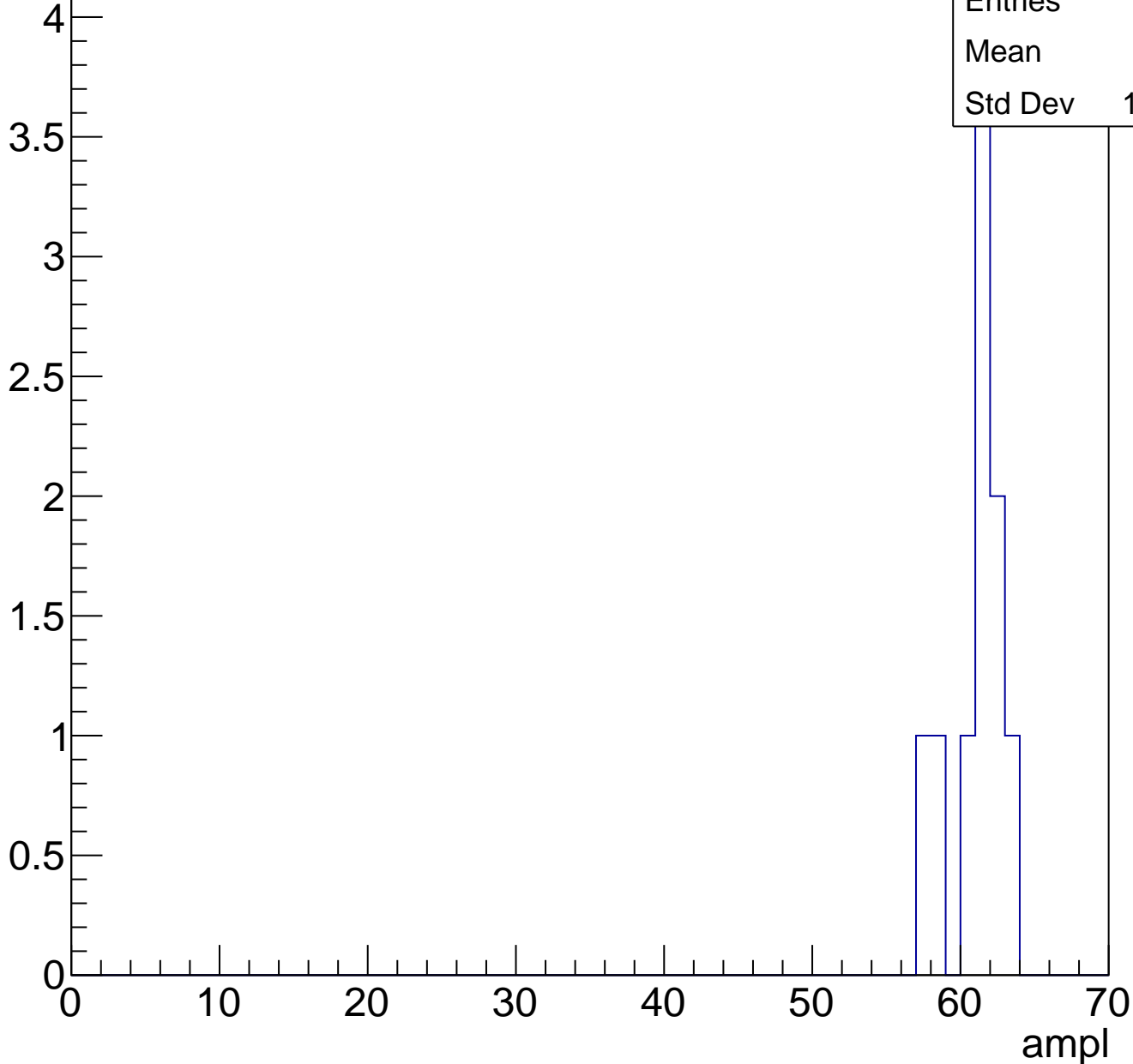
Entries	38
Mean	58.71
Std Dev	9.902



B0L002S, U4-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch100, adc0

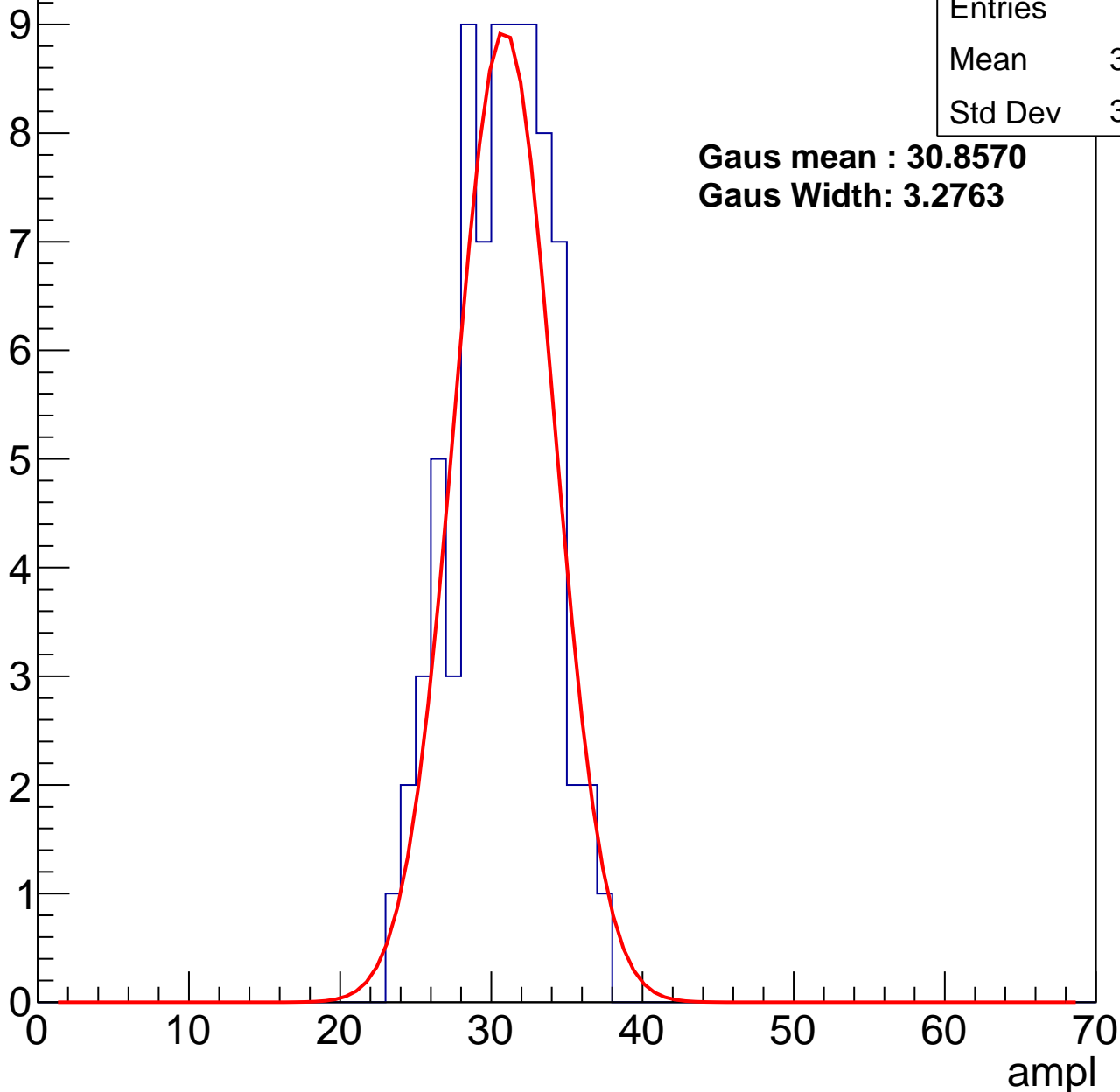
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.26
Std Dev	3.102

Gaus mean : 30.8570

Gaus Width: 3.2763



B0L002S, U4-ch100, adc1

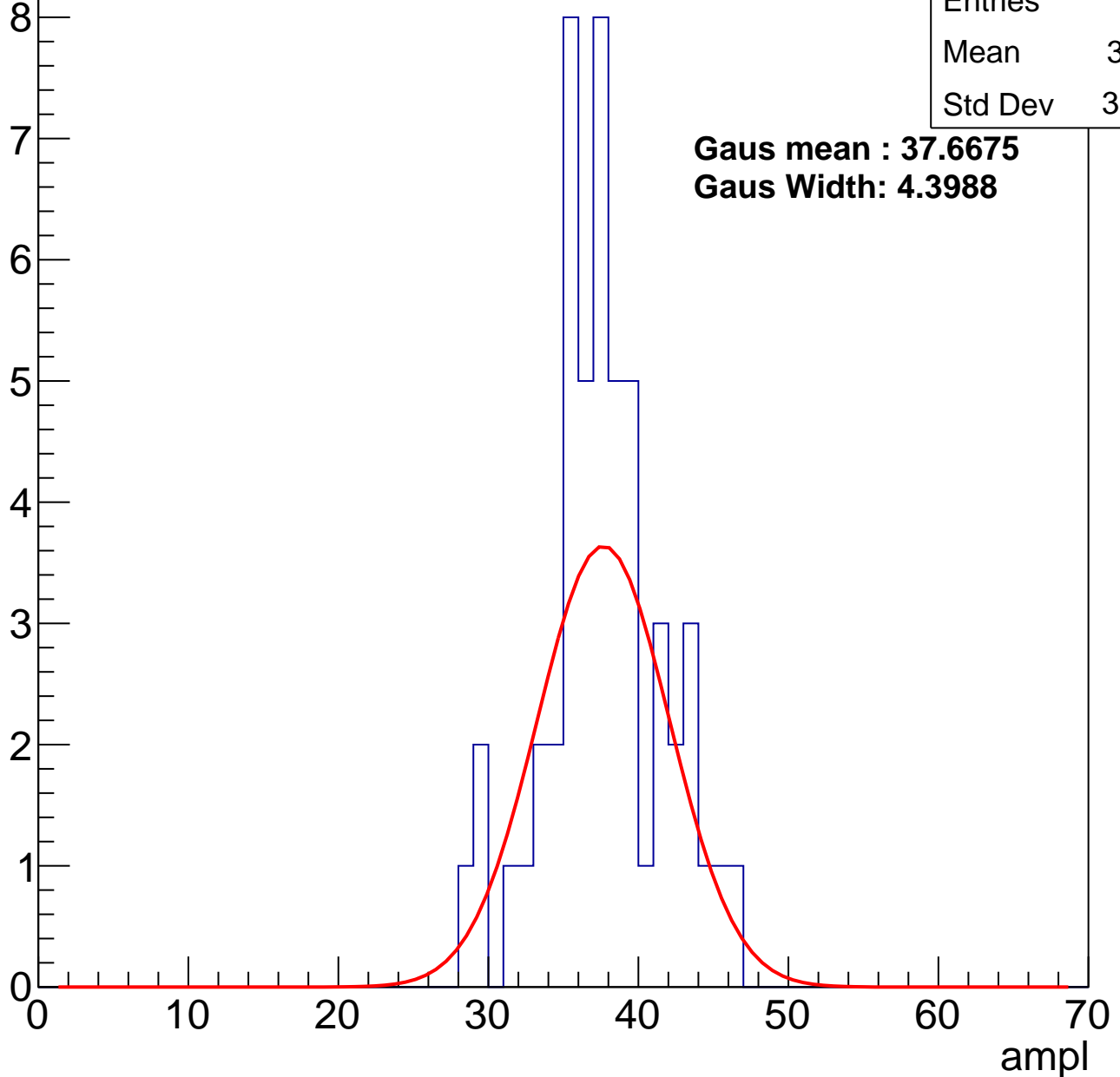
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	37.21
Std Dev	3.894

Gaus mean : 37.6675

Gaus Width: 4.3988



B0L002S, U4-ch100, adc2

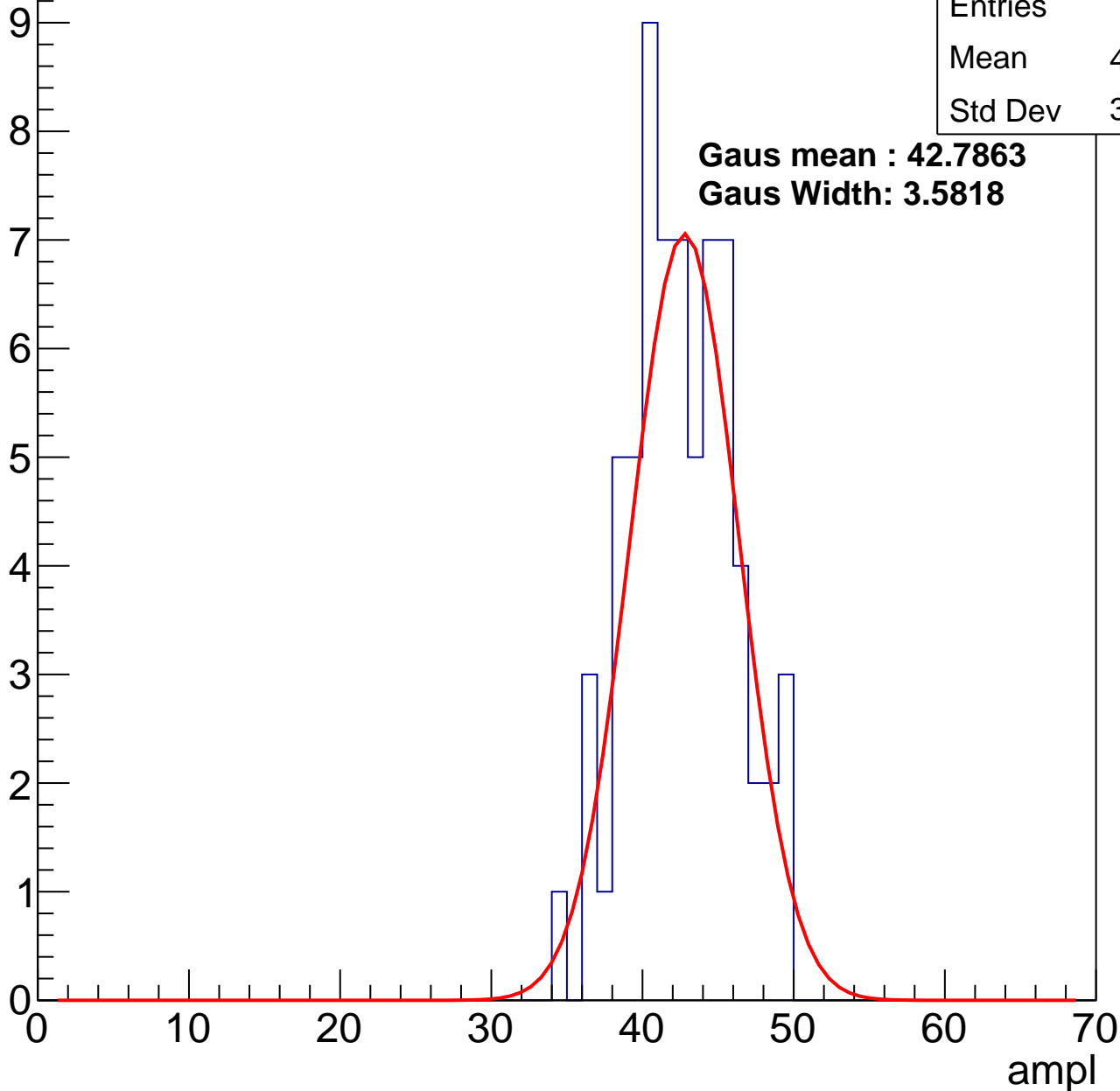
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.12
Std Dev	3.415

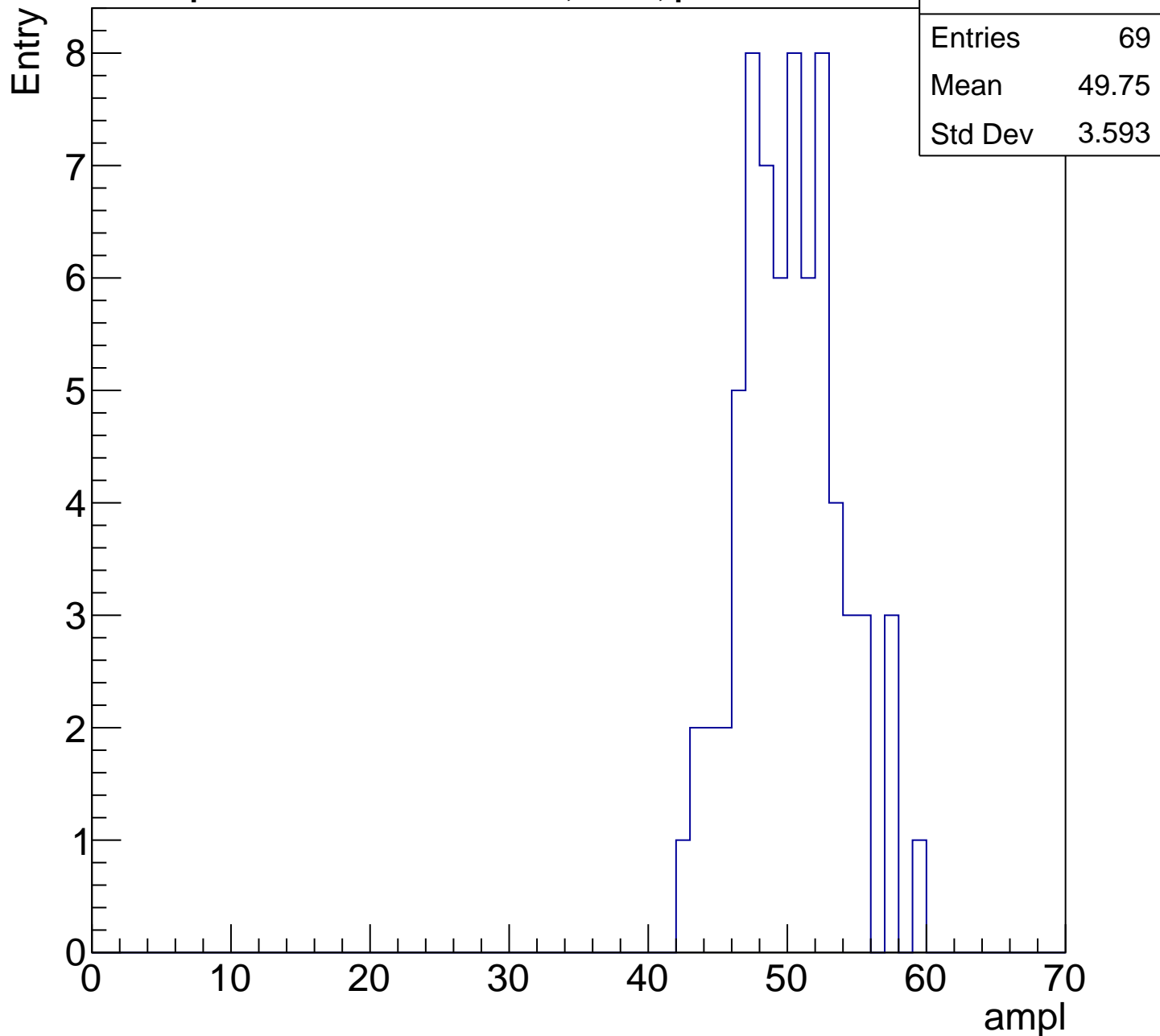
Gaus mean : 42.7863

Gaus Width: 3.5818



B0L002S, U4-ch100, adc3

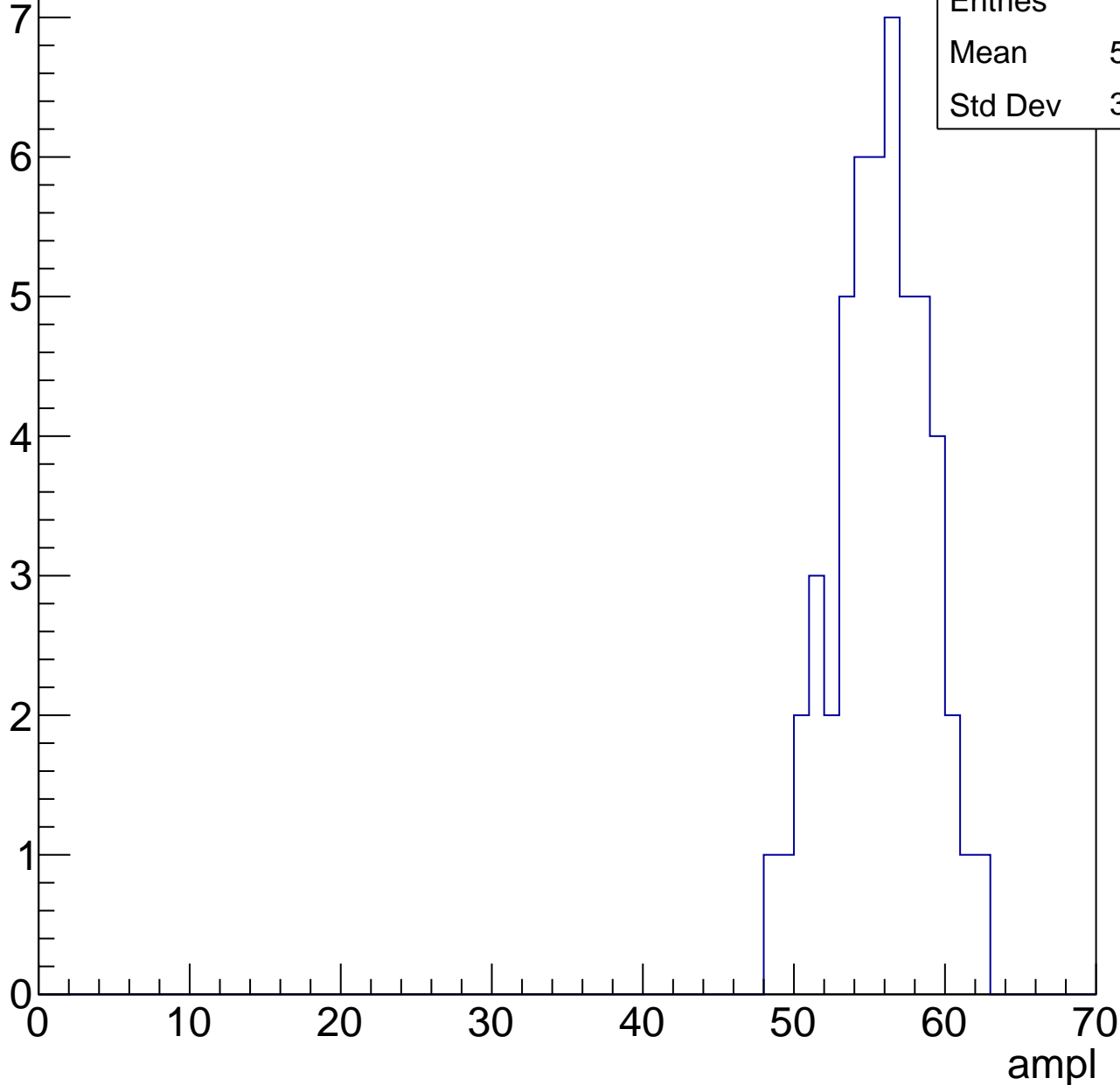
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U4-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



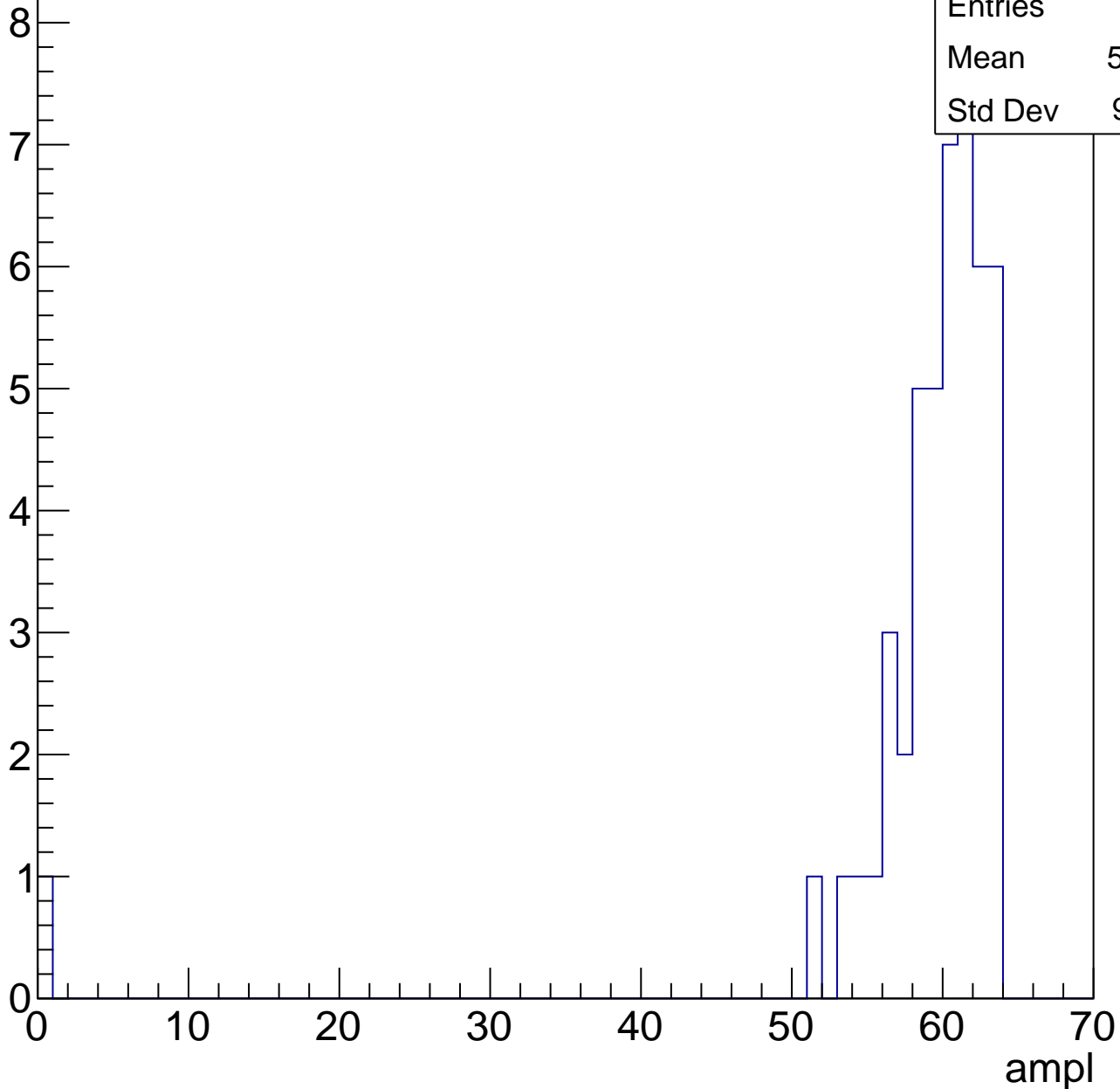
Entries	51
Mean	55.27
Std Dev	3.113

B0L002S, U4-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.26
Std Dev	9.021



B0L002S, U4-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	23
Std Dev	0

B0L002S, U4-ch101, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	30.88
Std Dev	5.274

Gaus mean : 31.4555

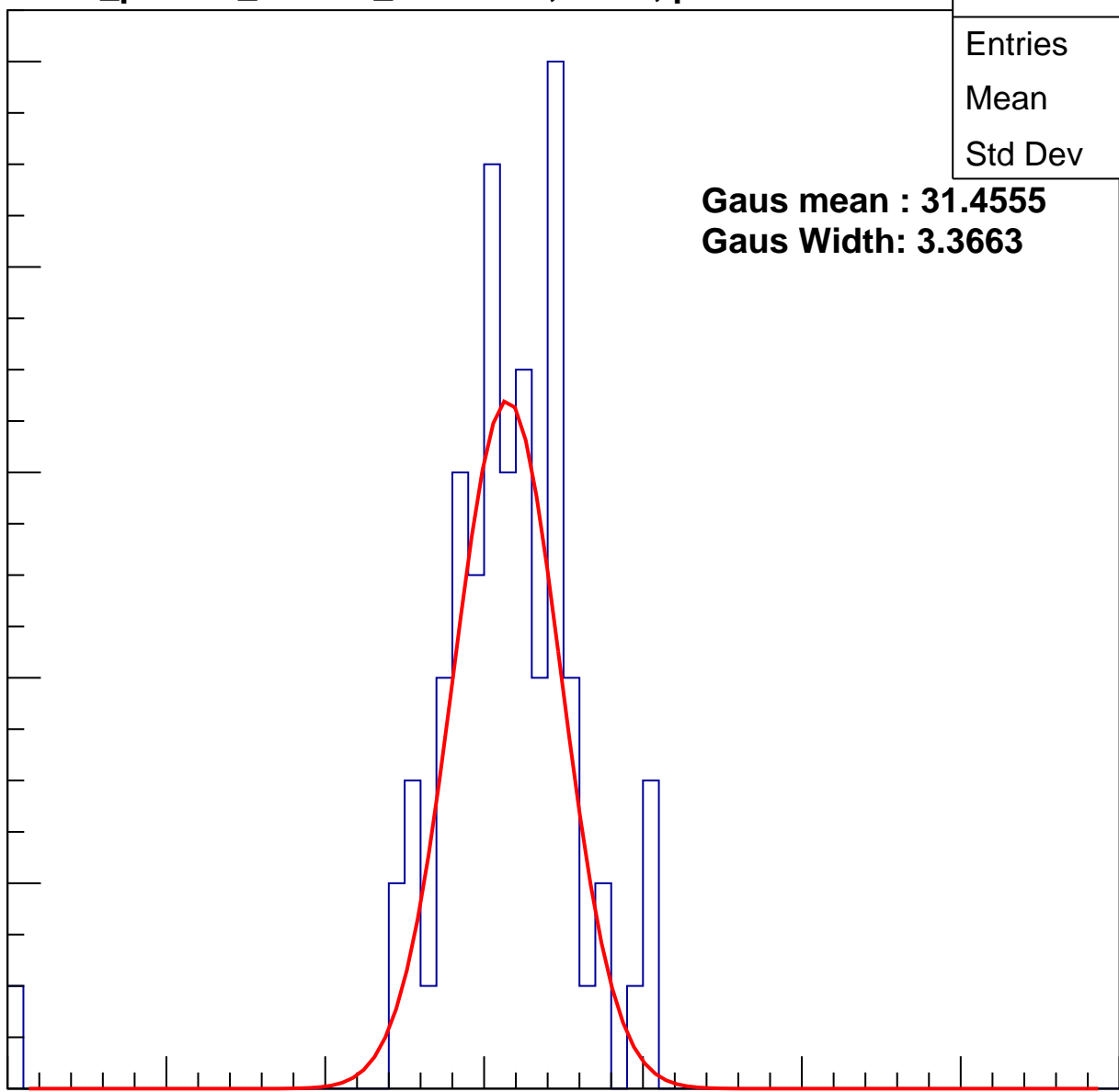
Gaus Width: 3.3663

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch101, adc1

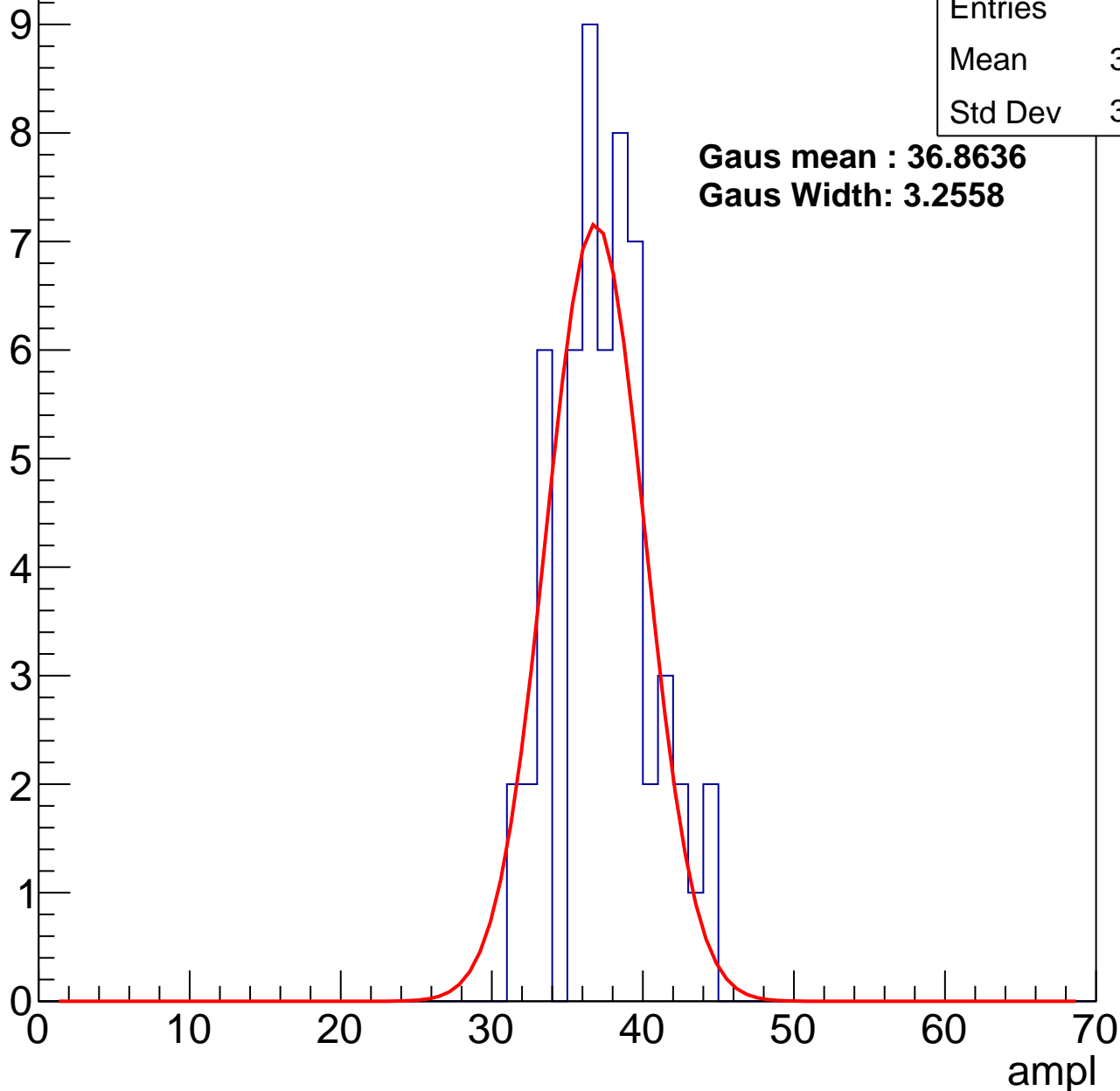
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.05
Std Dev	3.096

Gaus mean : 36.8636

Gaus Width: 3.2558



B0L002S, U4-ch101, adc2

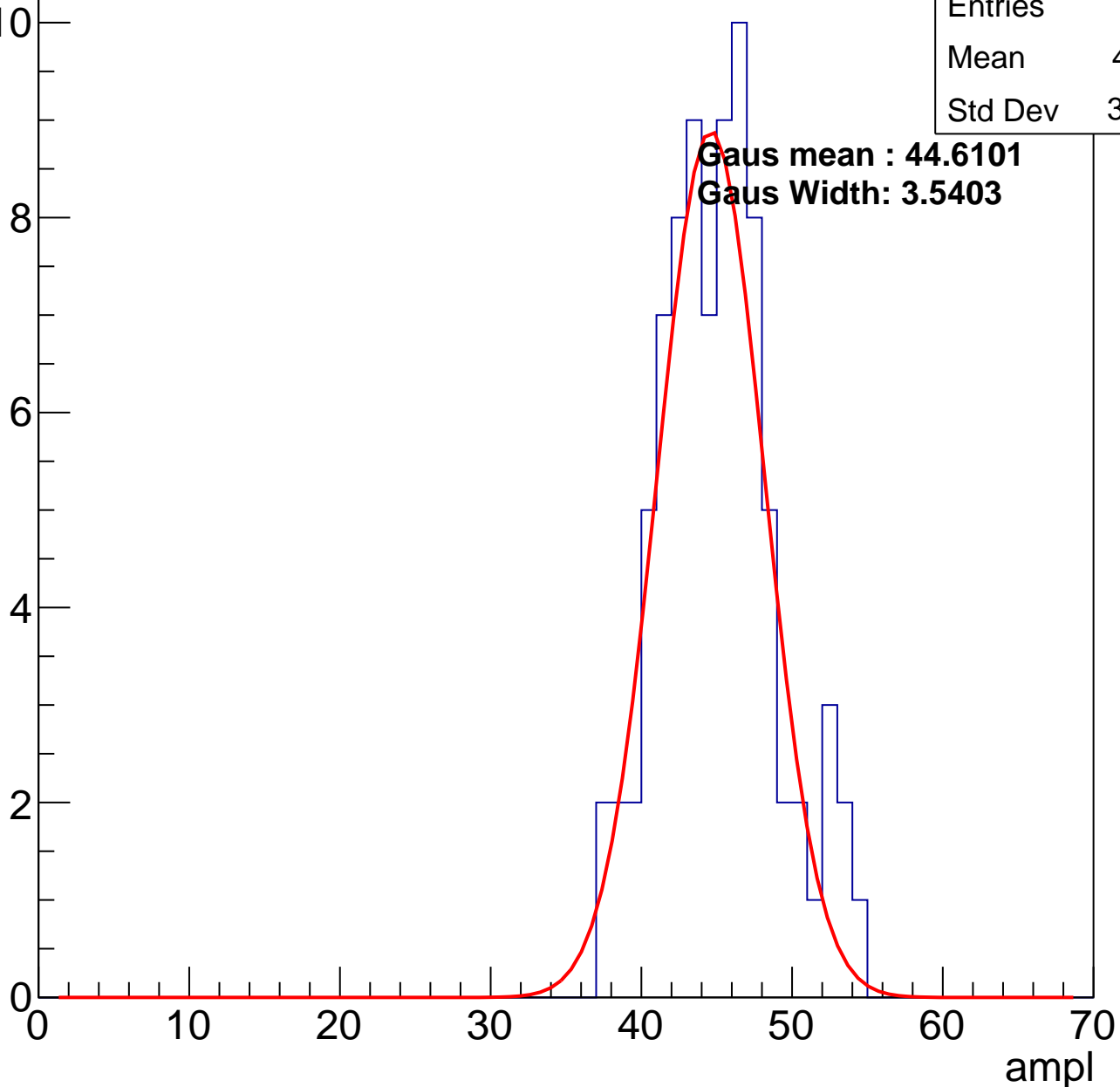
calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	44.61
Std Dev	3.755

Gaus mean : 44.6101

Gaus Width: 3.5403

Entry

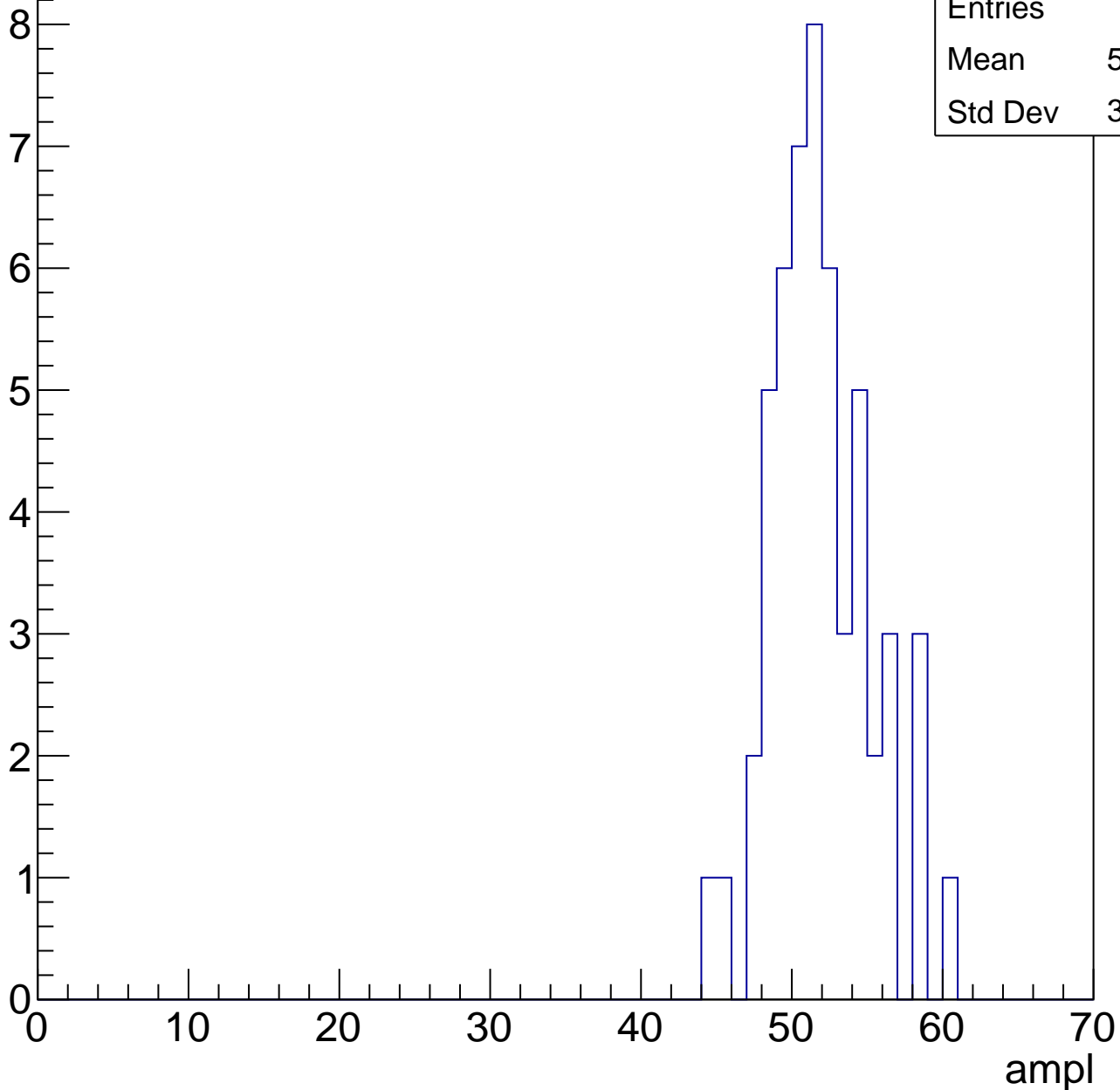


B0L002S, U4-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	51.47
Std Dev	3.306

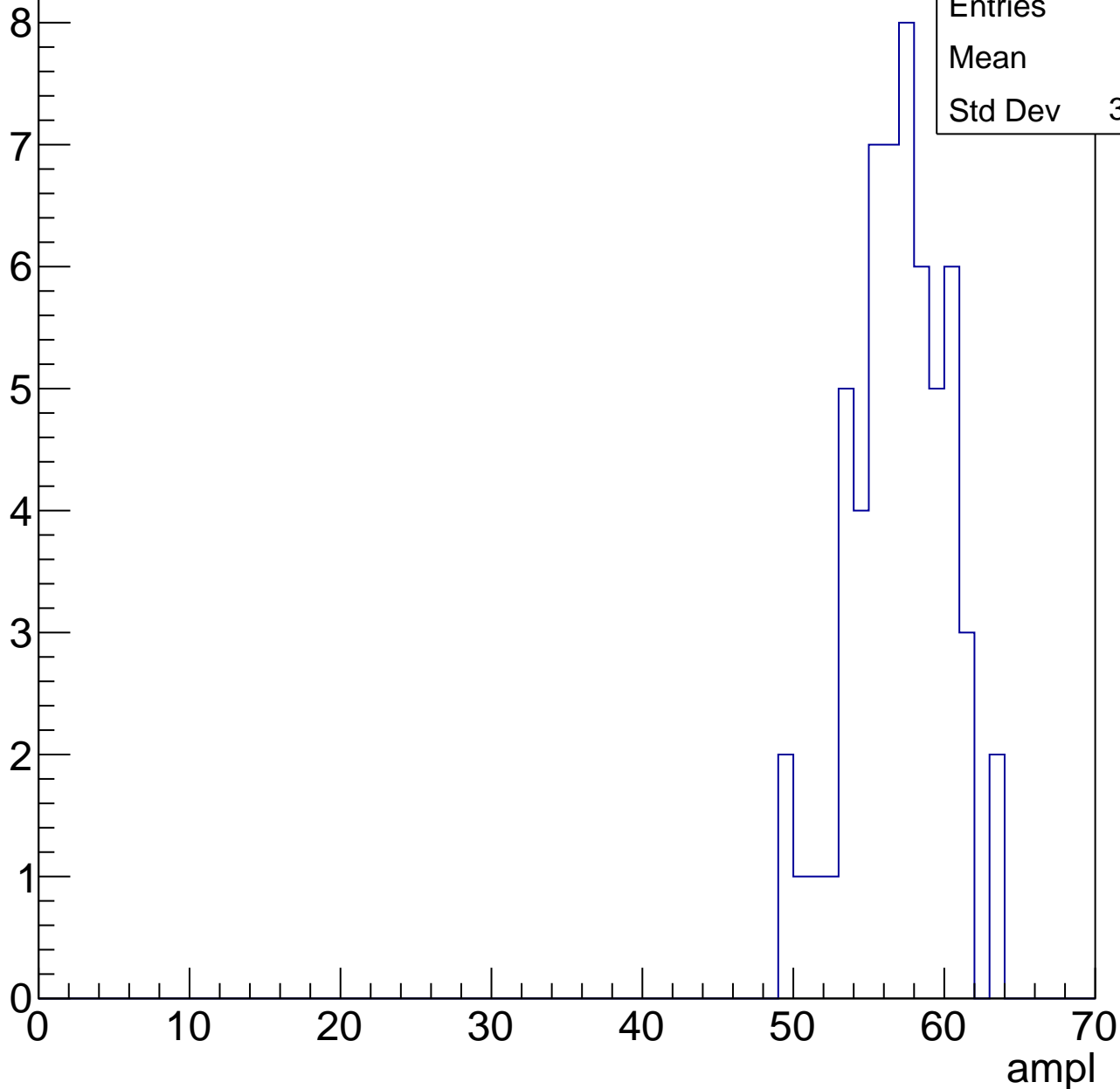


B0L002S, U4-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.5
Std Dev	3.147

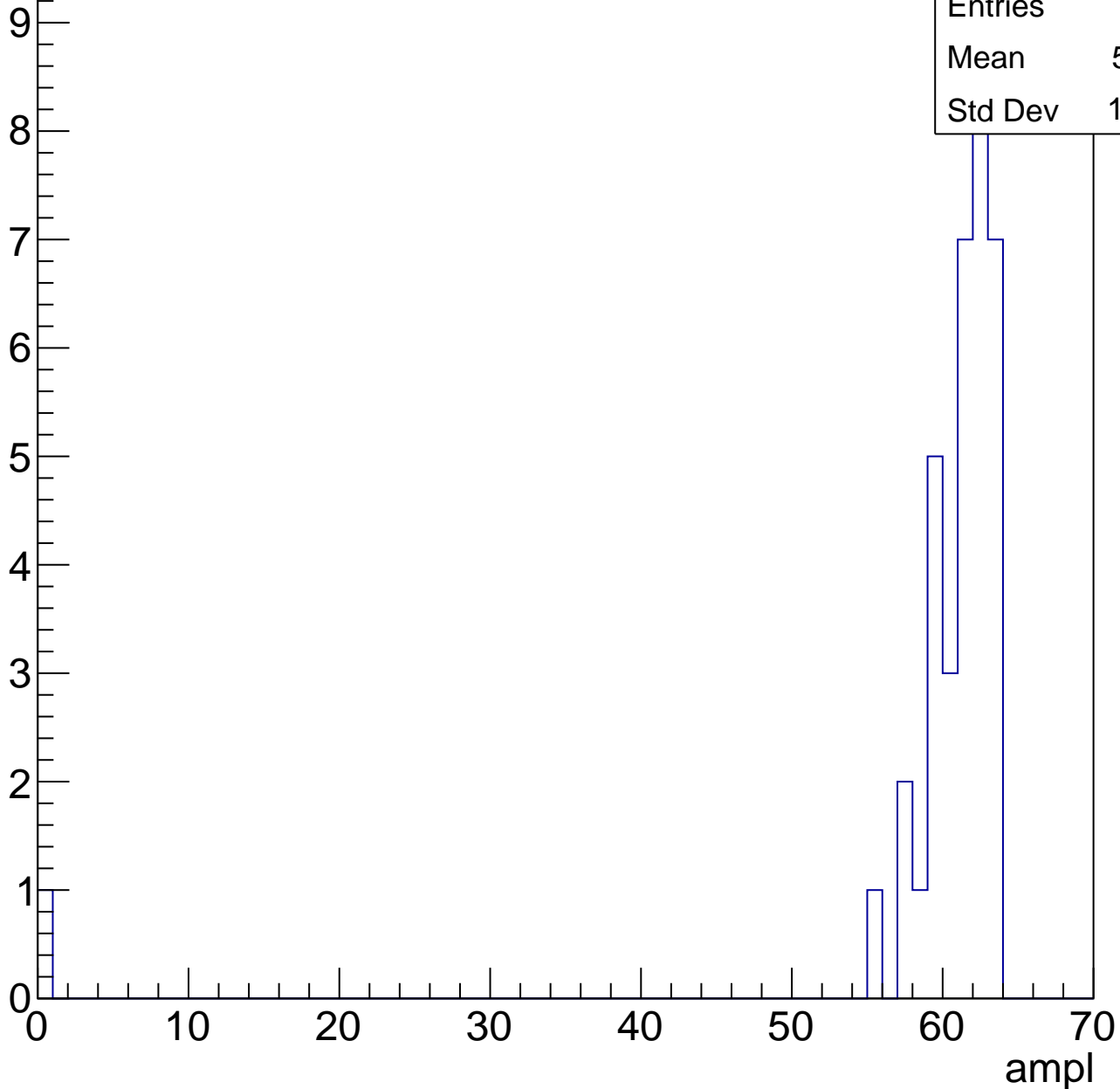


B0L002S, U4-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.11
Std Dev	10.18



B0L002S, U4-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

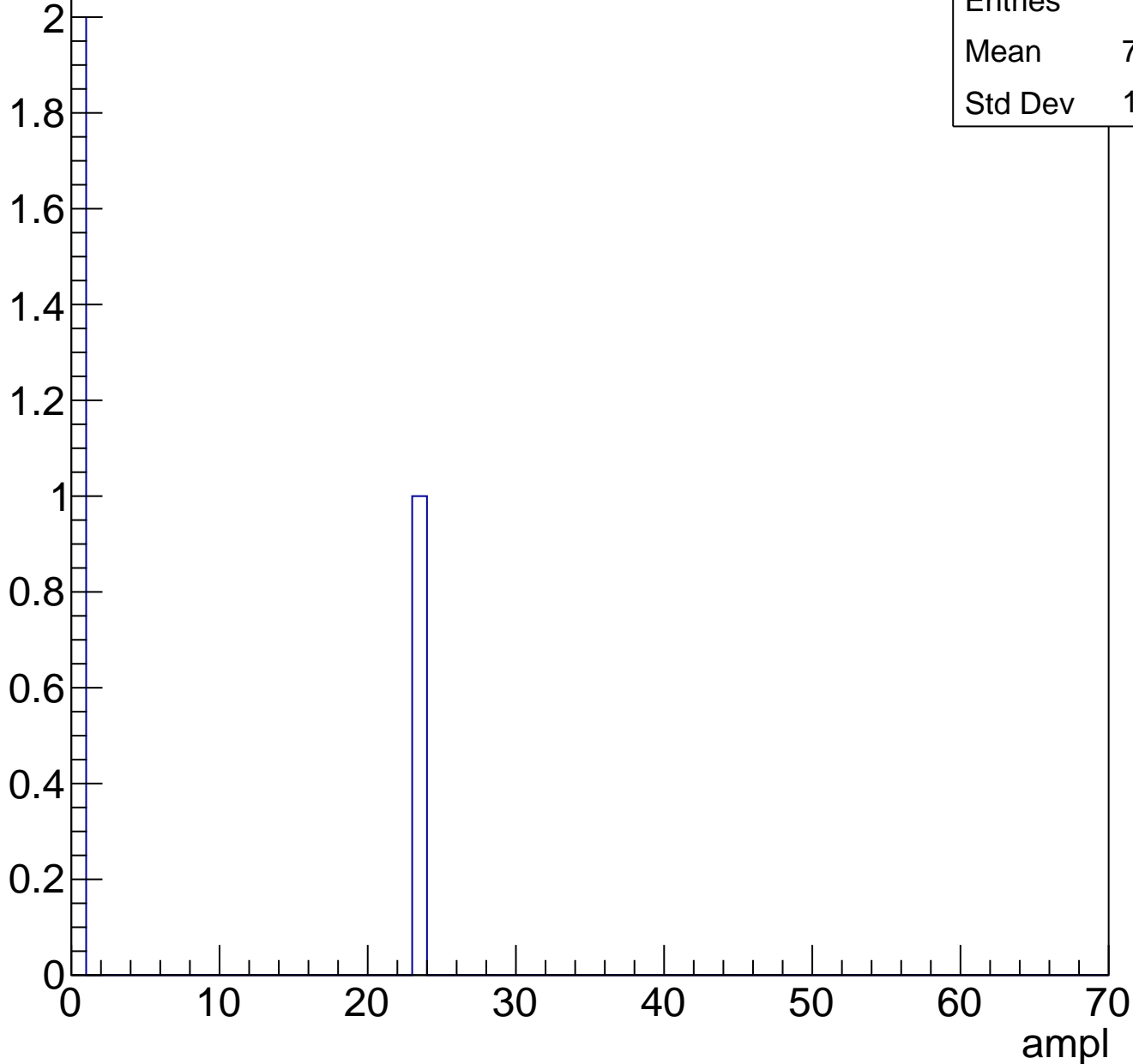
Entry



B0L002S, U4-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B0L002S, U4-ch102, adc0

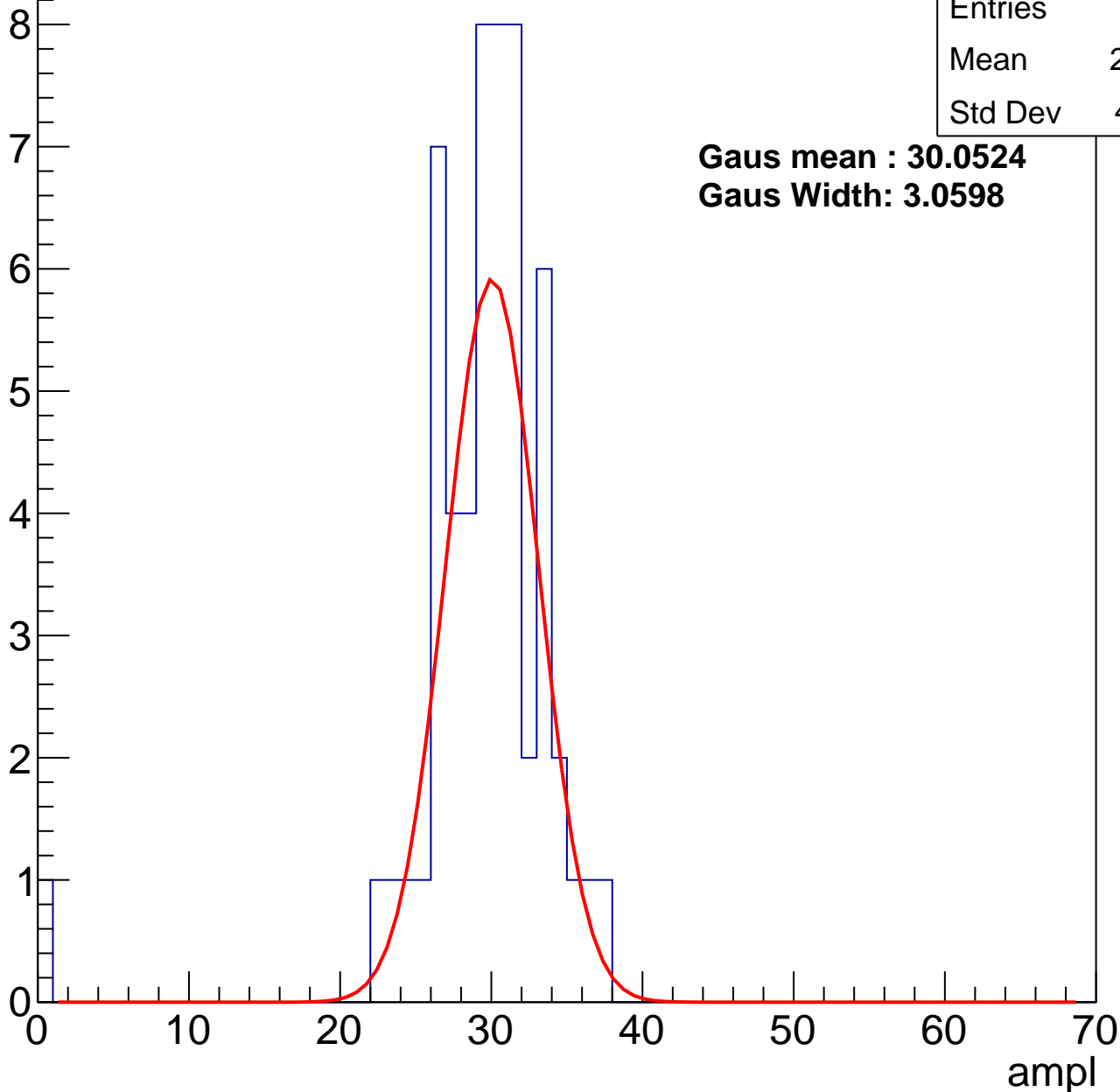
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.02
Std Dev	4.961

Gaus mean : 30.0524

Gaus Width: 3.0598



B0L002S, U4-ch102, adc1

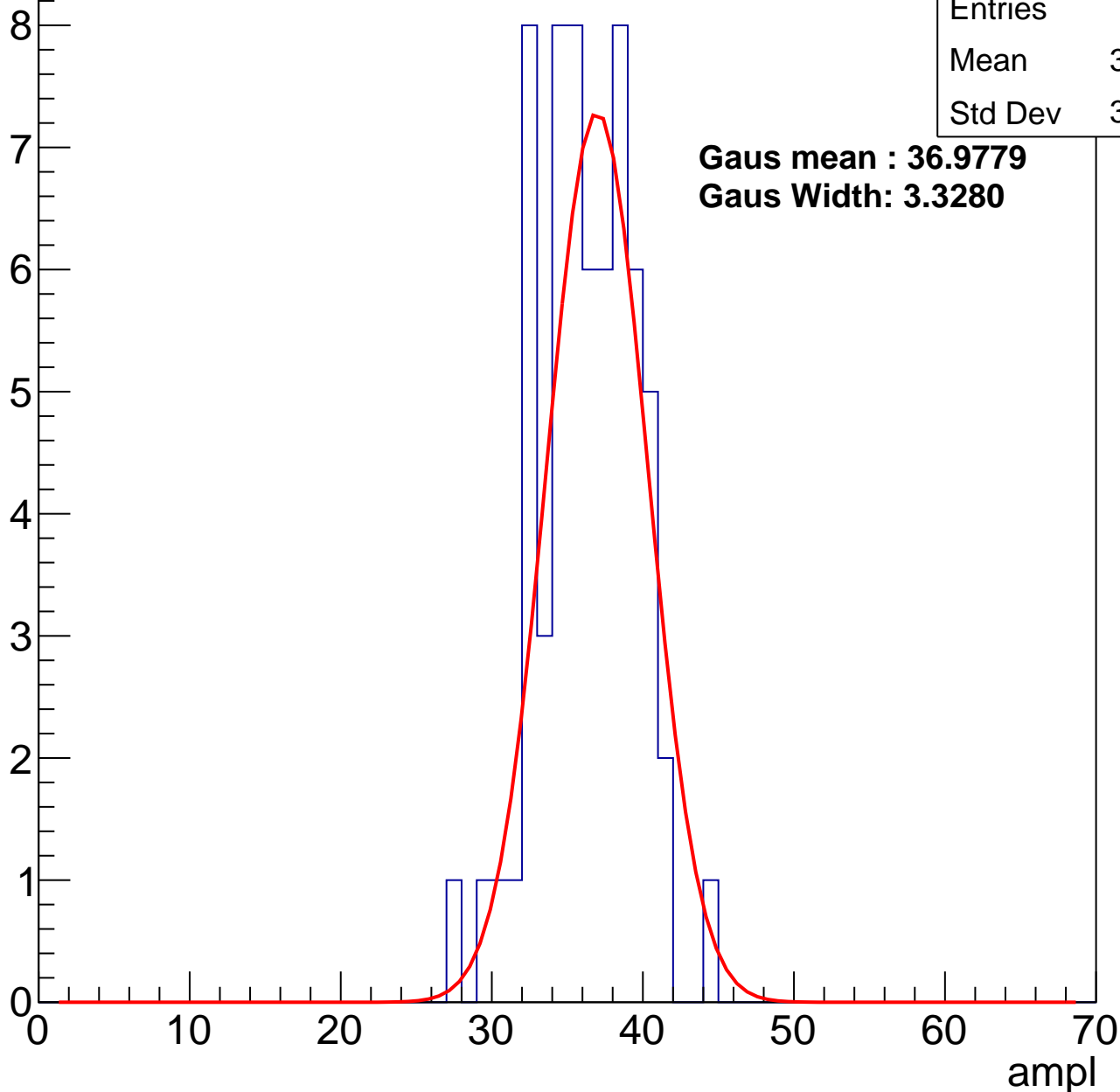
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.78
Std Dev	3.208

Gaus mean : 36.9779

Gaus Width: 3.3280



B0L002S, U4-ch102, adc2

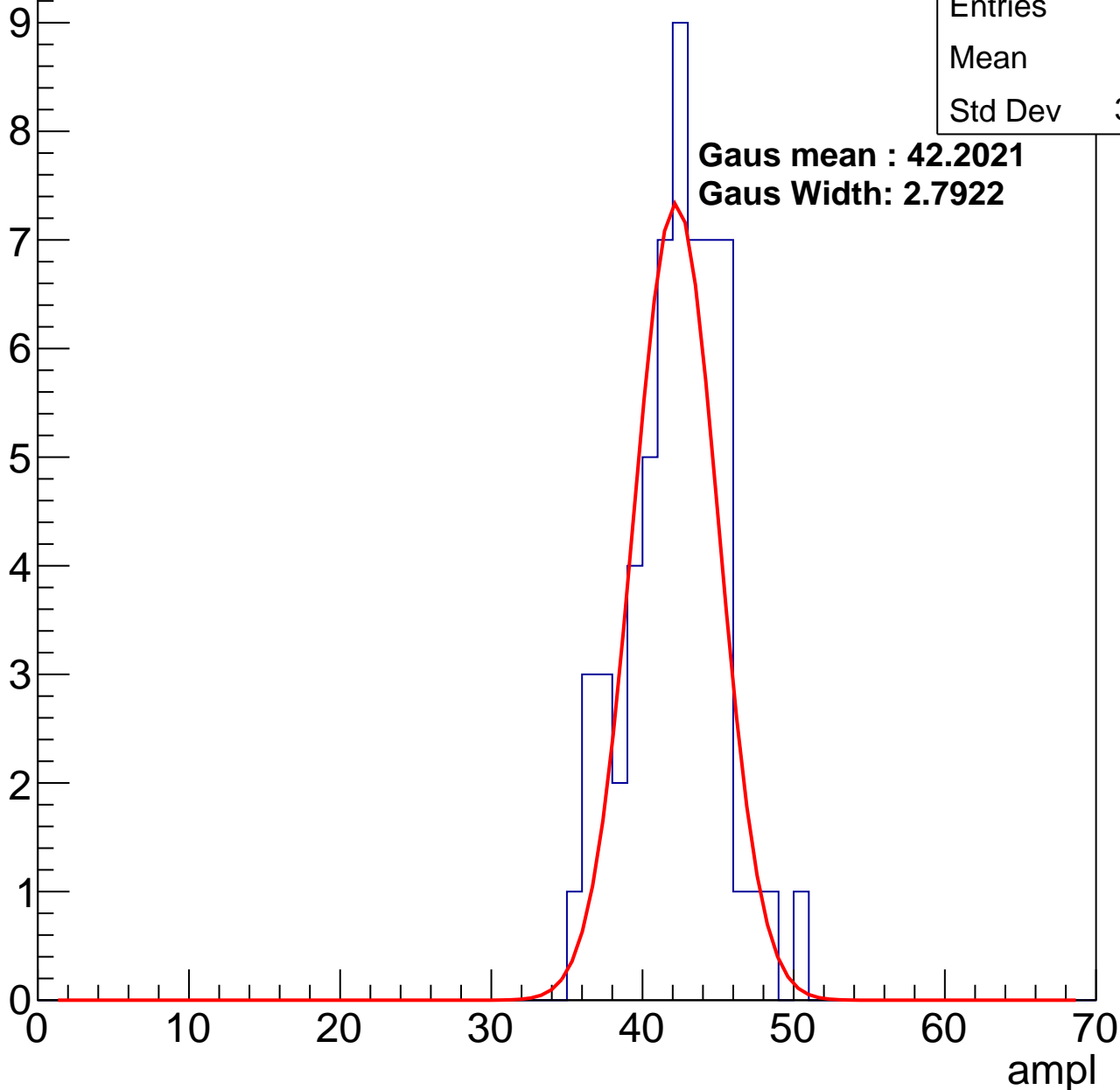
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	41.8
Std Dev	3.091

Gaus mean : 42.2021

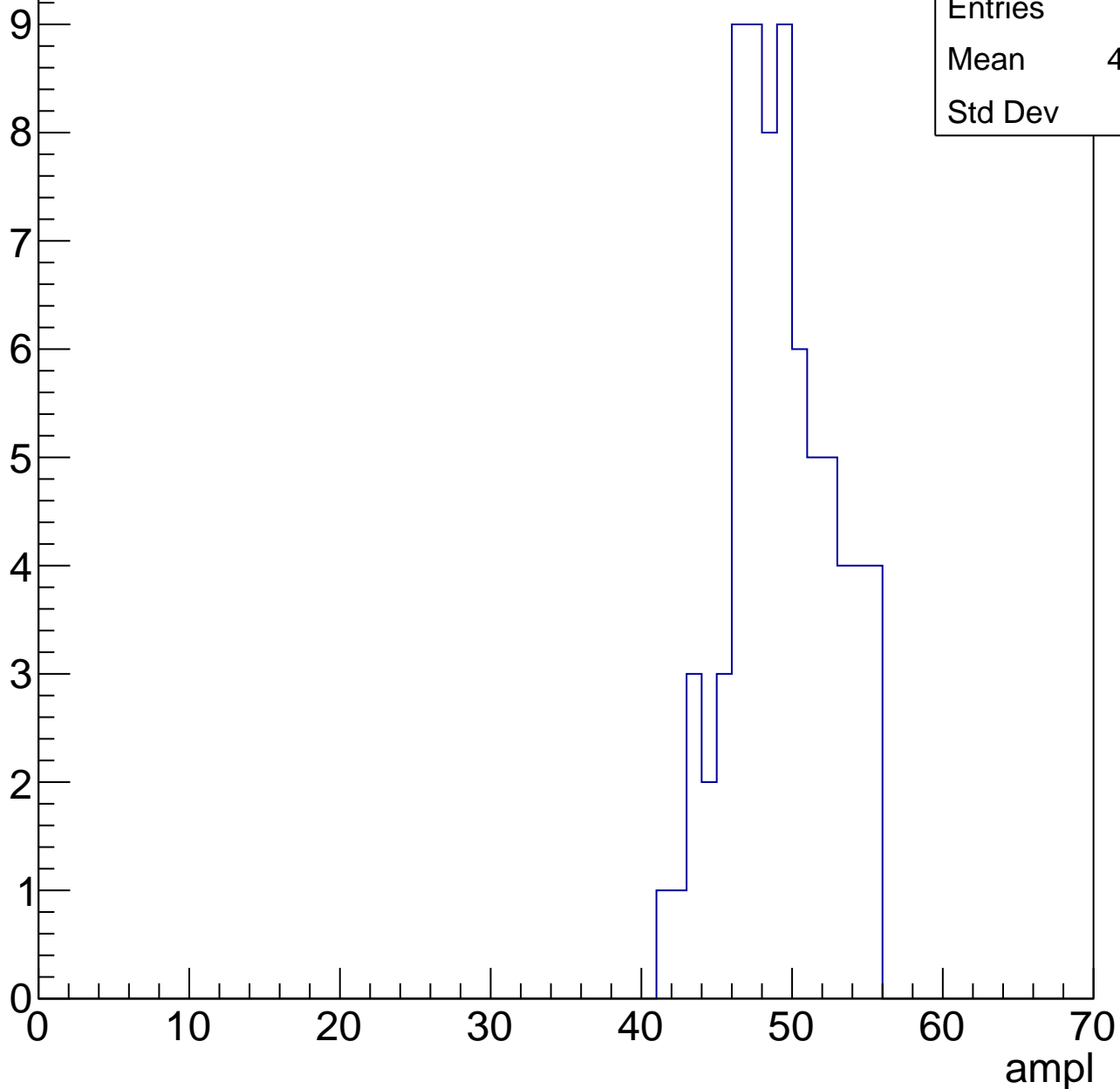
Gaus Width: 2.7922



B0L002S, U4-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

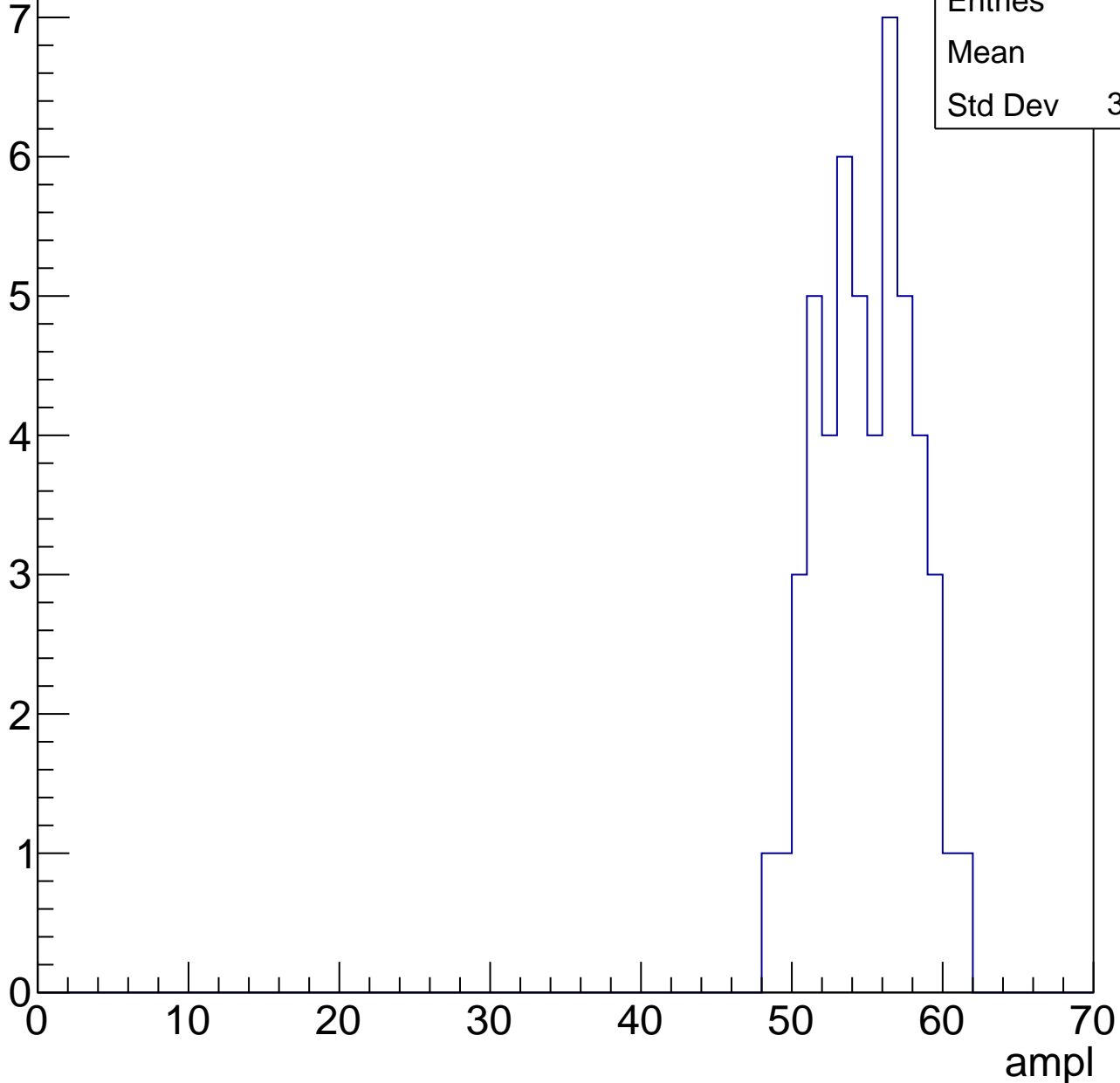


B0L002S, U4-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	54.5
Std Dev	3.048

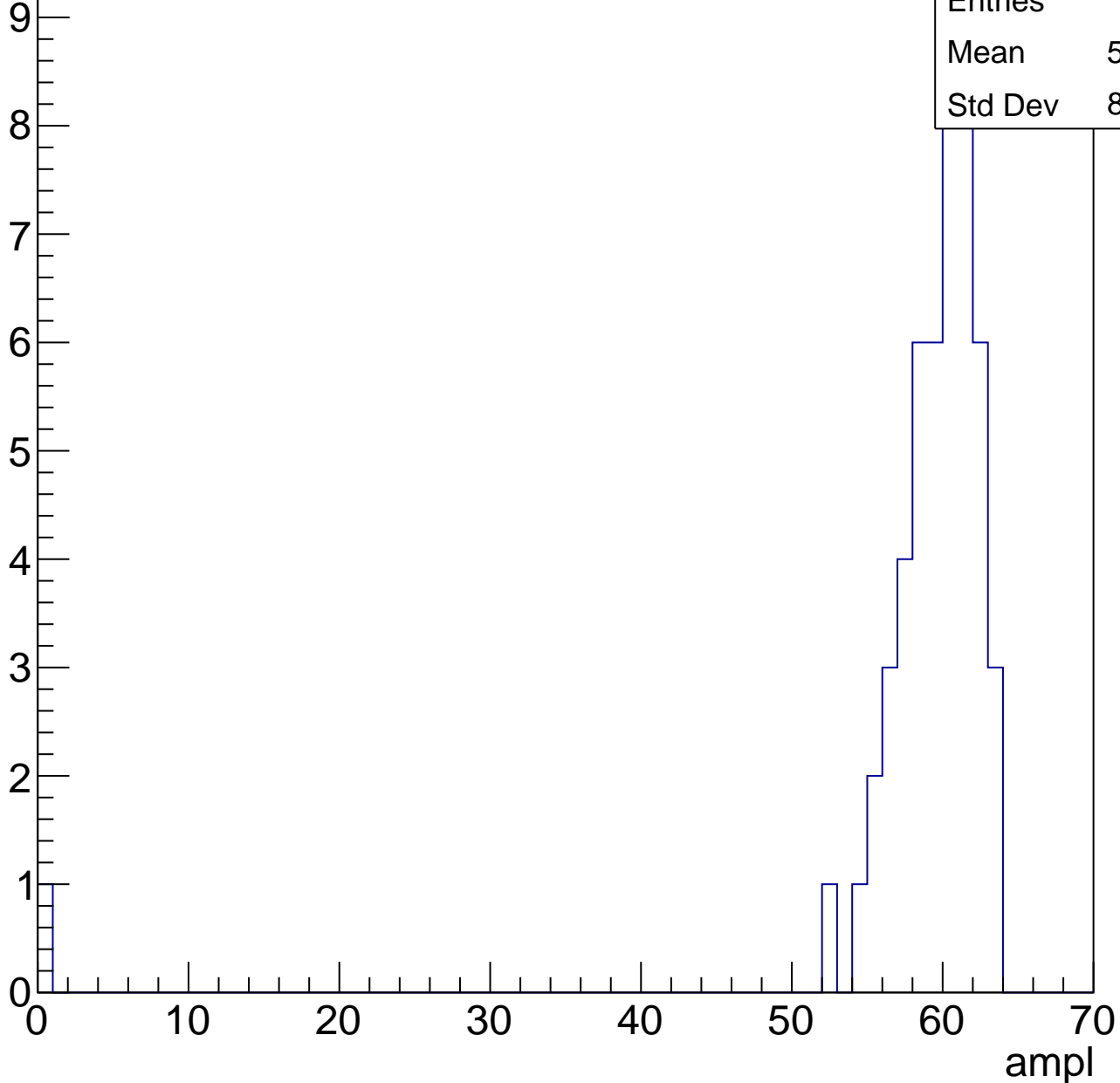


B0L002S, U4-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.06
Std Dev	8.643

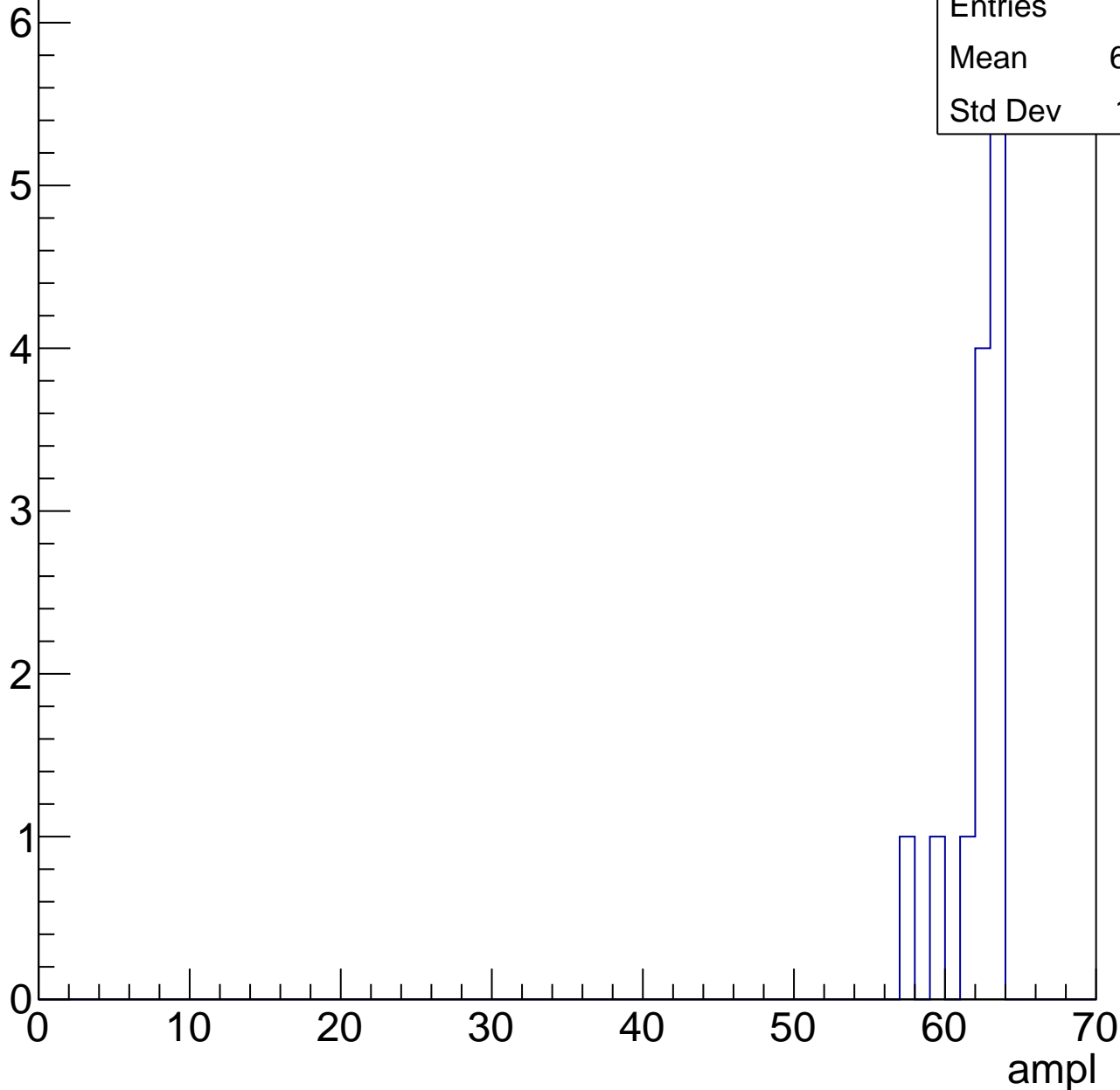


B0L002S, U4-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	61.77
Std Dev	1.761



B0L002S, U4-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch103, adc0

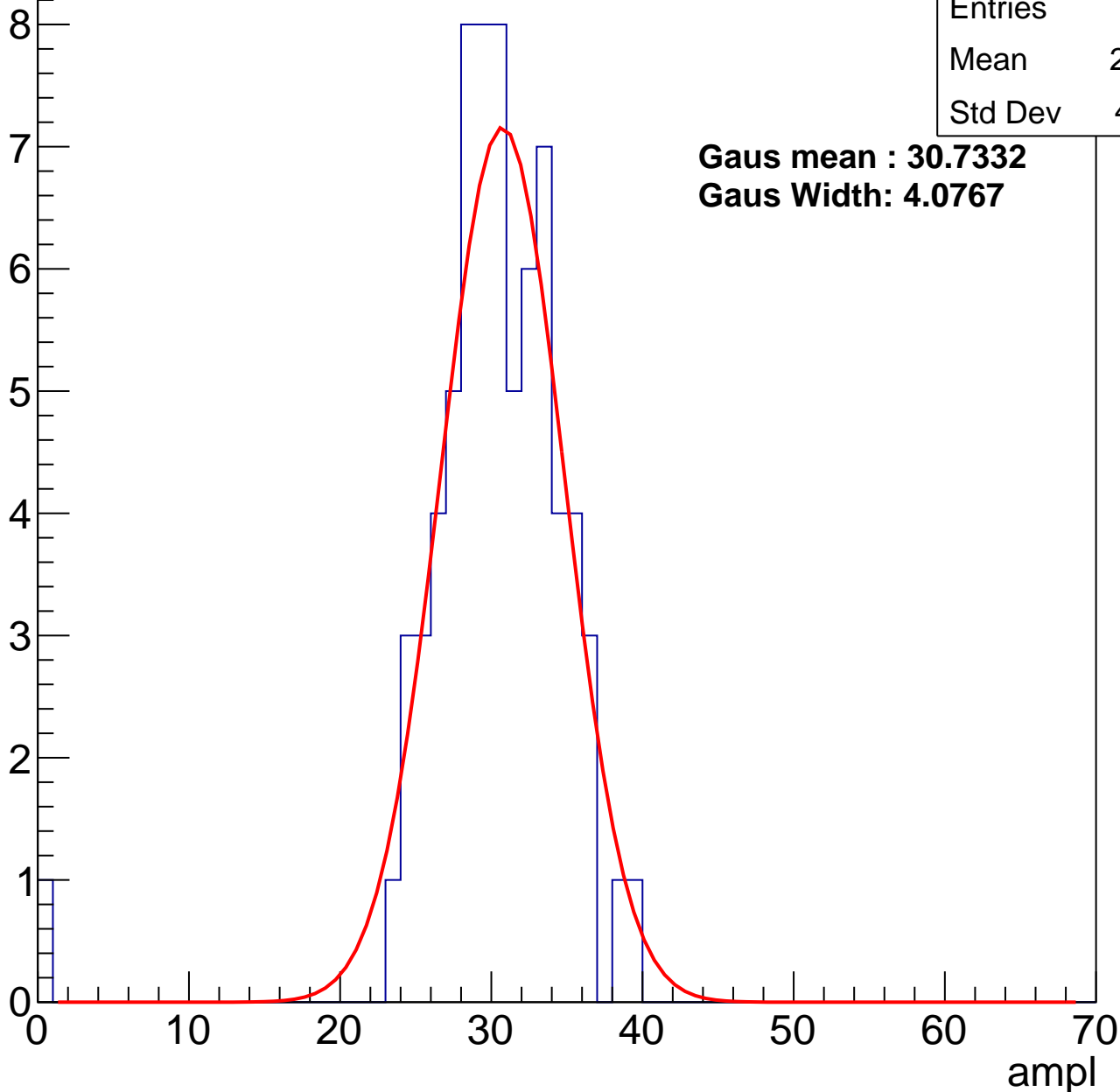
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.78
Std Dev	4.981

Gaus mean : 30.7332

Gaus Width: 4.0767



B0L002S, U4-ch103, adc1

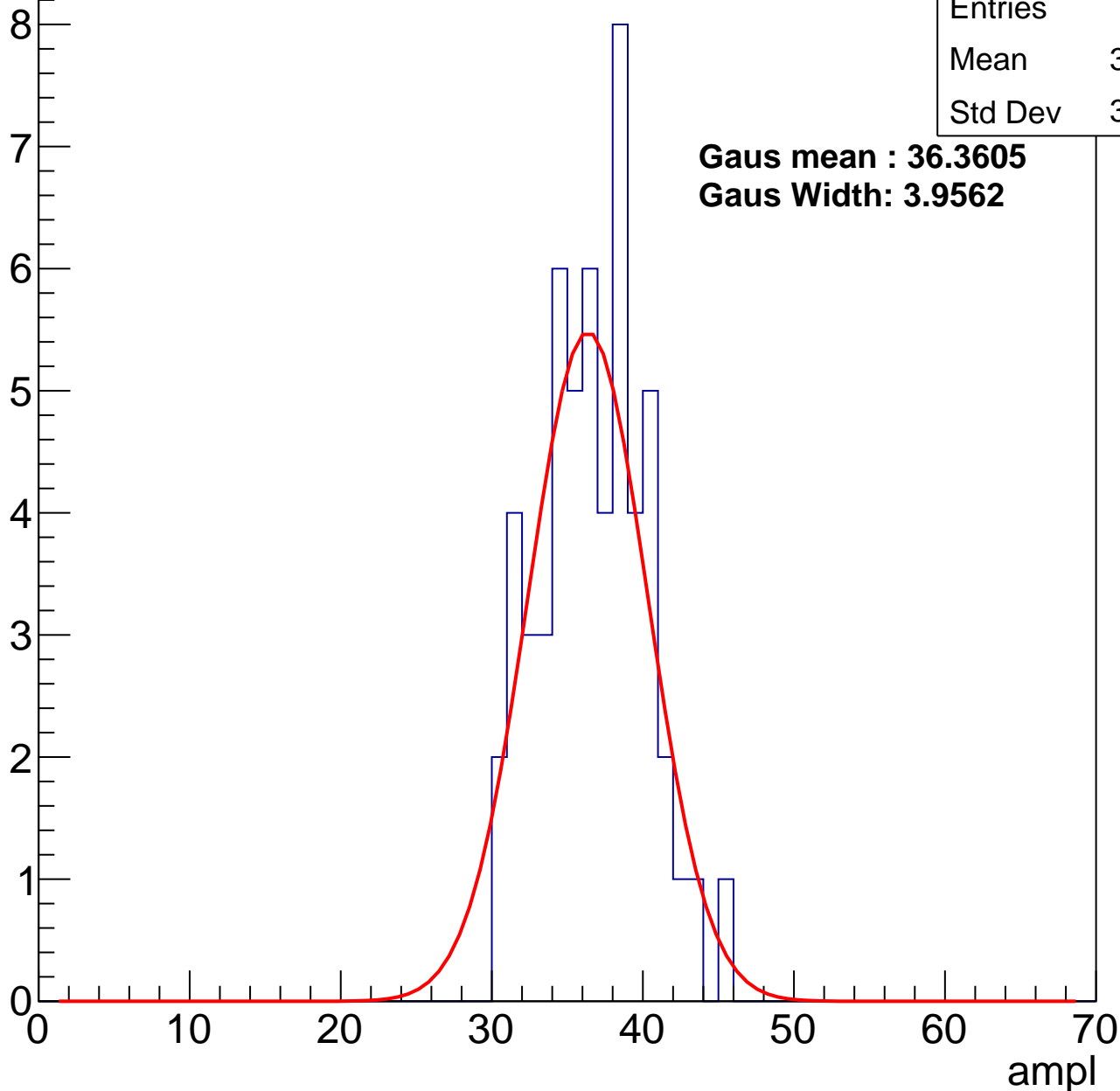
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.25
Std Dev	3.412

Gaus mean : 36.3605

Gaus Width: 3.9562



B0L002S, U4-ch103, adc2

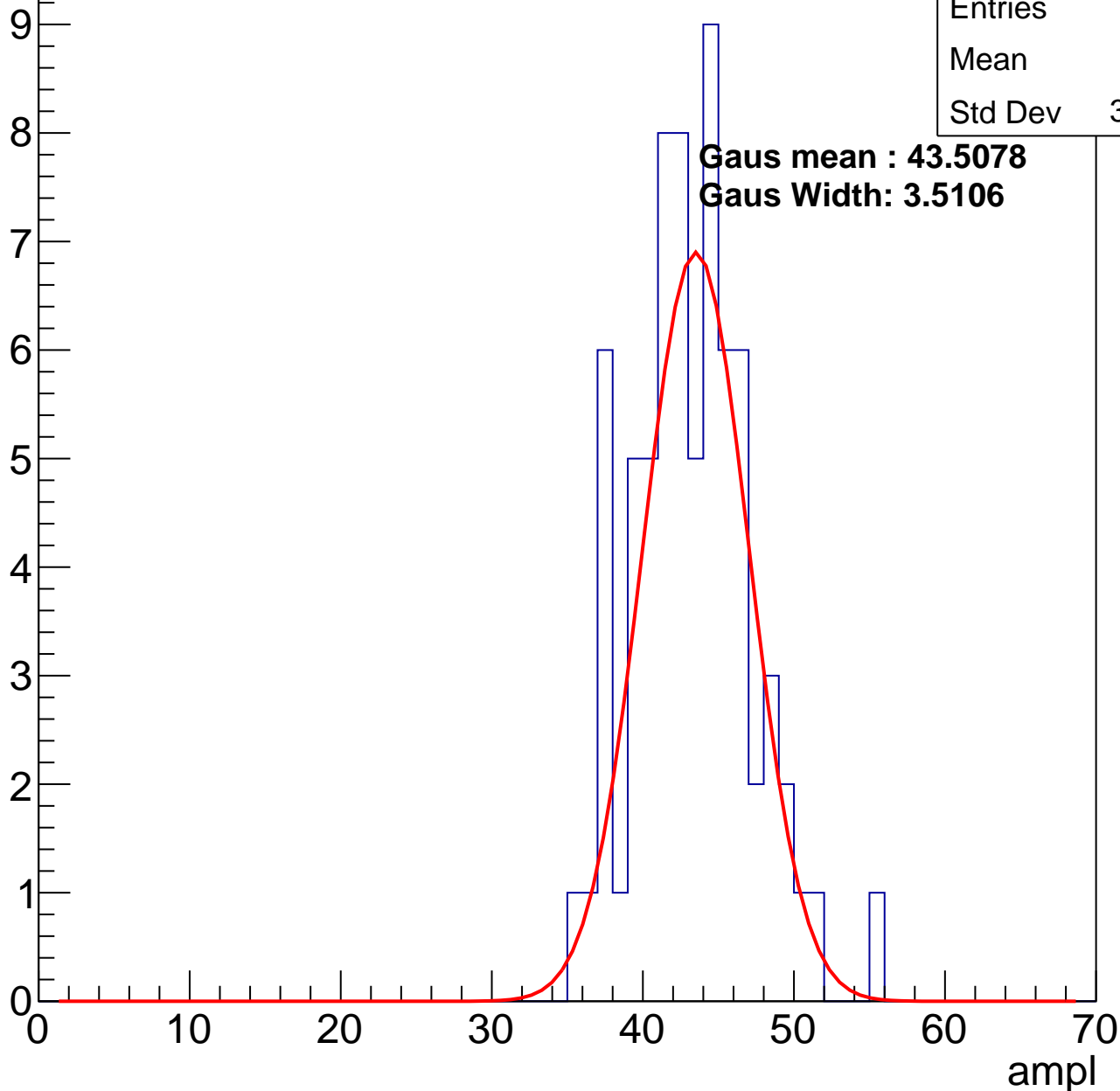
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	42.8
Std Dev	3.826

Gaus mean : 43.5078

Gaus Width: 3.5106

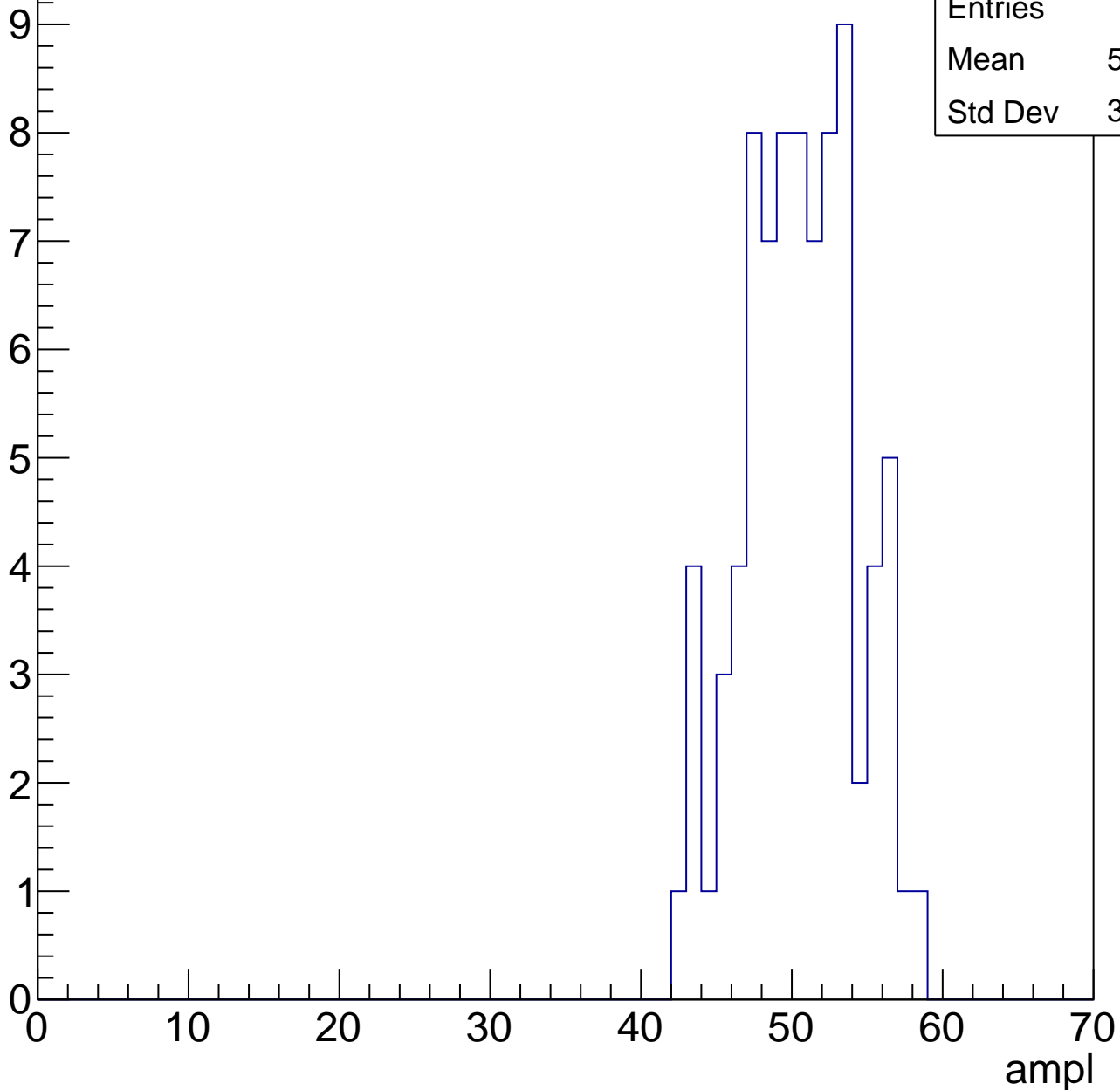


B0L002S, U4-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	50.05
Std Dev	3.665

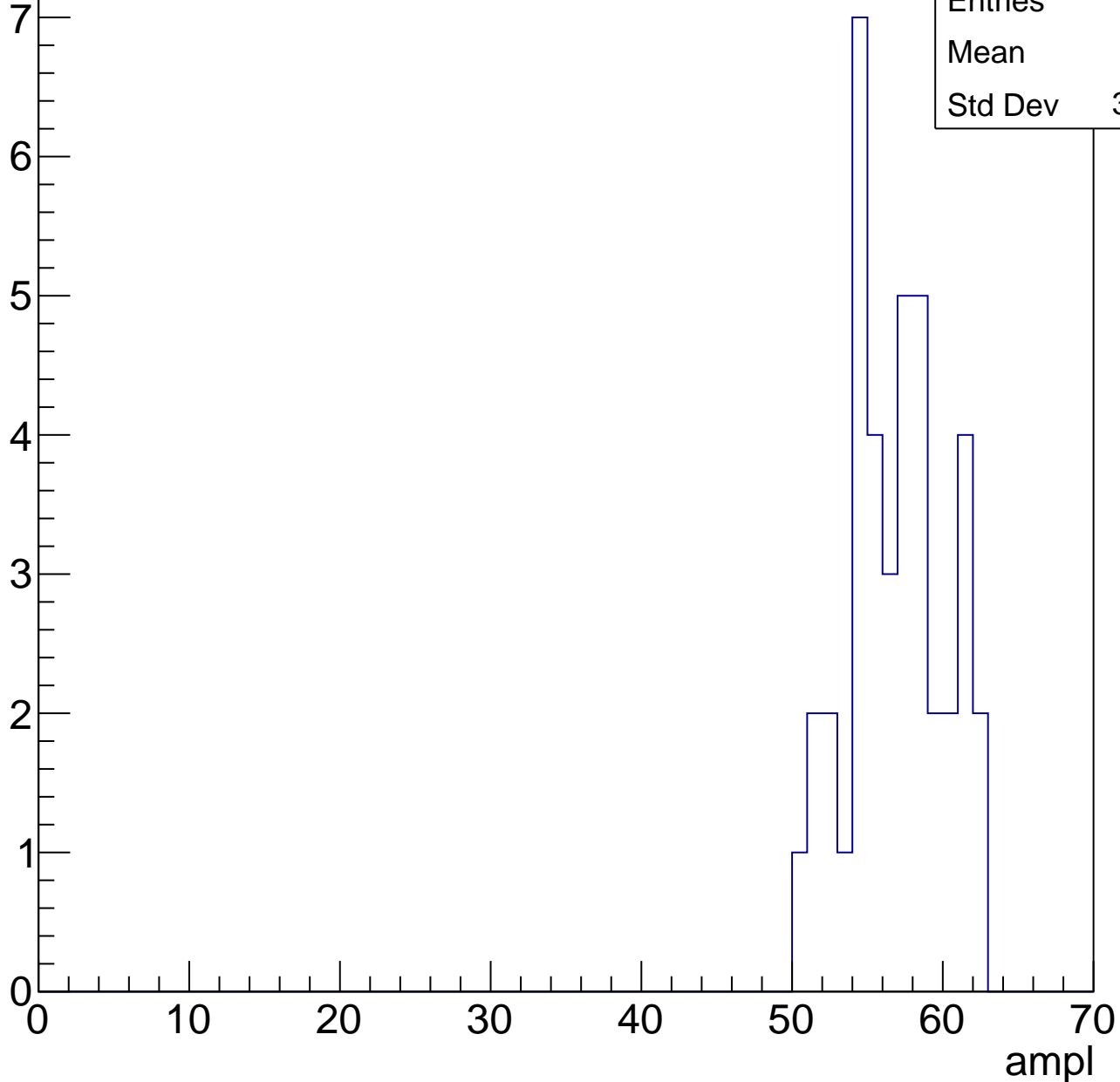


B0L002S, U4-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

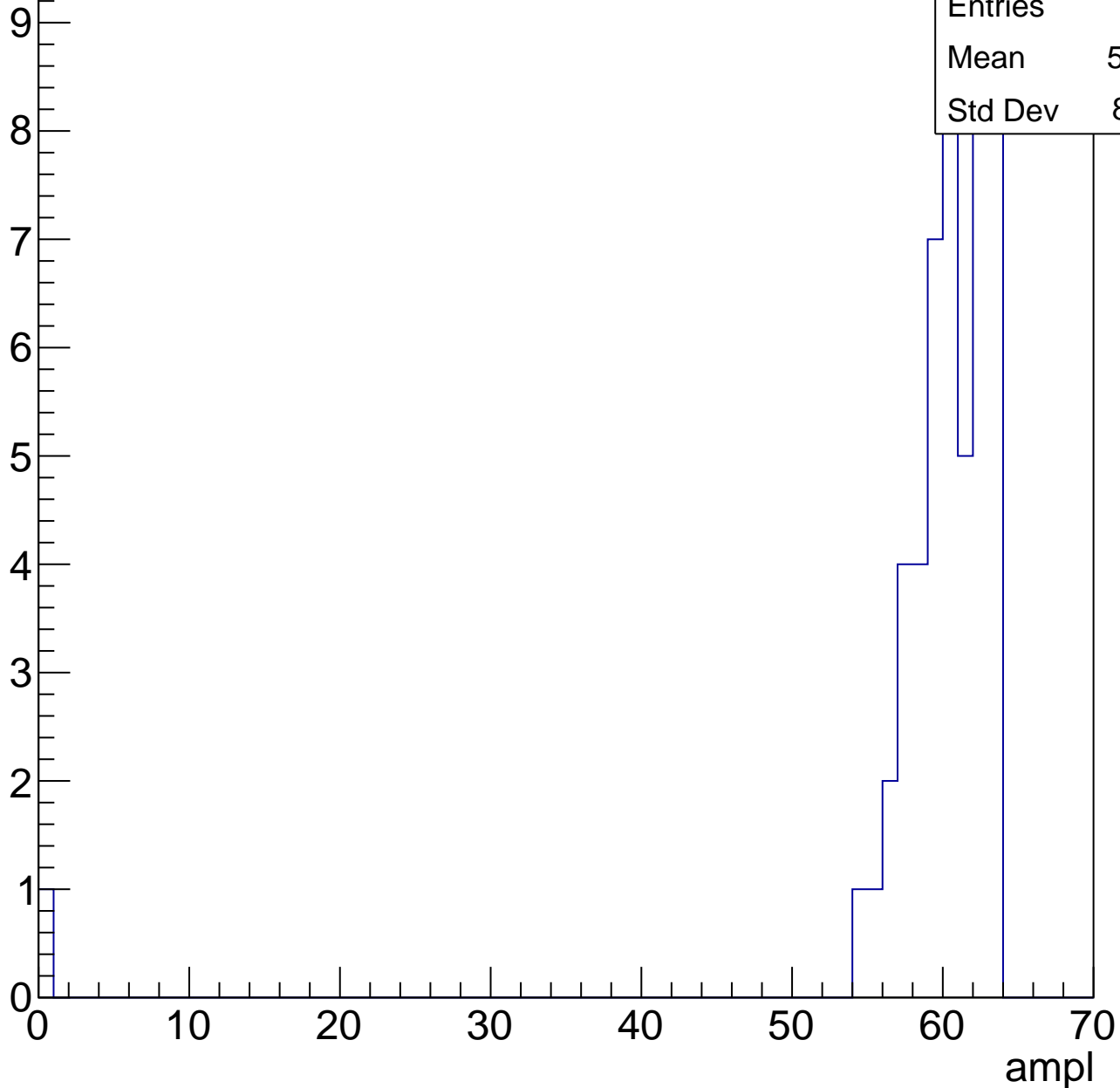
Entries	40
Mean	56.4
Std Dev	3.161



B0L002S, U4-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

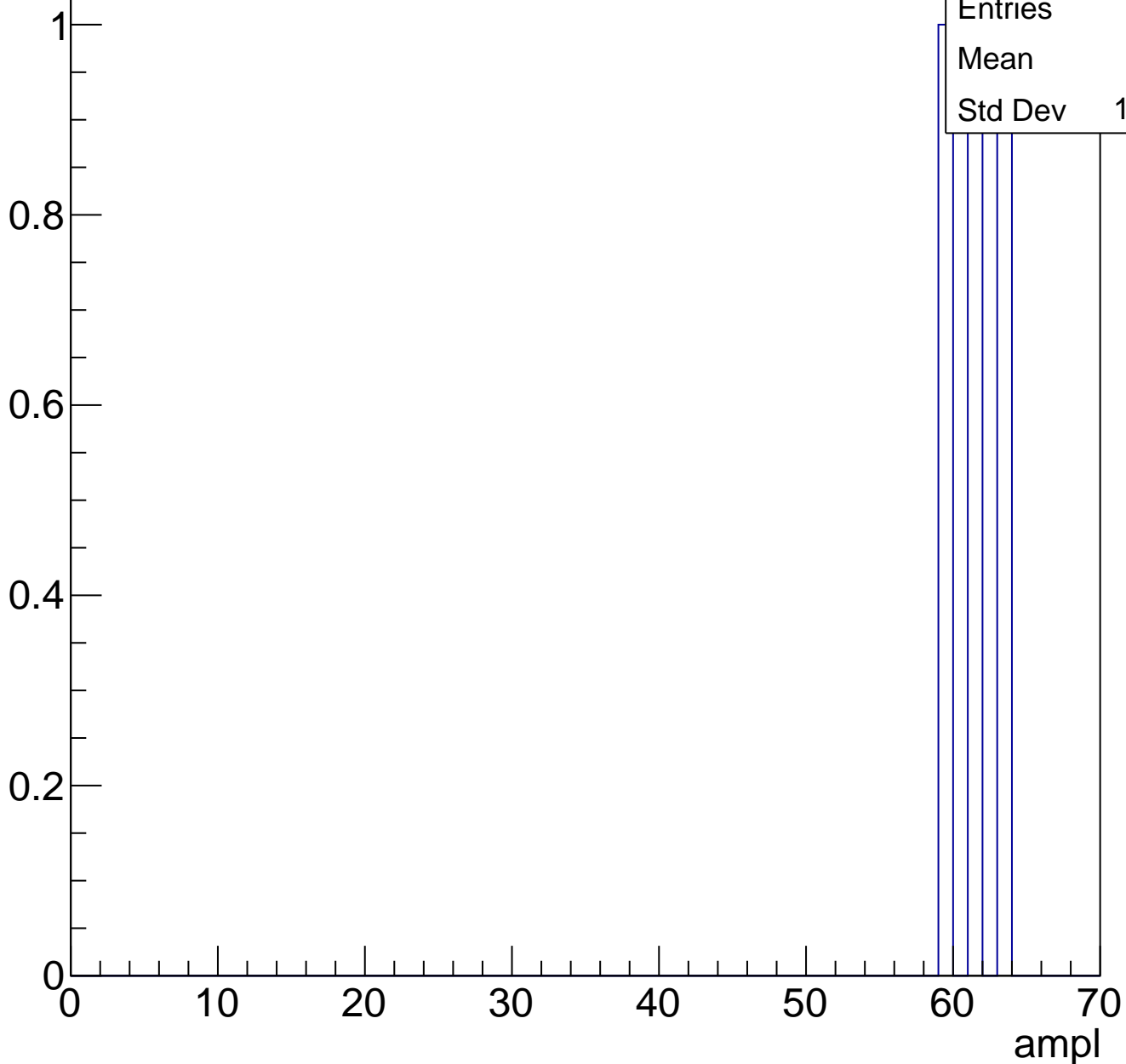
Entry



B0L002S, U4-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch104, adc0

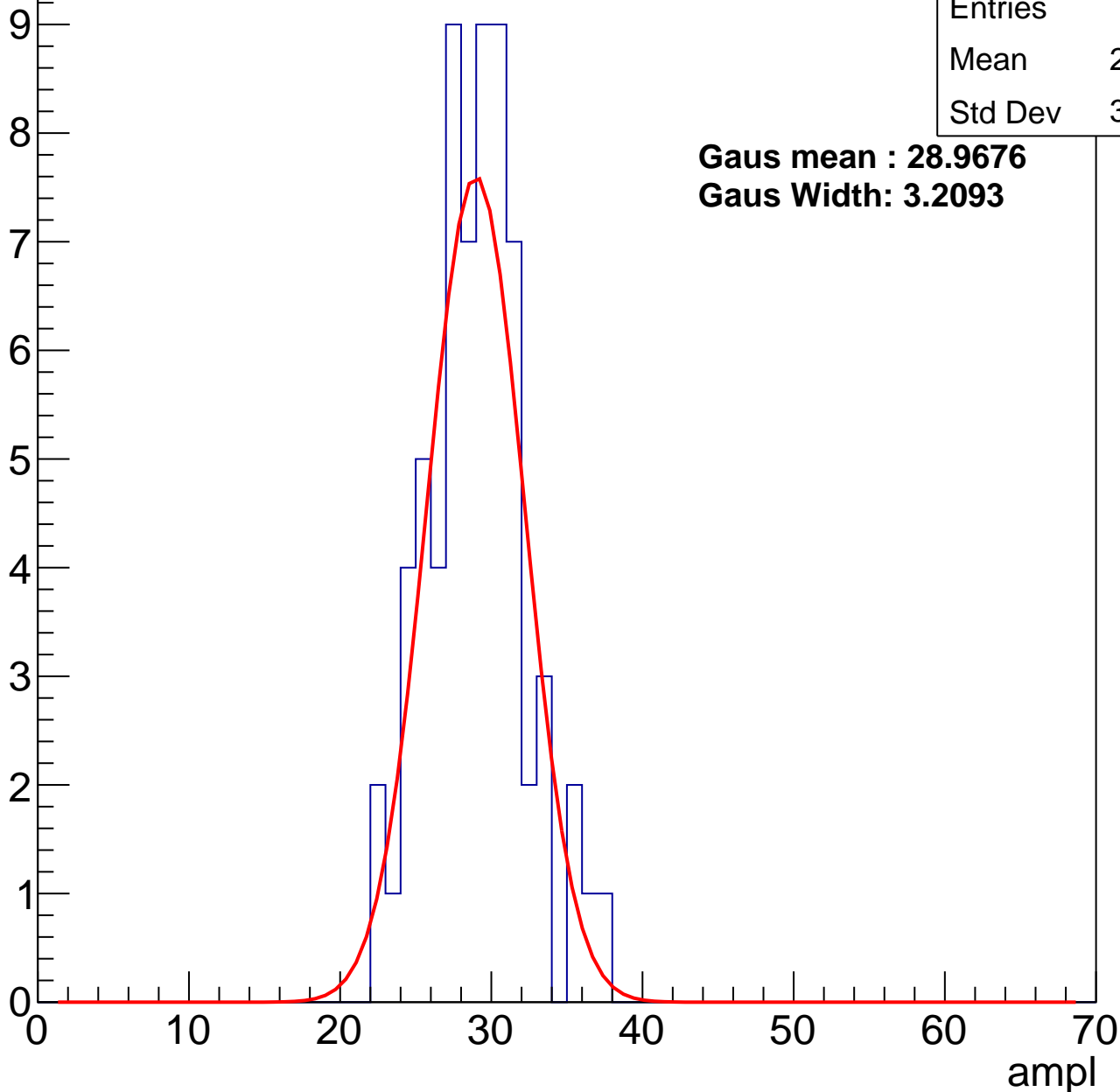
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.56
Std Dev	3.182

Gaus mean : 28.9676

Gaus Width: 3.2093



B0L002S, U4-ch104, adc1

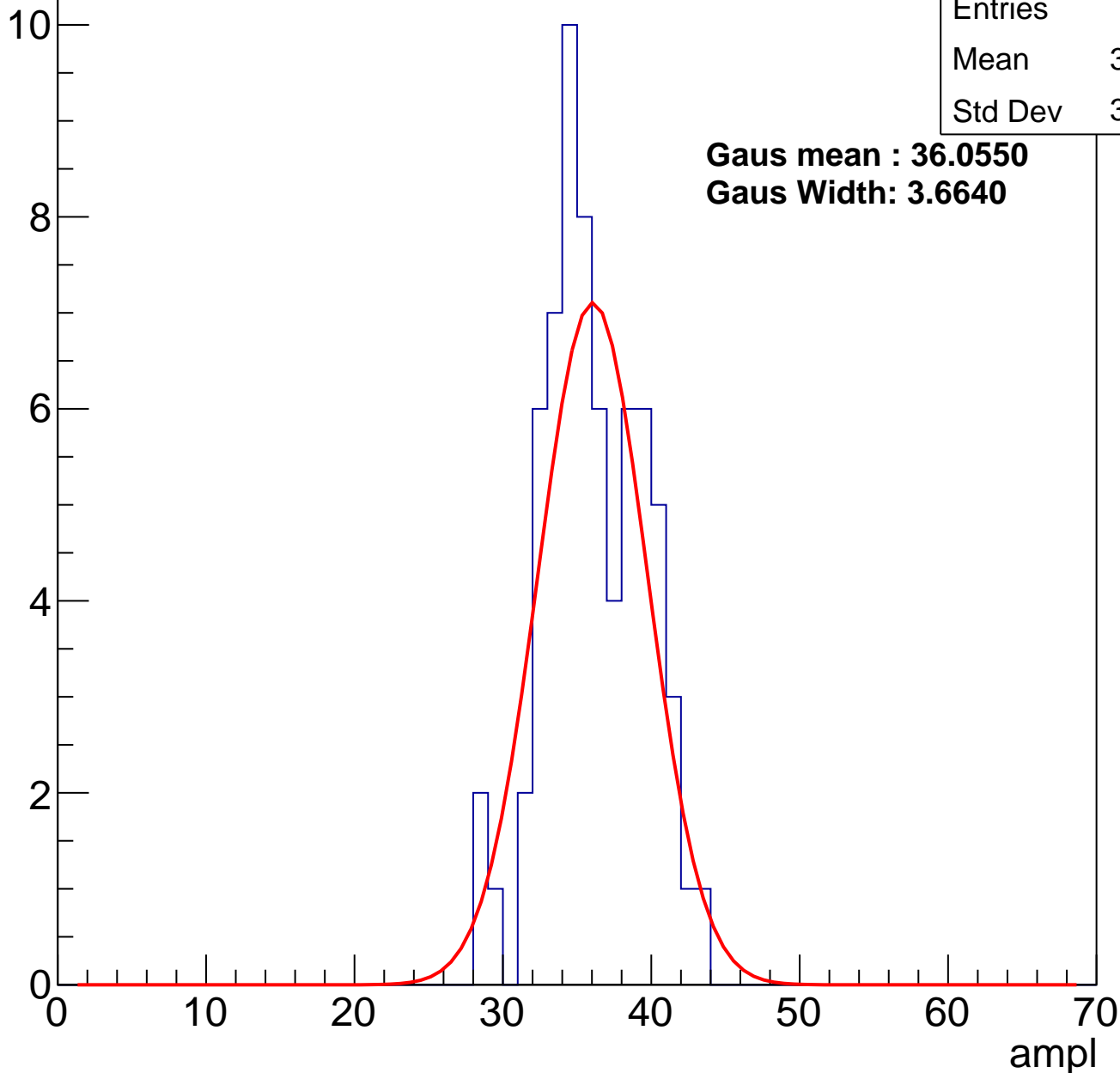
calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	35.65
Std Dev	3.316

Gaus mean : 36.0550

Gaus Width: 3.6640

Entry



B0L002S, U4-ch104, adc2

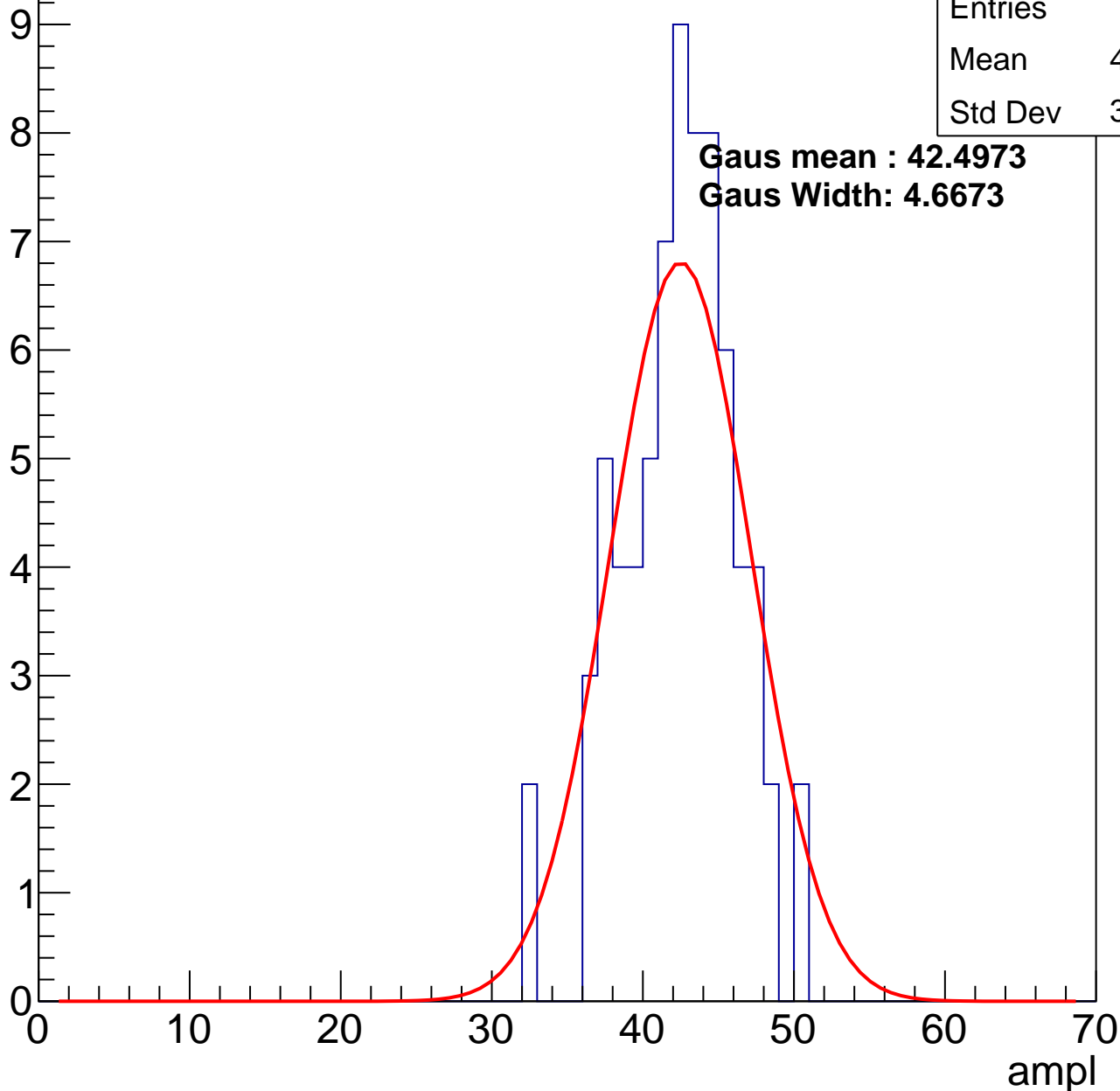
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	41.97
Std Dev	3.734

Gaus mean : 42.4973

Gaus Width: 4.6673

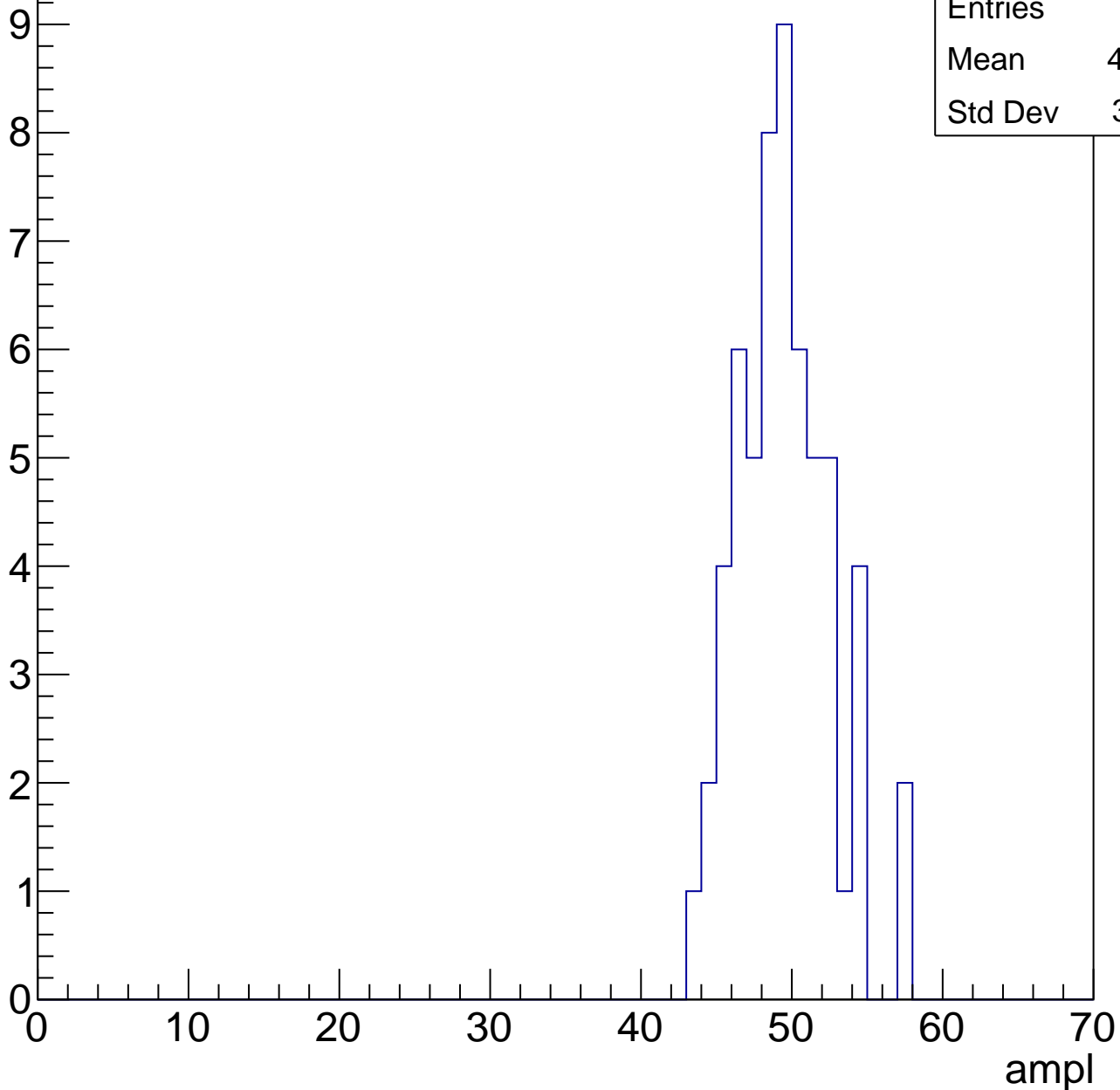


B0L002S, U4-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	49.05
Std Dev	3.071

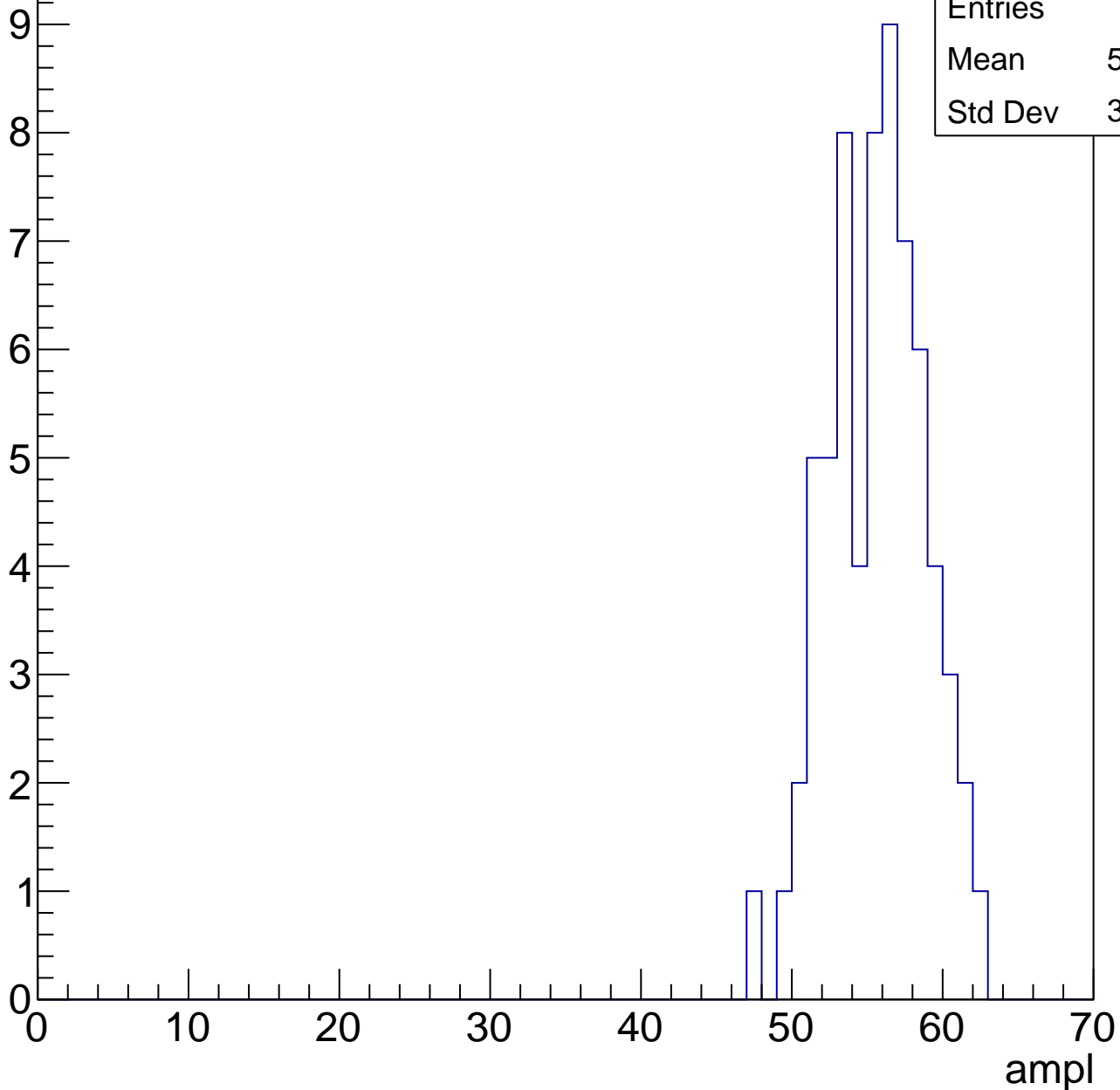


B0L002S, U4-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

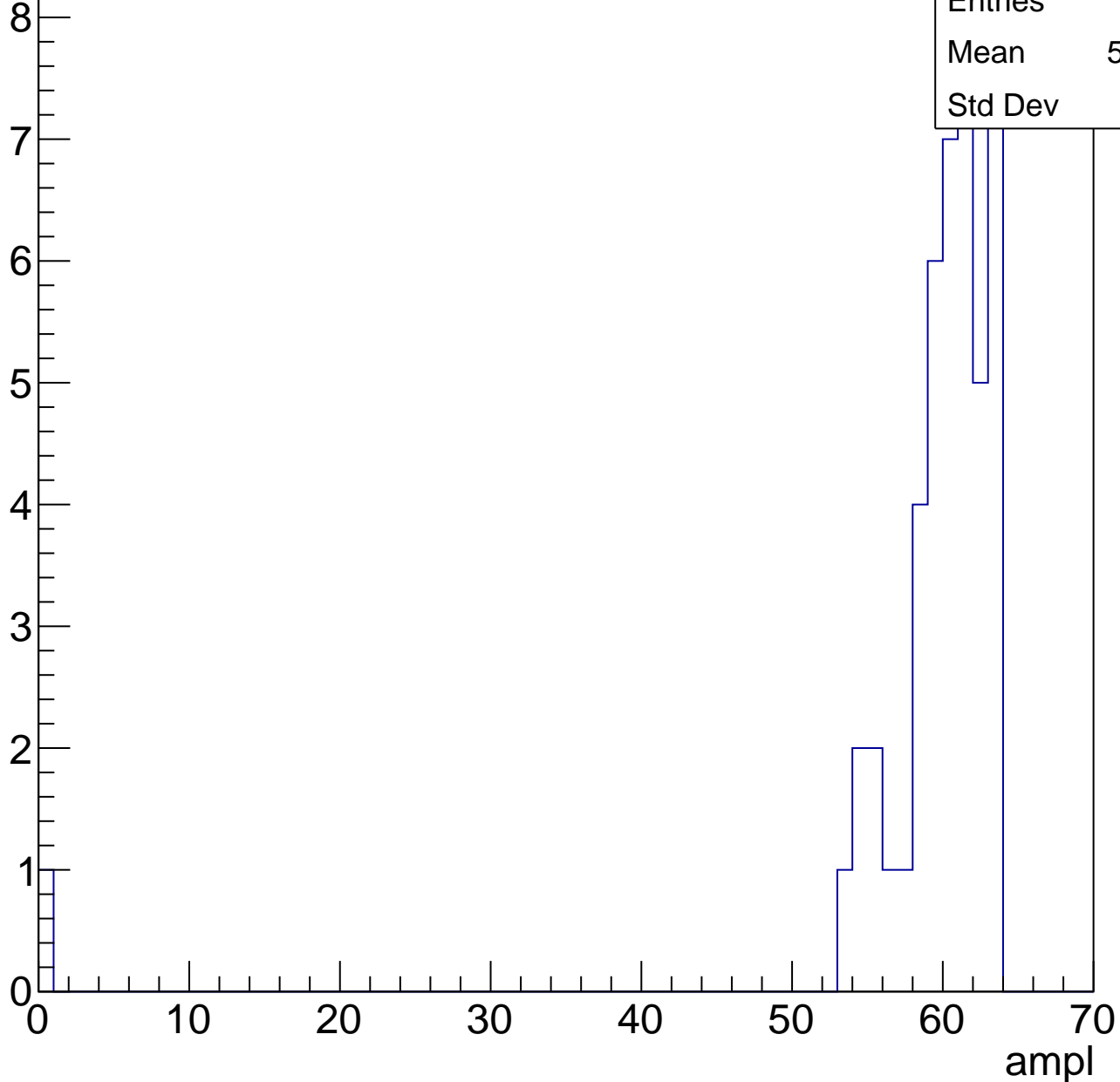
Entries	66
Mean	55.18
Std Dev	3.162



B0L002S, U4-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

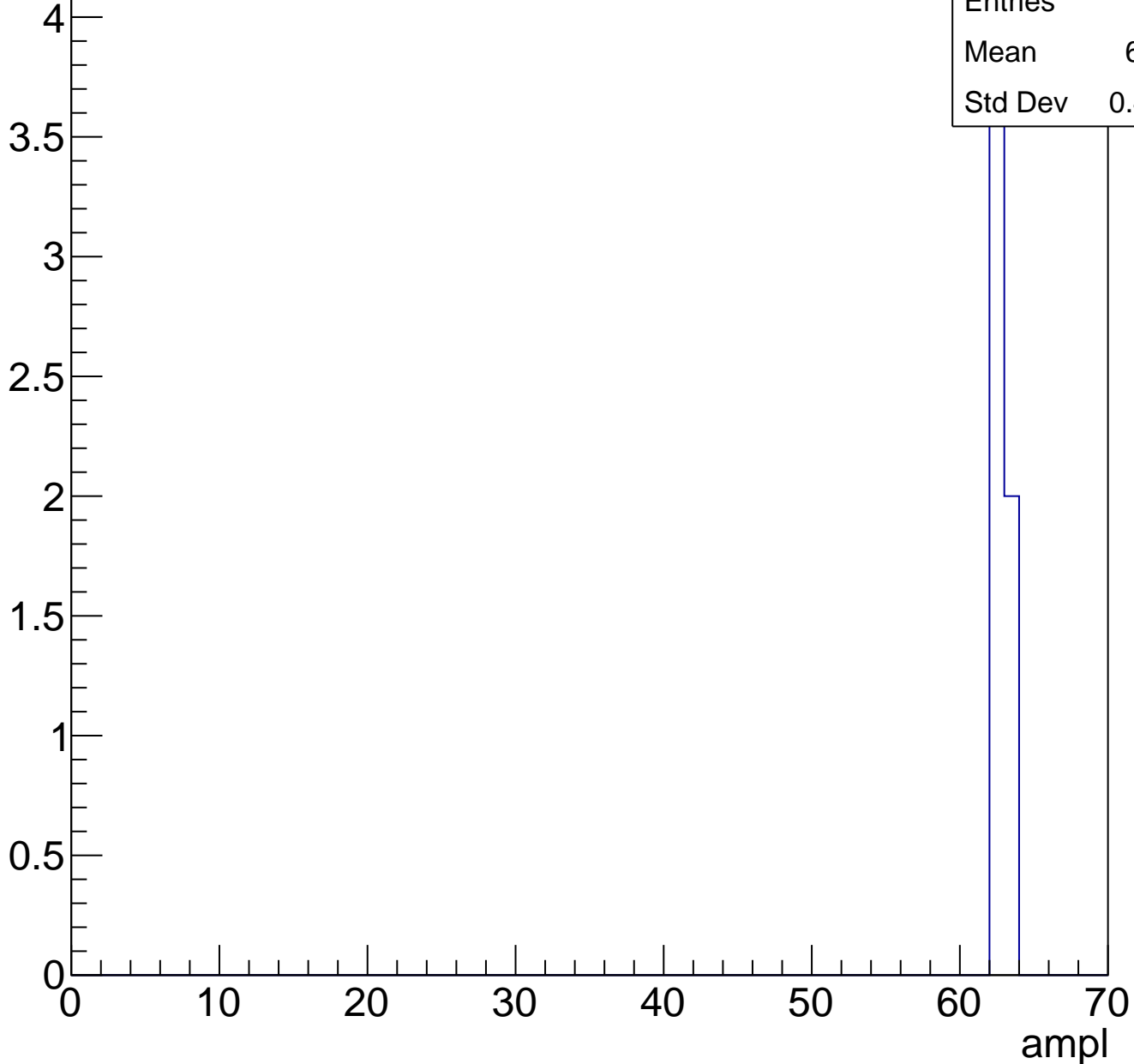


Entries	46
Mean	58.52
Std Dev	9.11

B0L002S, U4-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch105, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	29.57
Std Dev	3.615

Gaus mean : 30.6873

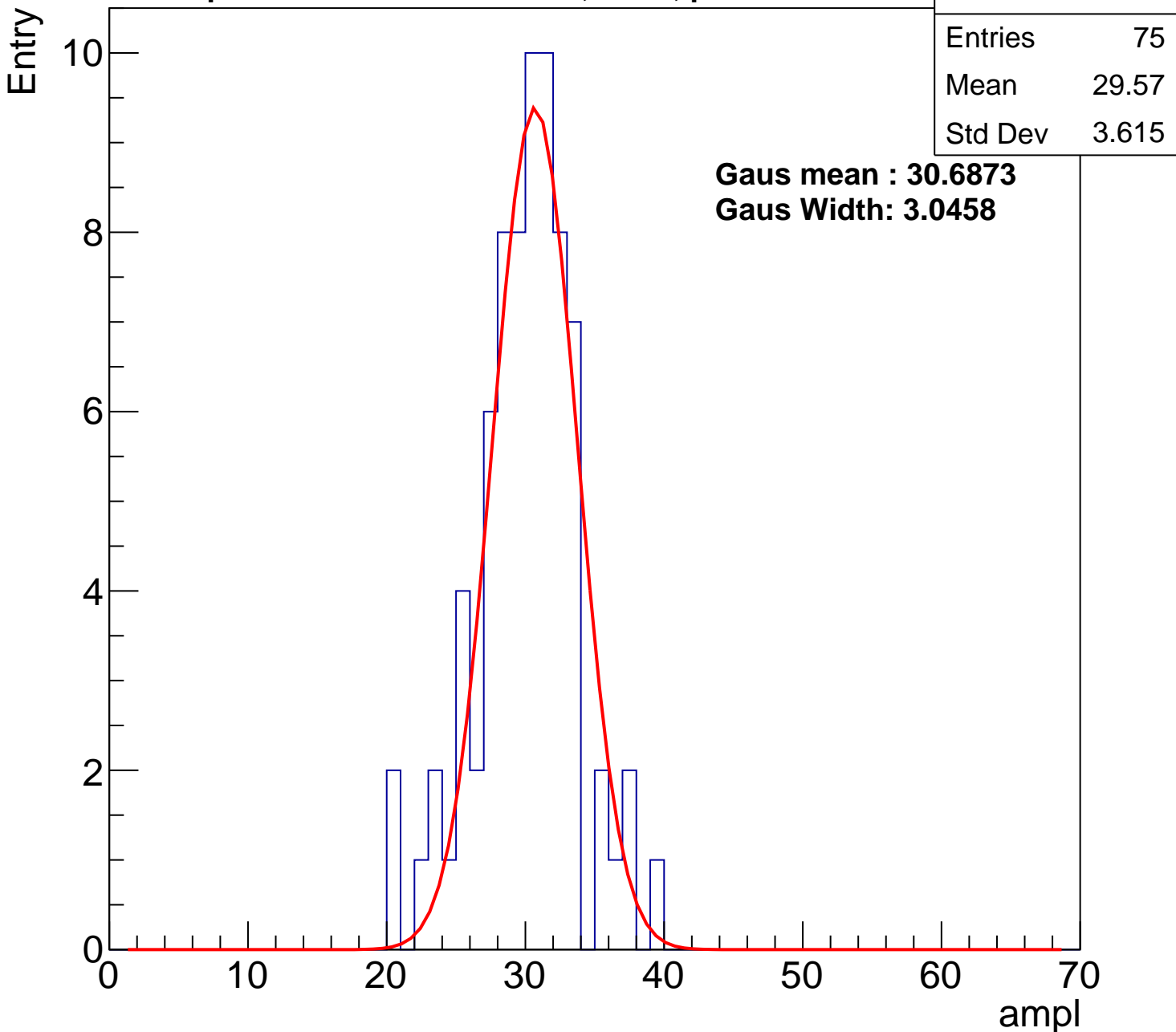
Gaus Width: 3.0458

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch105, adc1

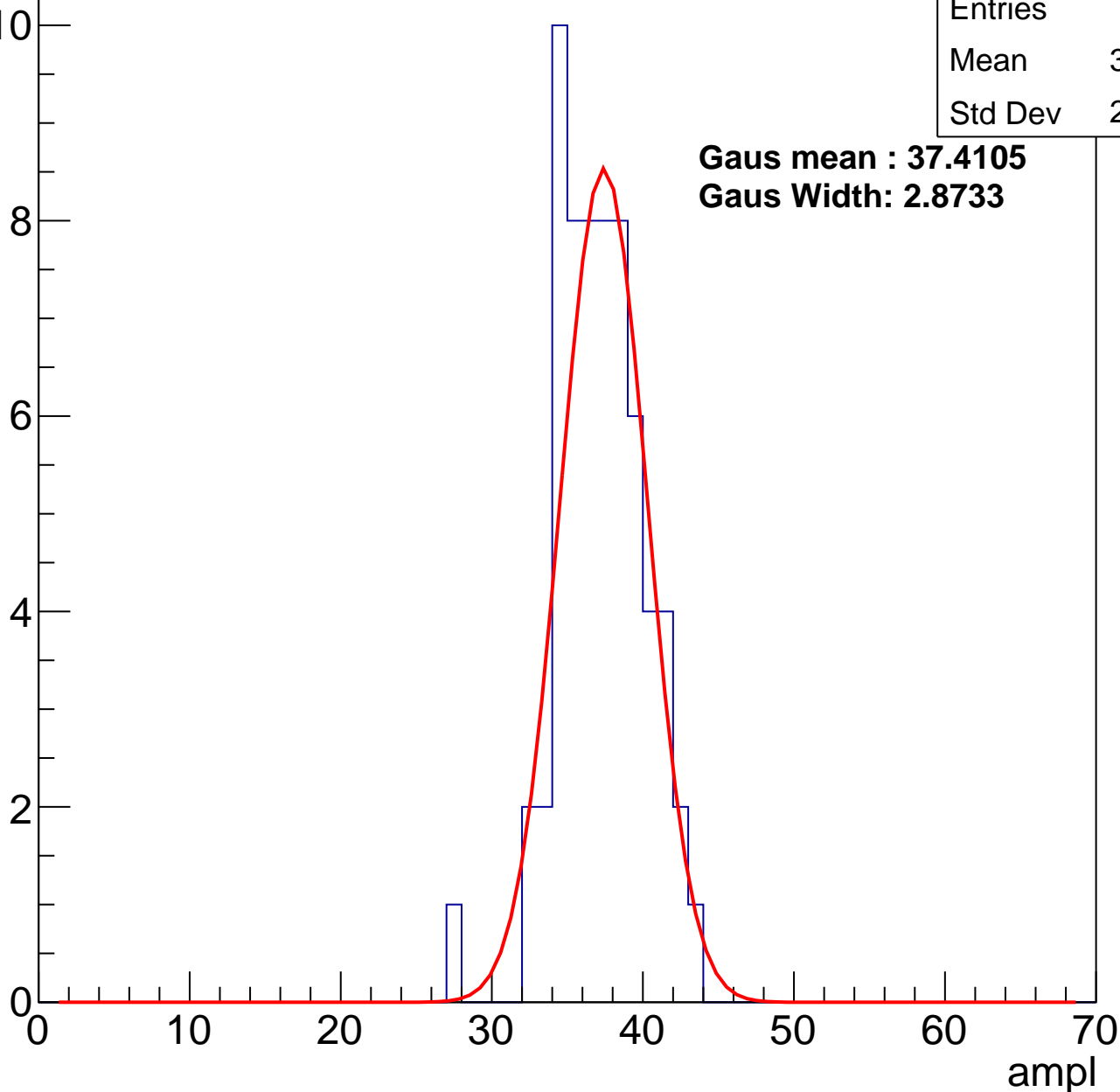
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.72
Std Dev	2.864

Gaus mean : 37.4105

Gaus Width: 2.8733



B0L002S, U4-ch105, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	43.39
Std Dev	3.311

Gaus mean : 43.8083

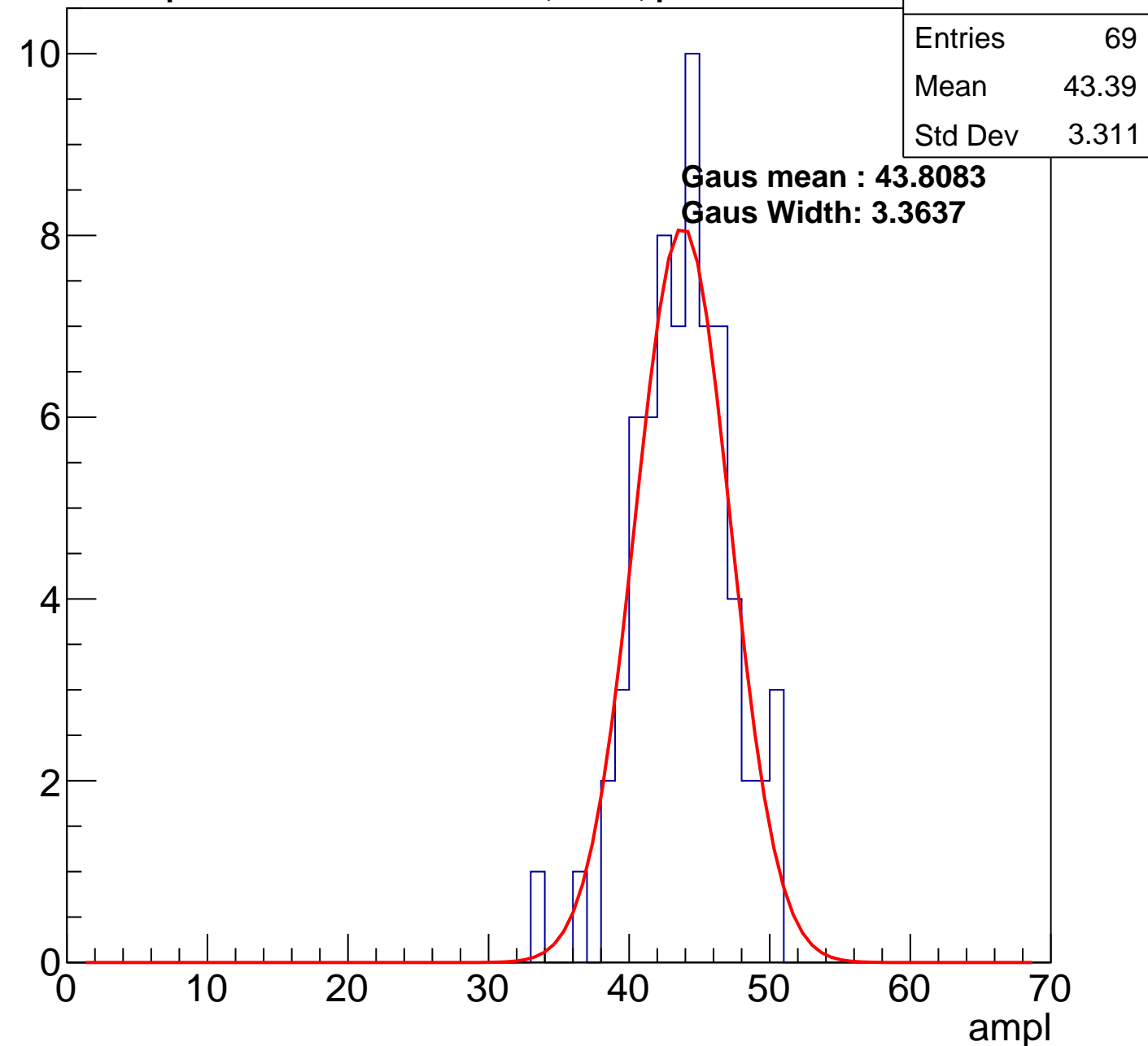
Gaus Width: 3.3637

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

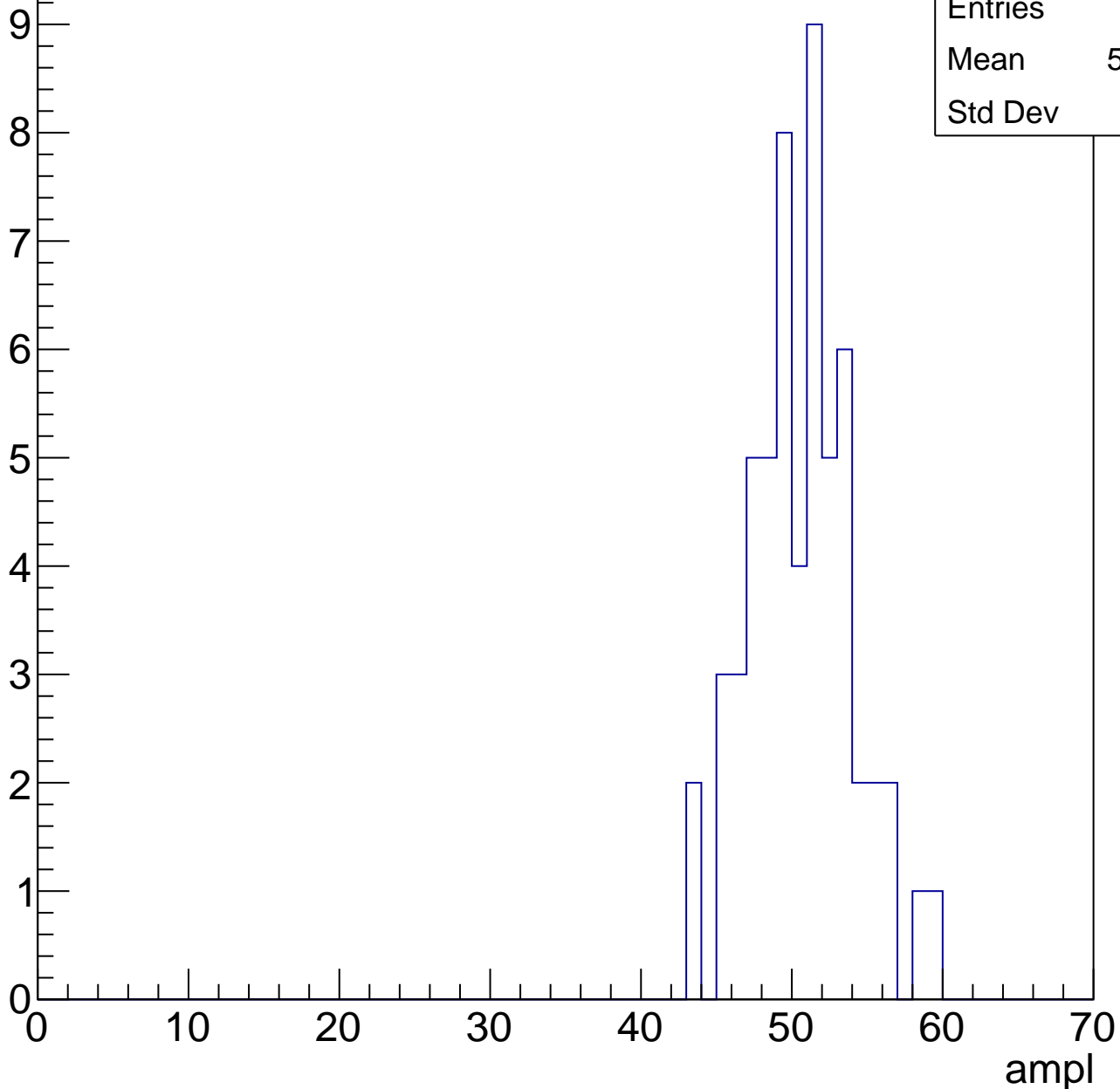


B0L002S, U4-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

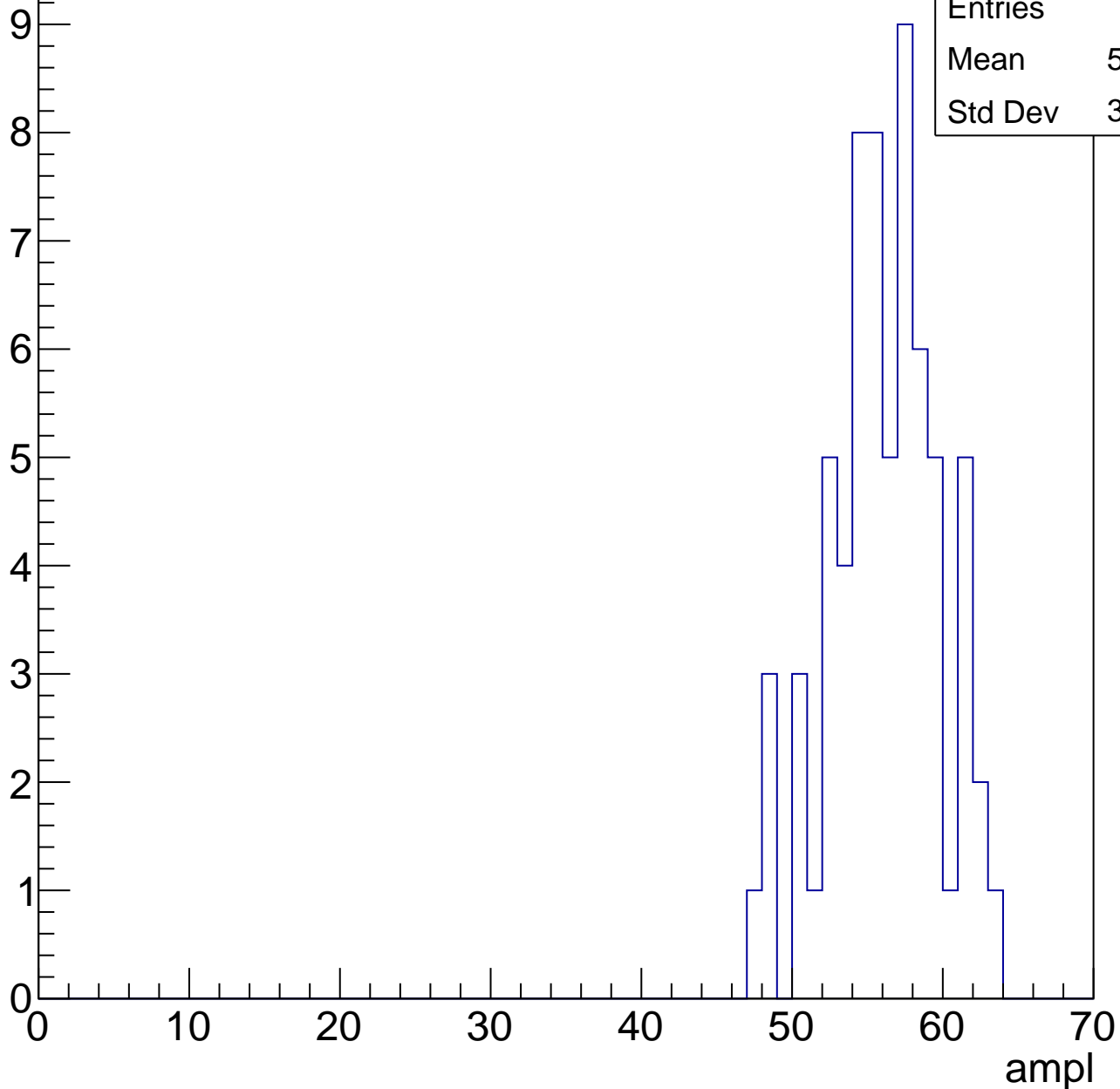
Entries	58
Mean	50.17
Std Dev	3.41



B0L002S, U4-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

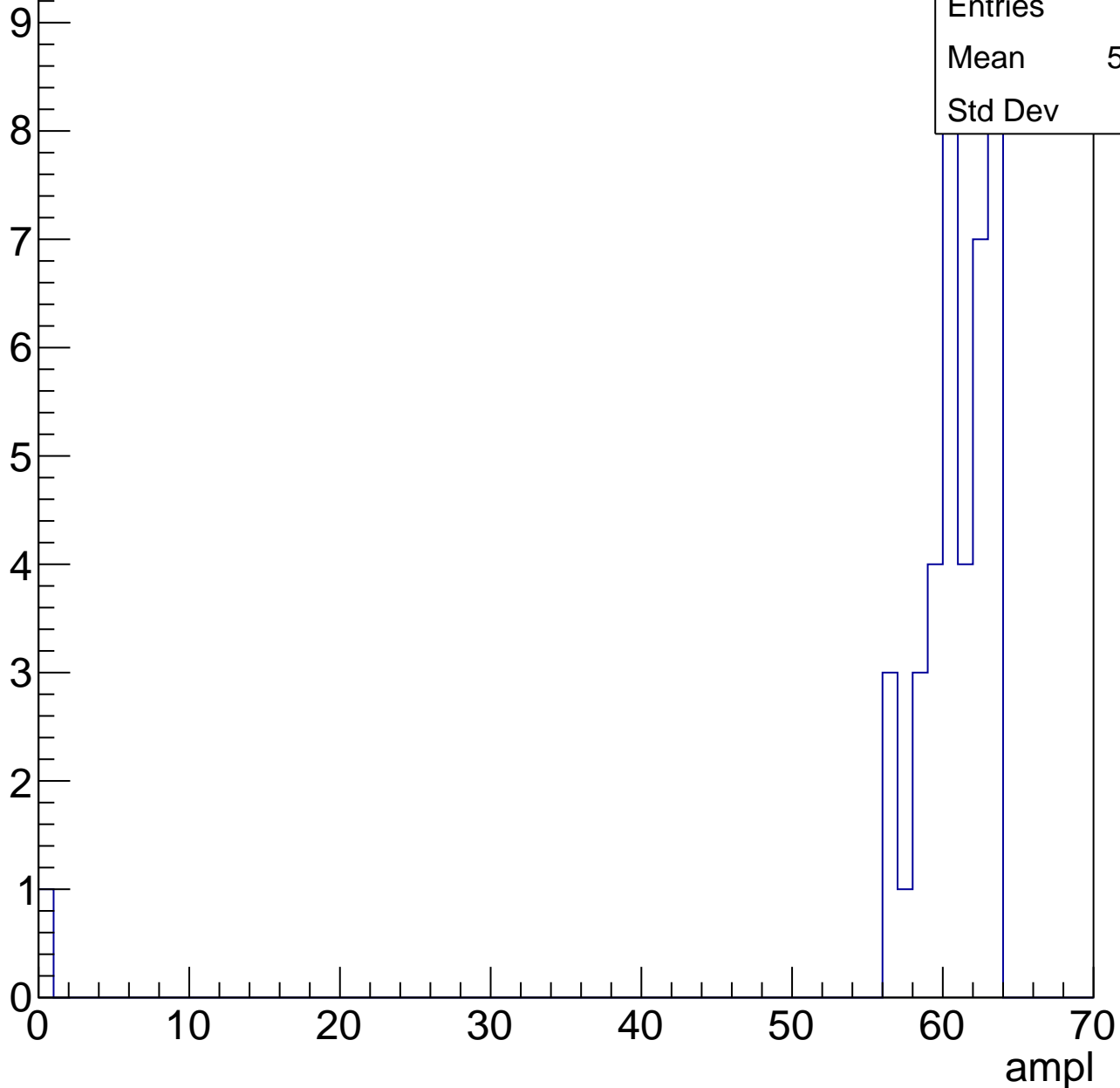


Entries	67
Mean	55.58
Std Dev	3.645

B0L002S, U4-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch106, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	29.08
Std Dev	3.366

Gaus mean : 29.7040

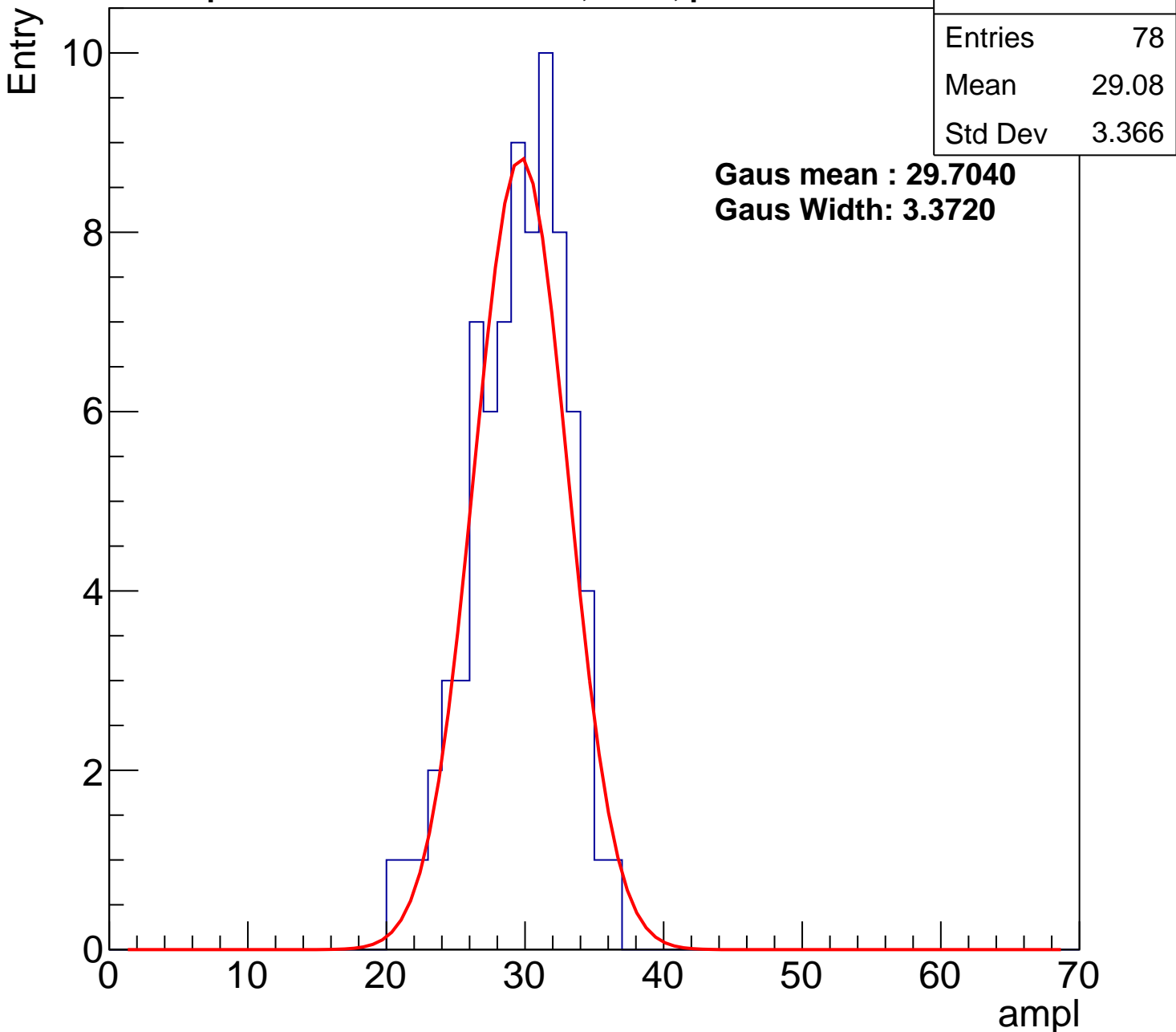
Gaus Width: 3.3720

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch106, adc1

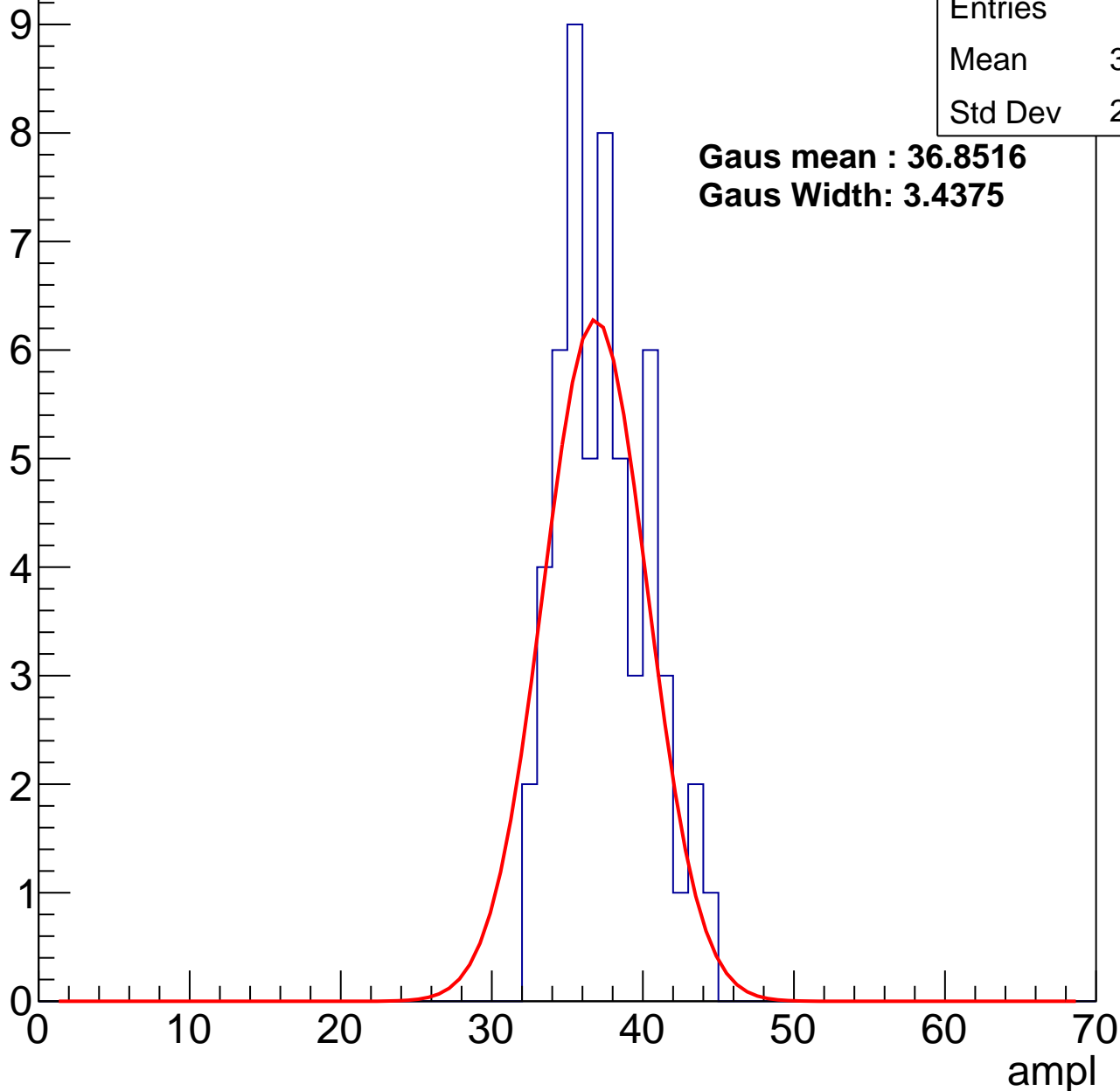
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.96
Std Dev	2.935

Gaus mean : 36.8516

Gaus Width: 3.4375



B0L002S, U4-ch106, adc2

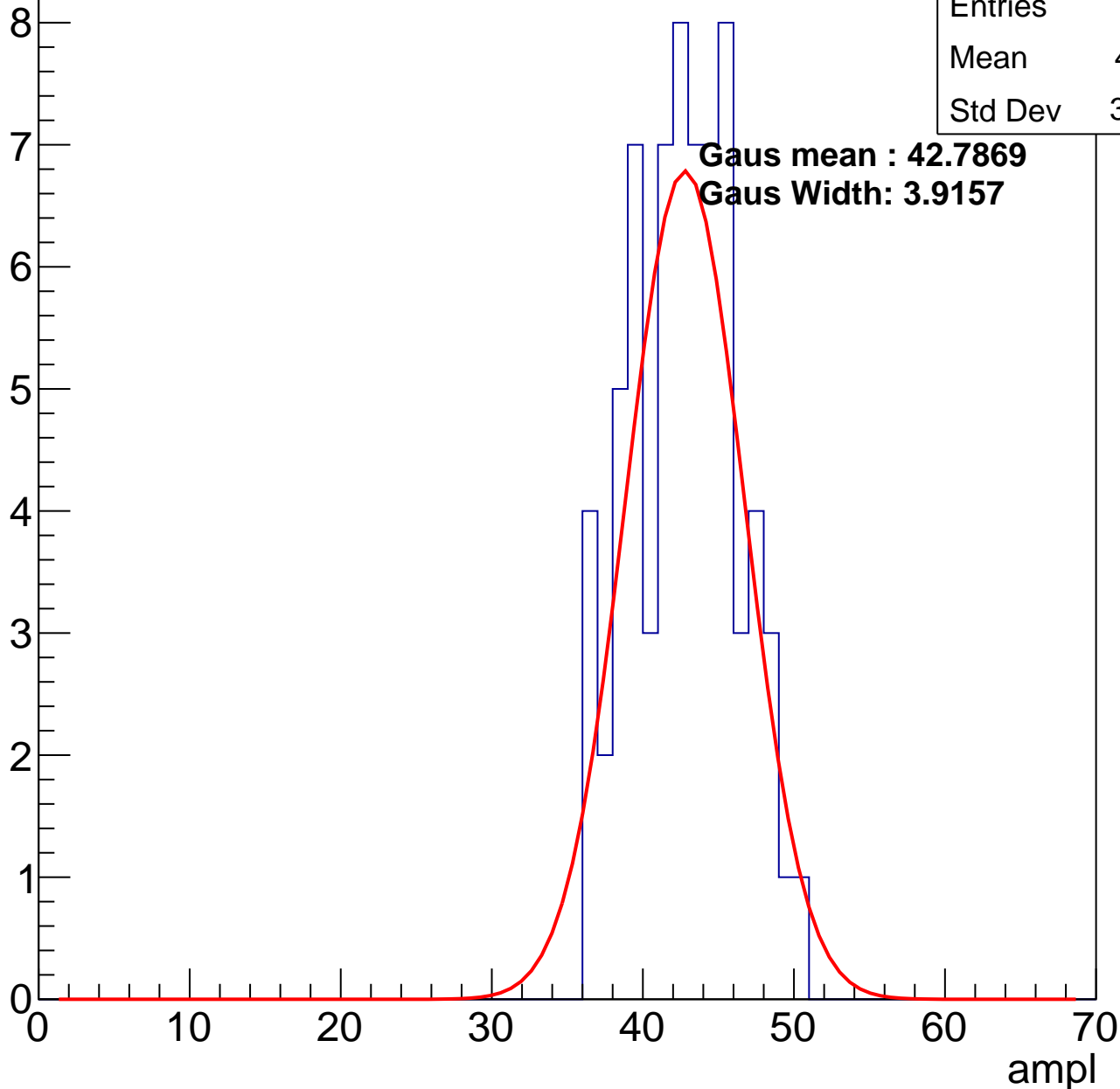
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.31
Std Dev	3.433

Gaus mean : 42.7869

Gaus Width: 3.9157

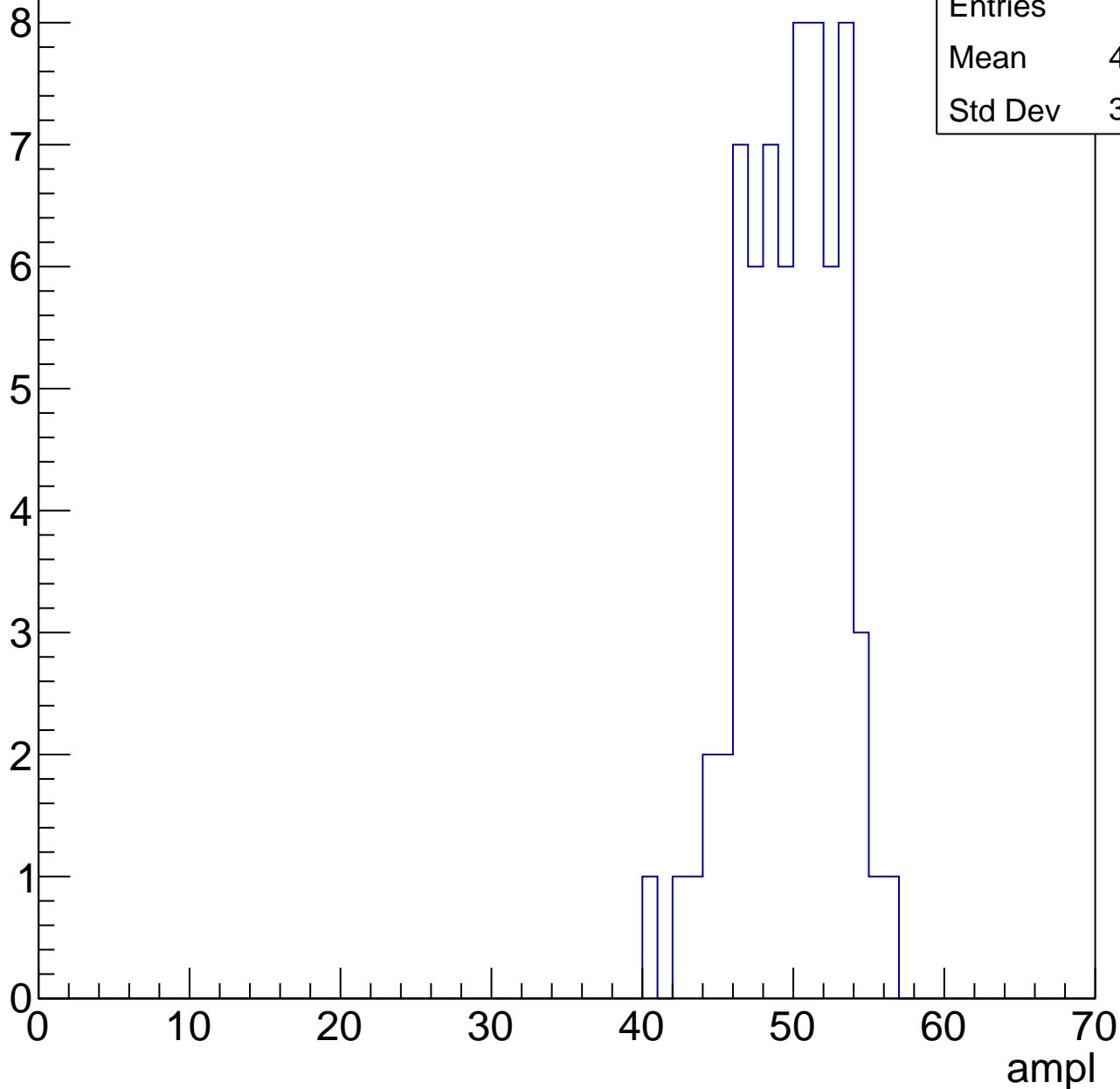


B0L002S, U4-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.32
Std Dev	3.247

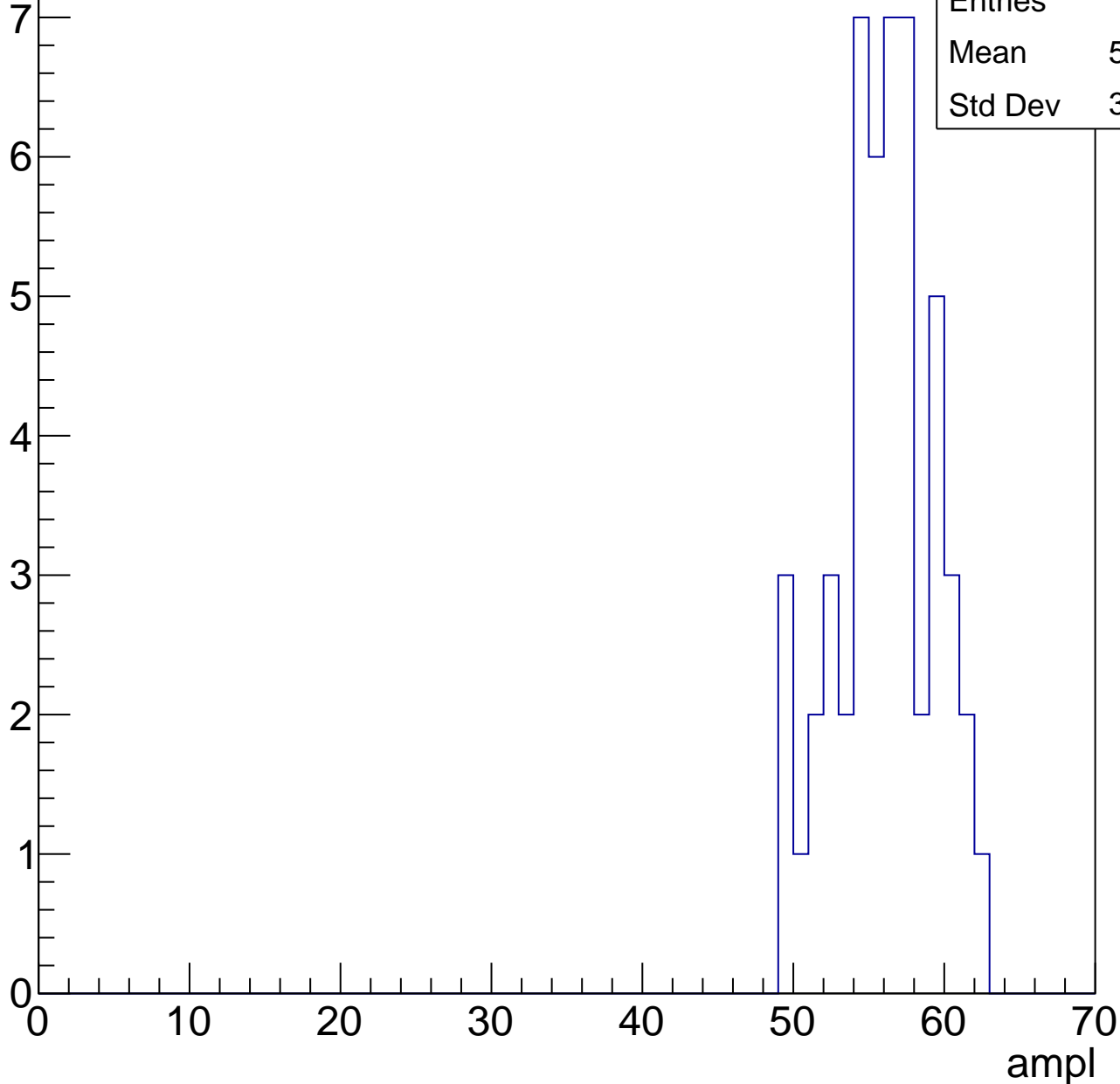


B0L002S, U4-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

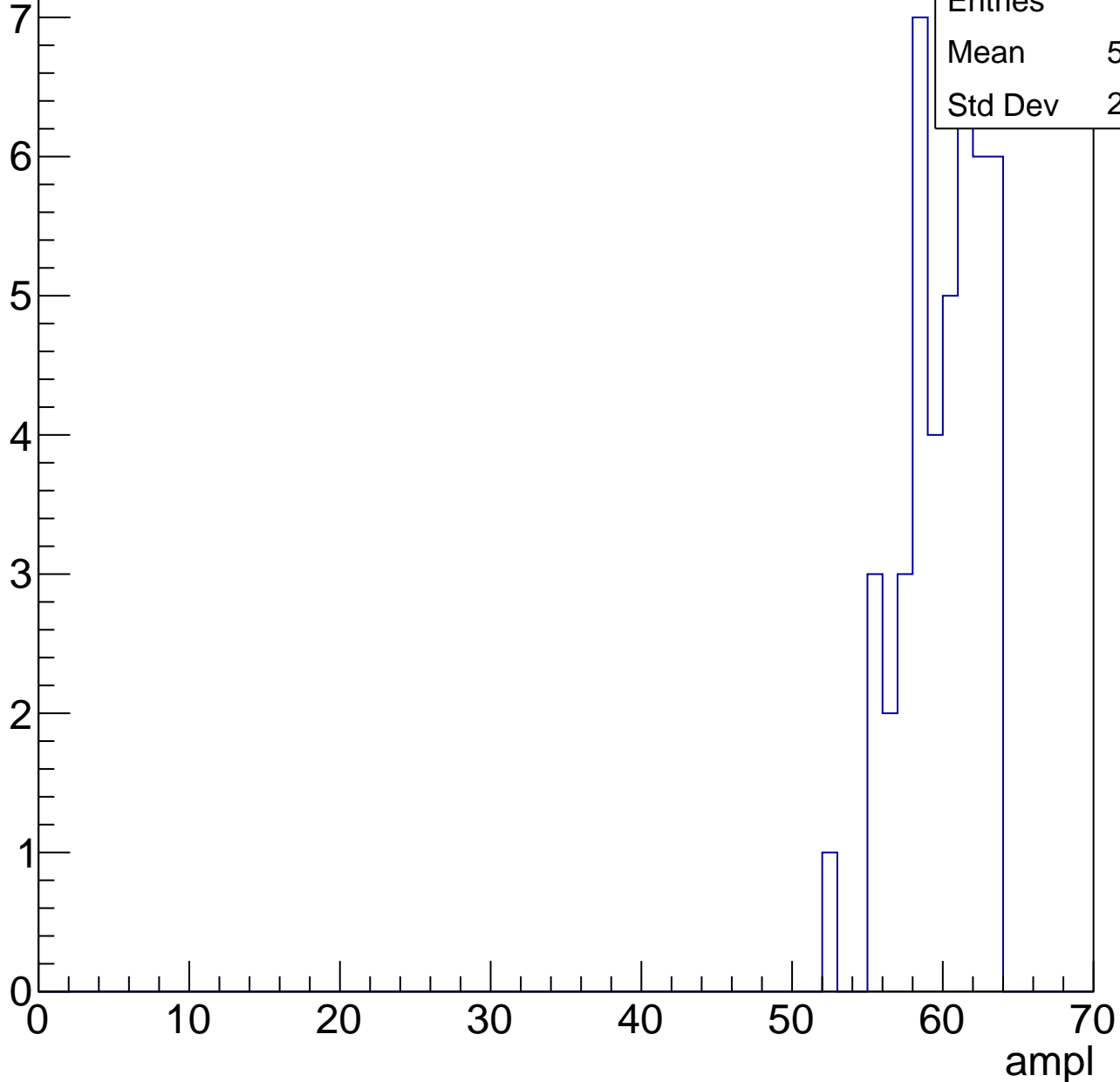
Entries	51
Mean	55.59
Std Dev	3.188



B0L002S, U4-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

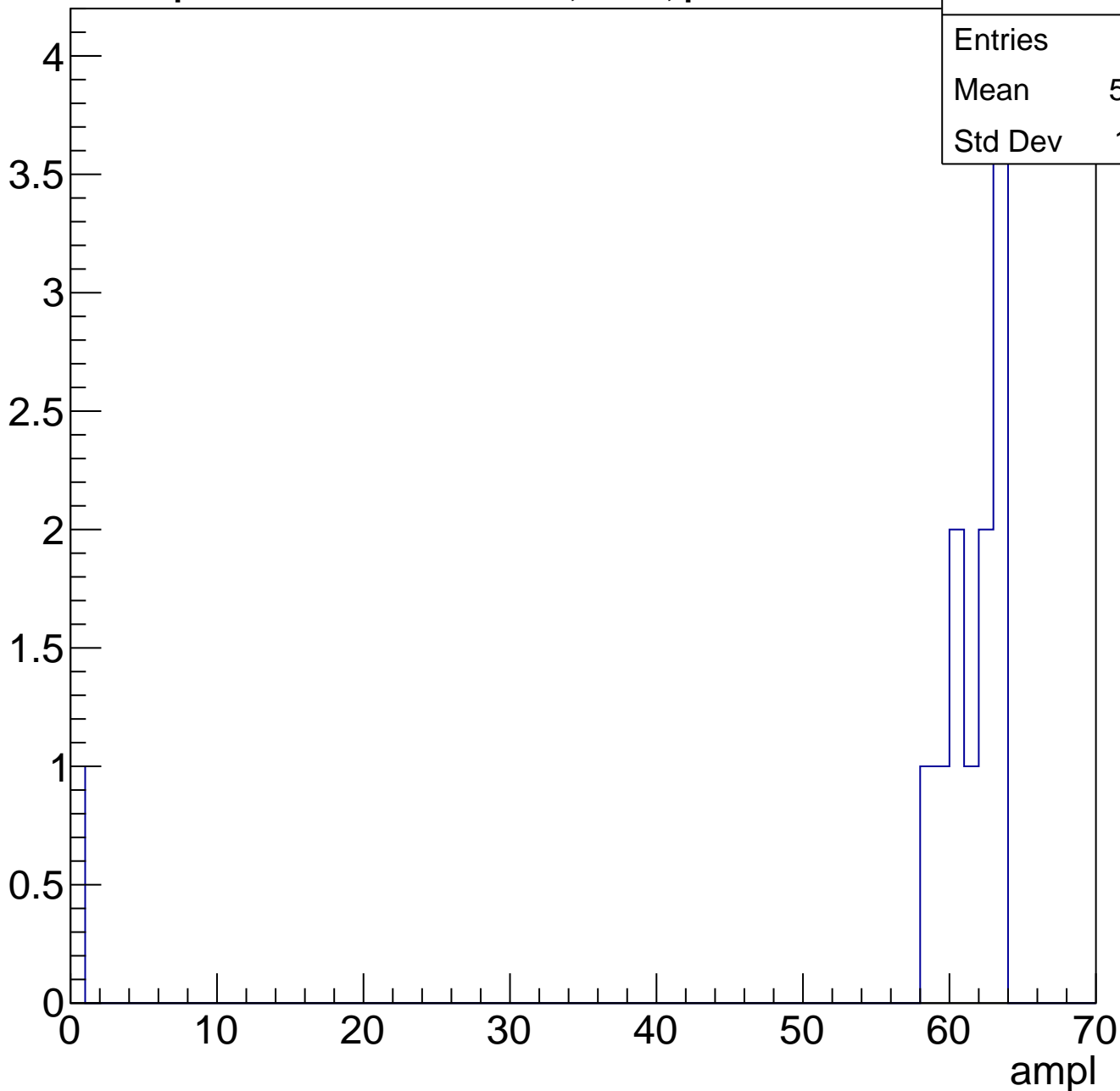
Entry



B0L002S, U4-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

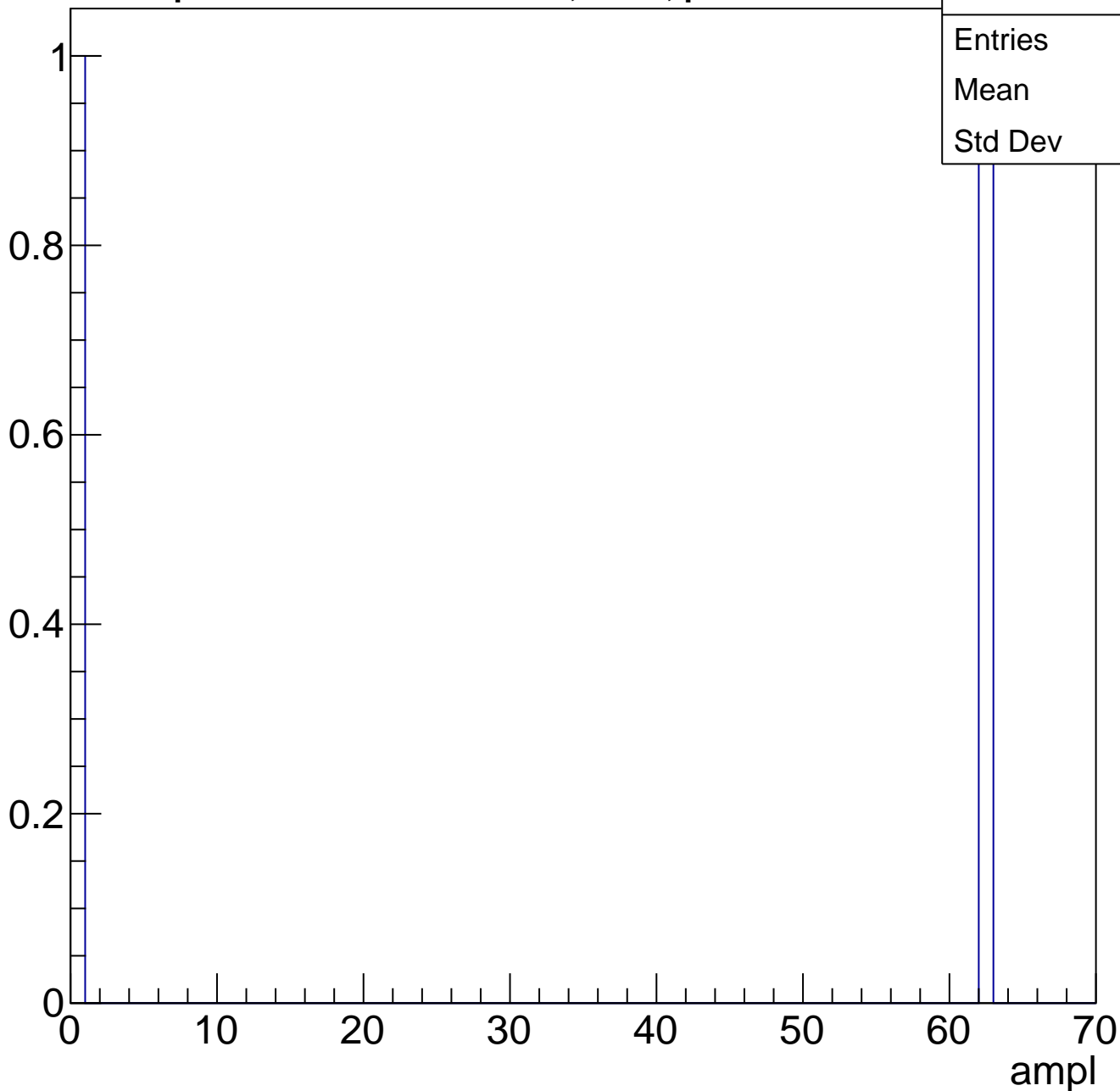
Entry



B0L002S, U4-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch107, adc0

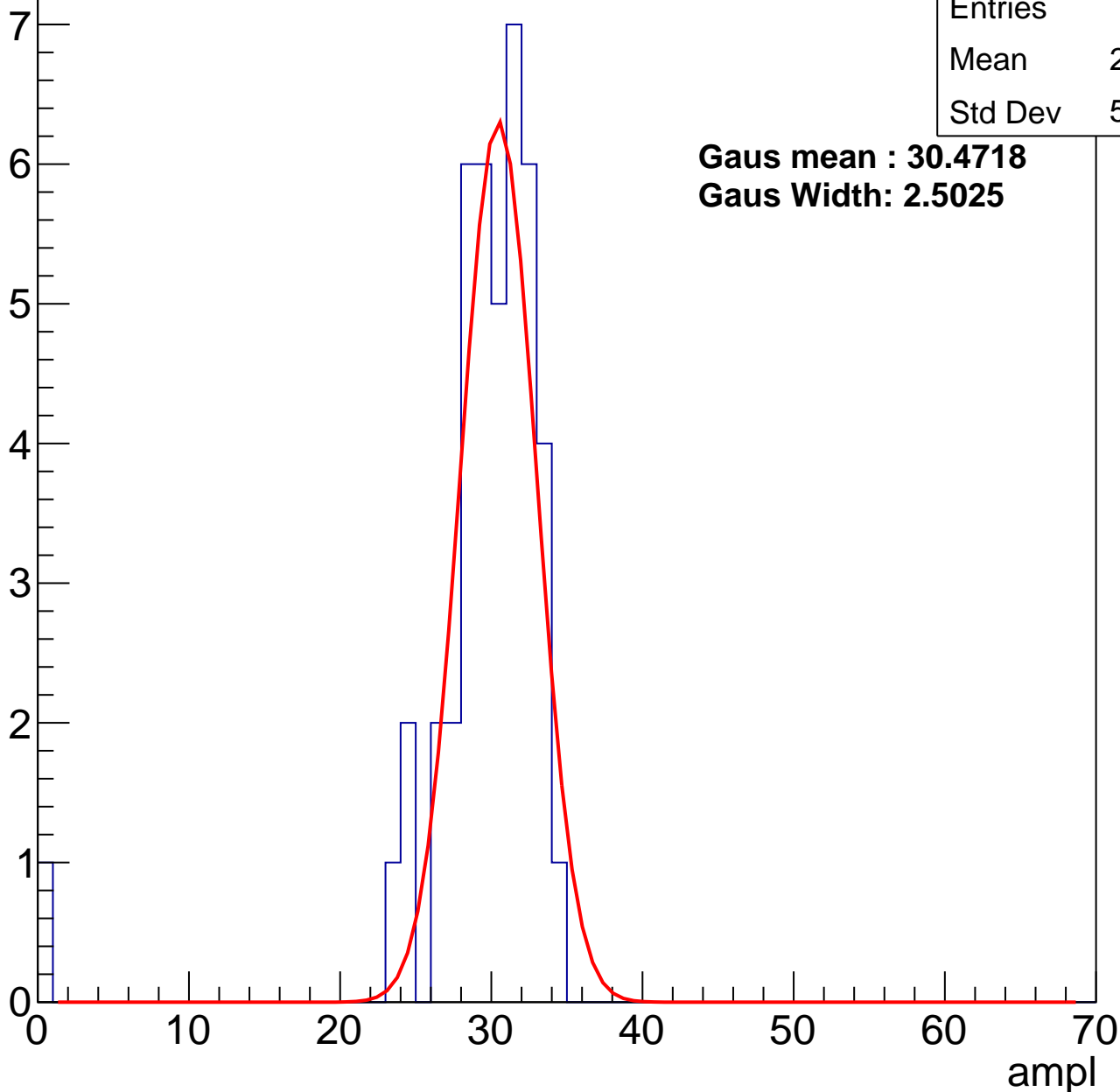
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	28.93
Std Dev	5.137

Gaus mean : 30.4718

Gaus Width: 2.5025



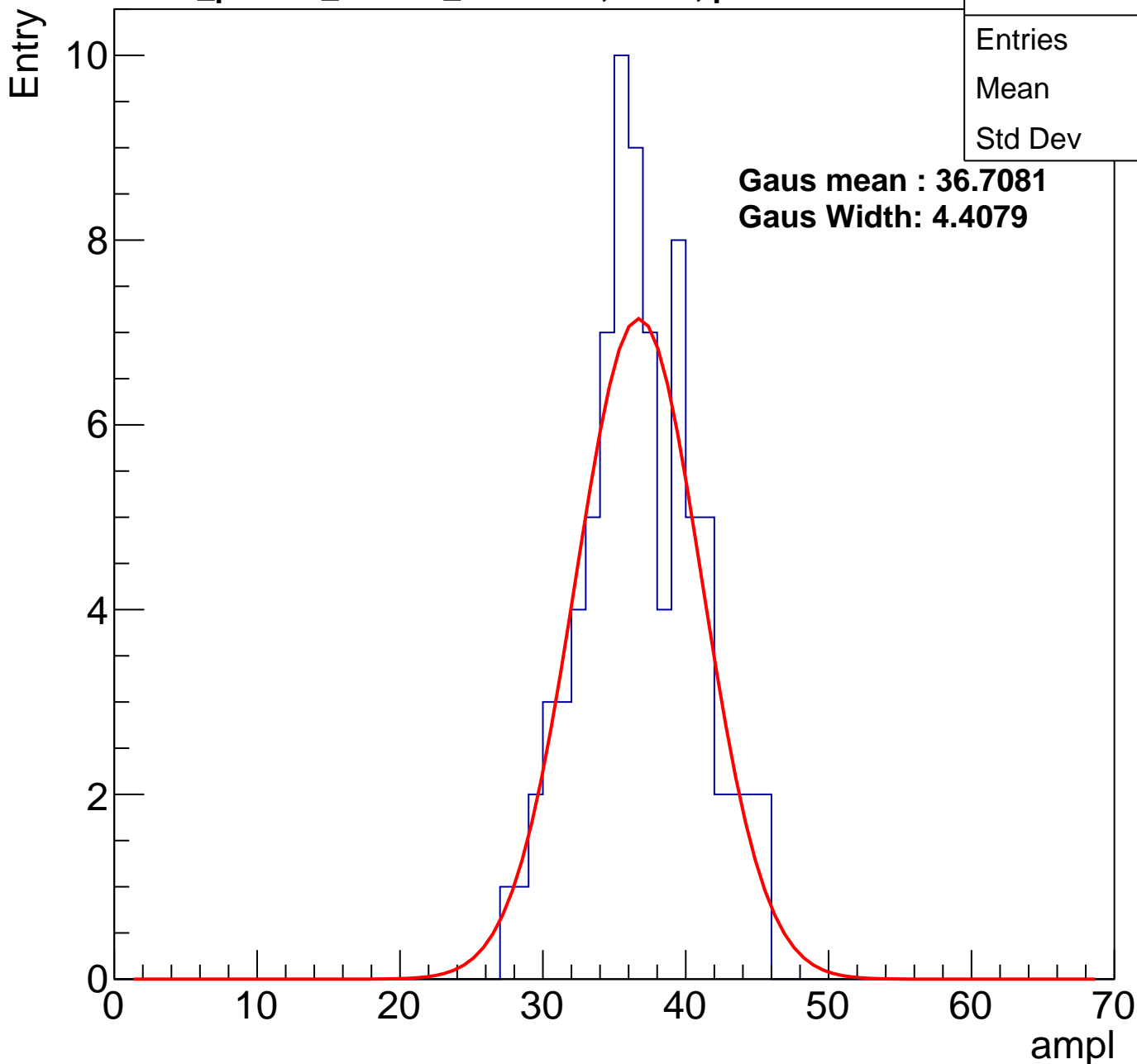
B0L002S, U4-ch107, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	36.3
Std Dev	4.02

Gaus mean : 36.7081

Gaus Width: 4.4079



B0L002S, U4-ch107, adc2

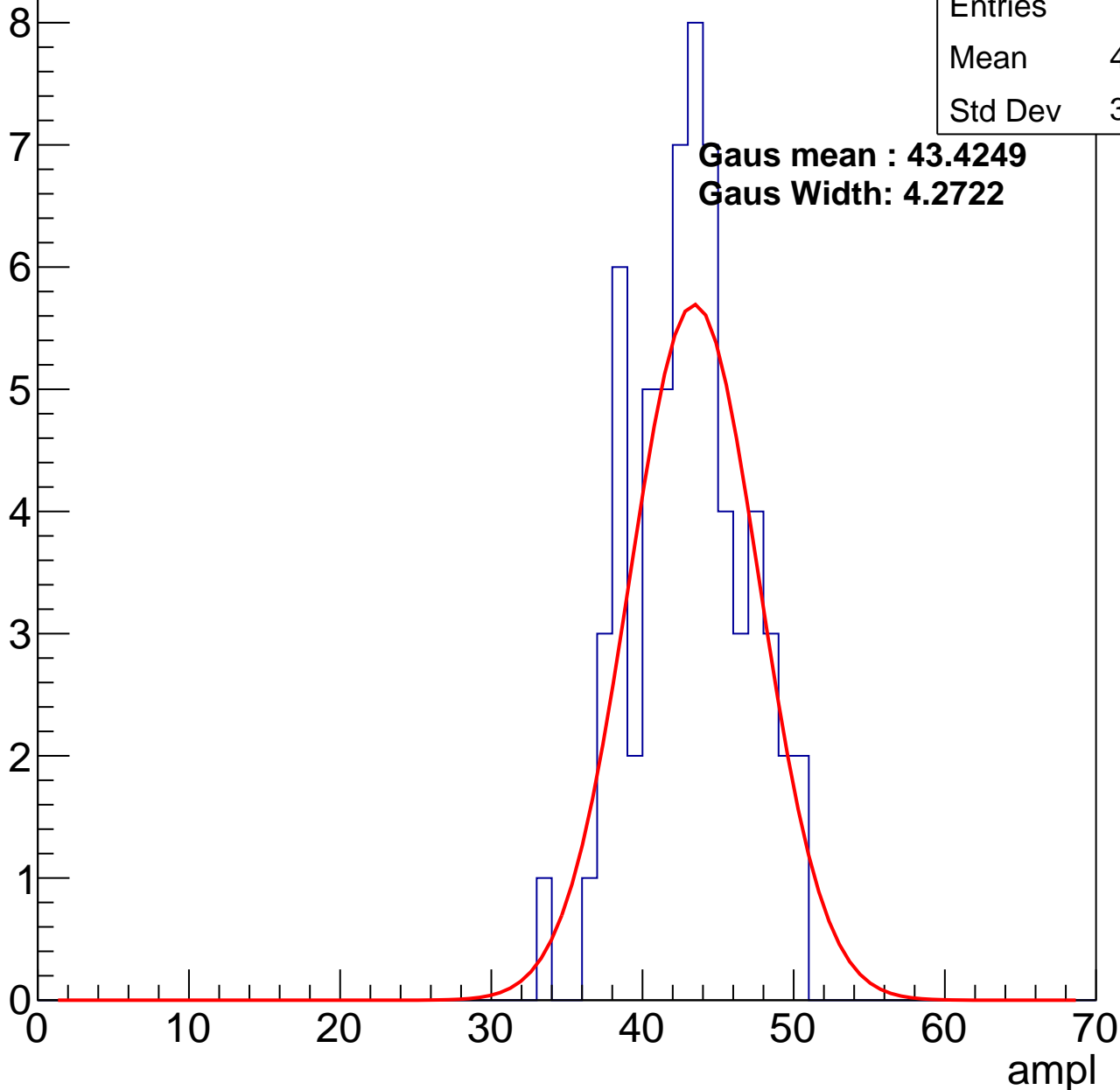
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.62
Std Dev	3.697

Gaus mean : 43.4249

Gaus Width: 4.2722

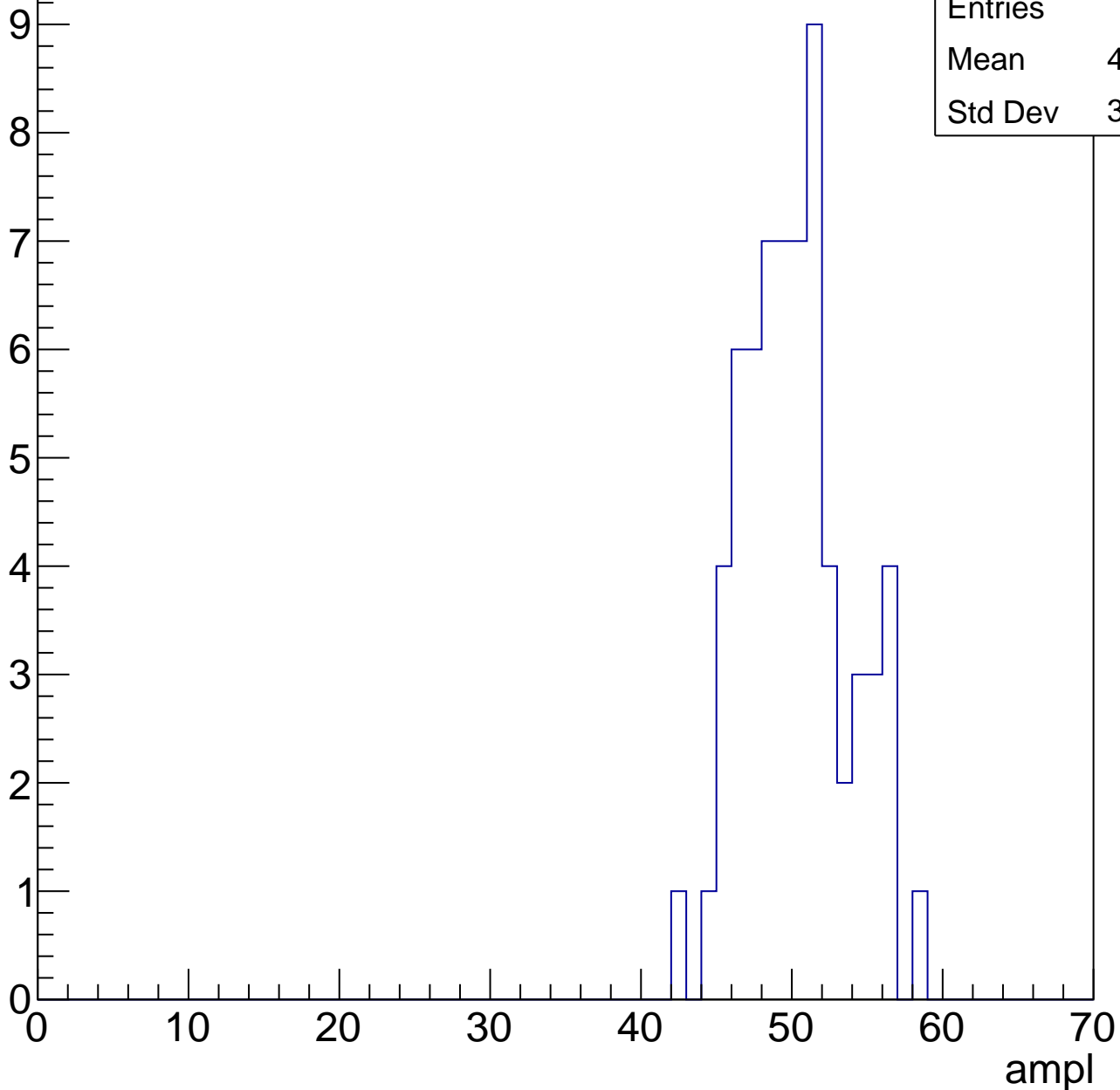


B0L002S, U4-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	49.77
Std Dev	3.409

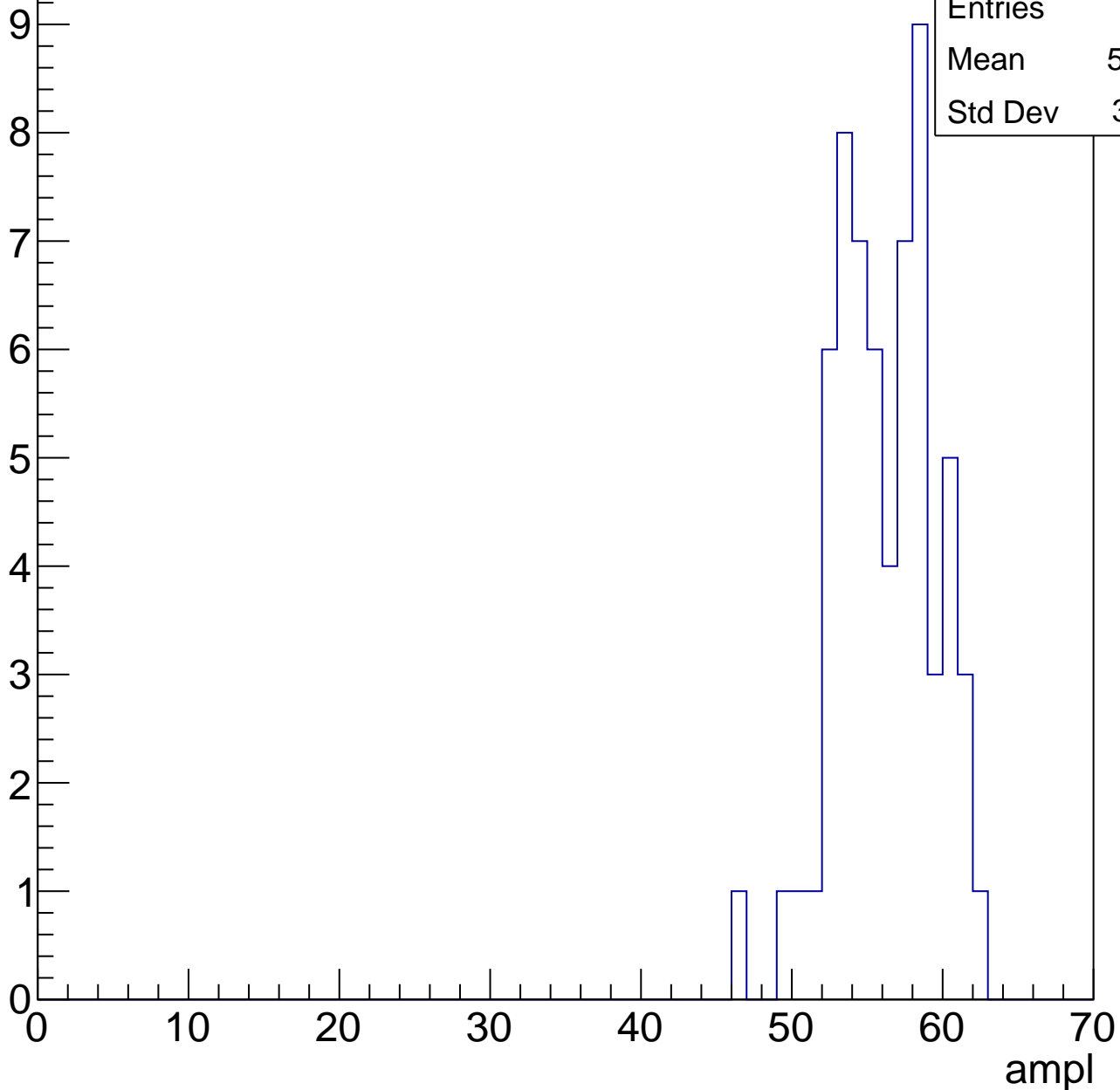


B0L002S, U4-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	55.67
Std Dev	3.251

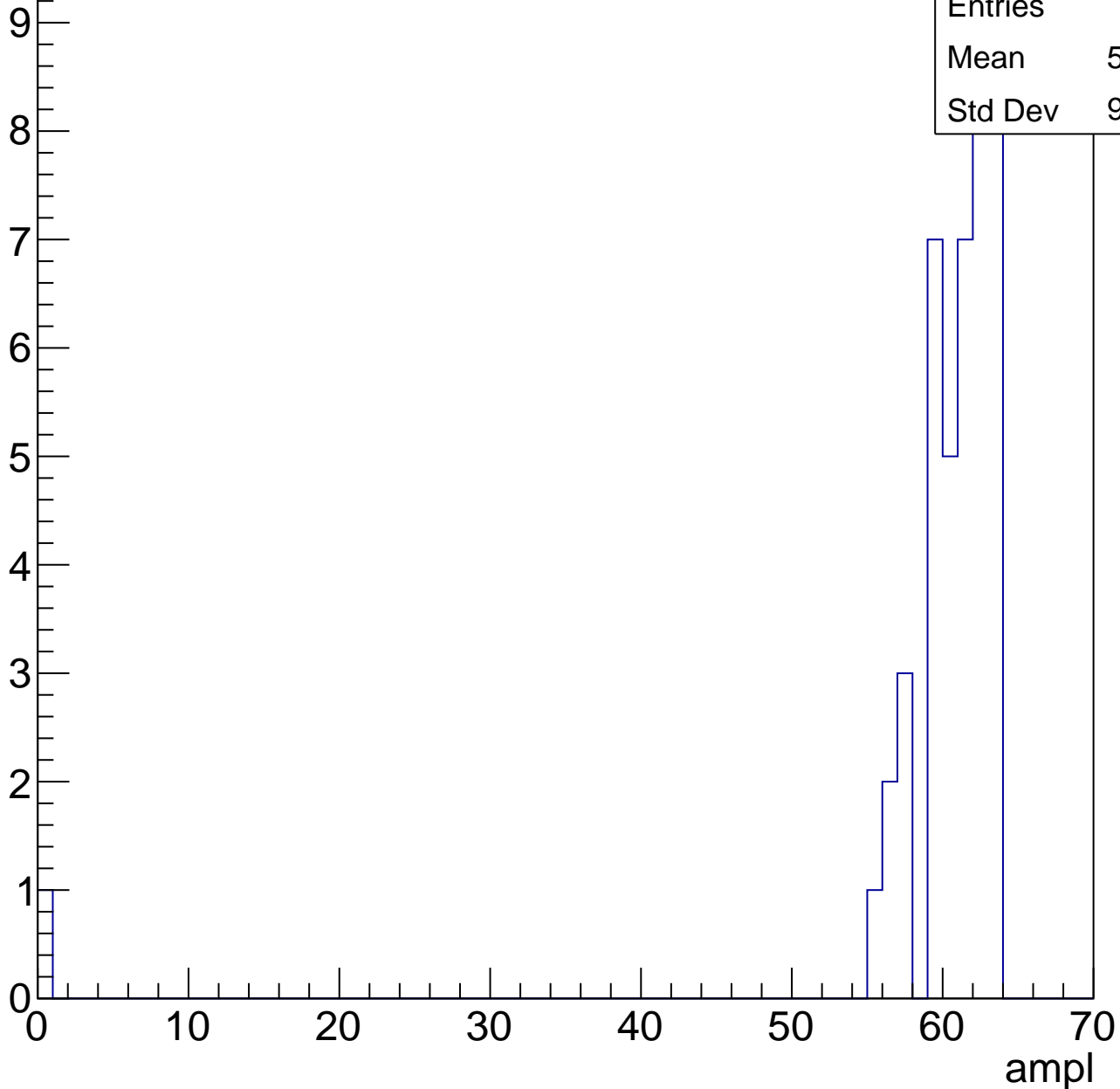


B0L002S, U4-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	59.07
Std Dev	9.362



B0L002S, U4-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

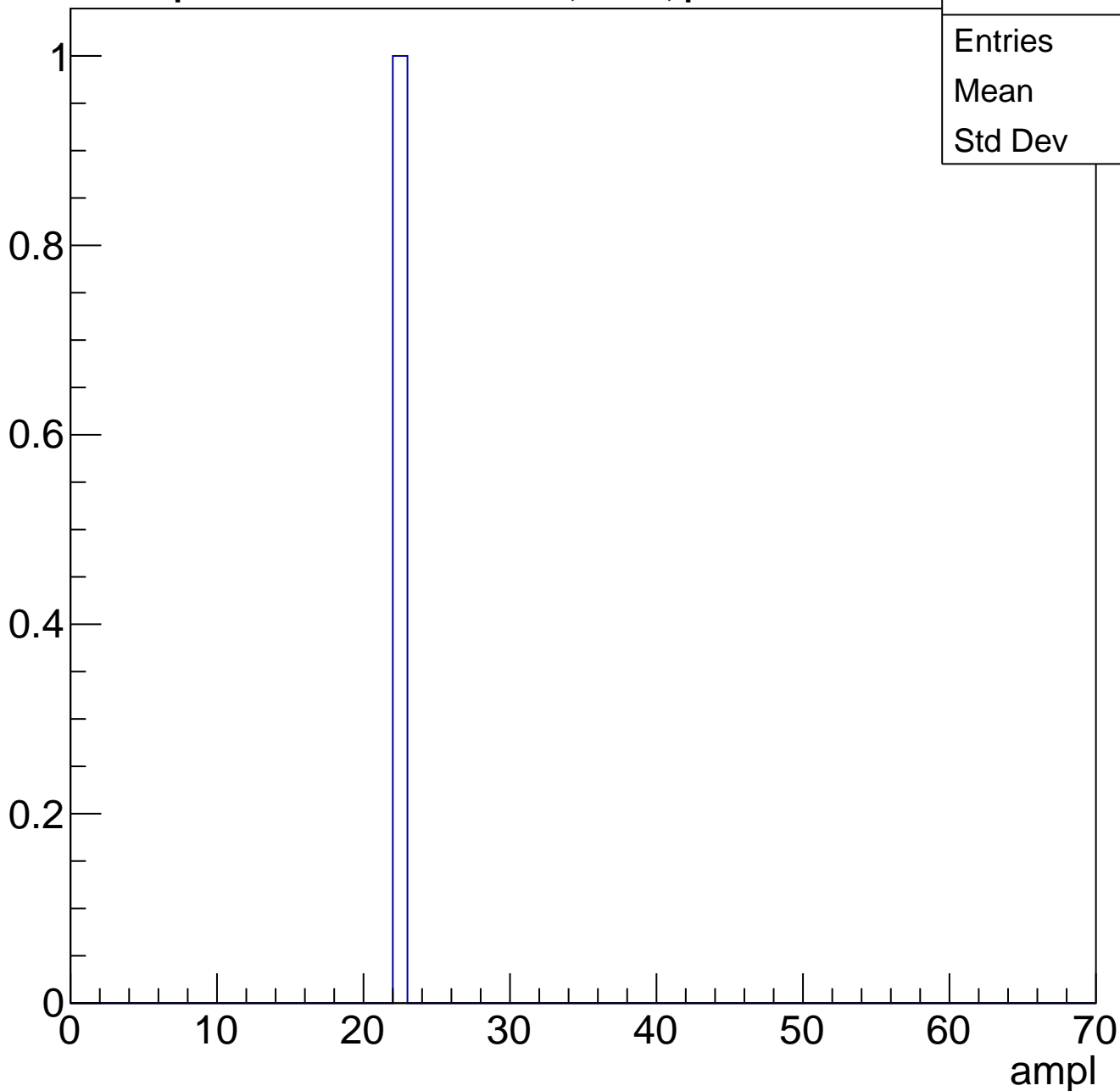
Entry



B0L002S, U4-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	22
Std Dev	0

B0L002S, U4-ch108, adc0

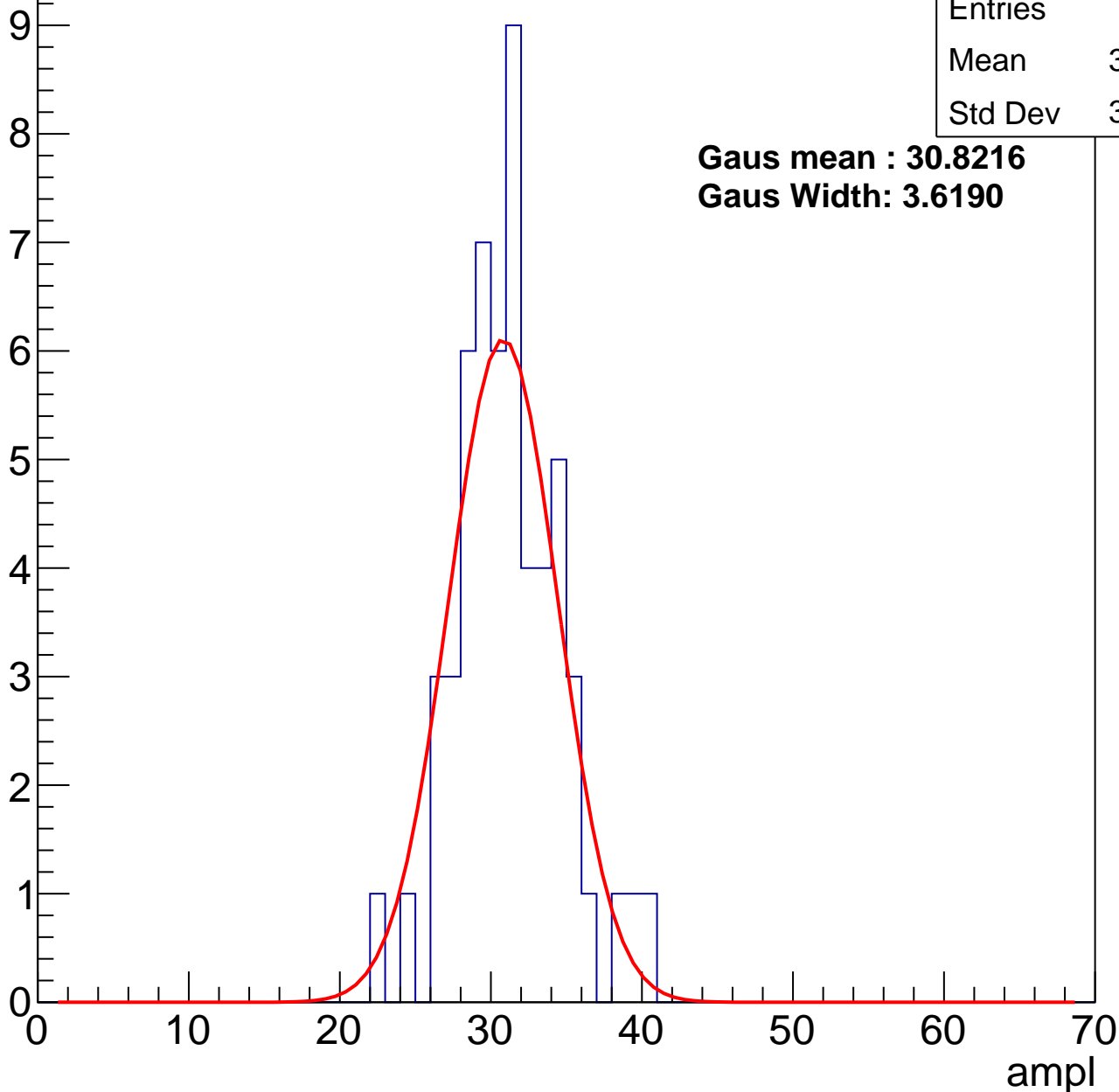
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	30.77
Std Dev	3.459

Gaus mean : 30.8216

Gaus Width: 3.6190



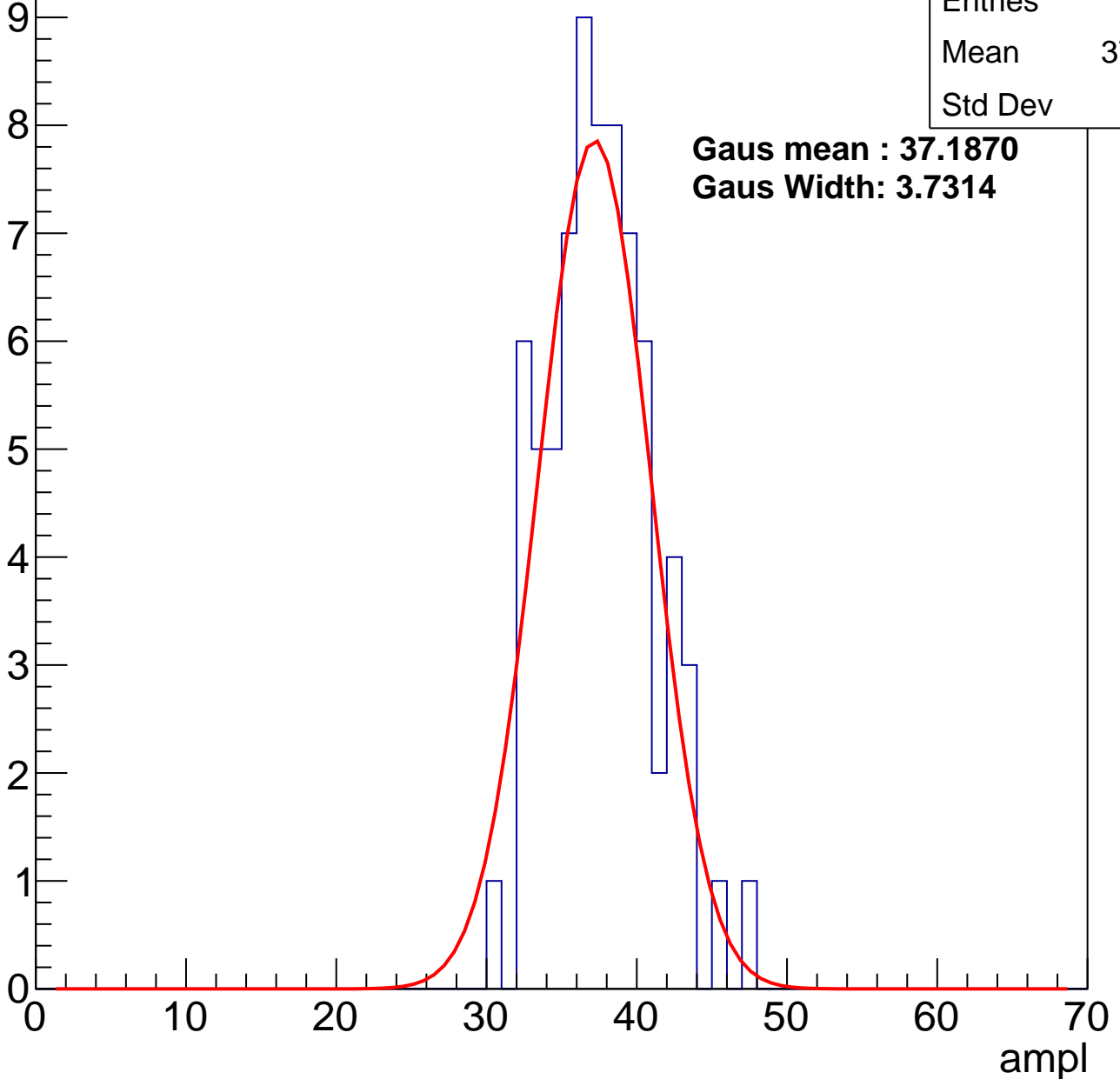
B0L002S, U4-ch108, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.12
Std Dev	3.42

Gaus mean : 37.1870
Gaus Width: 3.7314

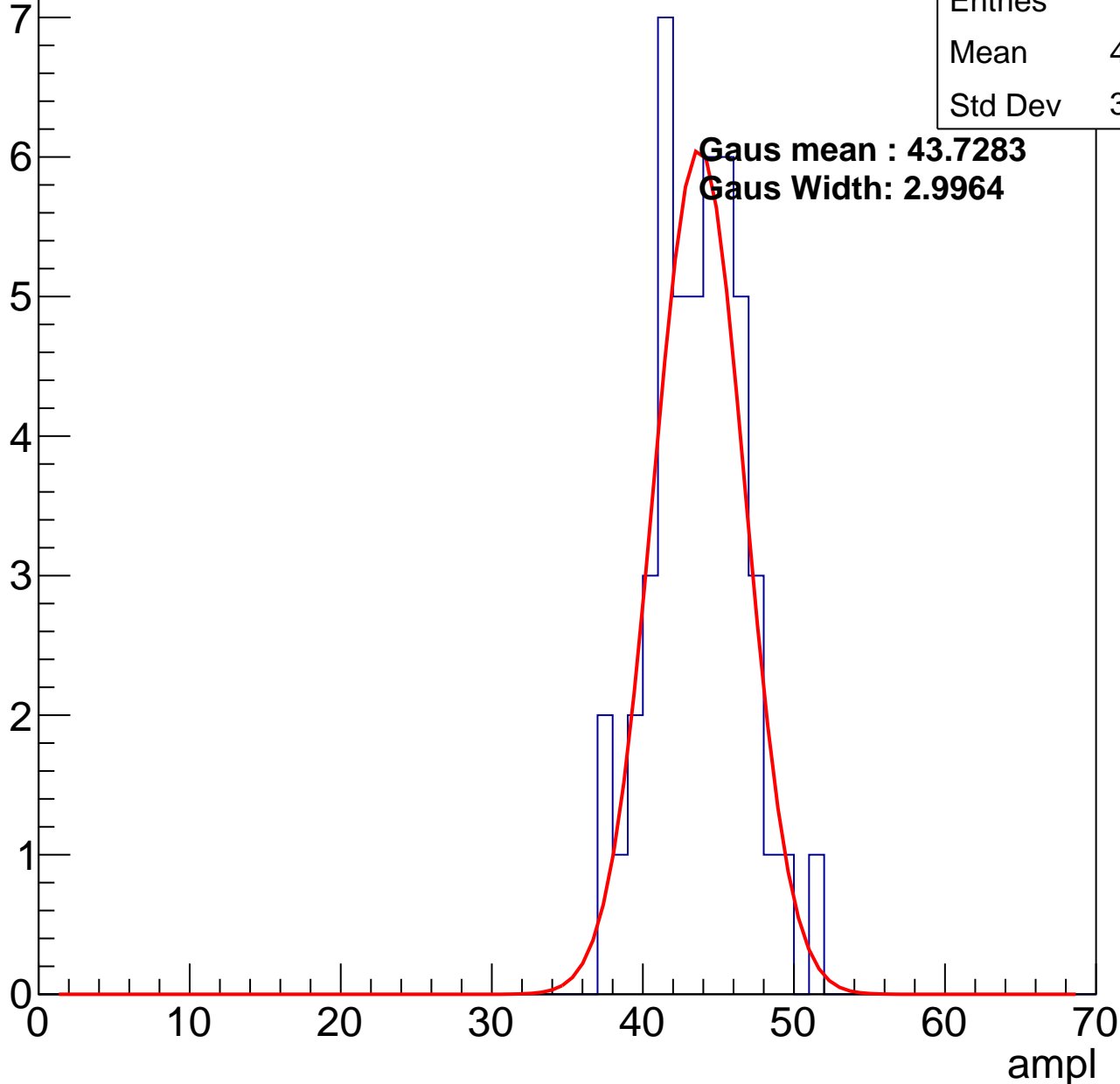


B0L002S, U4-ch108, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.23
Std Dev	3.009

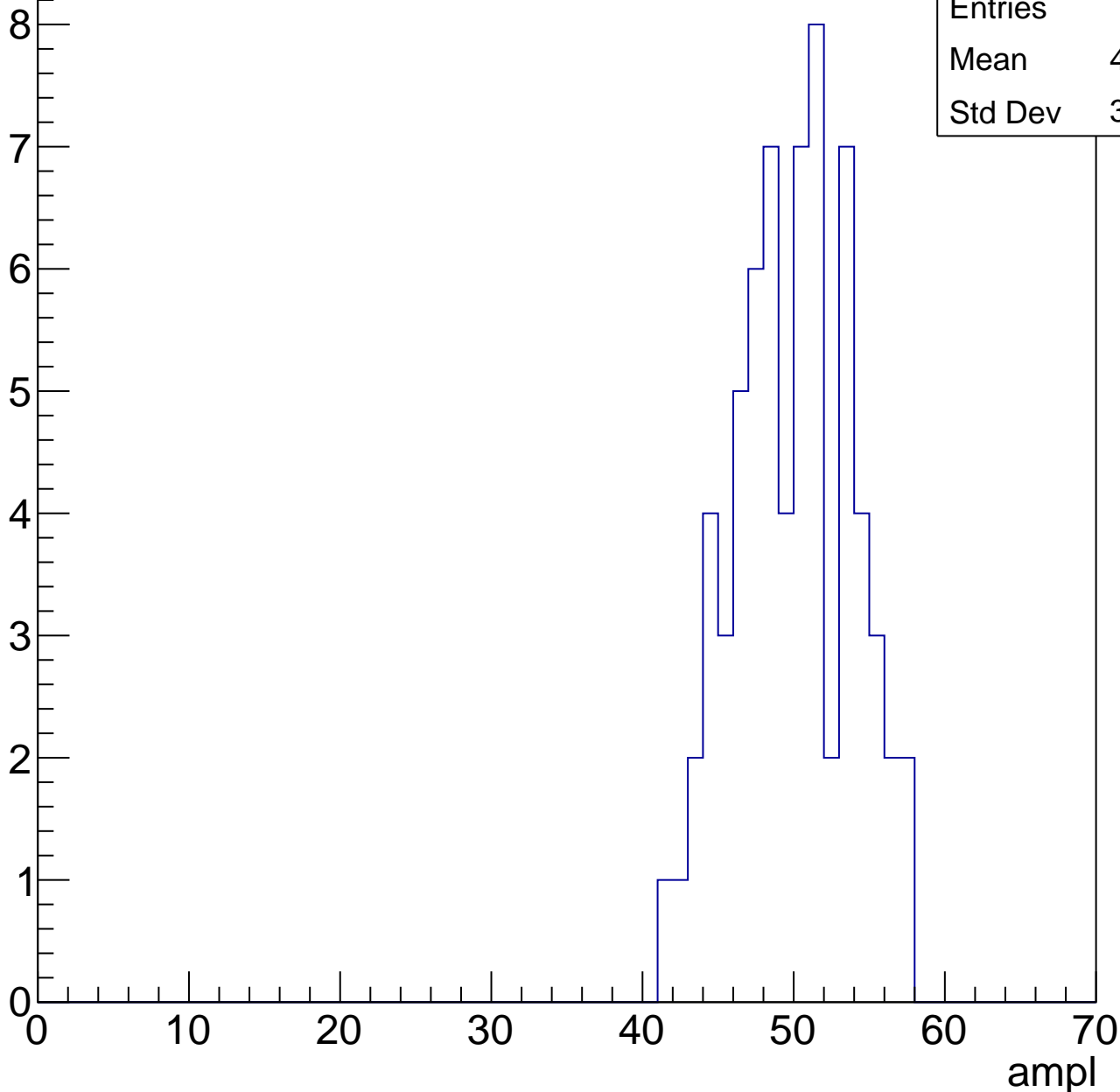


B0L002S, U4-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	49.47
Std Dev	3.829

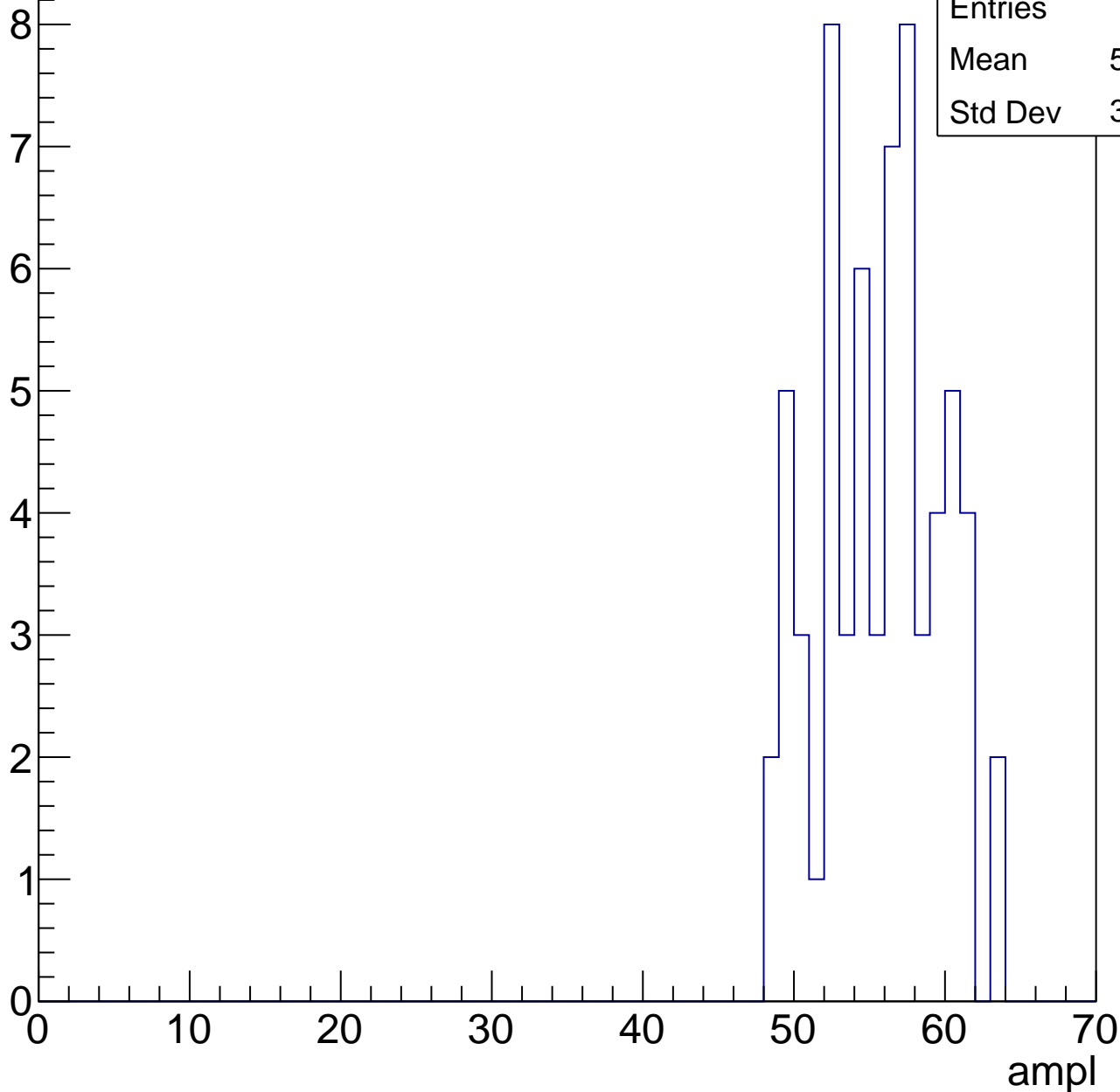


B0L002S, U4-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	55.22
Std Dev	3.919

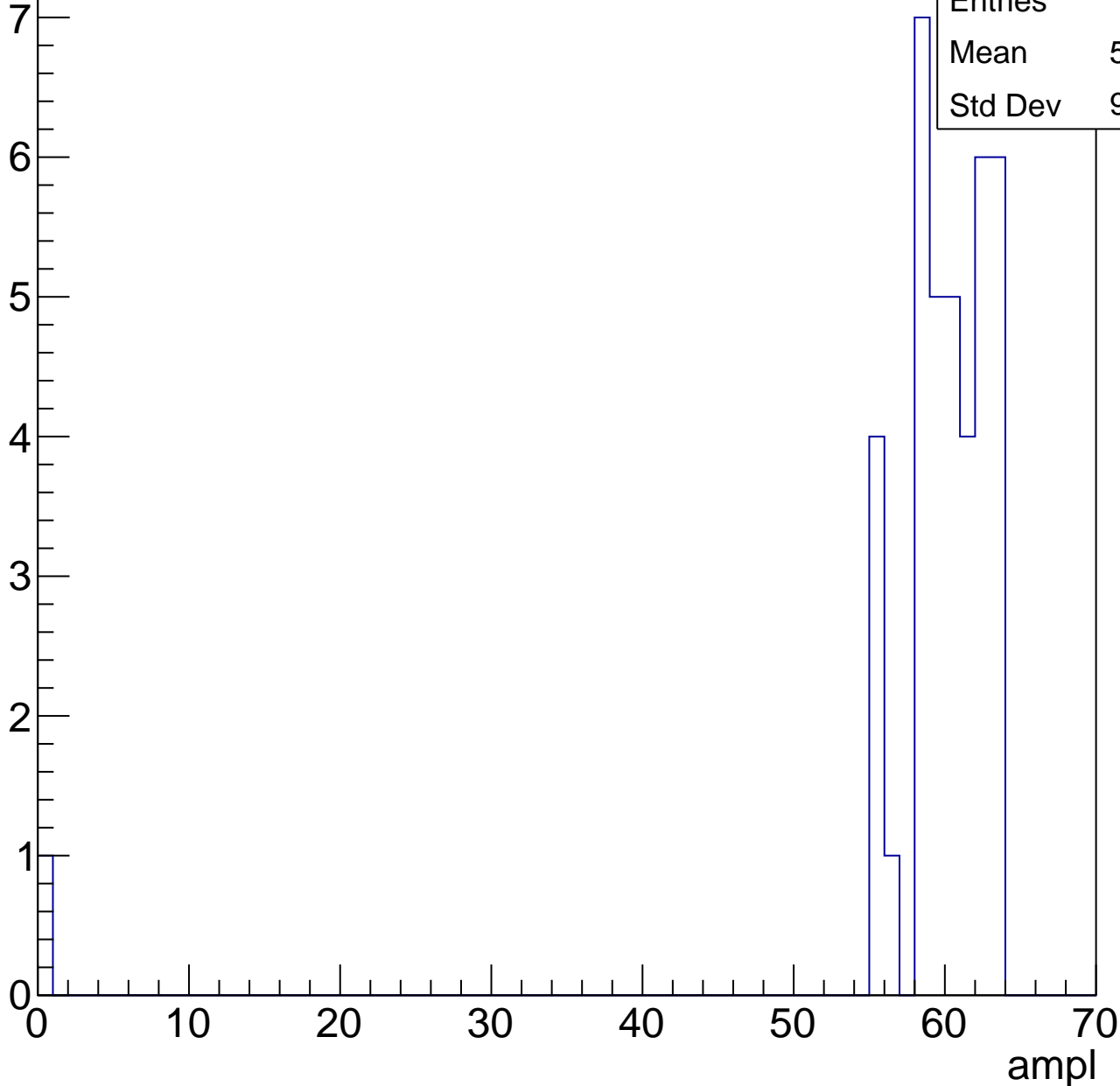


B0L002S, U4-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

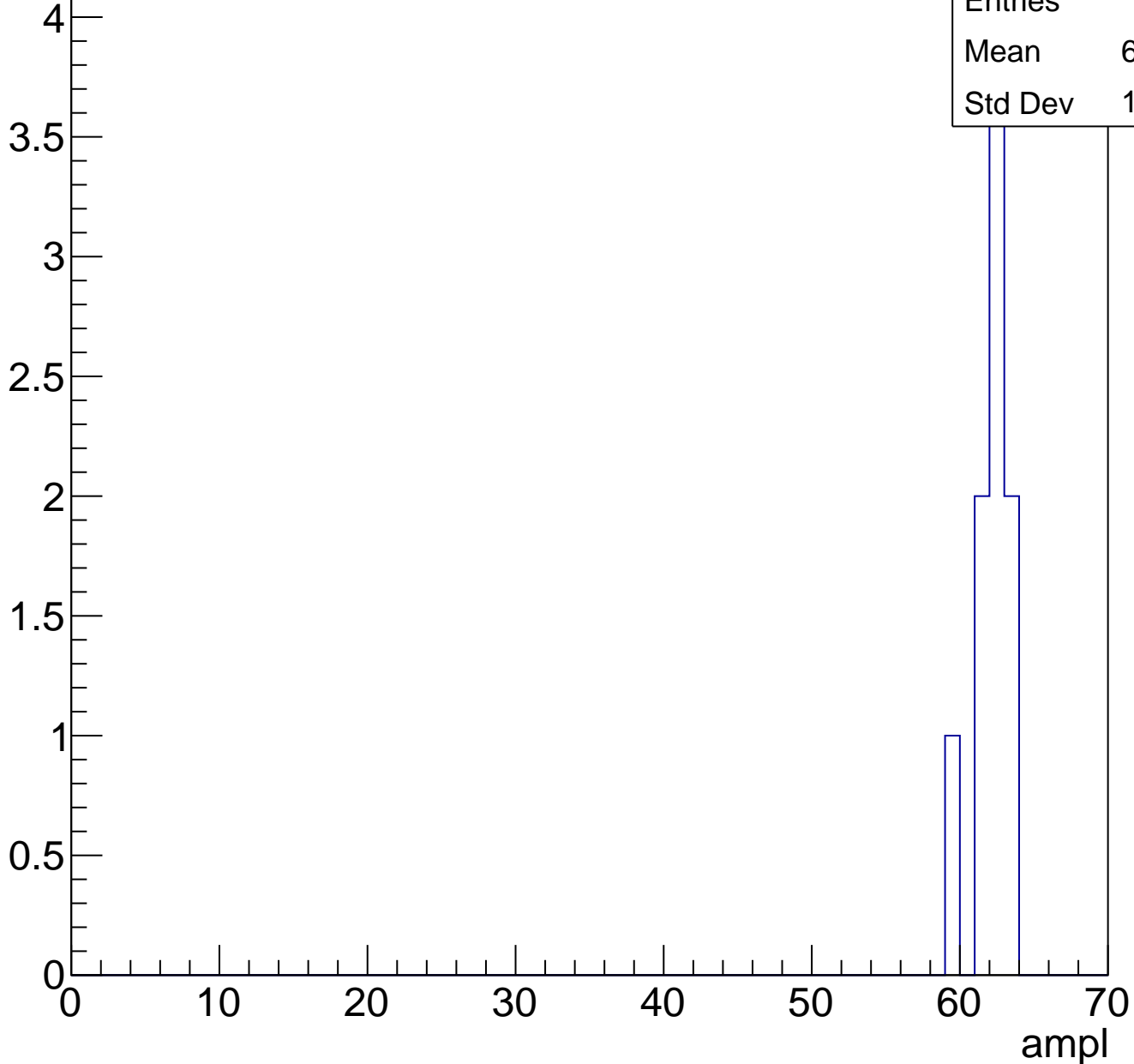
Entries	39
Mean	58.23
Std Dev	9.752



B0L002S, U4-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch109, adc0

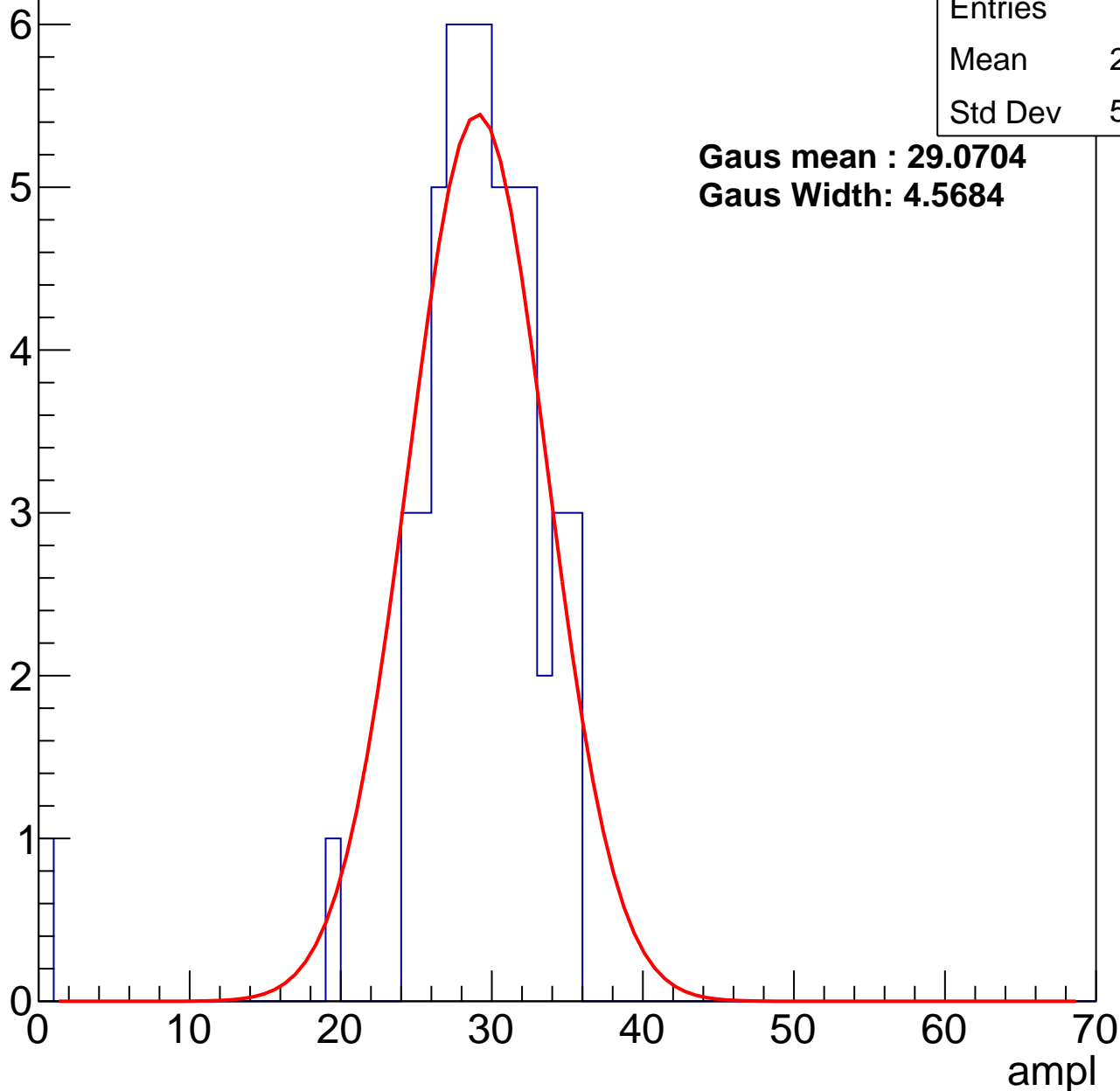
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	28.48
Std Dev	5.109

Gaus mean : 29.0704

Gaus Width: 4.5684



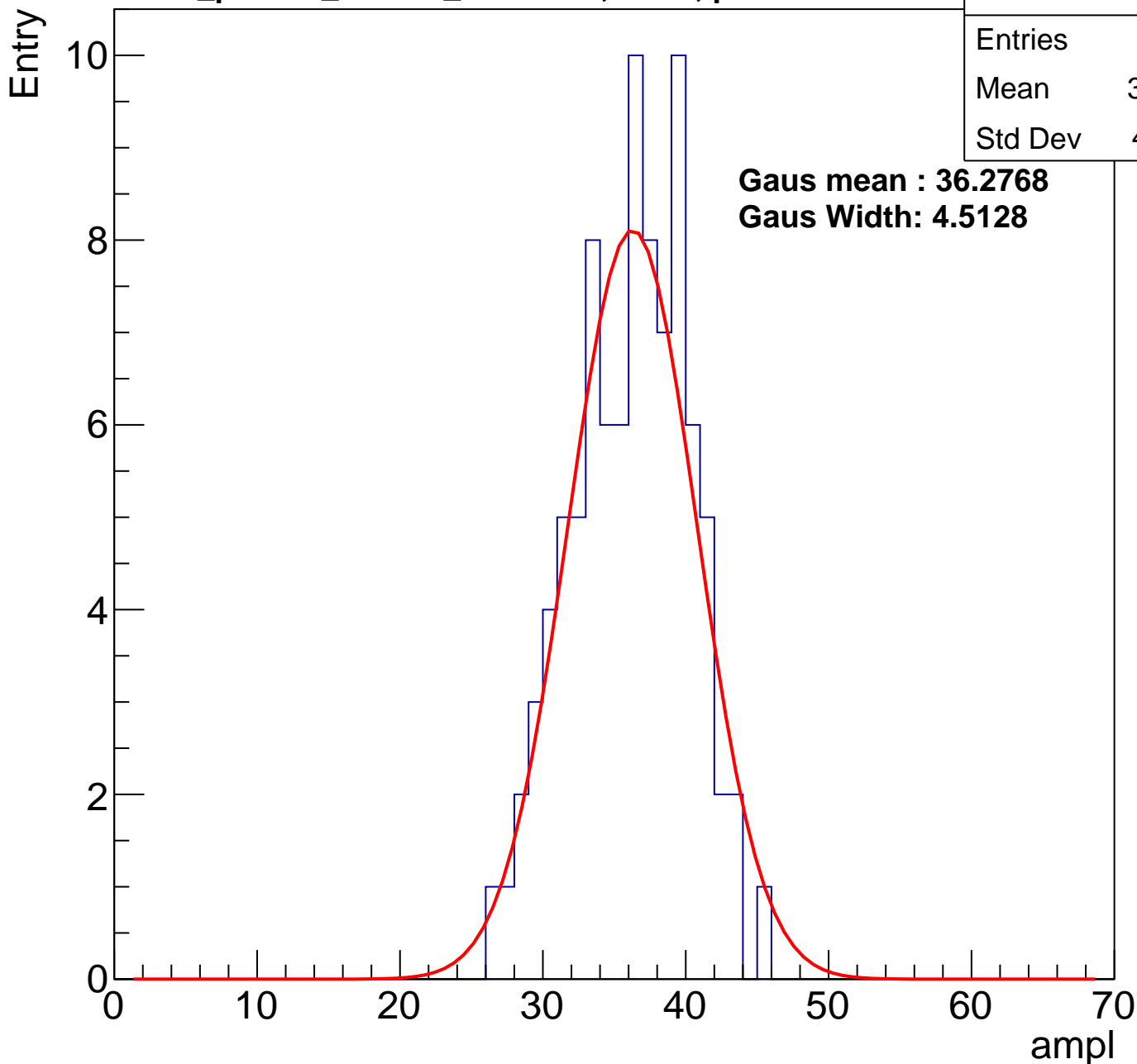
B0L002S, U4-ch109, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	35.66
Std Dev	4.031

Gaus mean : 36.2768

Gaus Width: 4.5128



B0L002S, U4-ch109, adc2

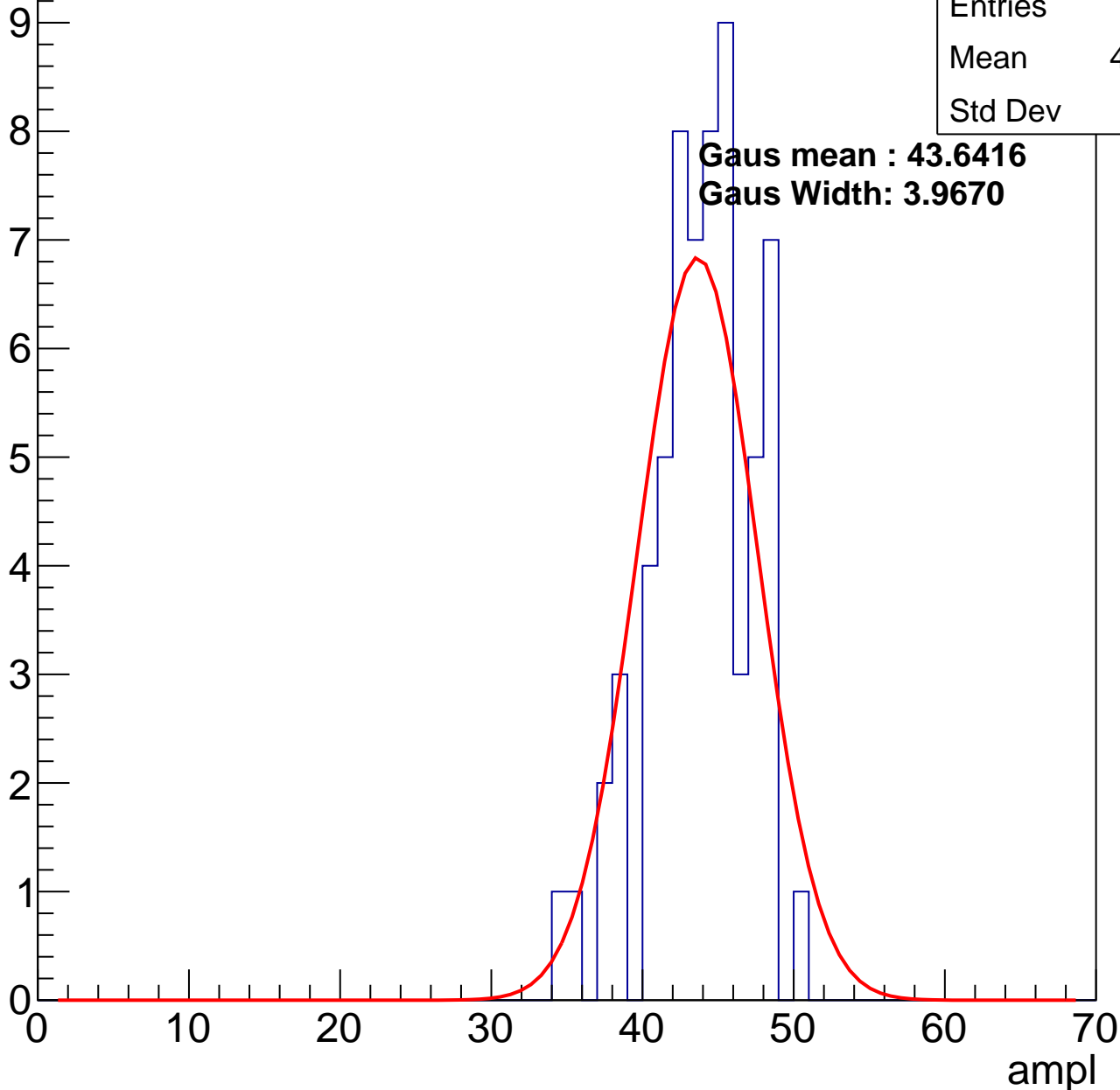
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.36
Std Dev	3.36

Gaus mean : 43.6416

Gaus Width: 3.9670

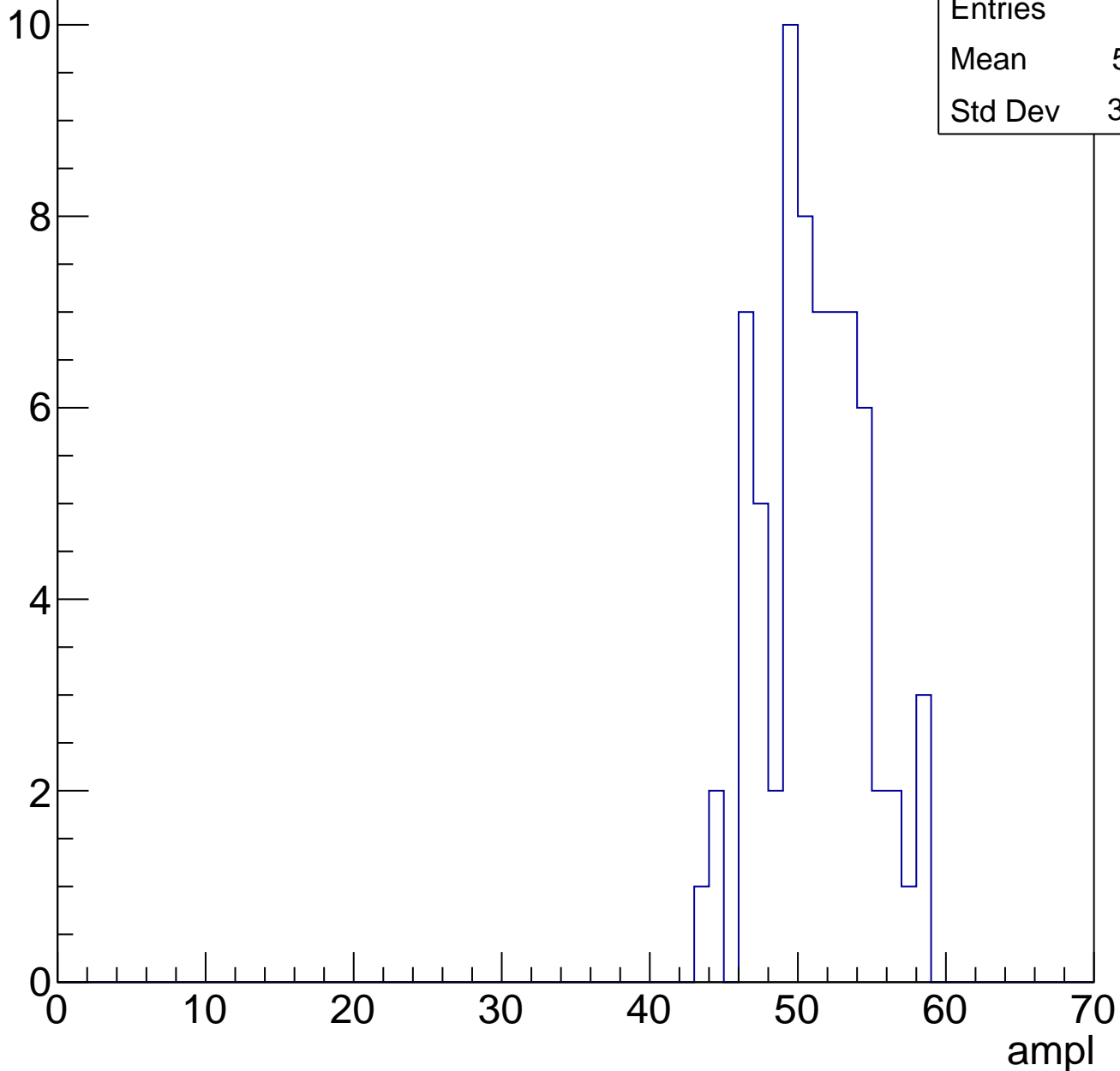


B0L002S, U4-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	50.61
Std Dev	3.436

Entry

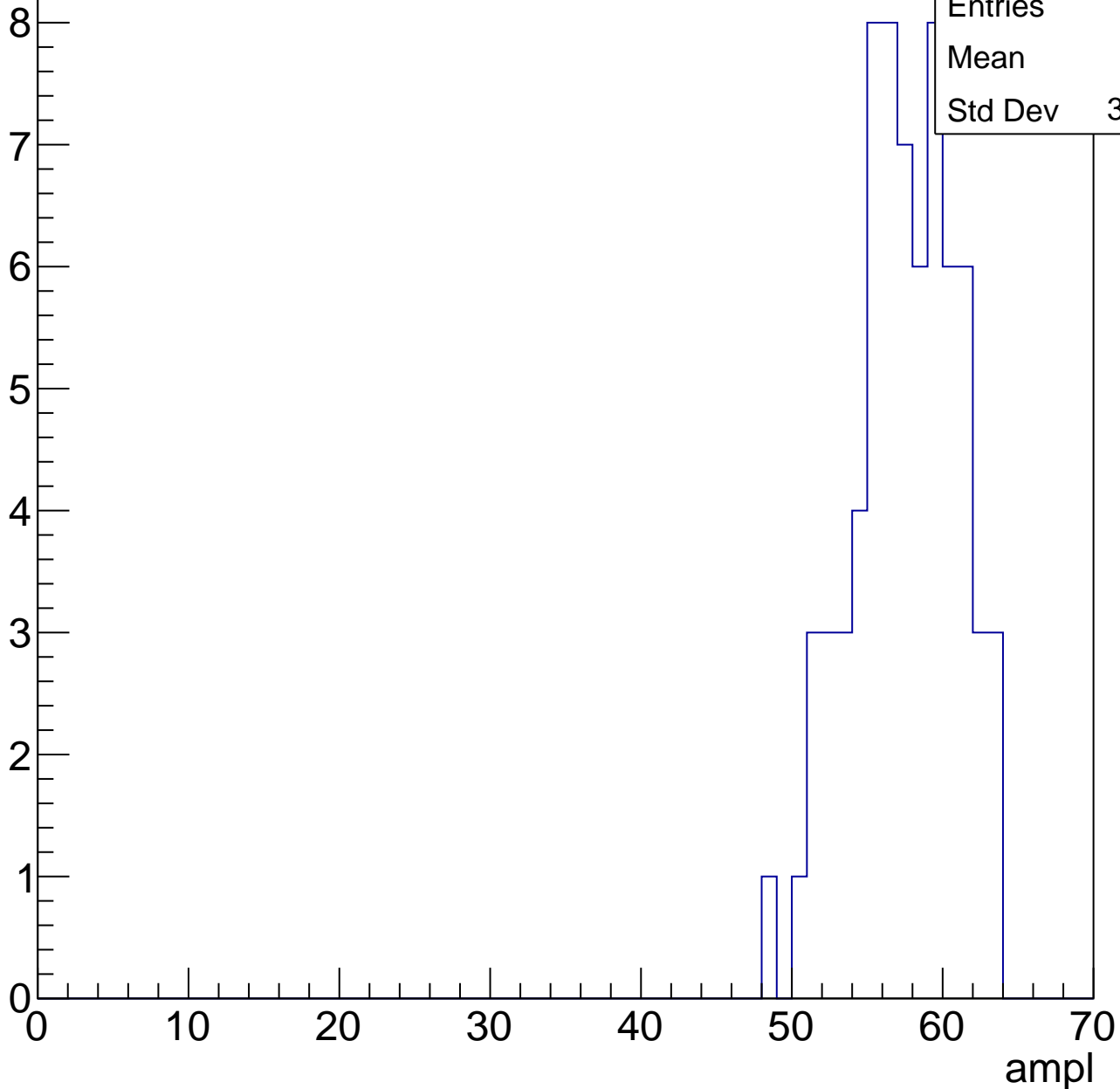


B0L002S, U4-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	57
Std Dev	3.397

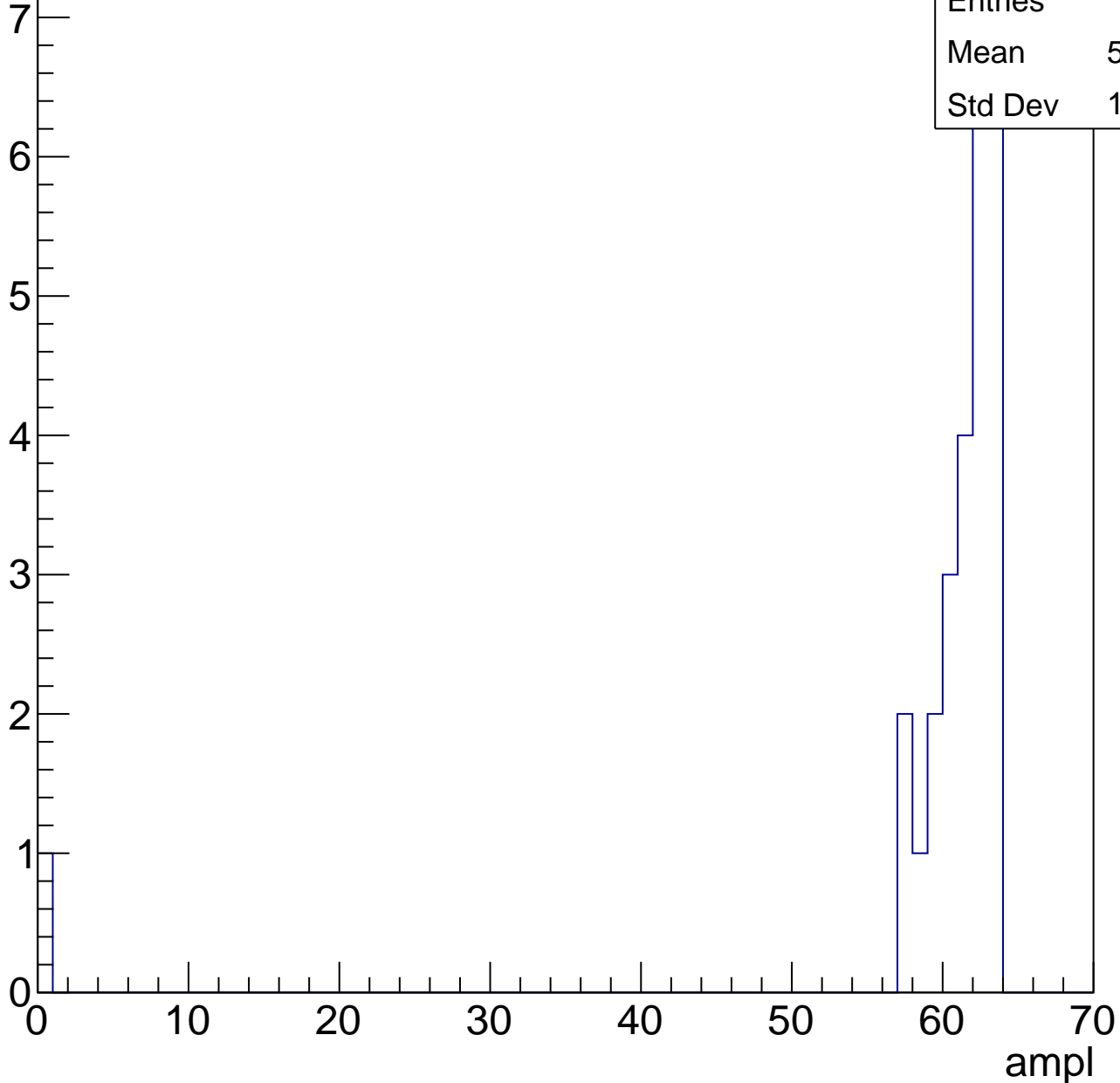


B0L002S, U4-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	27
Mean	58.85
Std Dev	11.68



B0L002S, U4-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch110, adc0

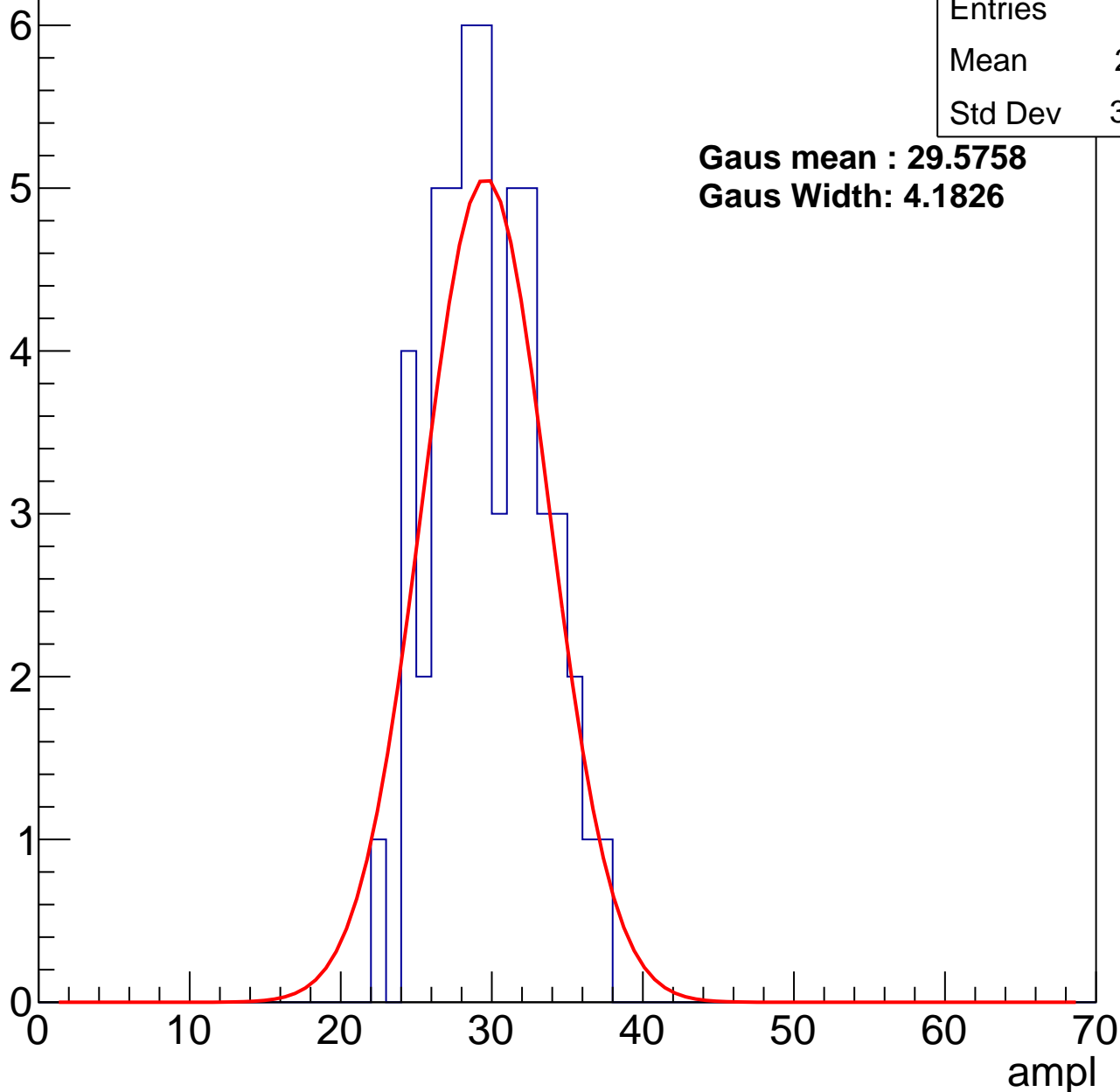
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	29.31
Std Dev	3.462

Gaus mean : 29.5758

Gaus Width: 4.1826



B0L002S, U4-ch110, adc1

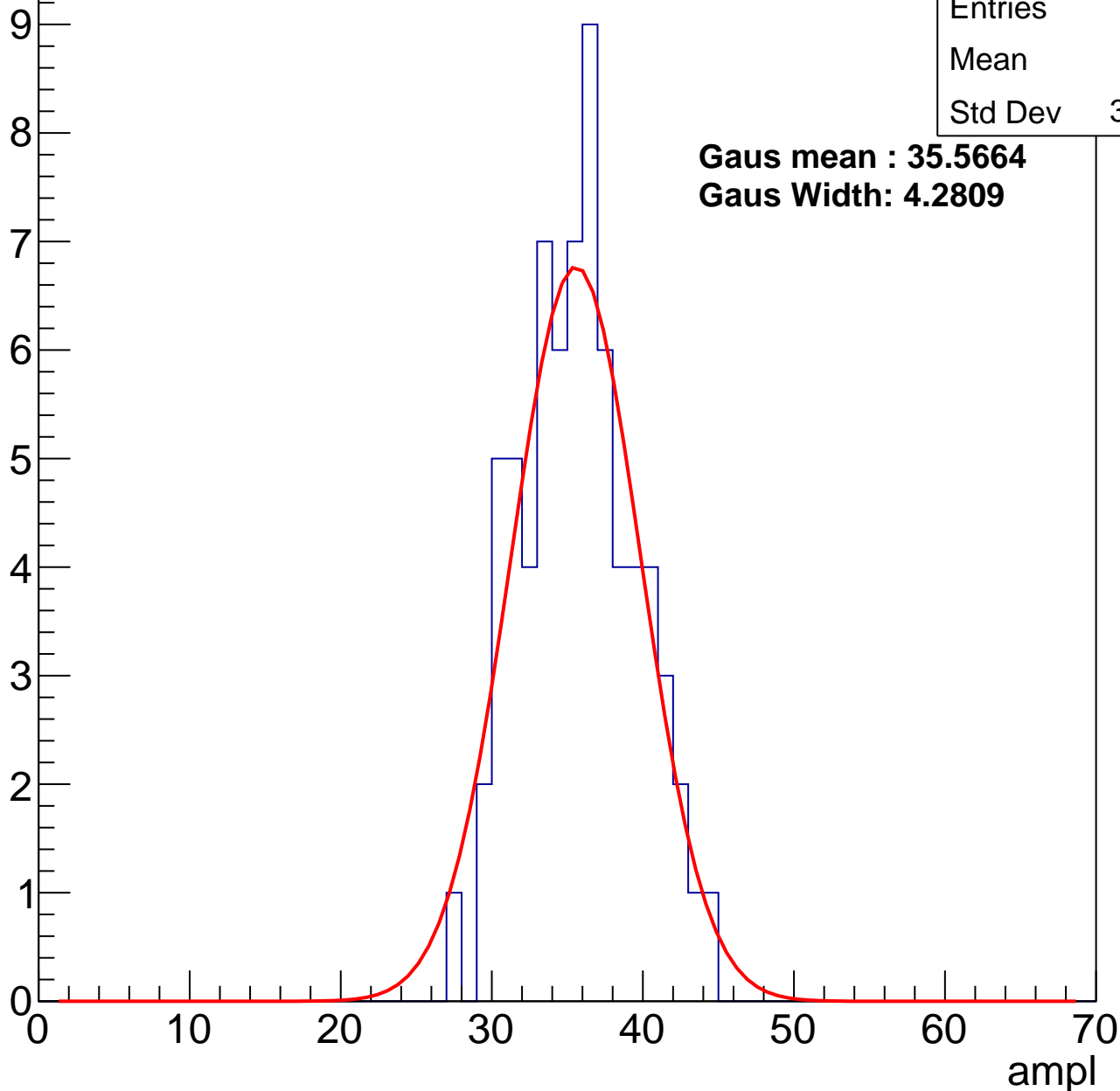
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	35.3
Std Dev	3.732

Gaus mean : 35.5664

Gaus Width: 4.2809



B0L002S, U4-ch110, adc2

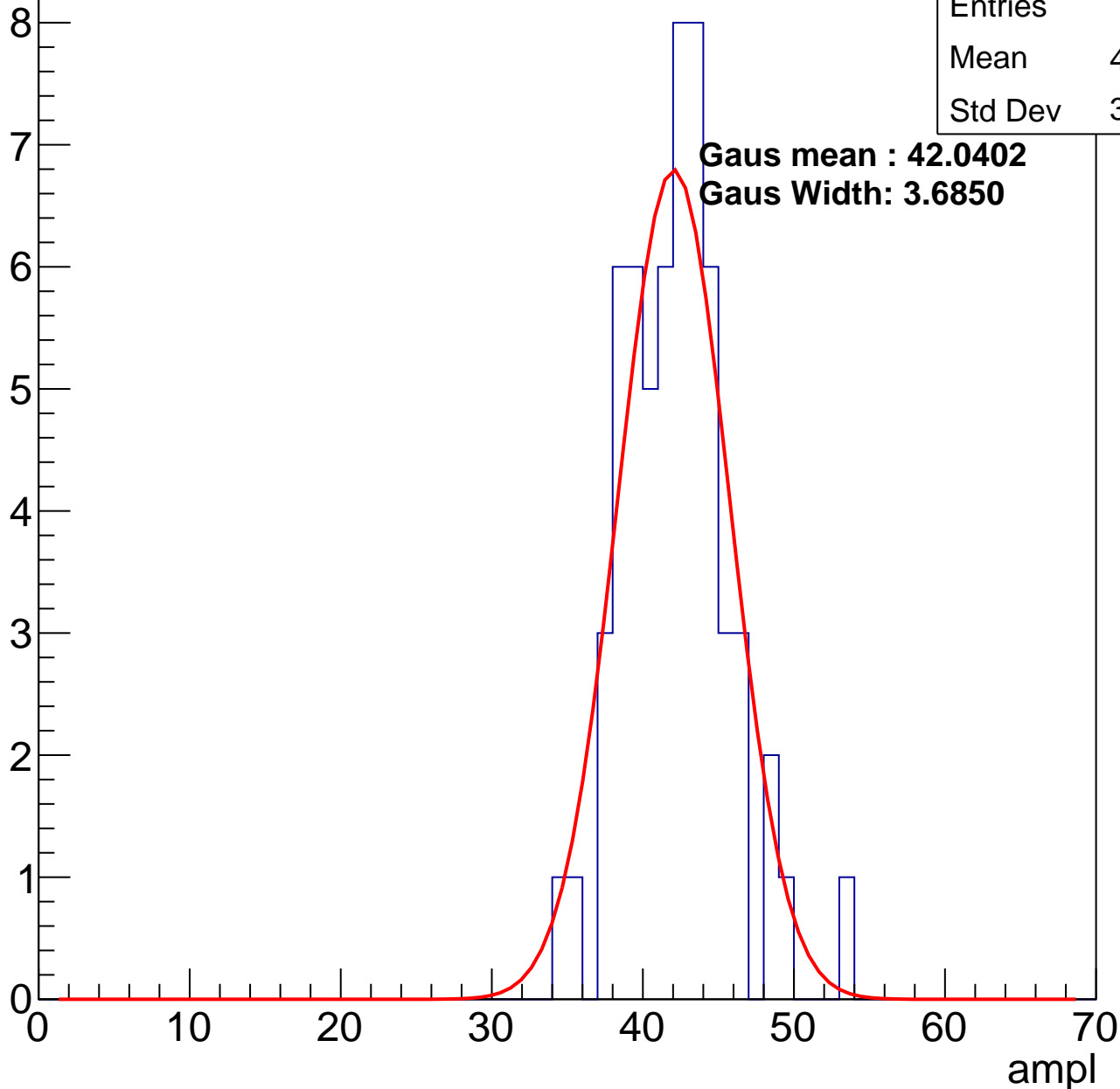
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	41.72
Std Dev	3.436

Gaus mean : 42.0402

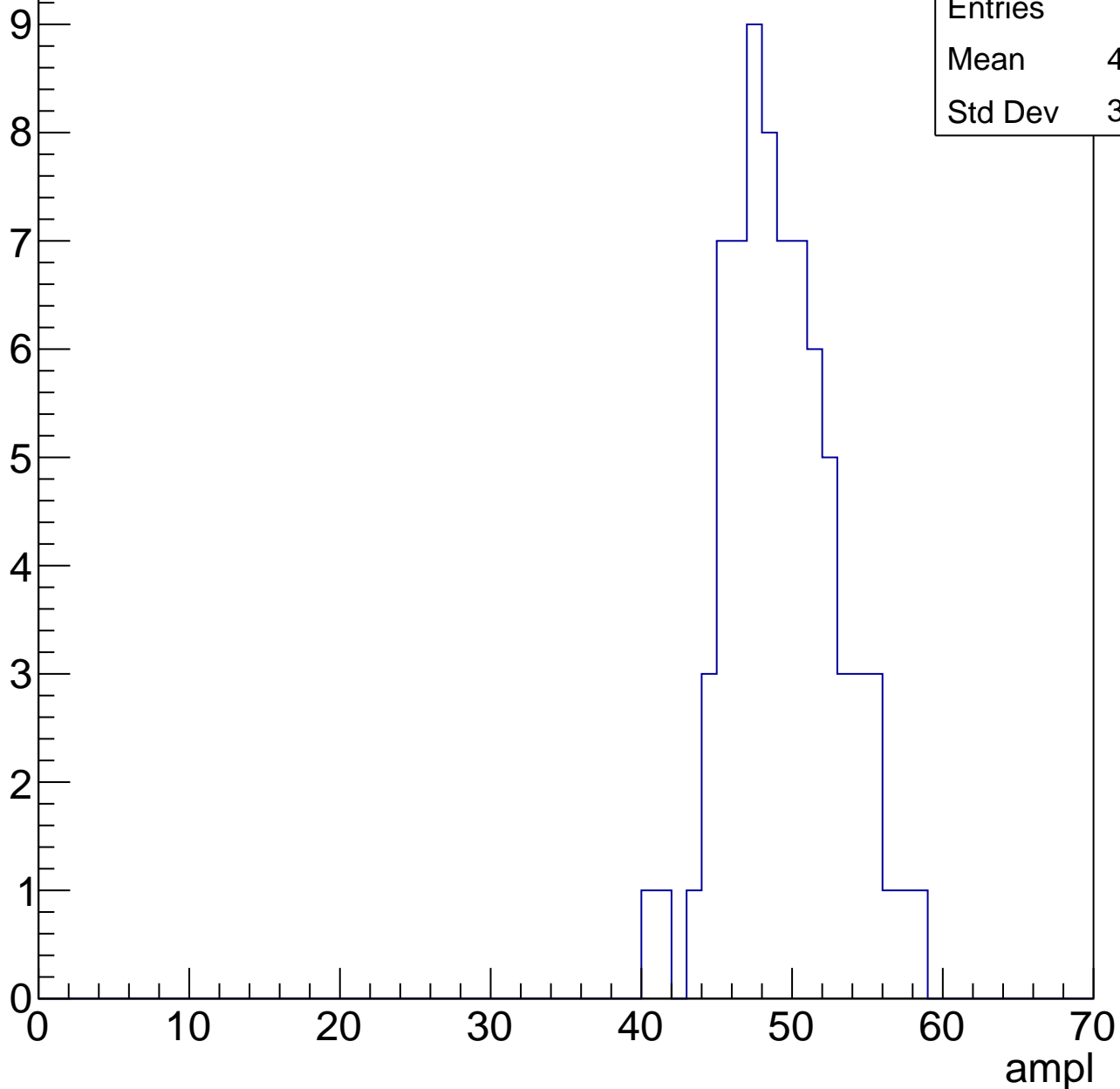
Gaus Width: 3.6850



B0L002S, U4-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



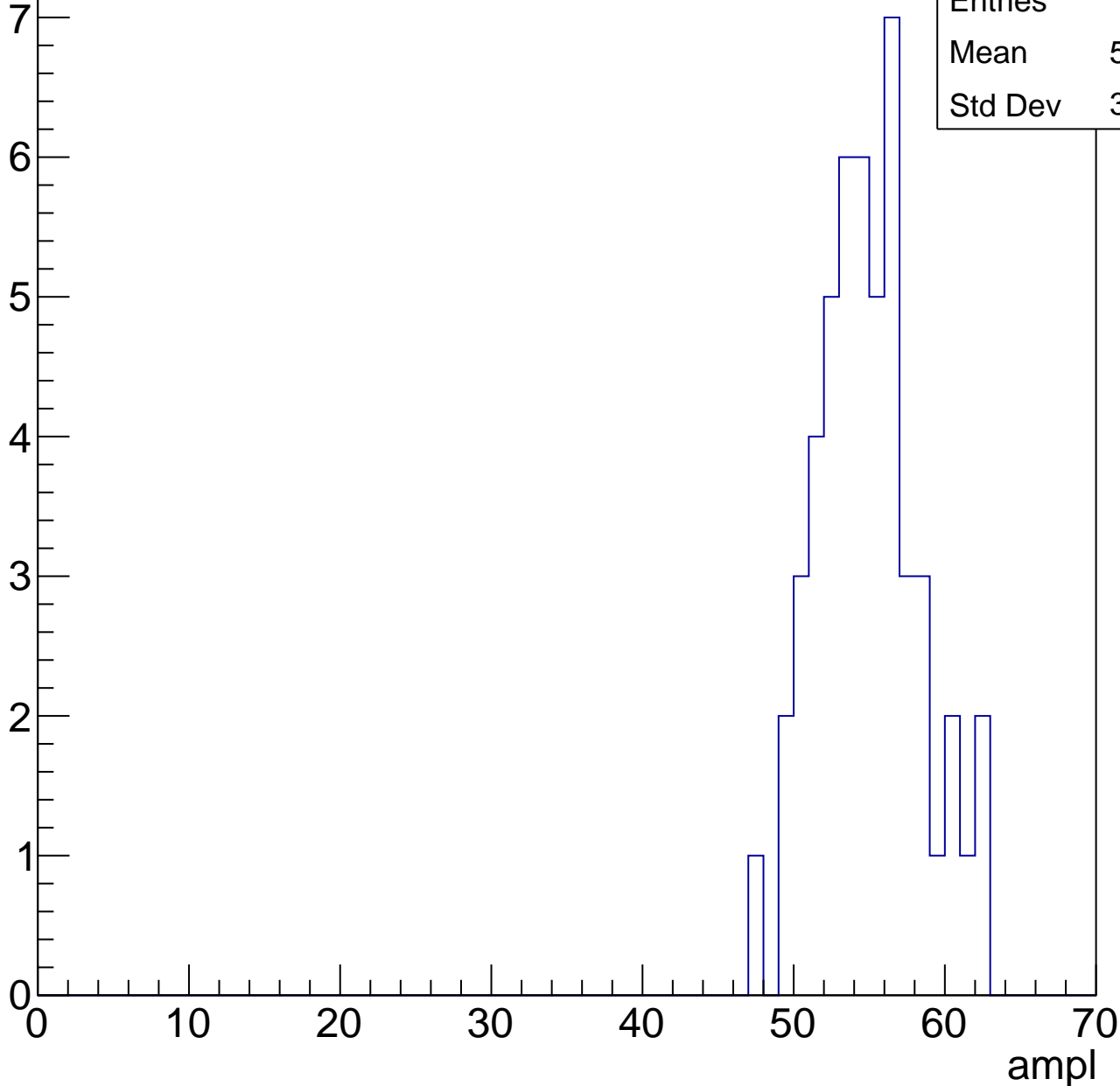
Entries	74
Mean	48.86
Std Dev	3.629

B0L002S, U4-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

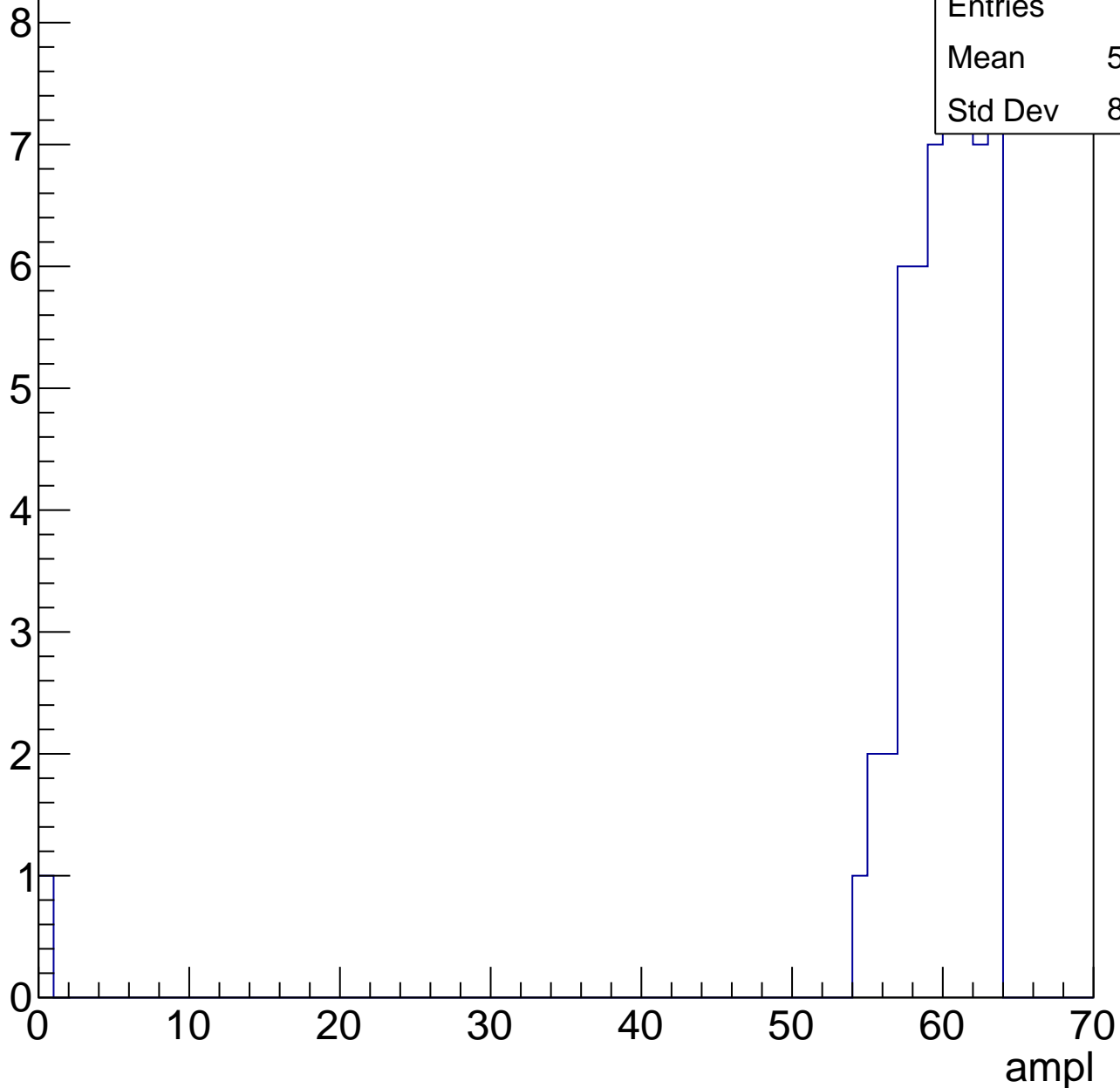
Entries	51
Mean	54.45
Std Dev	3.374



B0L002S, U4-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

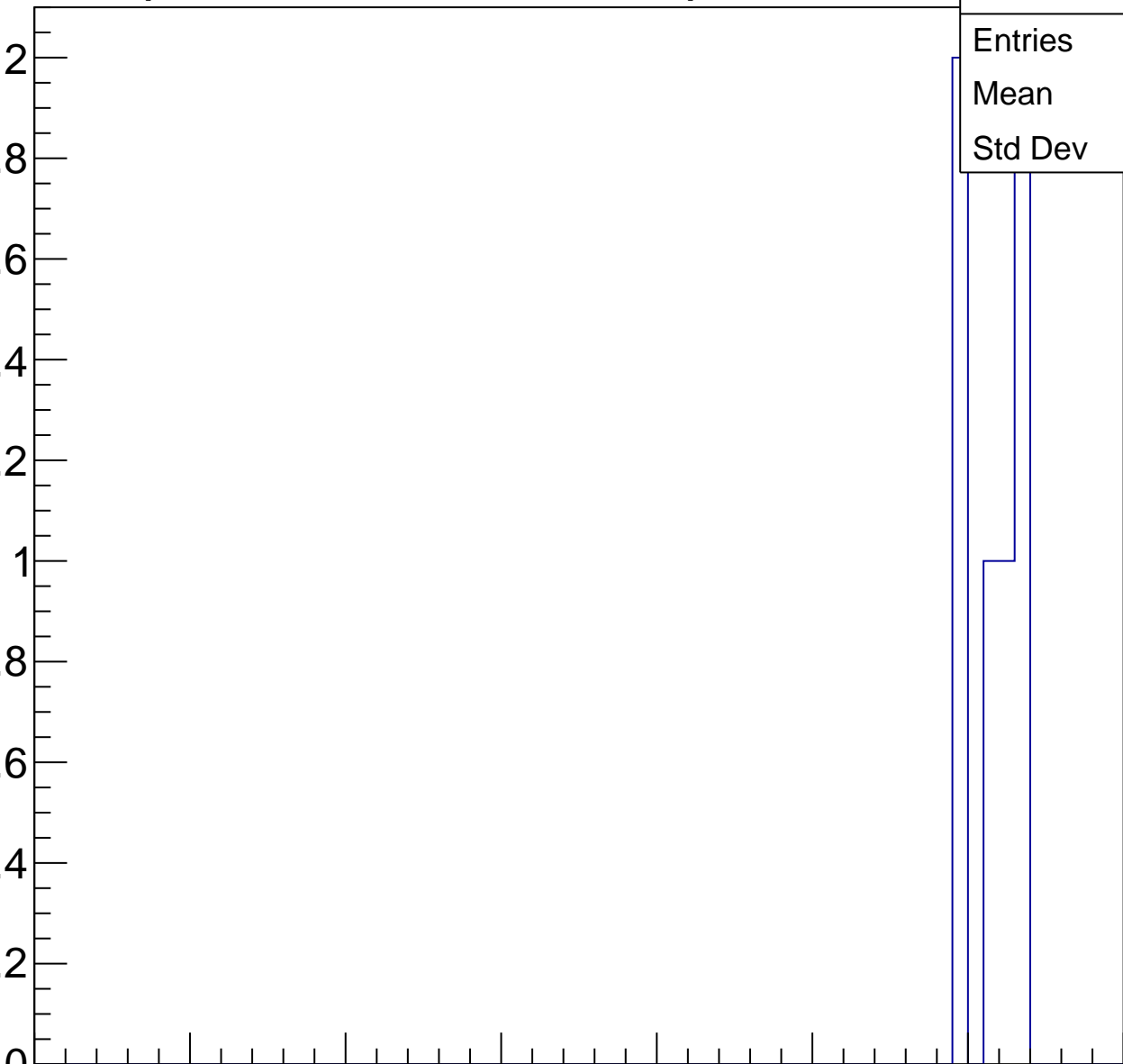
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.675

0 10 20 30 40 50 60 70

ampl



B0L002S, U4-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U4-ch111, adc0

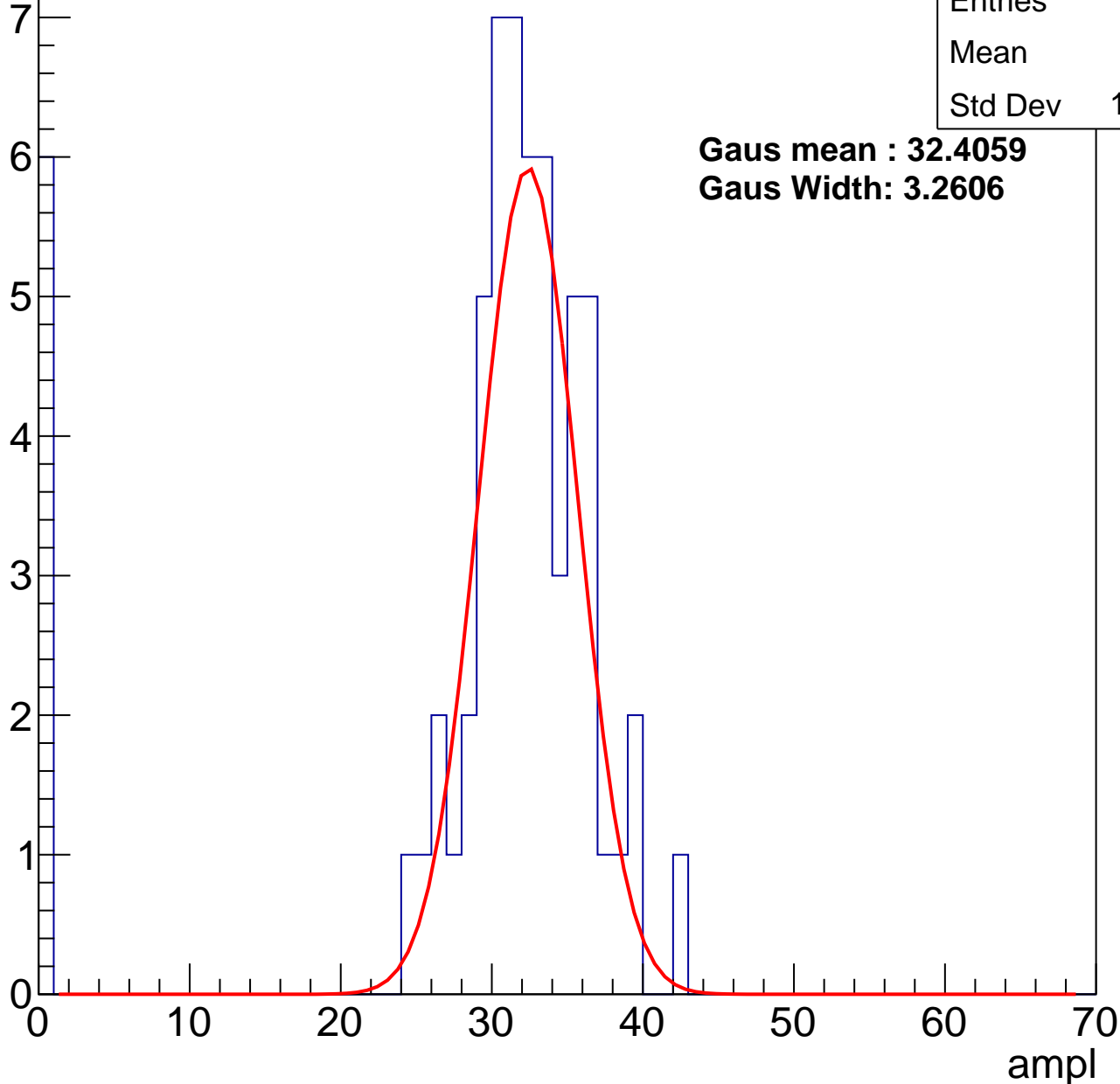
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29
Std Dev	10.09

Gaus mean : 32.4059

Gaus Width: 3.2606



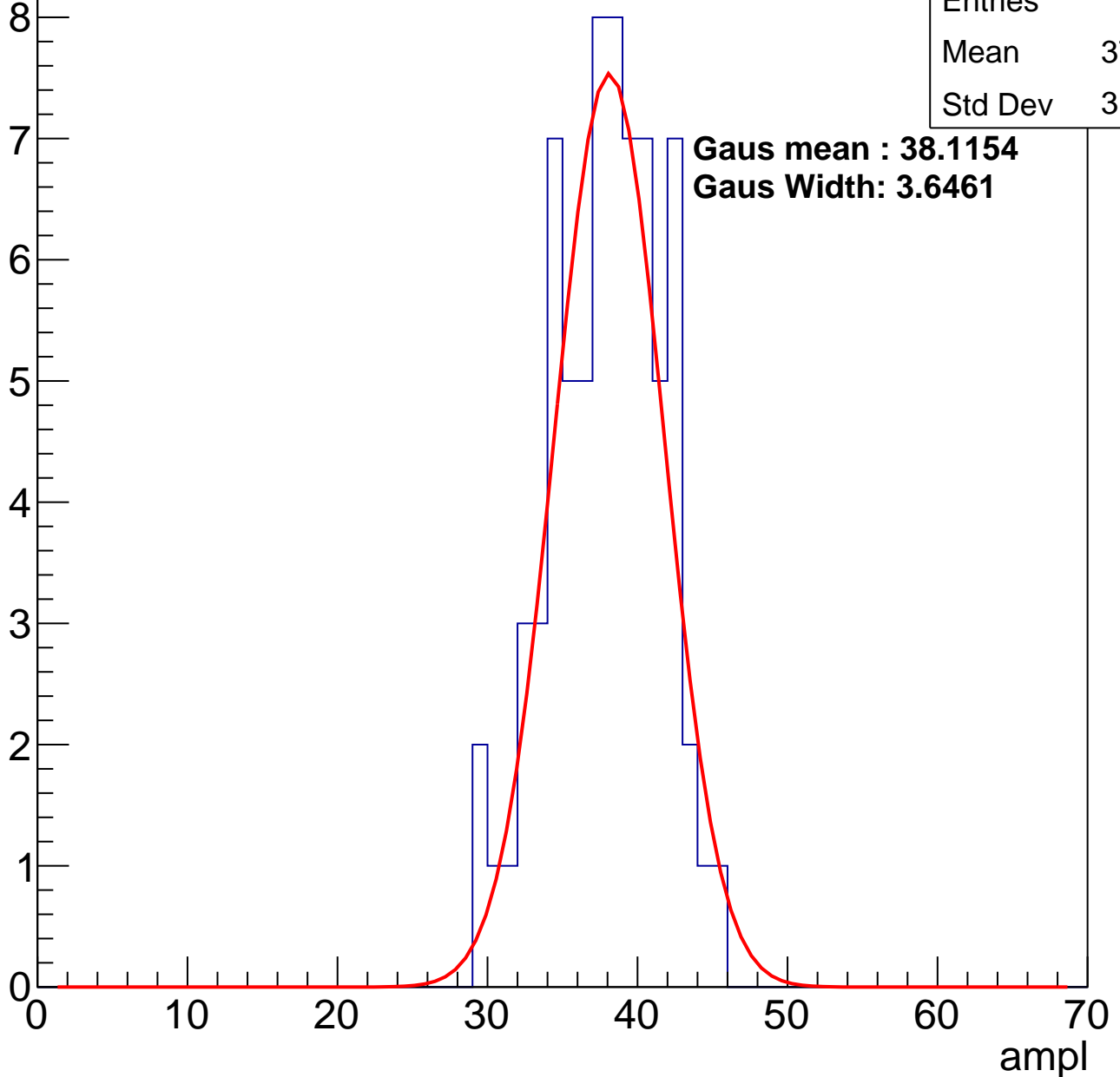
B0L002S, U4-ch111, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	37.45
Std Dev	3.604

Gaus mean : 38.1154
Gaus Width: 3.6461



B0L002S, U4-ch111, adc2

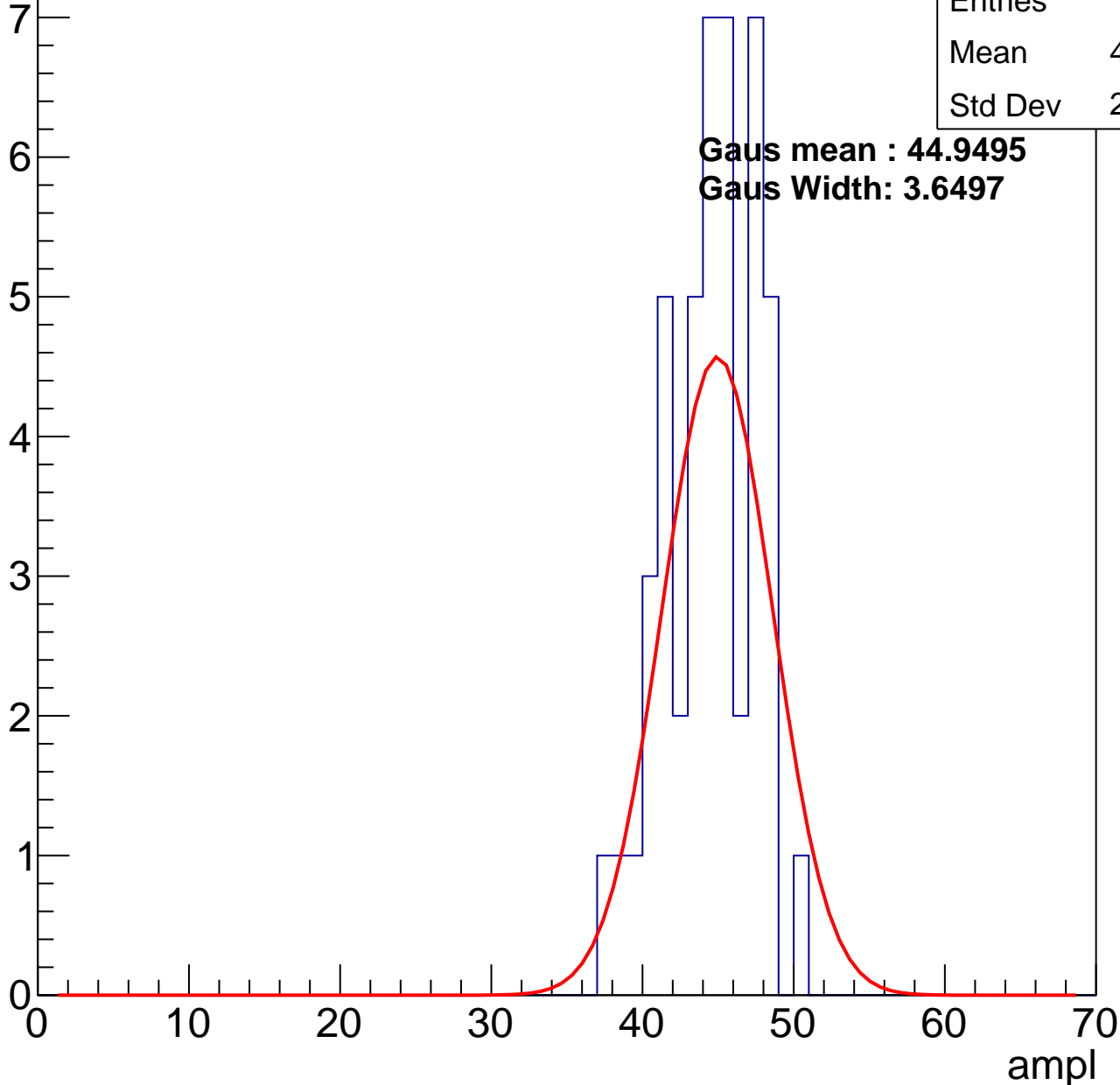
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.09
Std Dev	2.952

Gaus mean : 44.9495

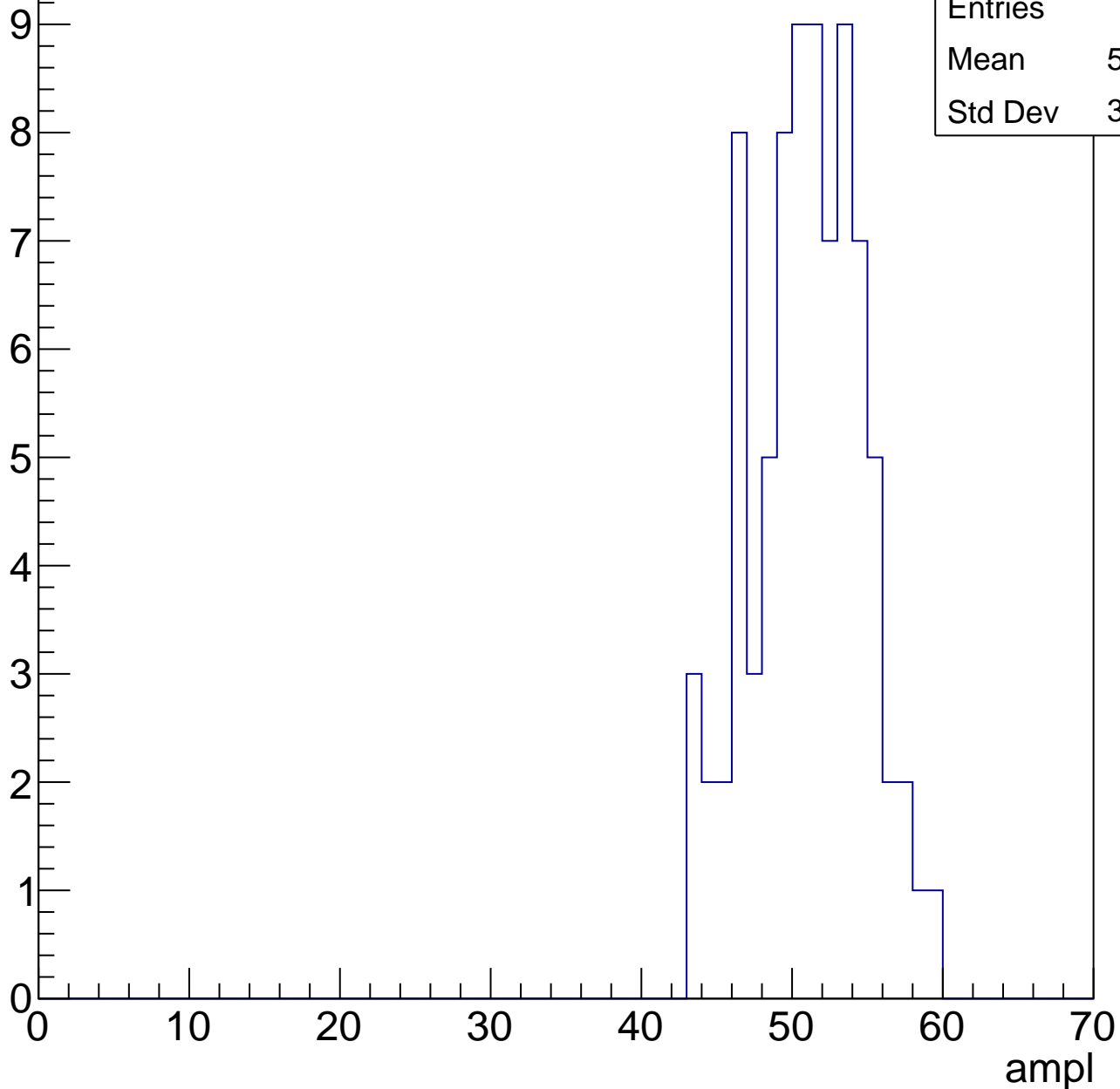
Gaus Width: 3.6497



B0L002S, U4-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



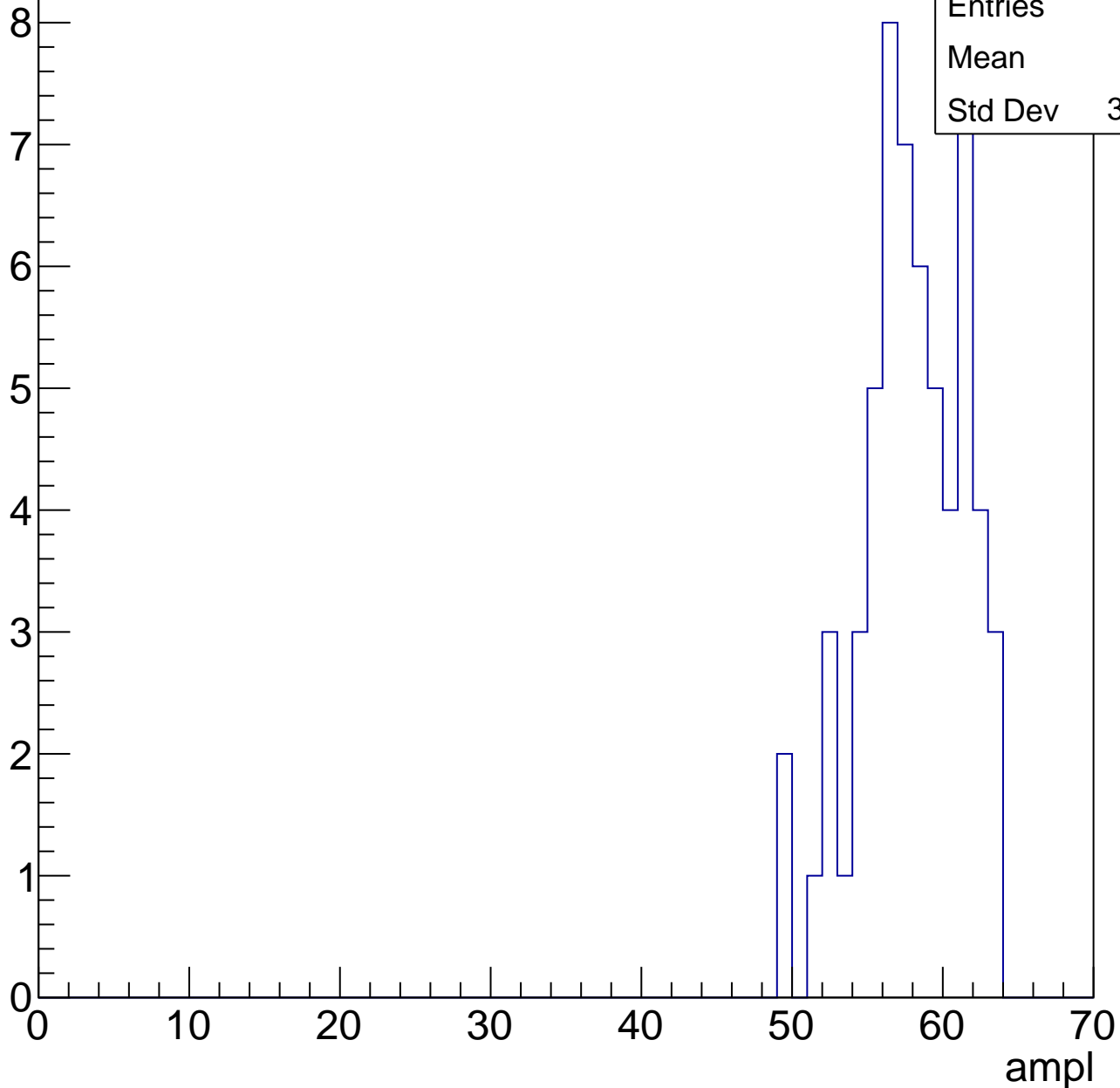
Entries	83
Mean	50.53
Std Dev	3.638

B0L002S, U4-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

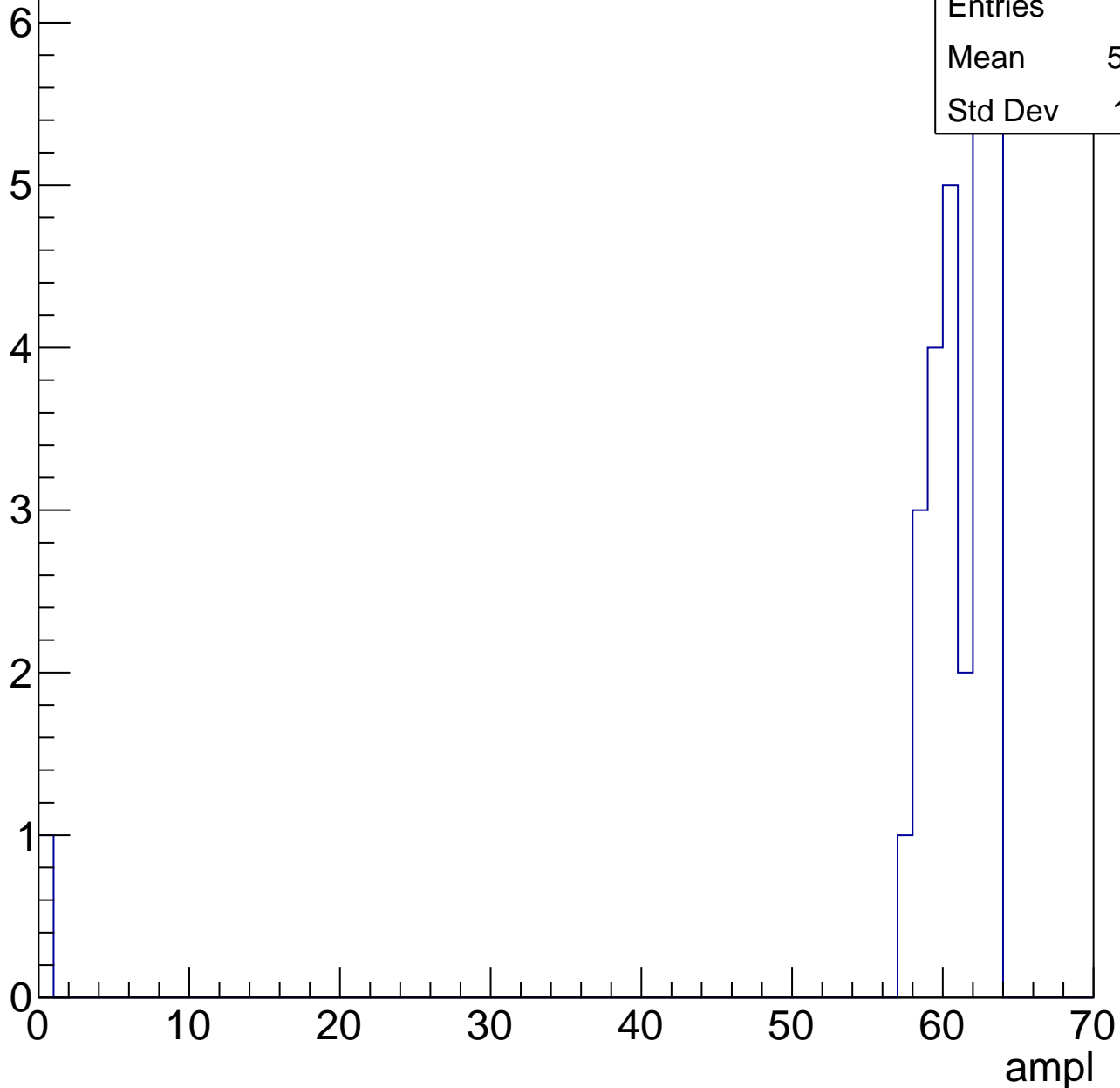
Entries	60
Mean	57.5
Std Dev	3.399



B0L002S, U4-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch112, adc0

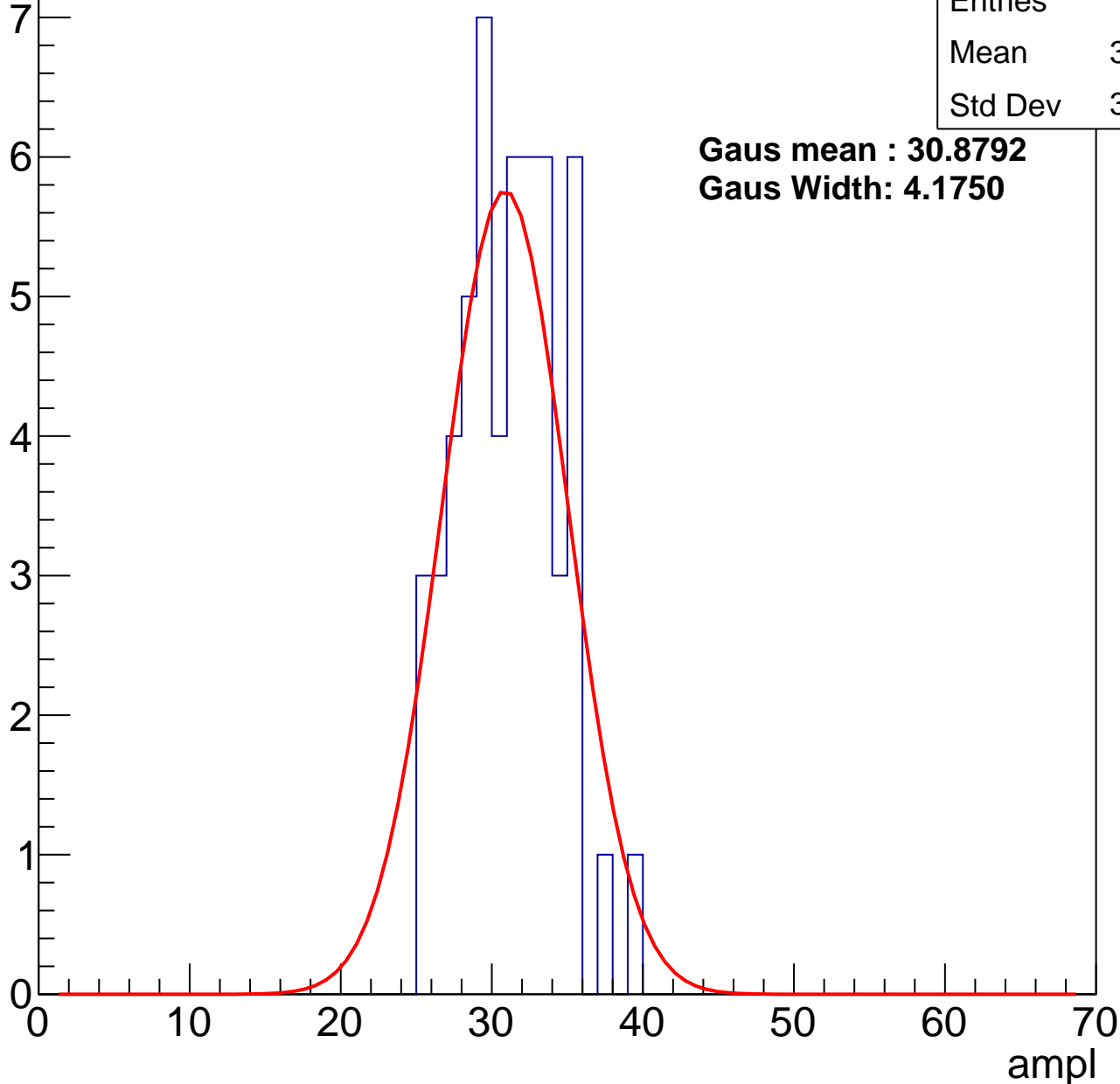
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	30.69
Std Dev	3.224

Gaus mean : 30.8792

Gaus Width: 4.1750



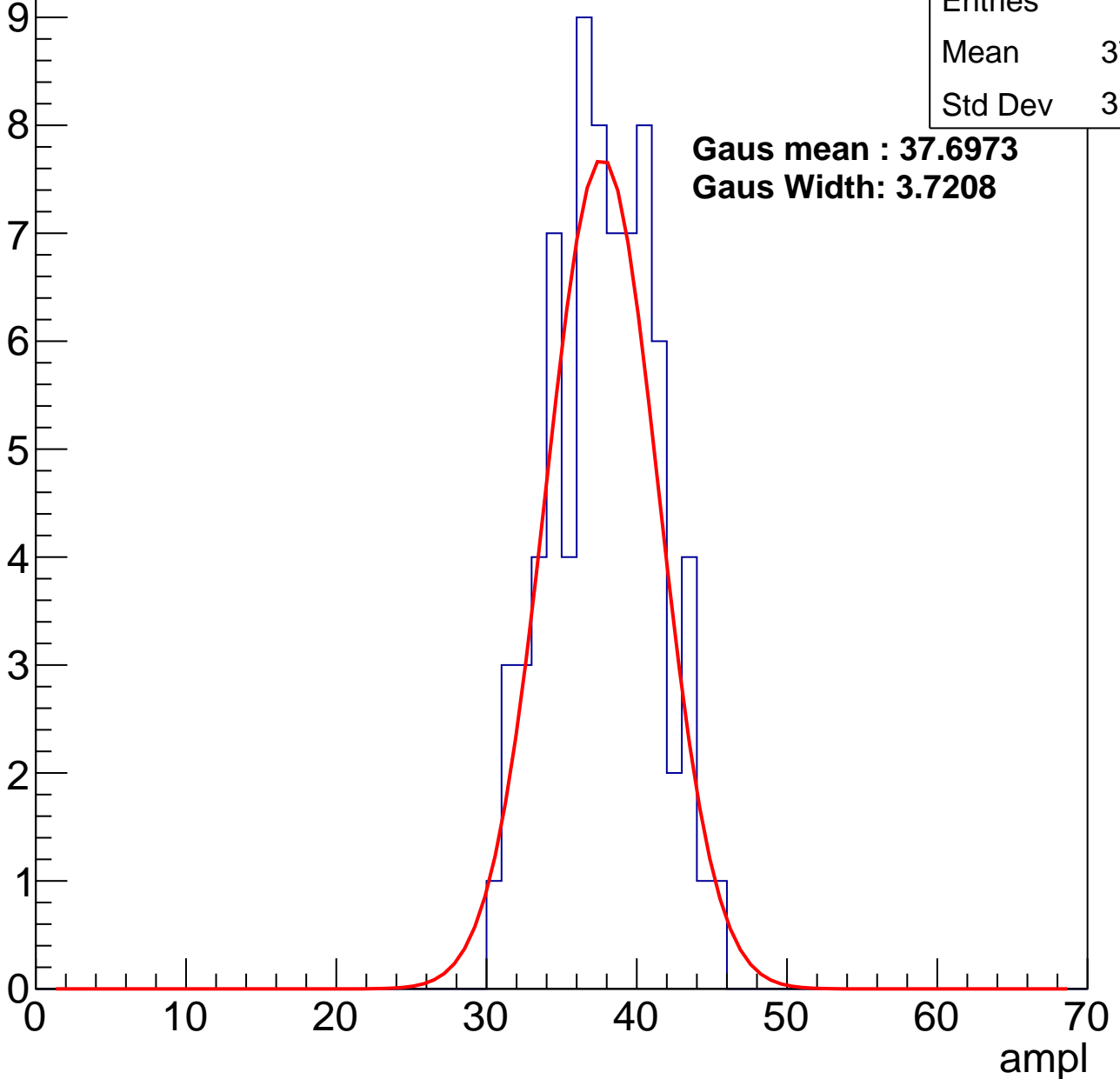
B0L002S, U4-ch112, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.32
Std Dev	3.438

Gaus mean : 37.6973
Gaus Width: 3.7208



B0L002S, U4-ch112, adc2

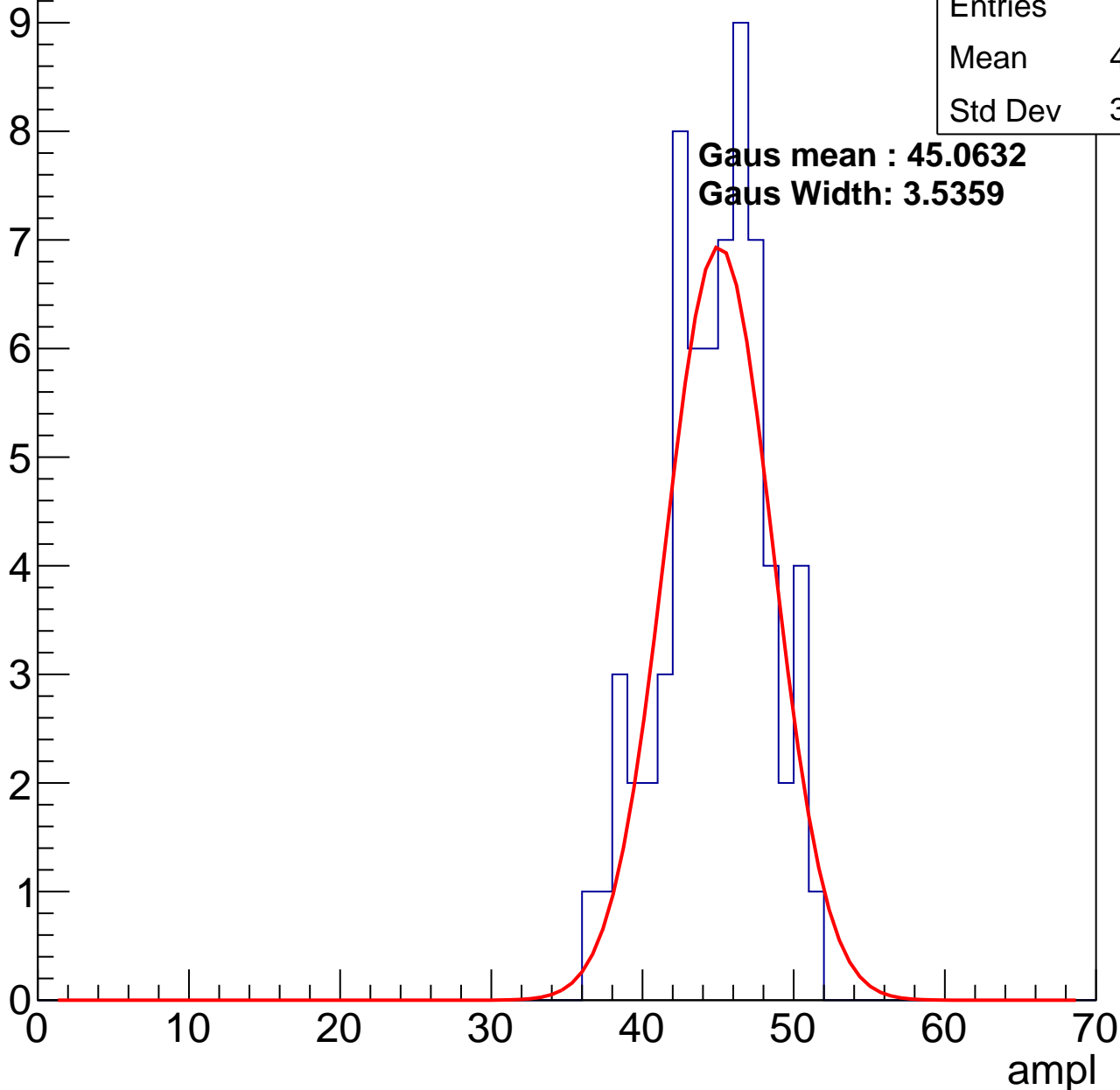
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.32
Std Dev	3.425

Gaus mean : 45.0632

Gaus Width: 3.5359

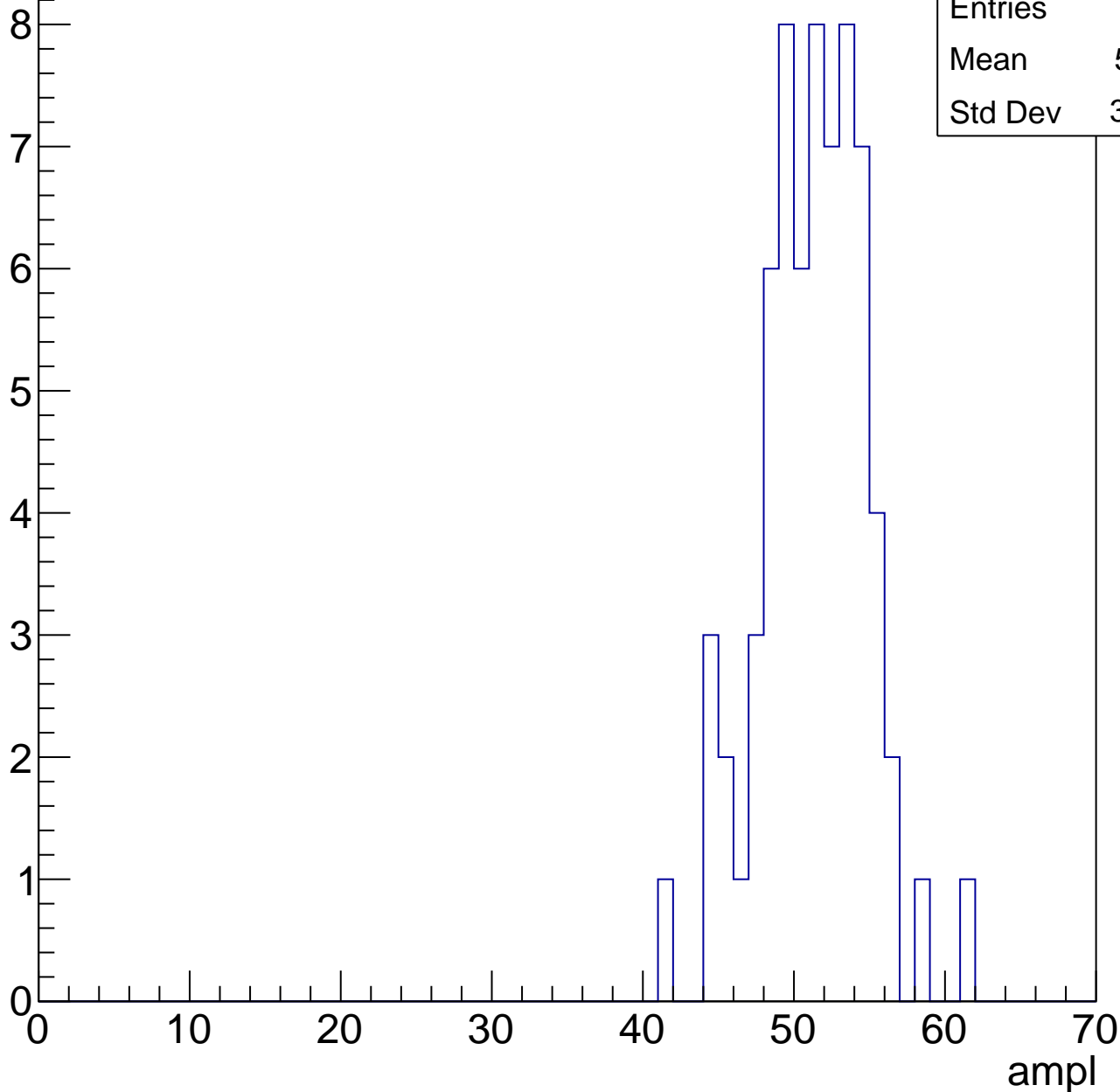


B0L002S, U4-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

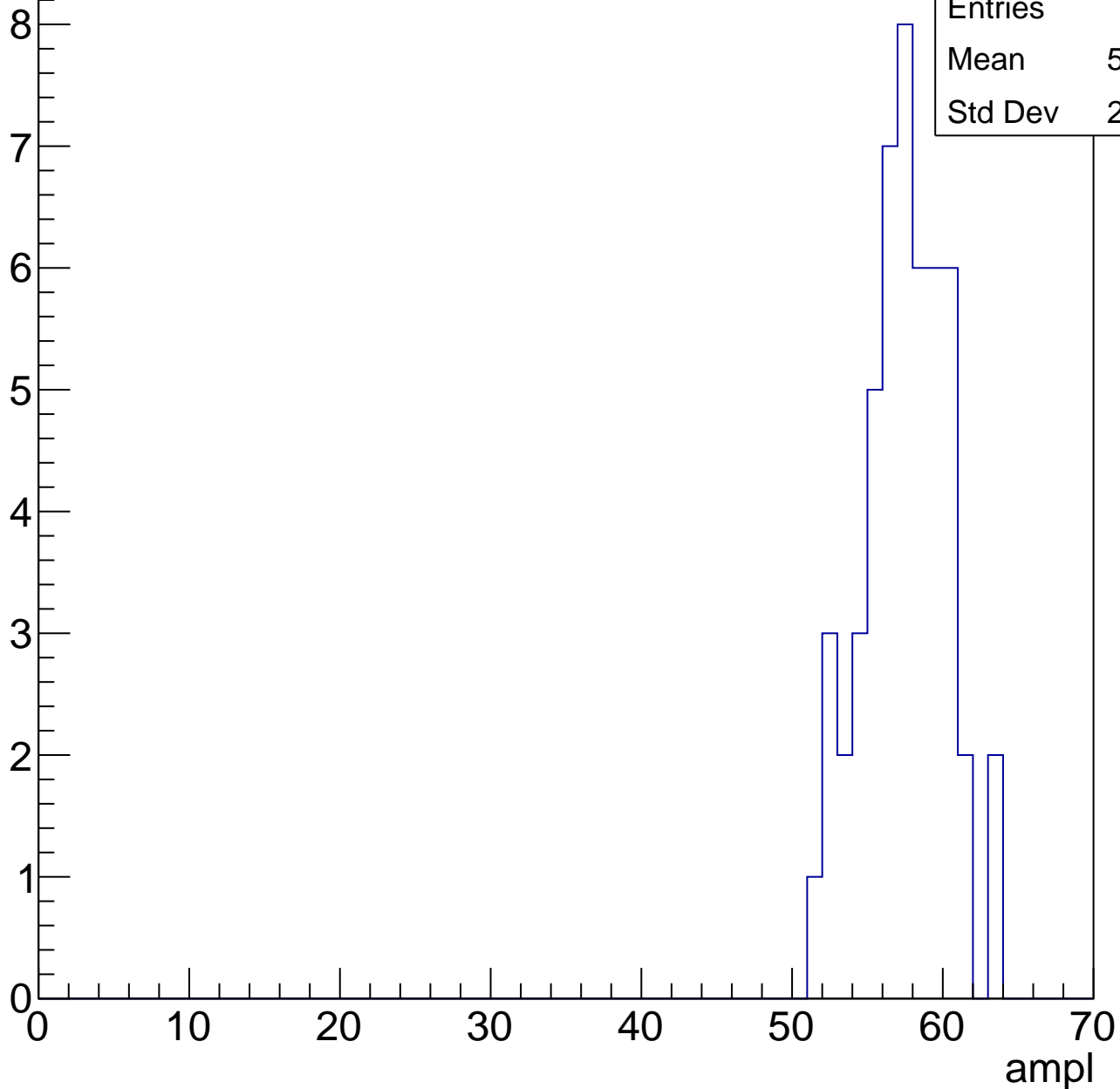
Entries	68
Mean	50.81
Std Dev	3.528



B0L002S, U4-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

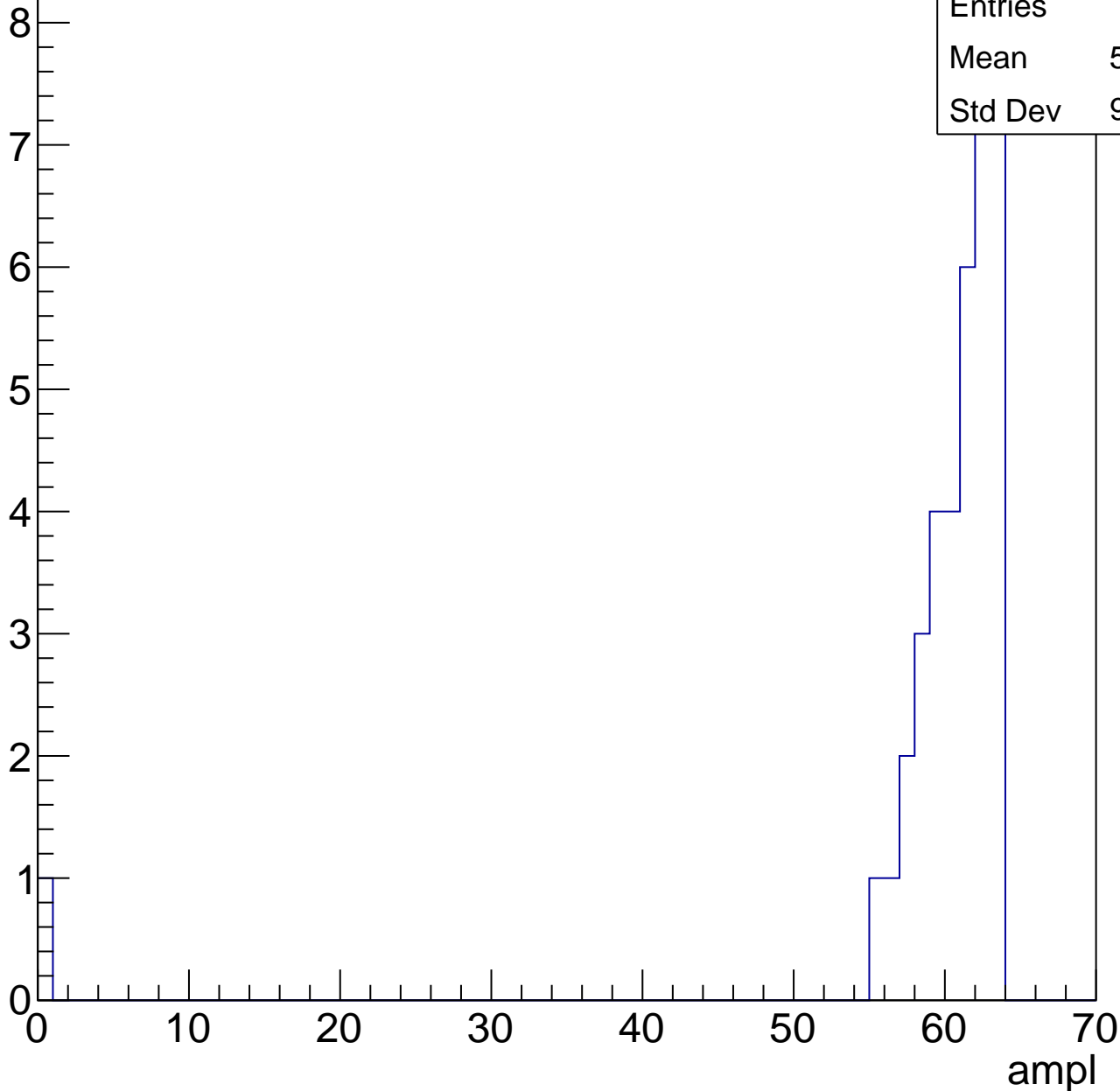


B0L002S, U4-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.97
Std Dev	9.927



B0L002S, U4-ch112, adc6

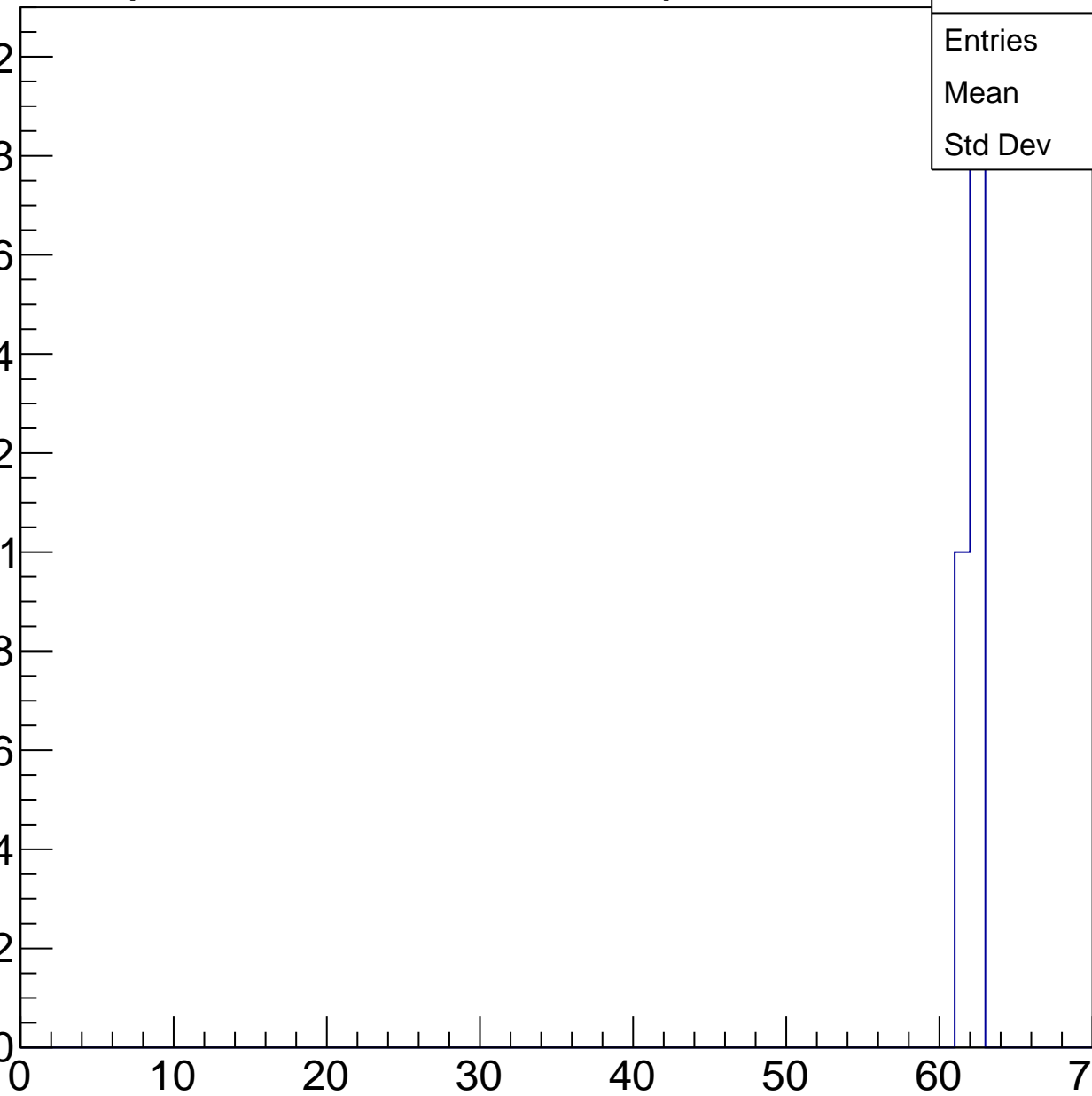
calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl



B0L002S, U4-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	24
Std Dev	0

B0L002S, U4-ch113, adc0

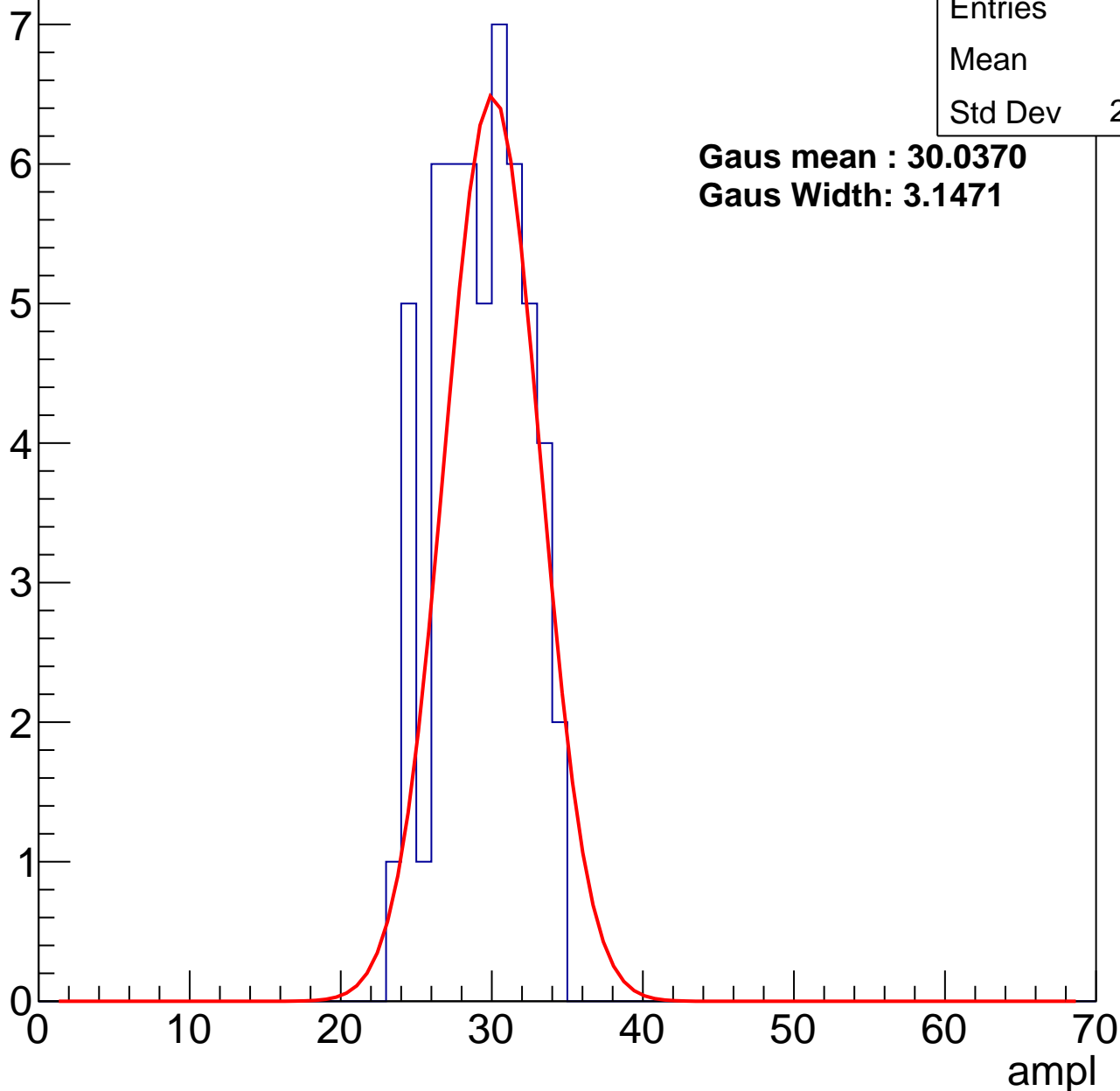
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	28.8
Std Dev	2.883

Gaus mean : 30.0370

Gaus Width: 3.1471



B0L002S, U4-ch113, adc1

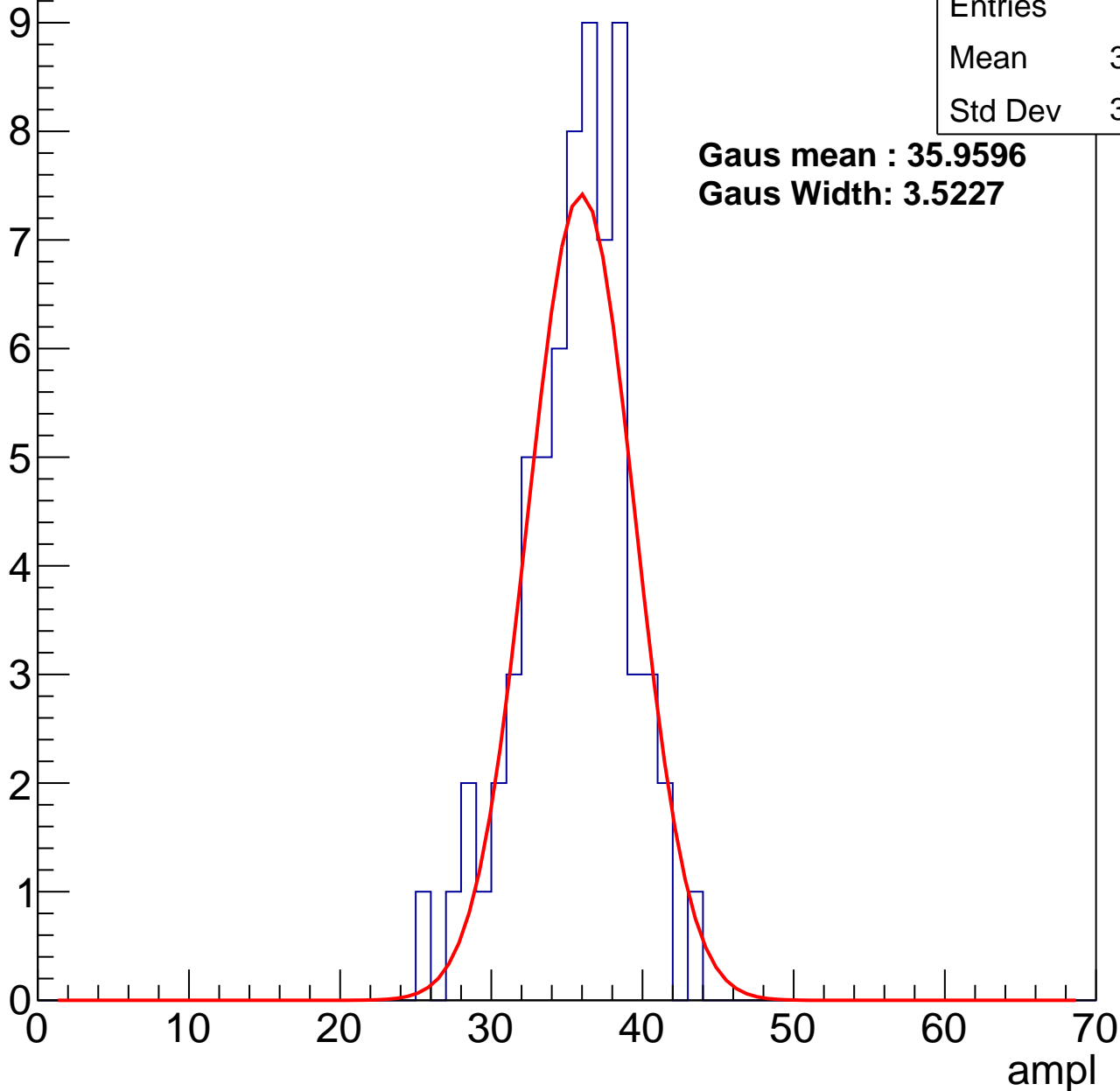
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.09
Std Dev	3.522

Gaus mean : 35.9596

Gaus Width: 3.5227



B0L002S, U4-ch113, adc2

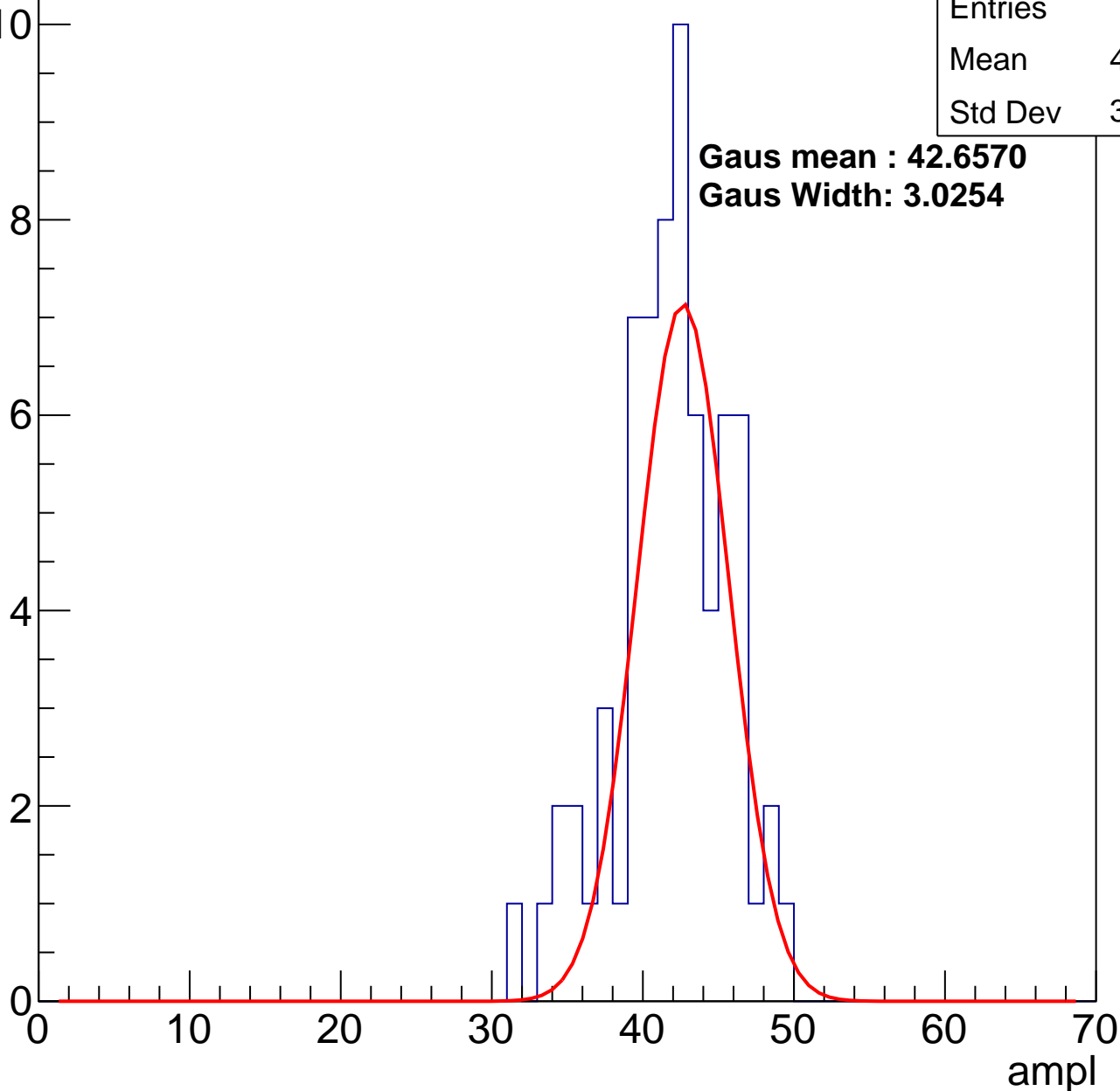
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	41.45
Std Dev	3.713

Gaus mean : 42.6570

Gaus Width: 3.0254

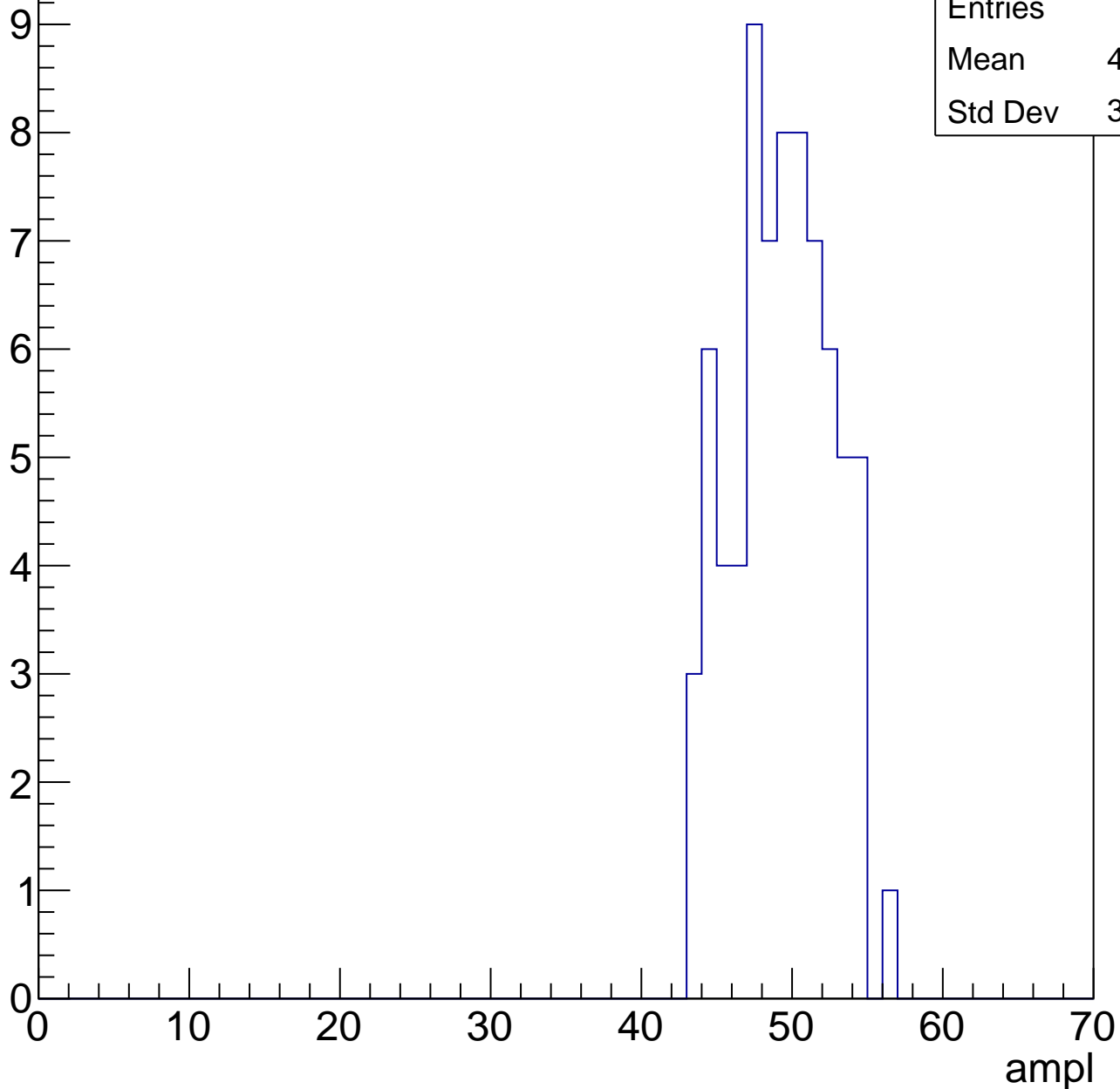


B0L002S, U4-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

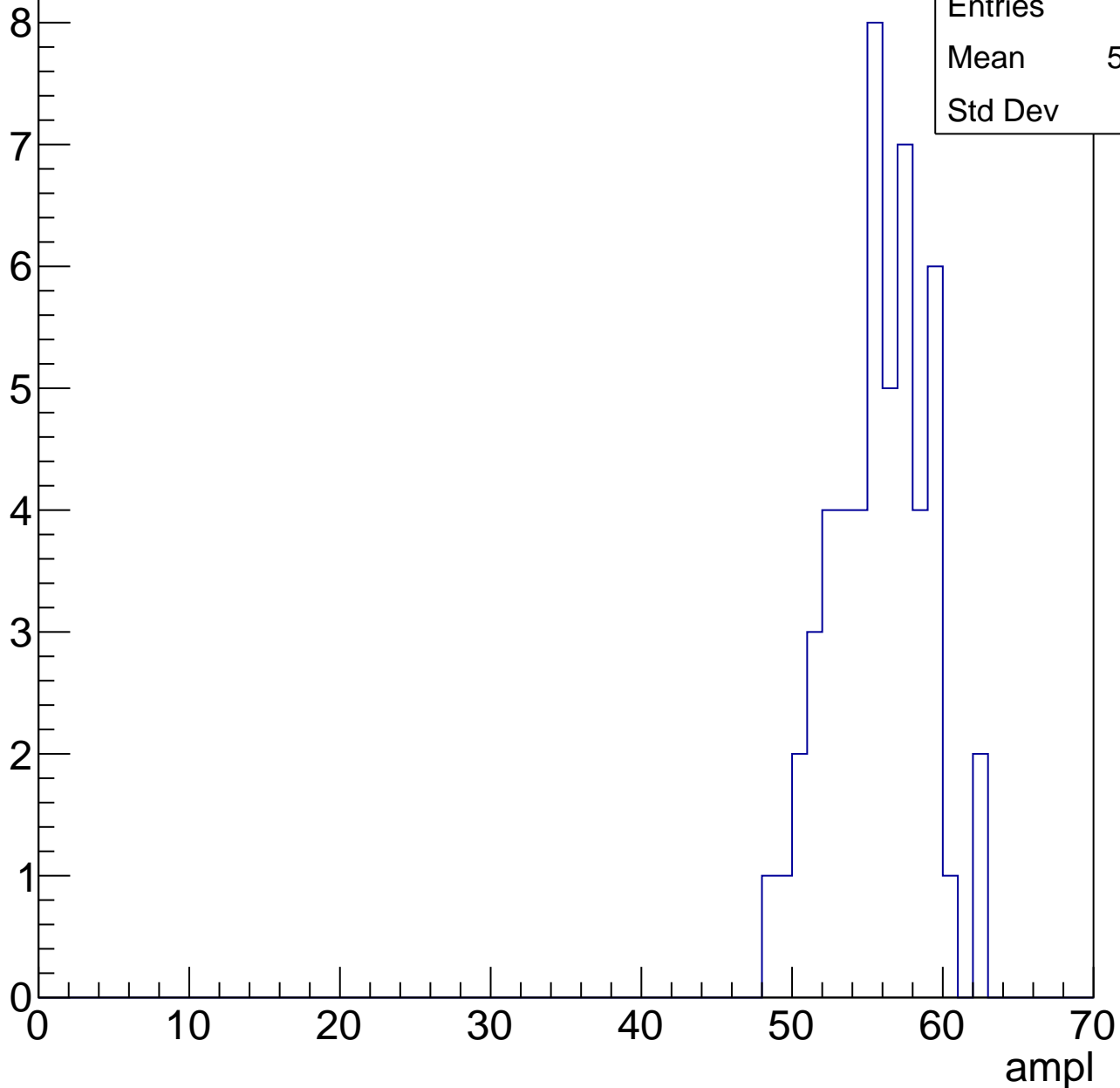
Entries	73
Mean	48.88
Std Dev	3.192



B0L002S, U4-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

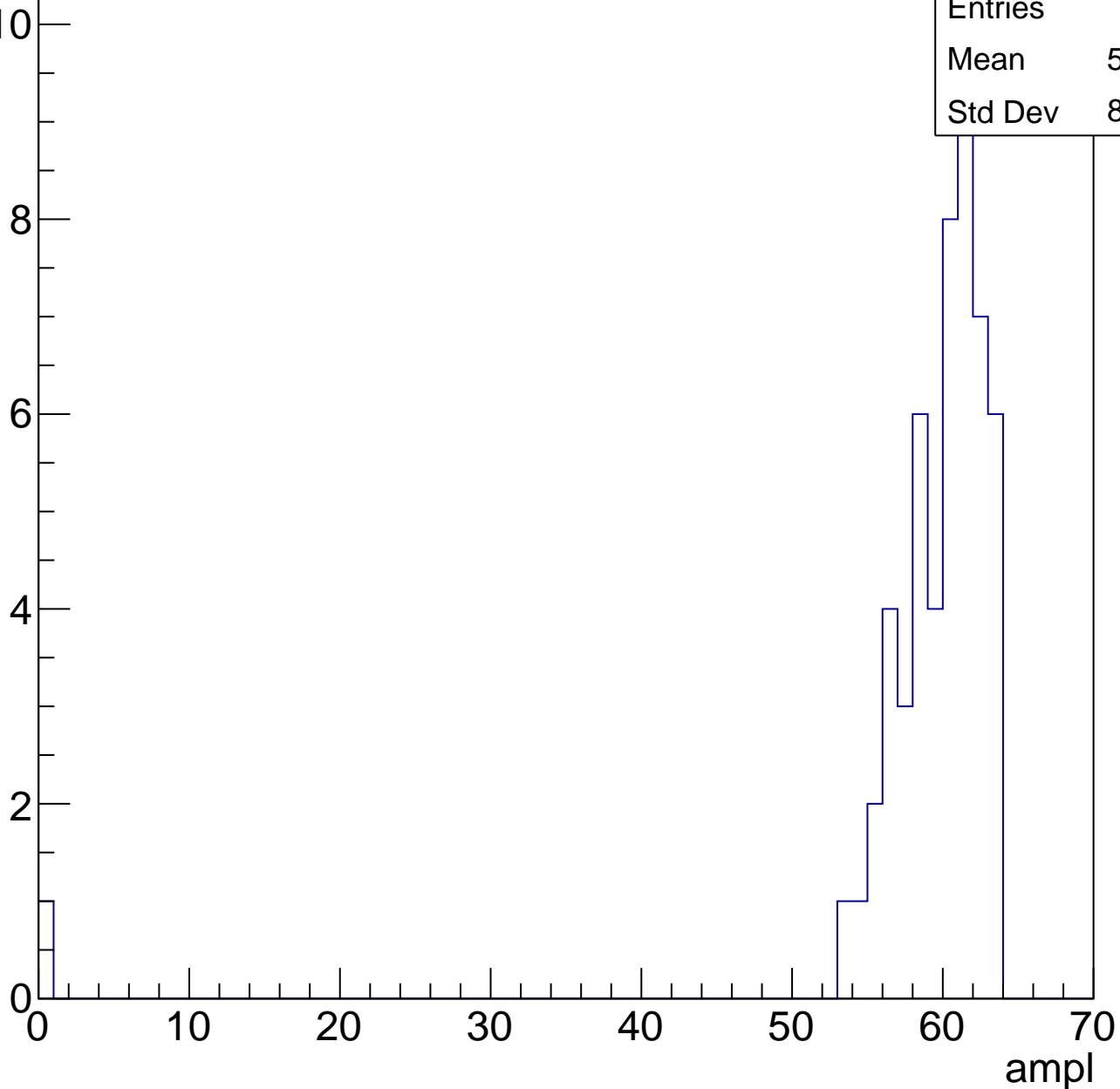


B0L002S, U4-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	58.45
Std Dev	8.489



B0L002S, U4-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	43
Std Dev	20

ampl

B0L002S, U4-ch114, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	30.82
Std Dev	4.956

Gaus mean : 31.0161

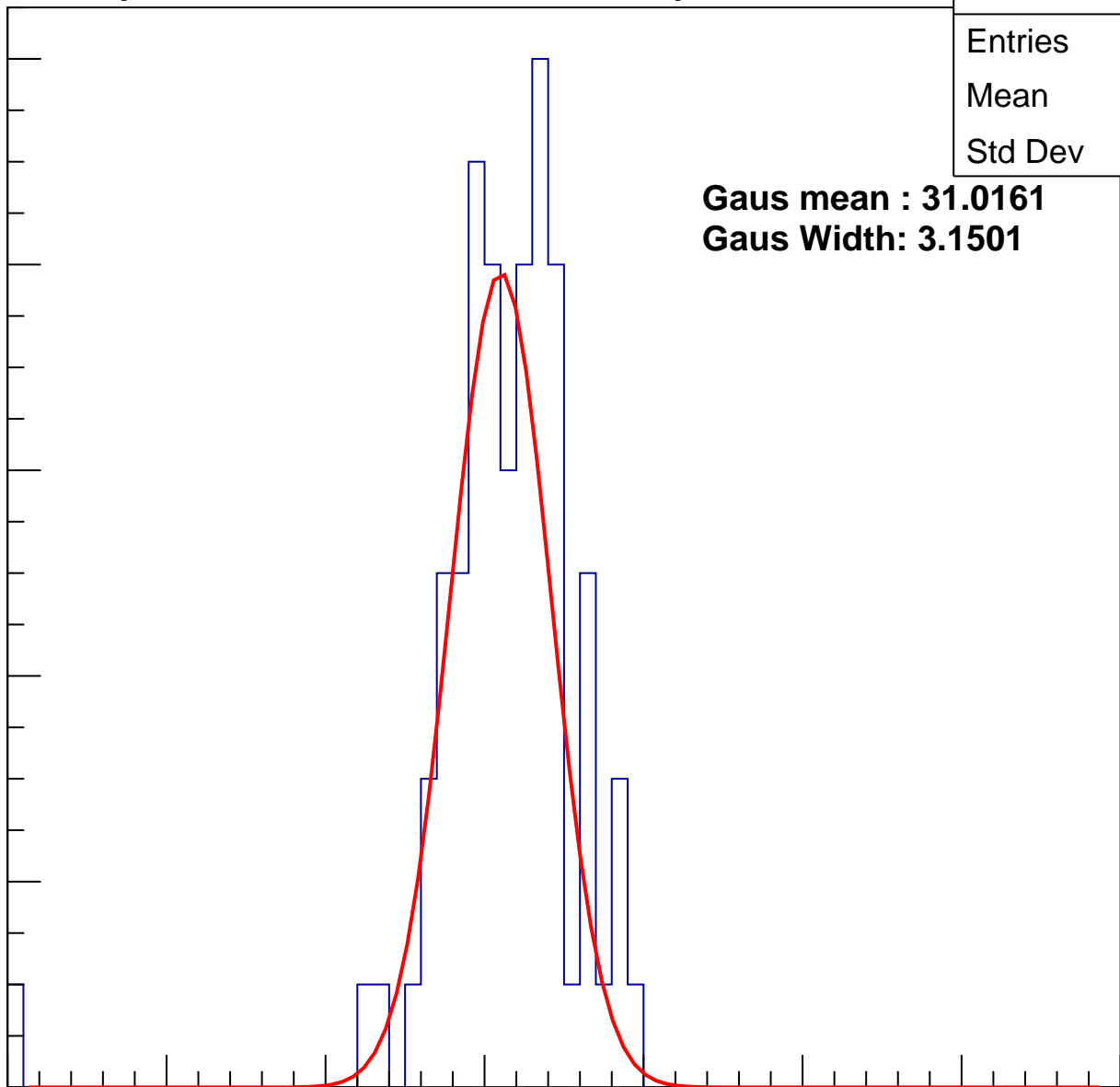
Gaus Width: 3.1501

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch114, adc1

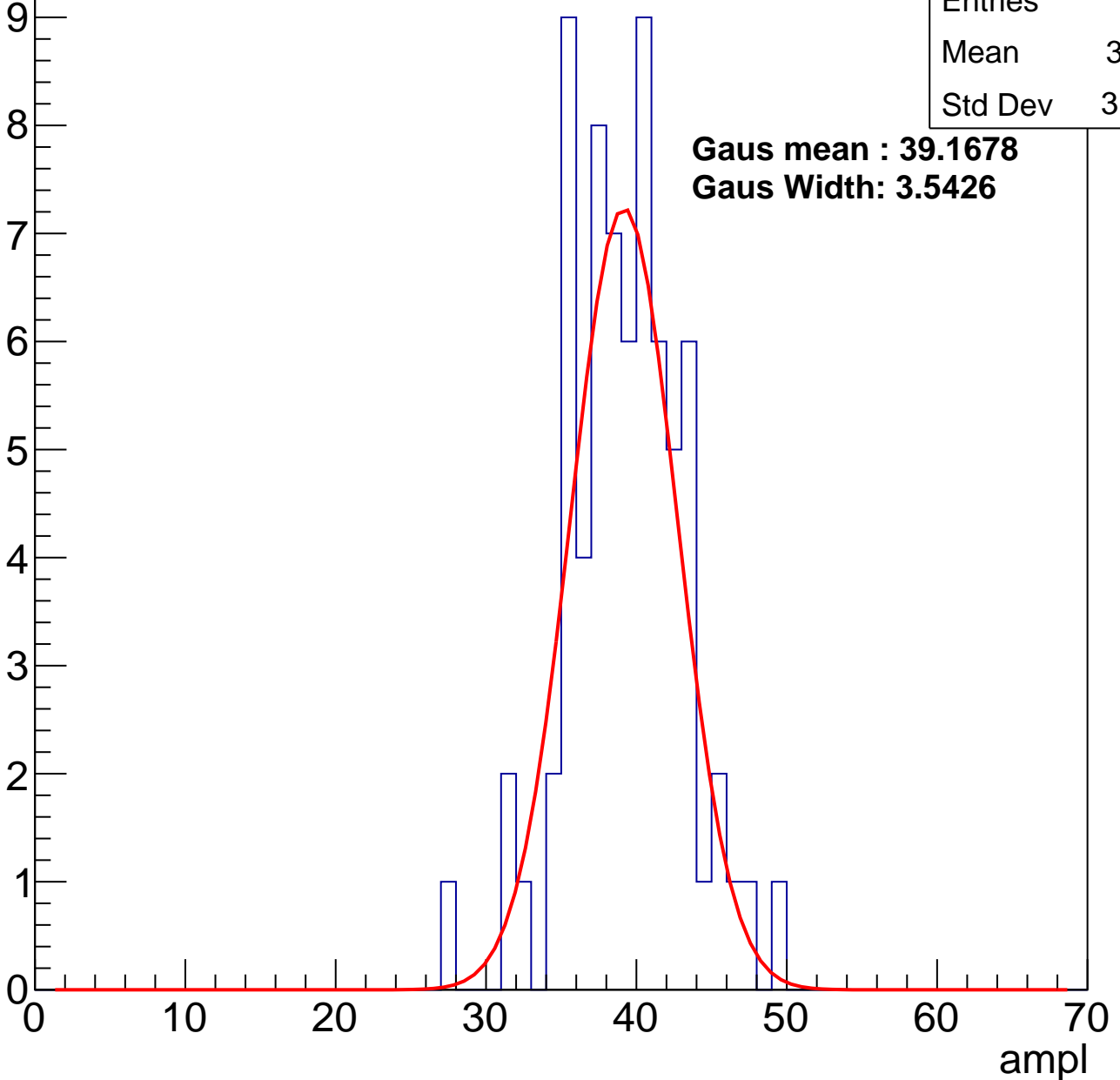
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	38.81
Std Dev	3.857

Gaus mean : 39.1678

Gaus Width: 3.5426



B0L002S, U4-ch114, adc2

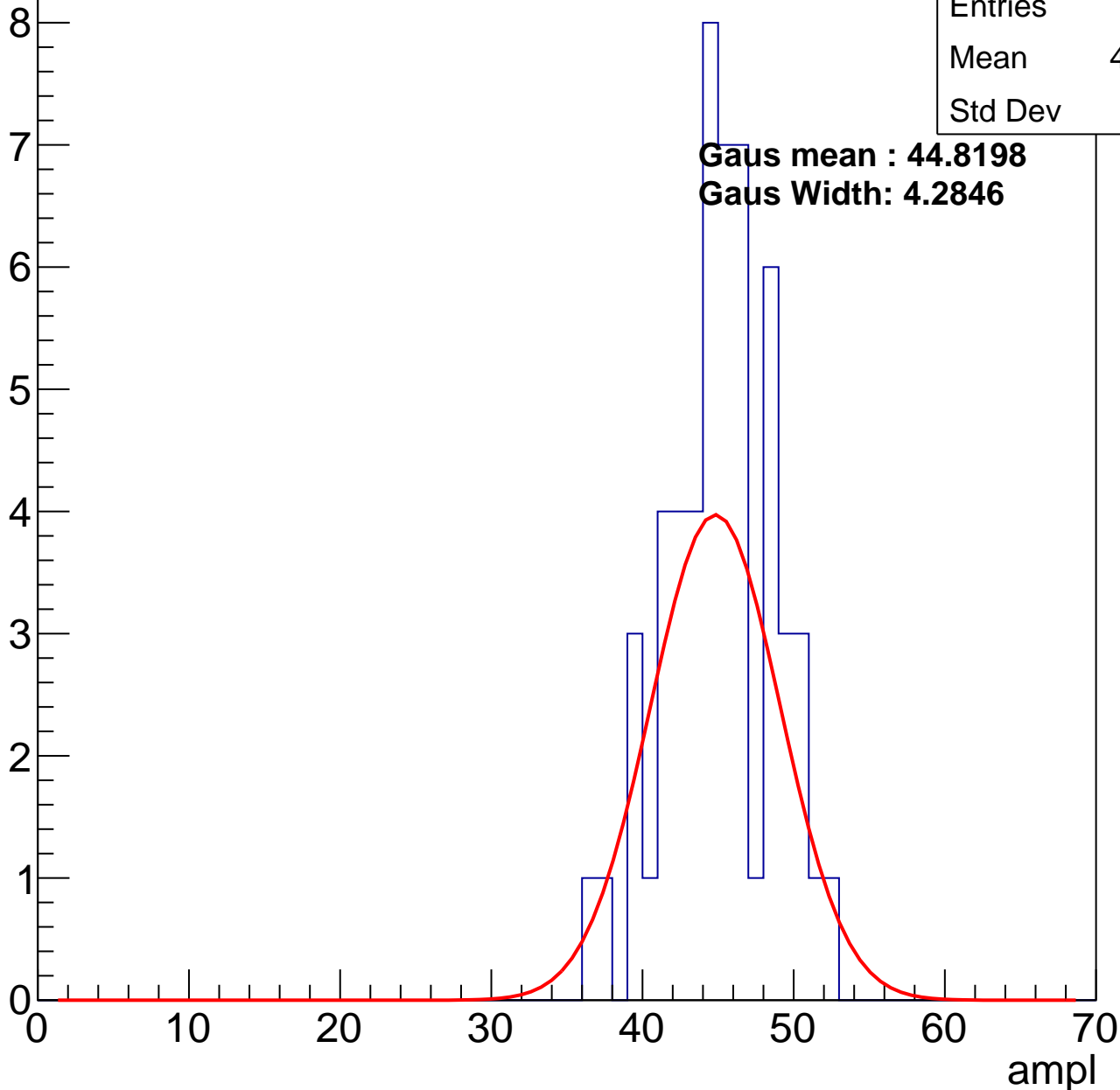
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.69
Std Dev	3.49

Gaus mean : 44.8198

Gaus Width: 4.2846

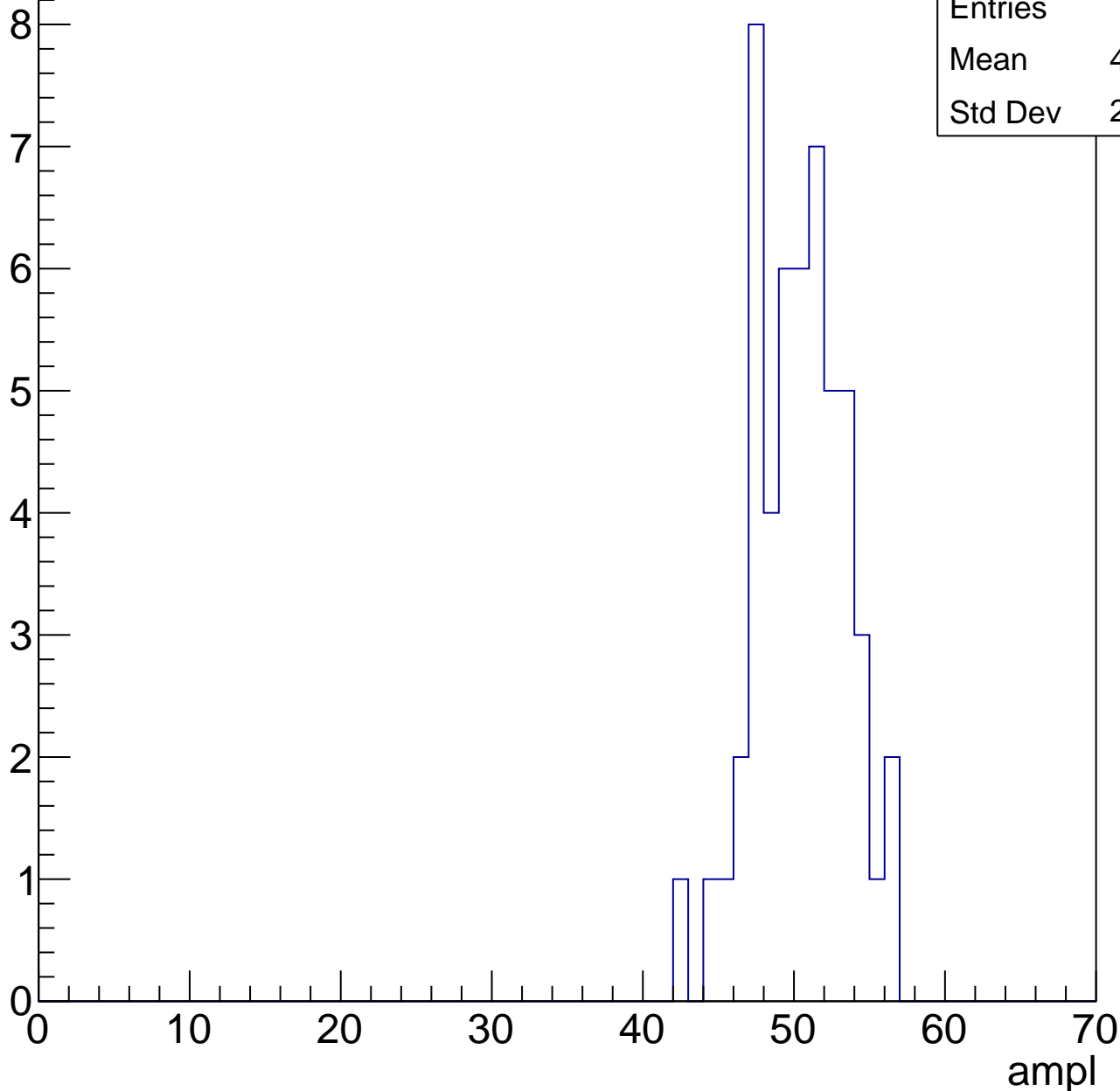


B0L002S, U4-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

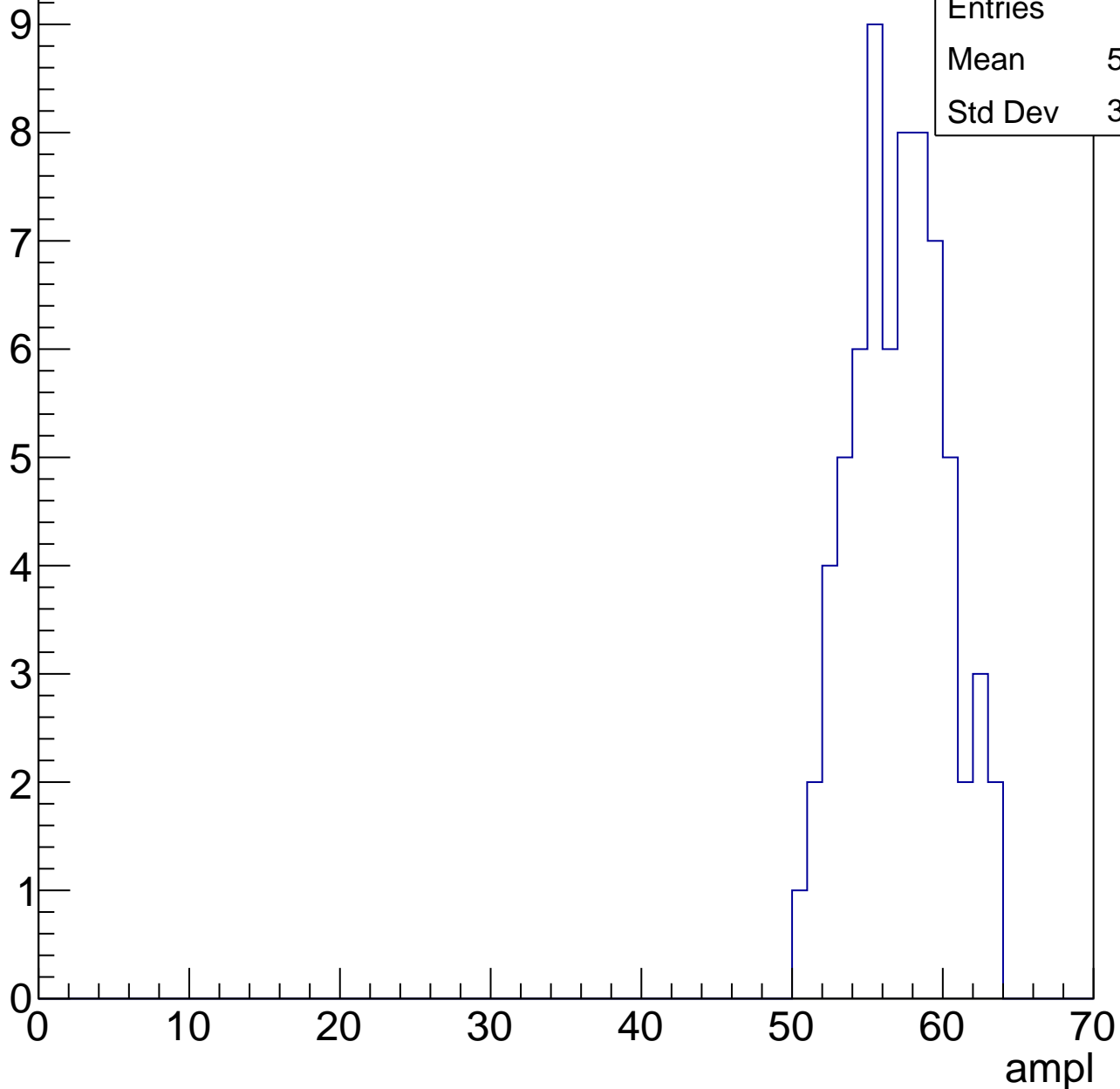
Entries	52
Mean	49.92
Std Dev	2.999



B0L002S, U4-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



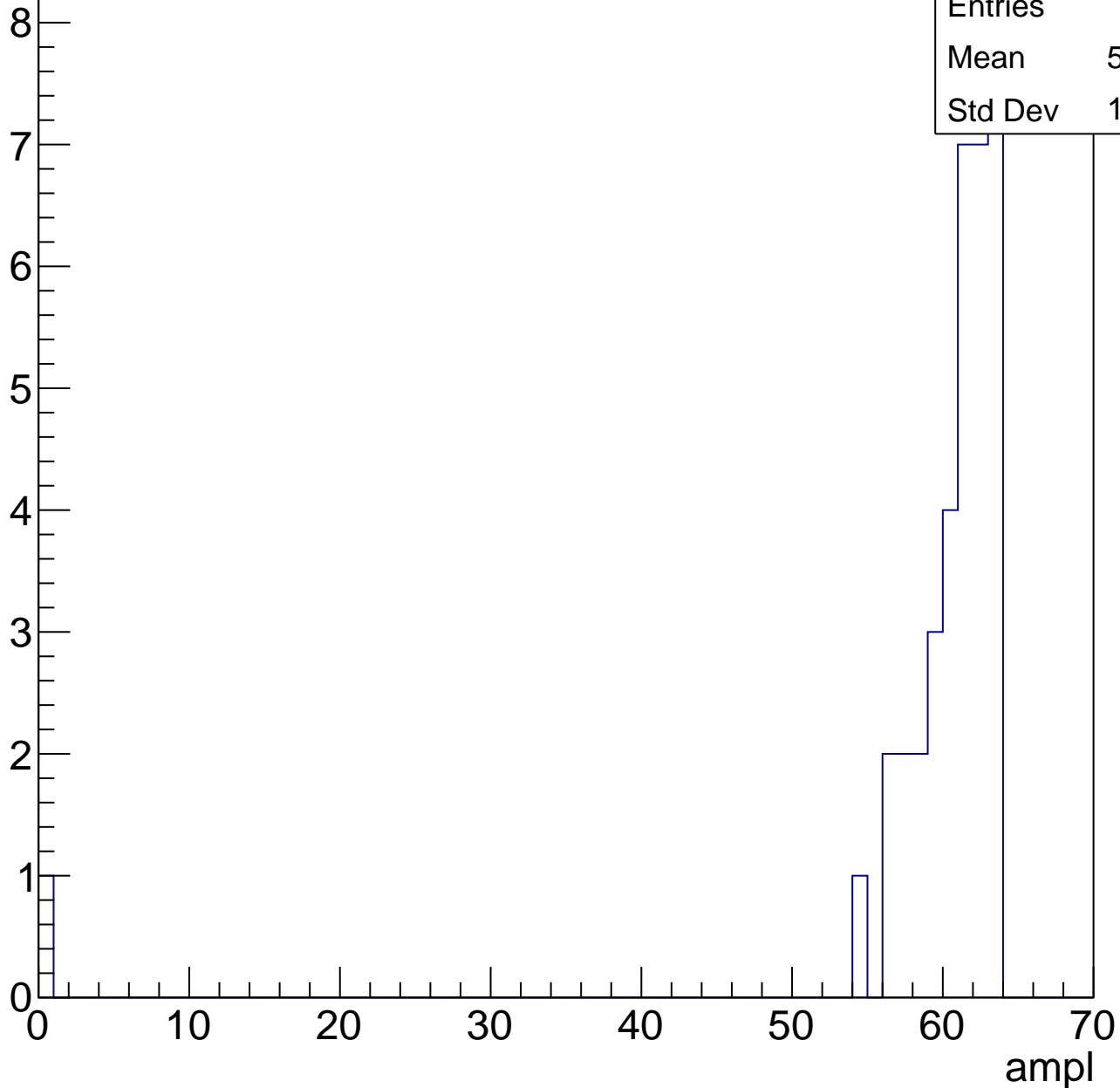
Entries	68
Mean	56.57
Std Dev	3.079

B0L002S, U4-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

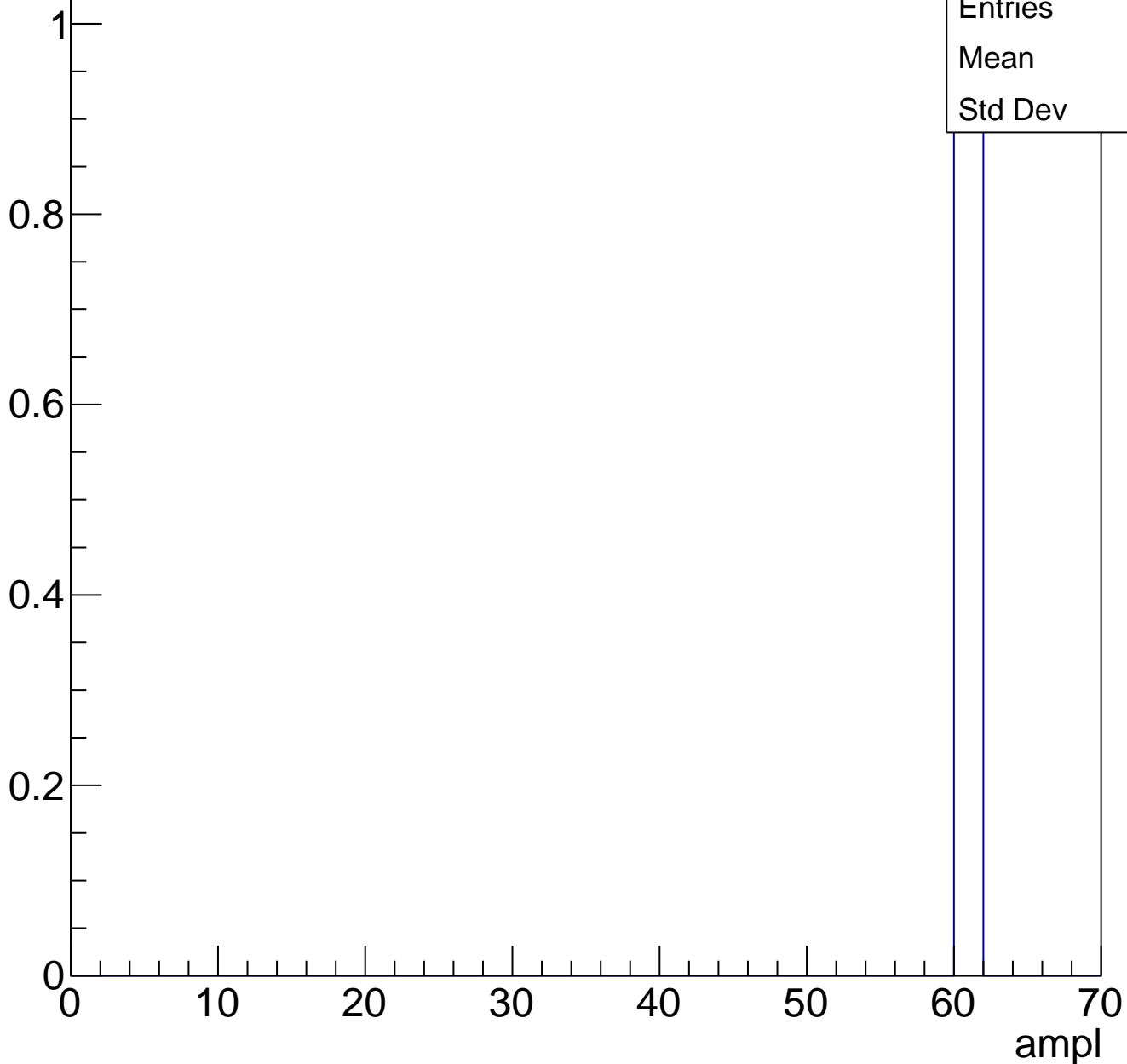
Entries	37
Mean	58.86
Std Dev	10.08



B0L002S, U4-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U4-ch115, adc0

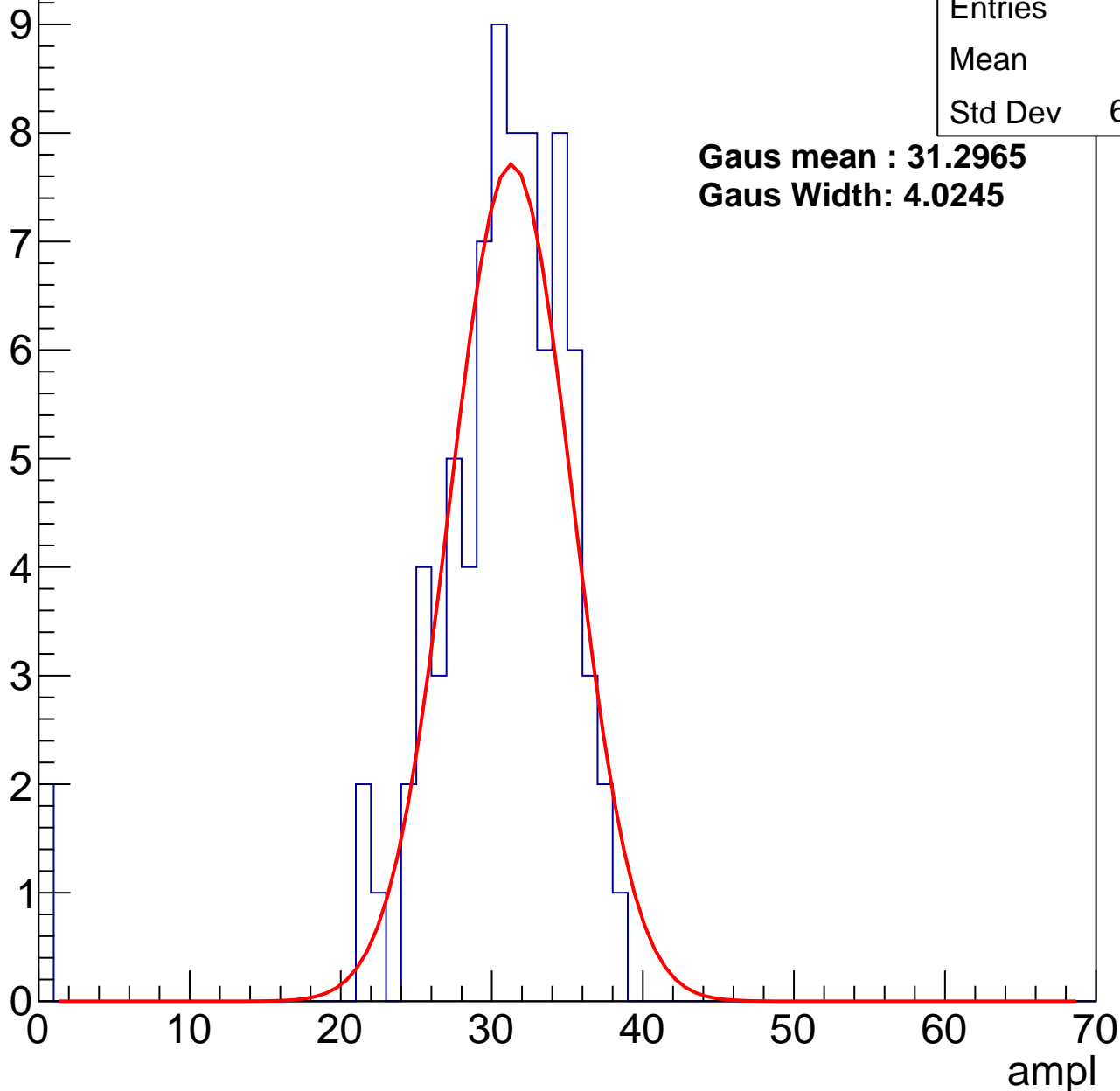
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	29.8
Std Dev	6.038

Gaus mean : 31.2965

Gaus Width: 4.0245



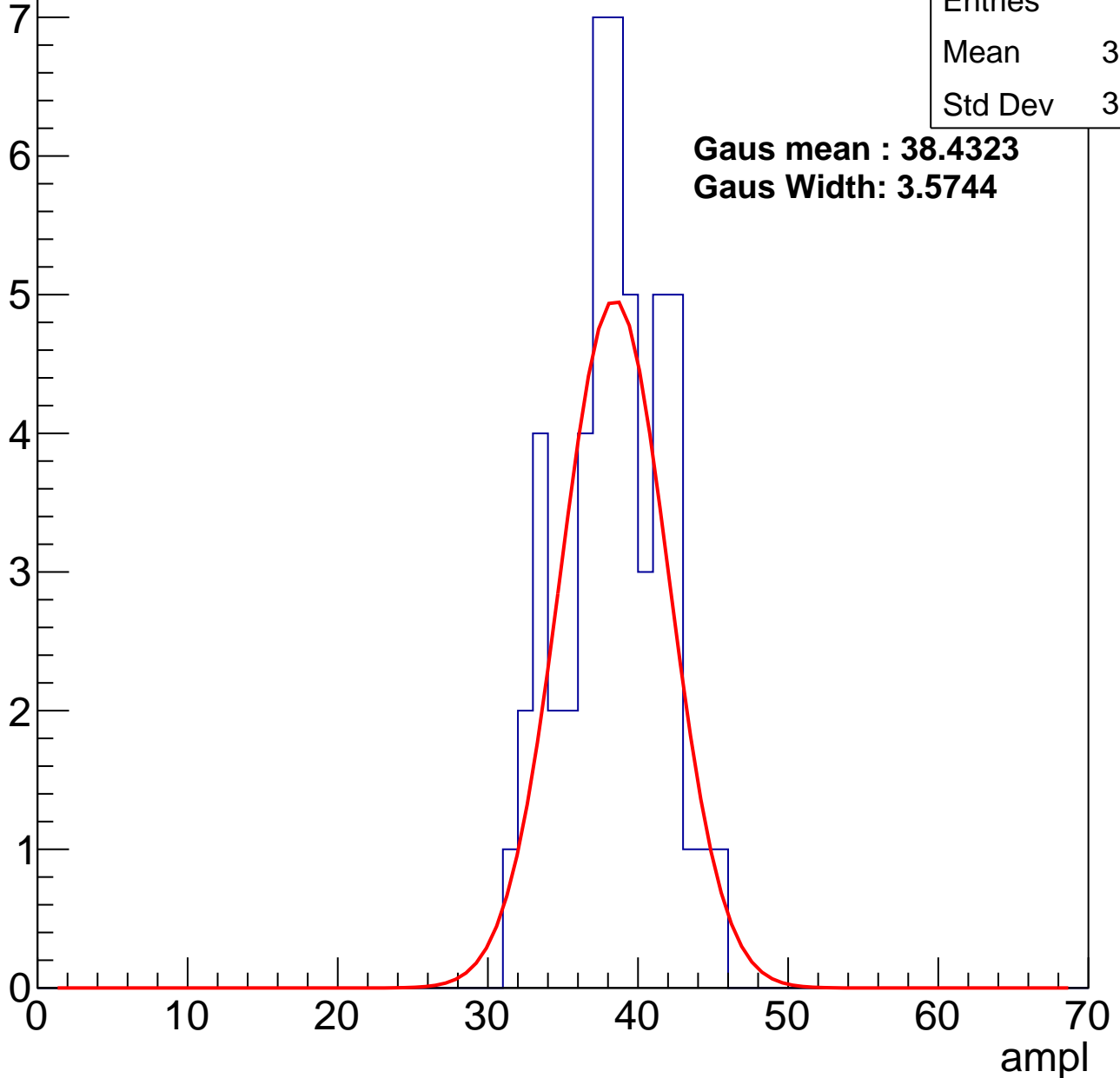
B0L002S, U4-ch115, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	37.92
Std Dev	3.304

Gaus mean : 38.4323
Gaus Width: 3.5744



B0L002S, U4-ch115, adc2

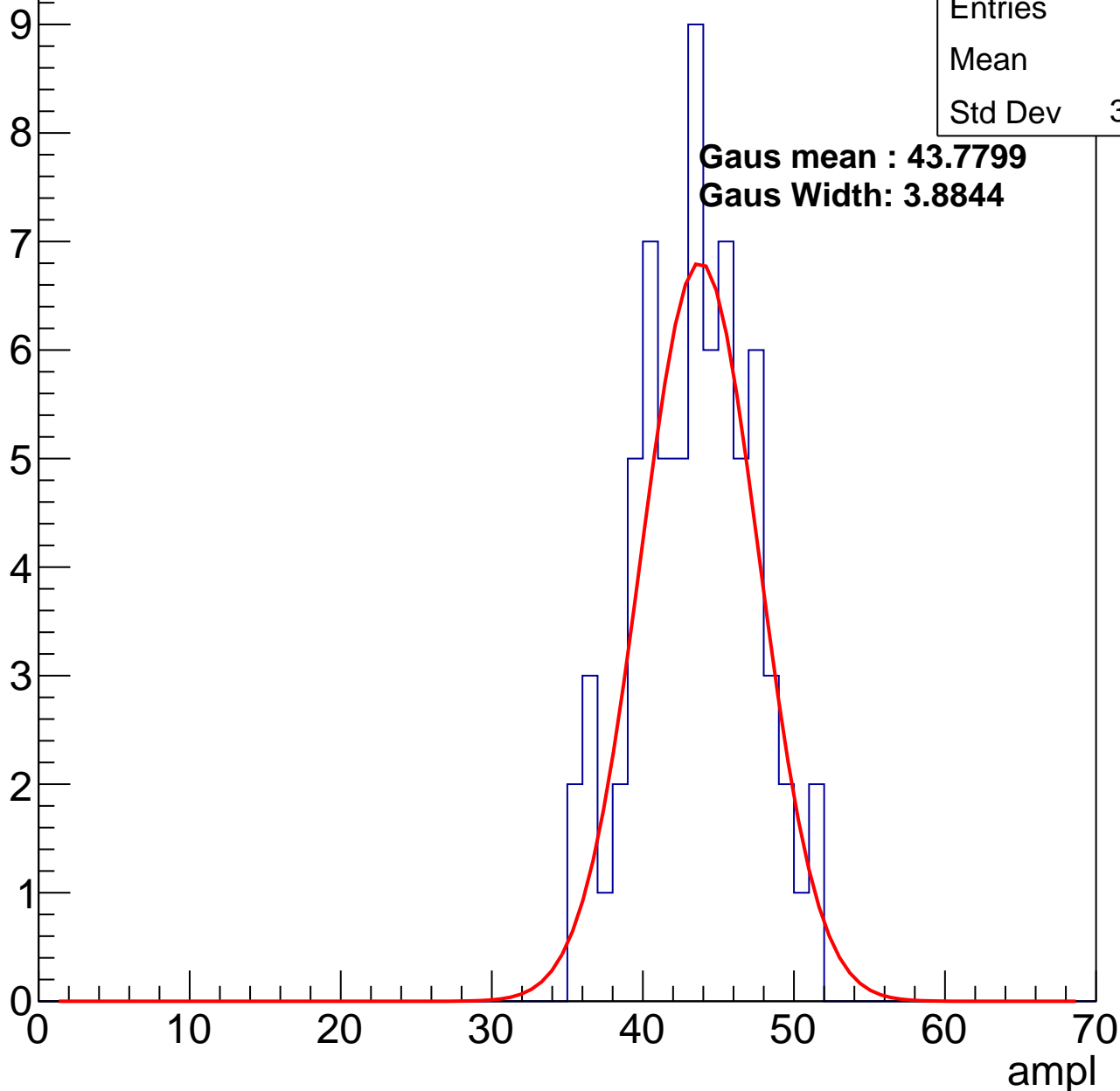
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43
Std Dev	3.805

Gaus mean : 43.7799

Gaus Width: 3.8844

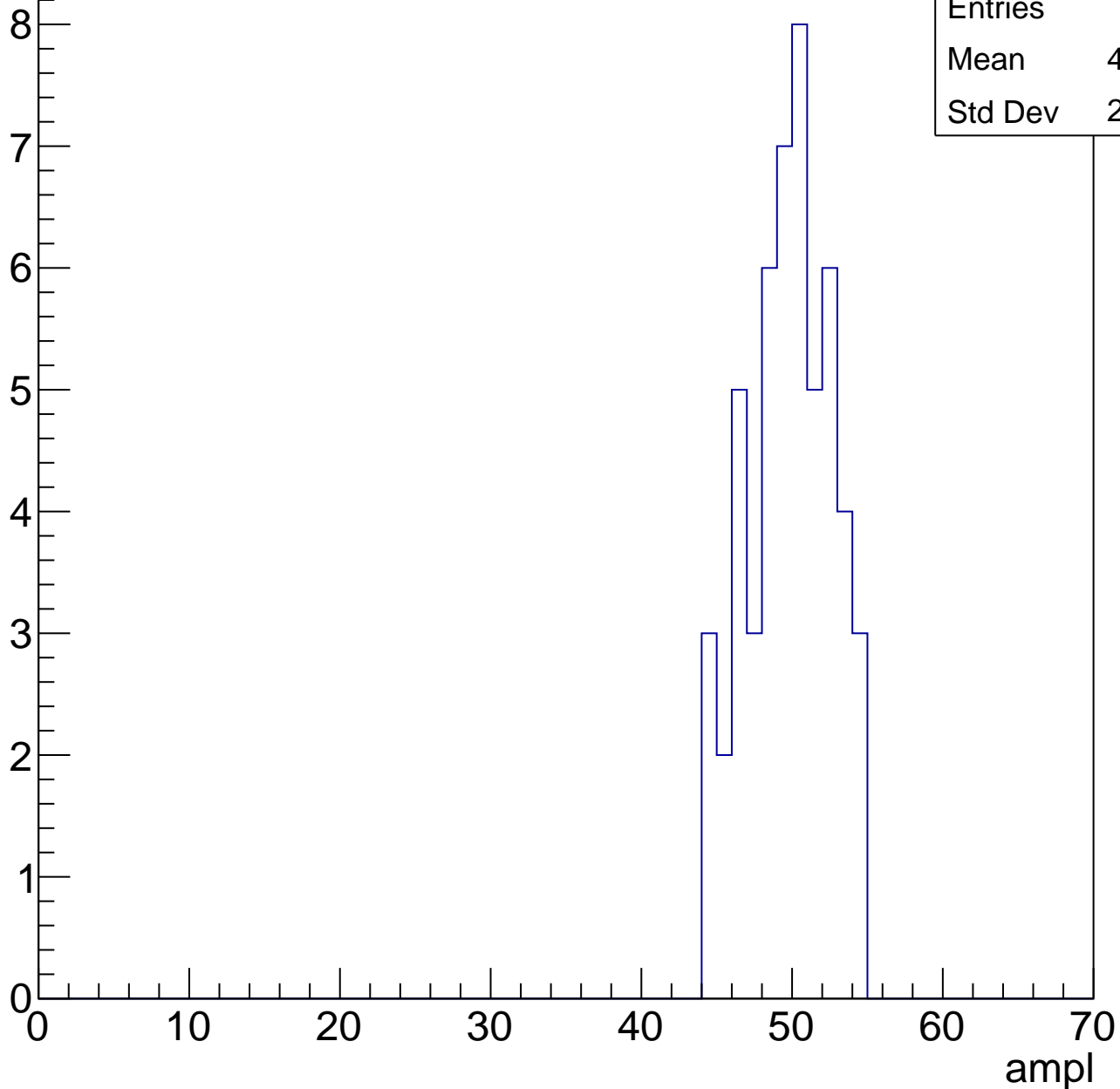


B0L002S, U4-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	49.33
Std Dev	2.723

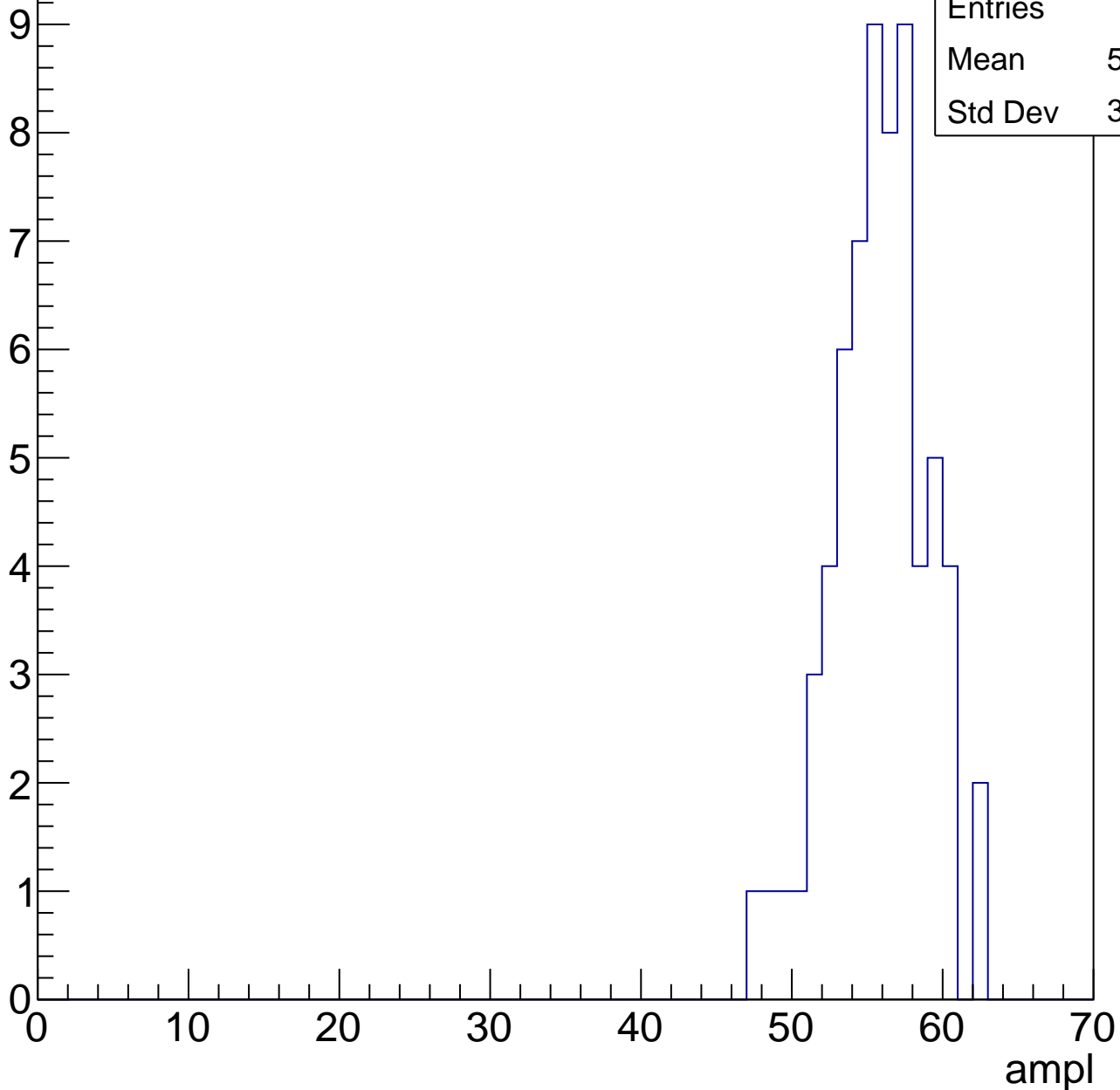


B0L002S, U4-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

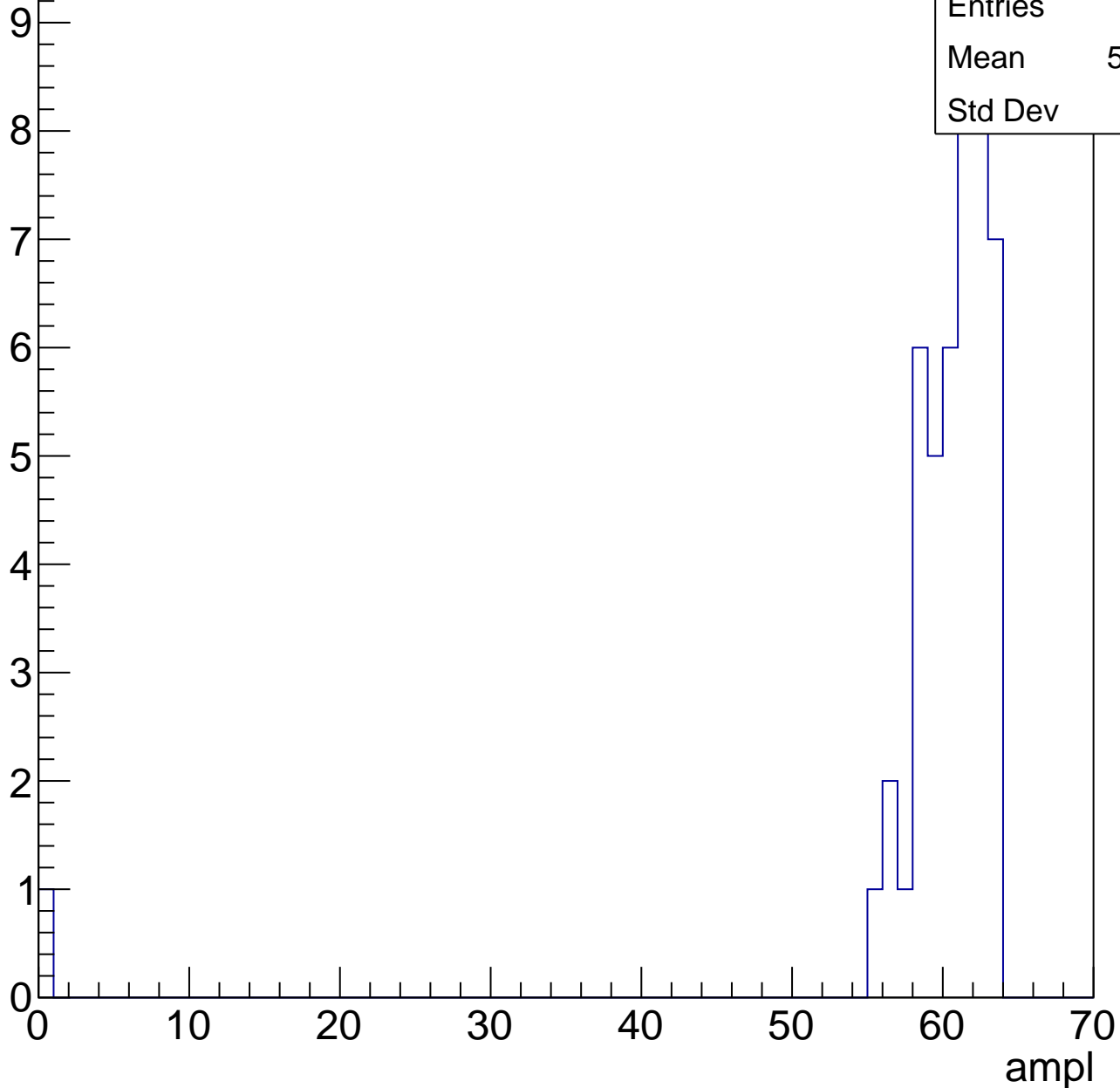
Entries	65
Mean	55.35
Std Dev	3.135



B0L002S, U4-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch116, adc0

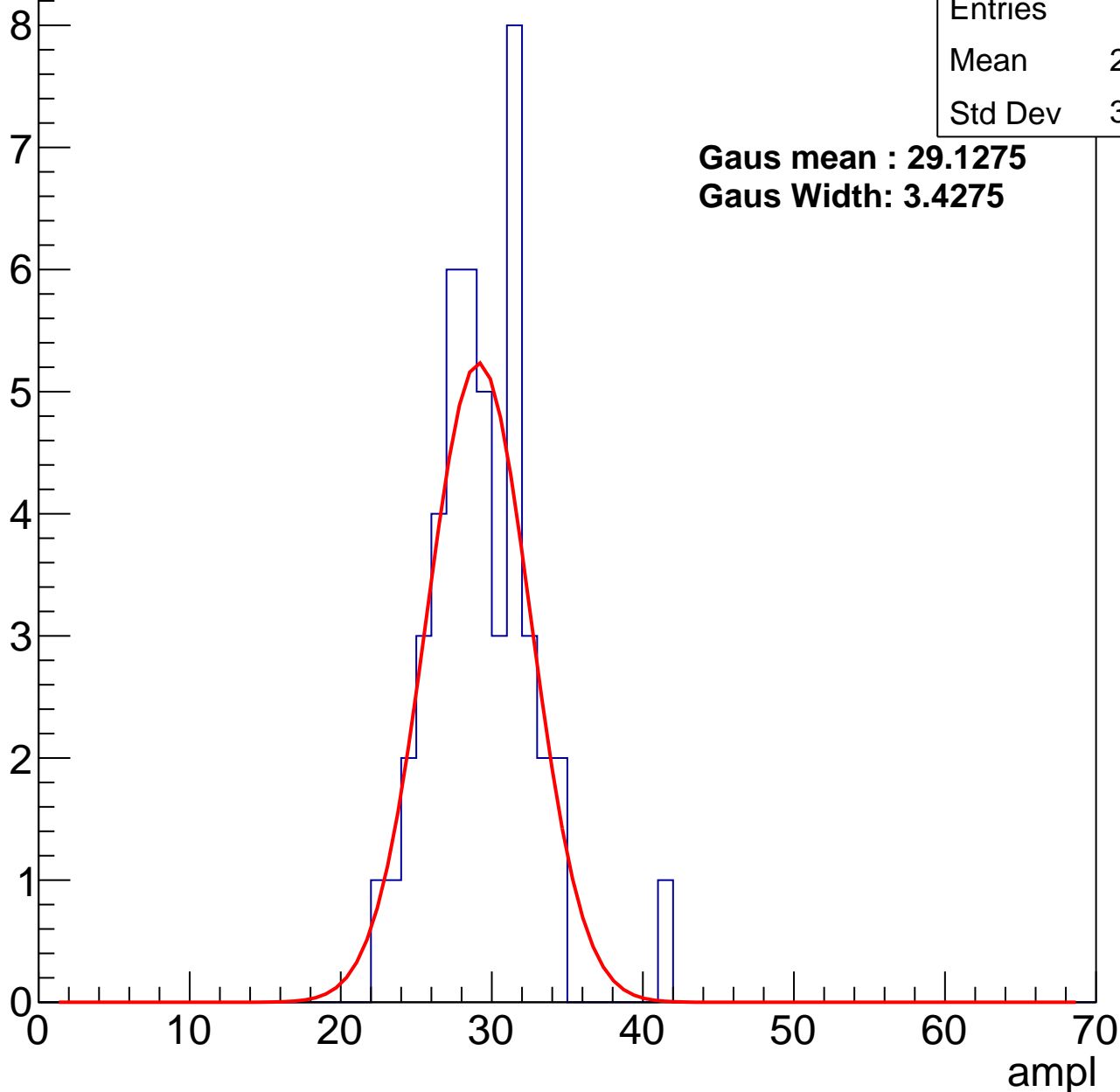
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	28.85
Std Dev	3.377

Gaus mean : 29.1275

Gaus Width: 3.4275

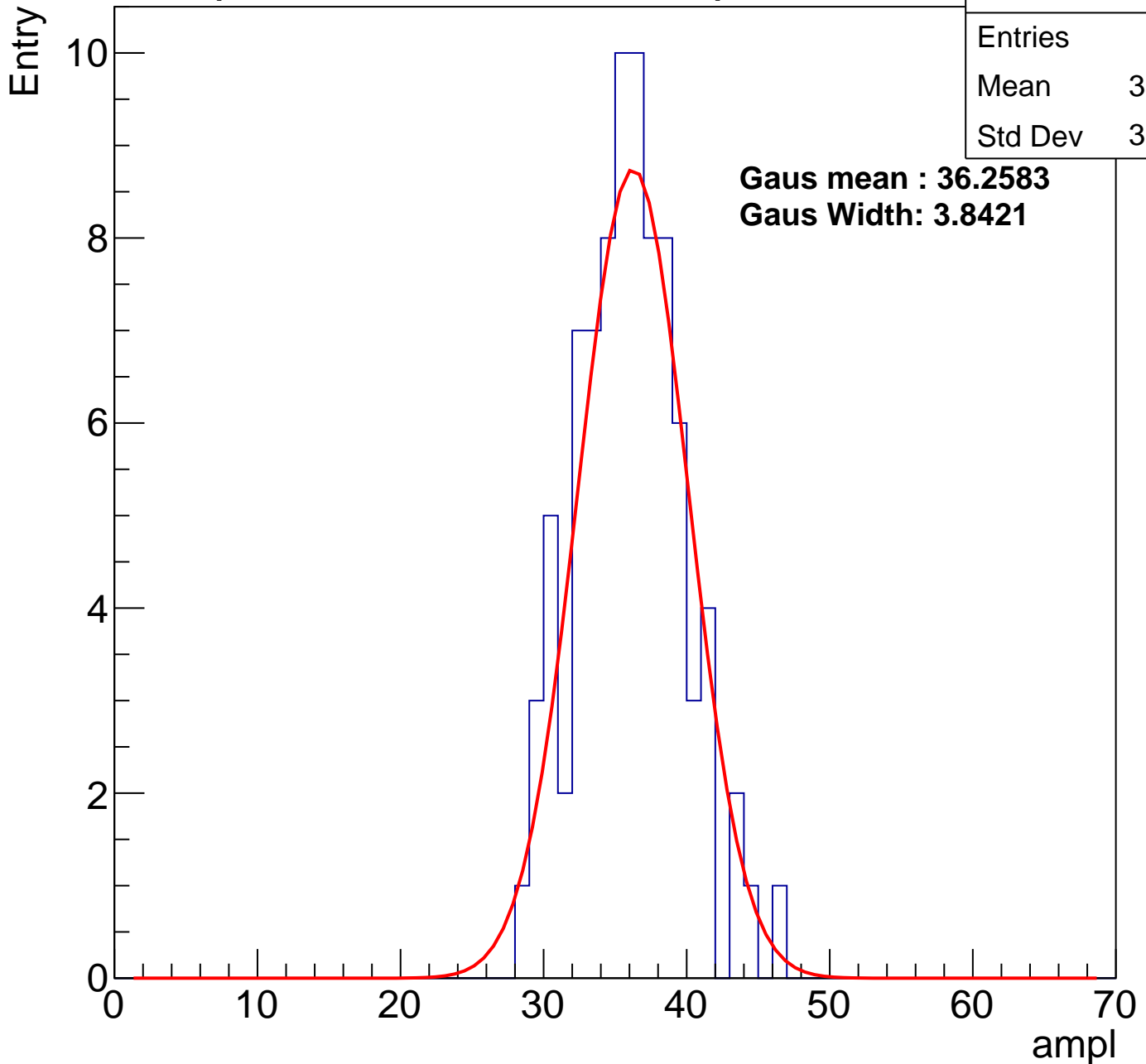


B0L002S, U4-ch116, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	86
Mean	35.56
Std Dev	3.649

Gaus mean : 36.2583
Gaus Width: 3.8421



B0L002S, U4-ch116, adc2

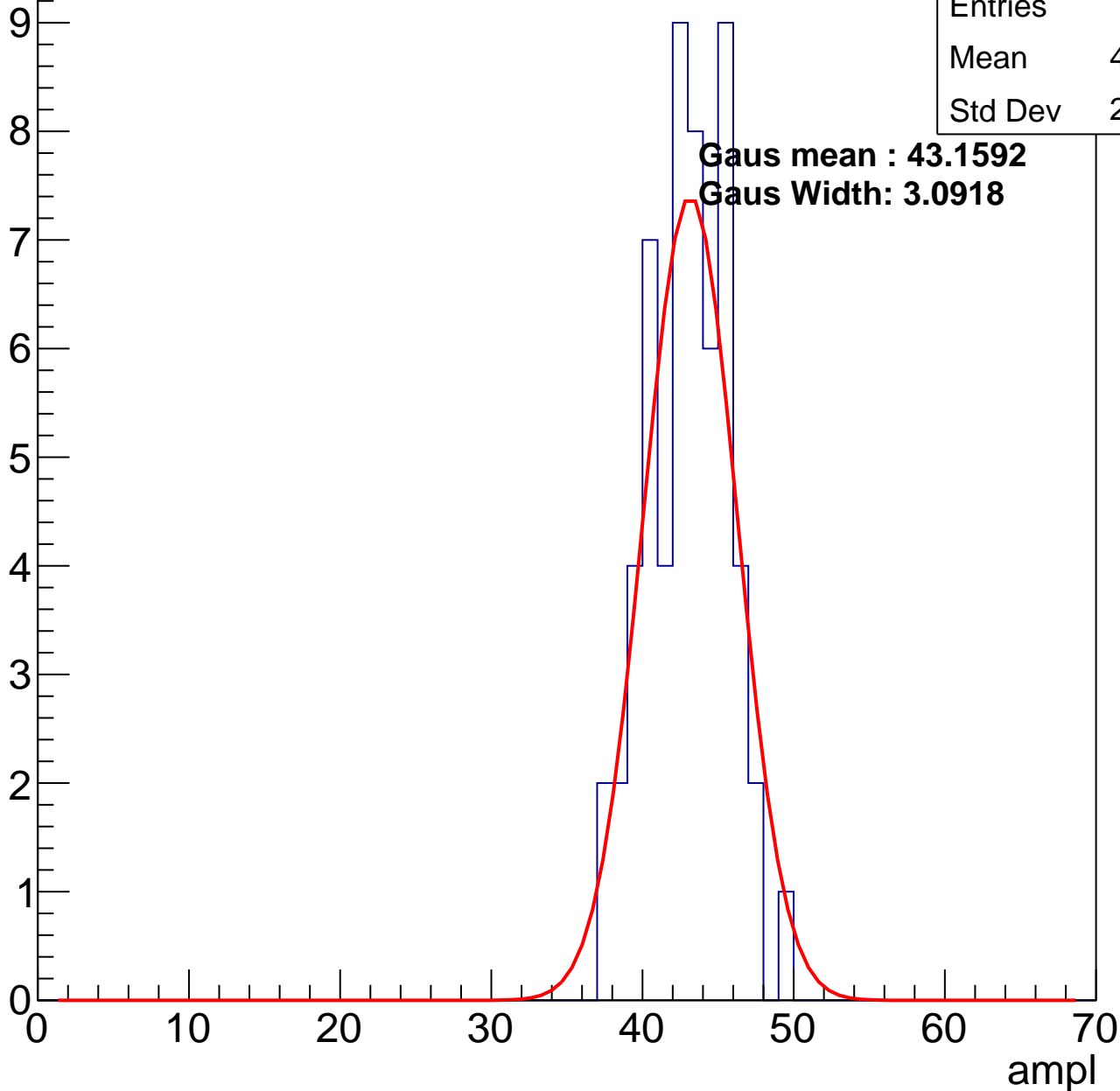
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	42.55
Std Dev	2.653

Gaus mean : 43.1592

Gaus Width: 3.0918

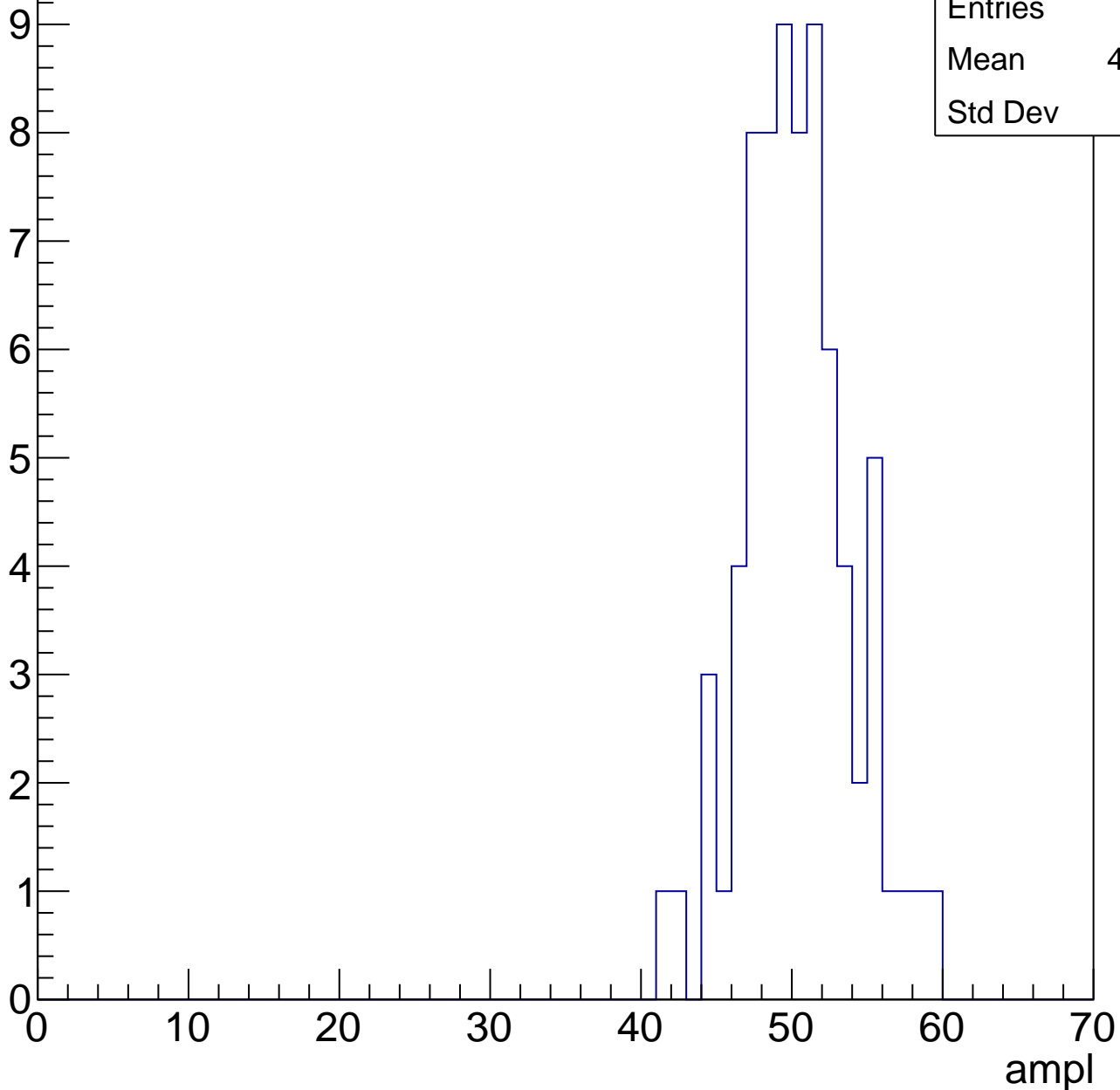


B0L002S, U4-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.88
Std Dev	3.53

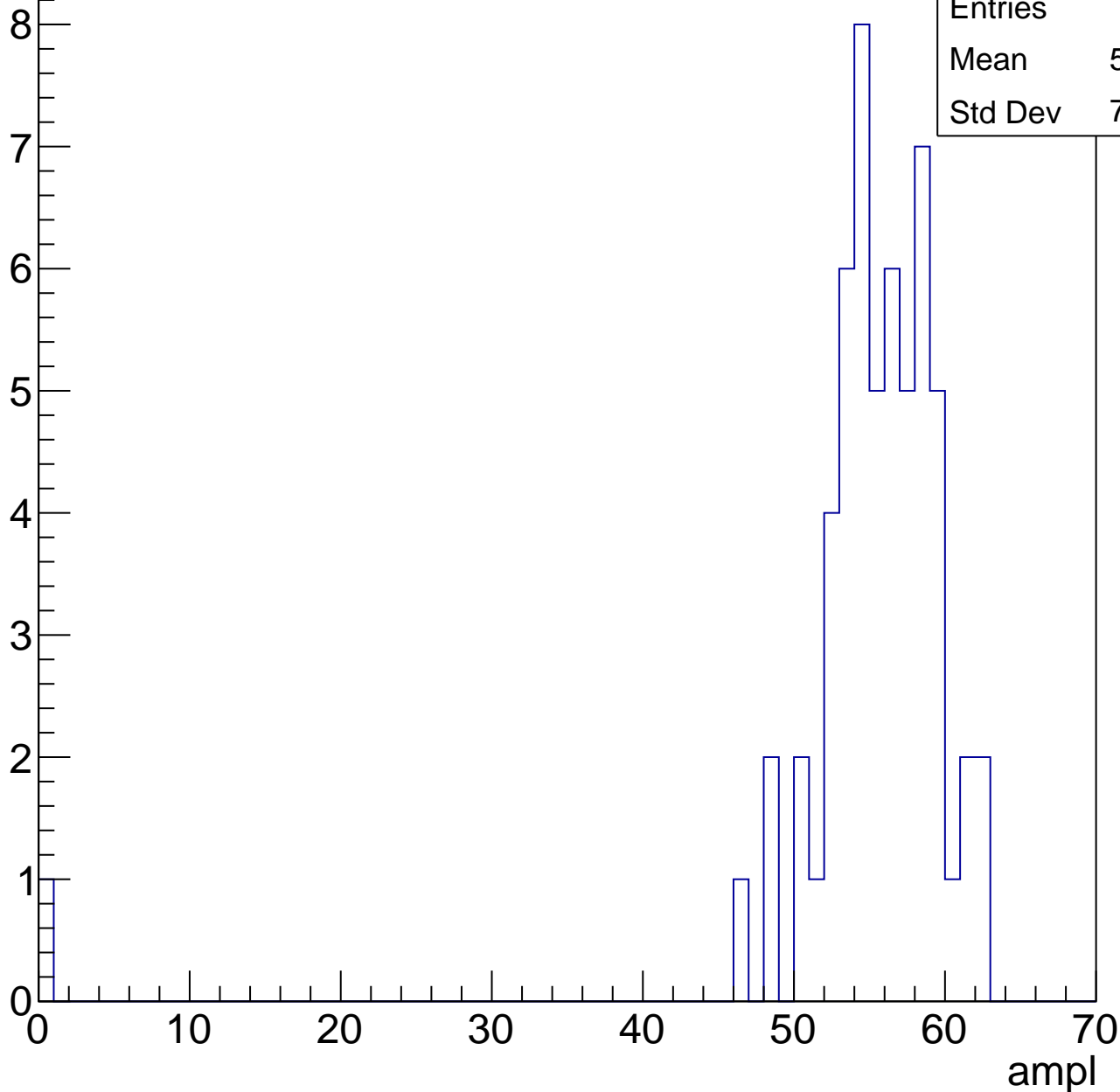


B0L002S, U4-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

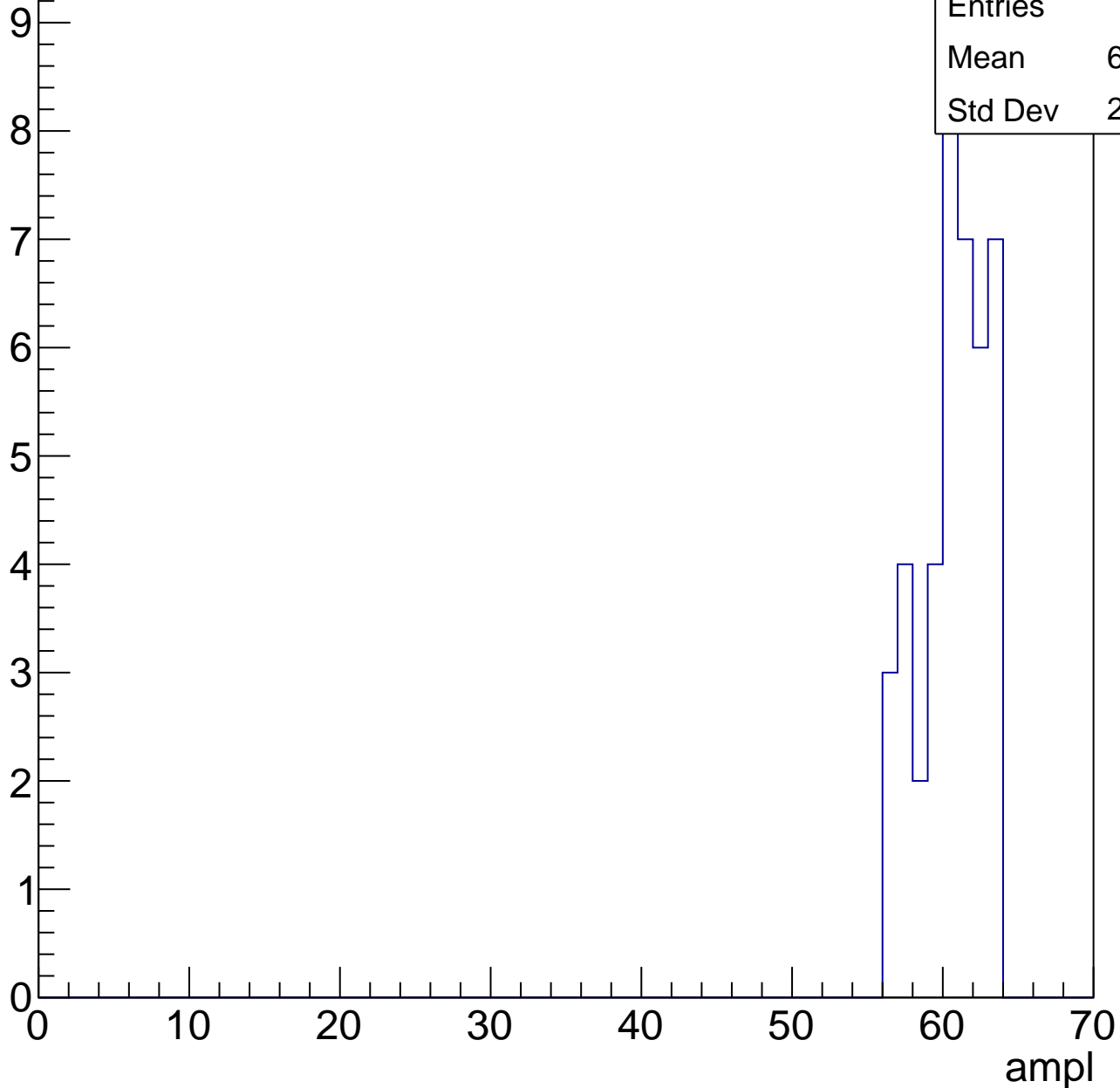
Entries	58
Mean	54.38
Std Dev	7.965



B0L002S, U4-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U4-ch117, adc0

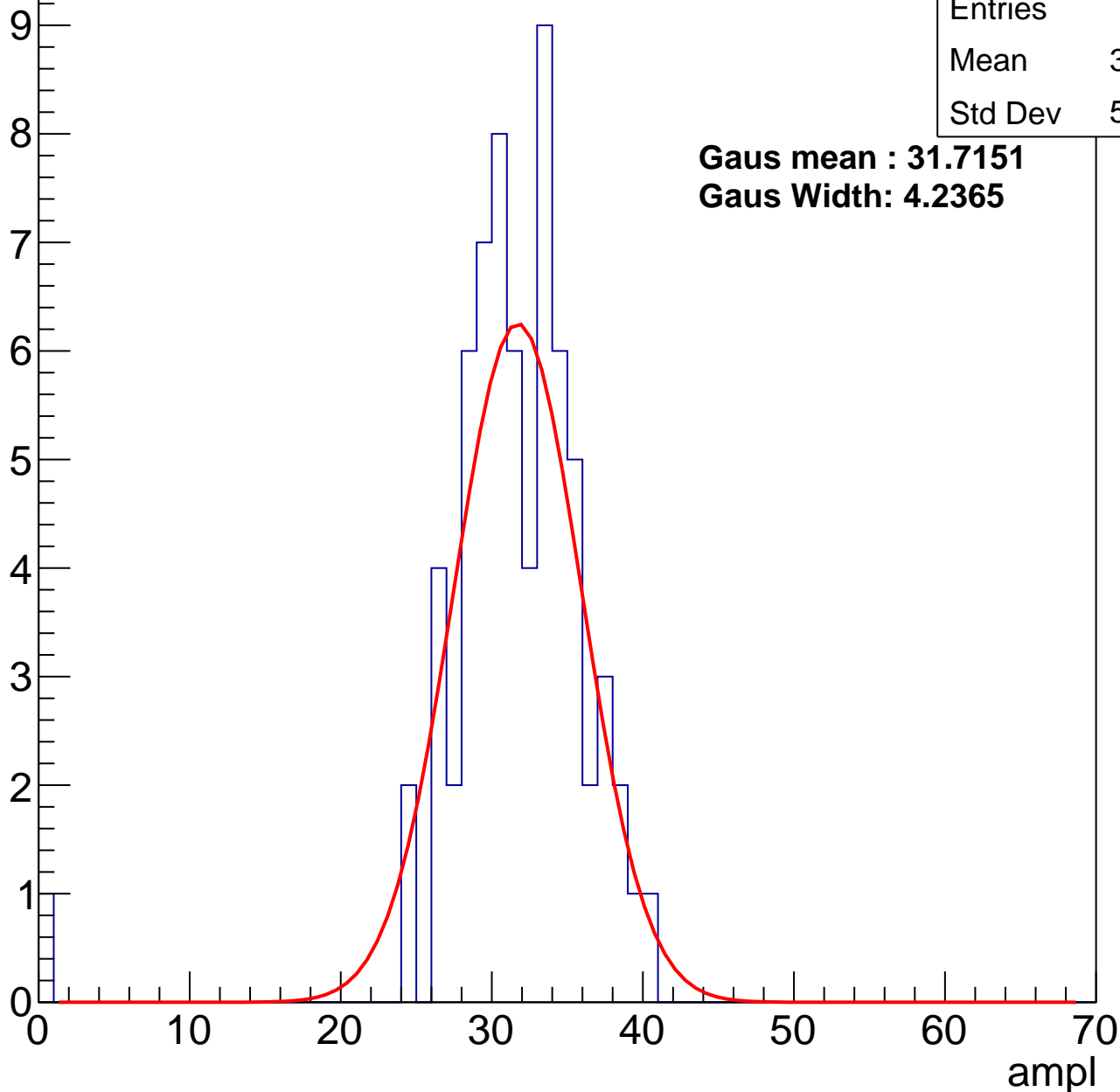
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	31.09
Std Dev	5.186

Gaus mean : 31.7151

Gaus Width: 4.2365



B0L002S, U4-ch117, adc1

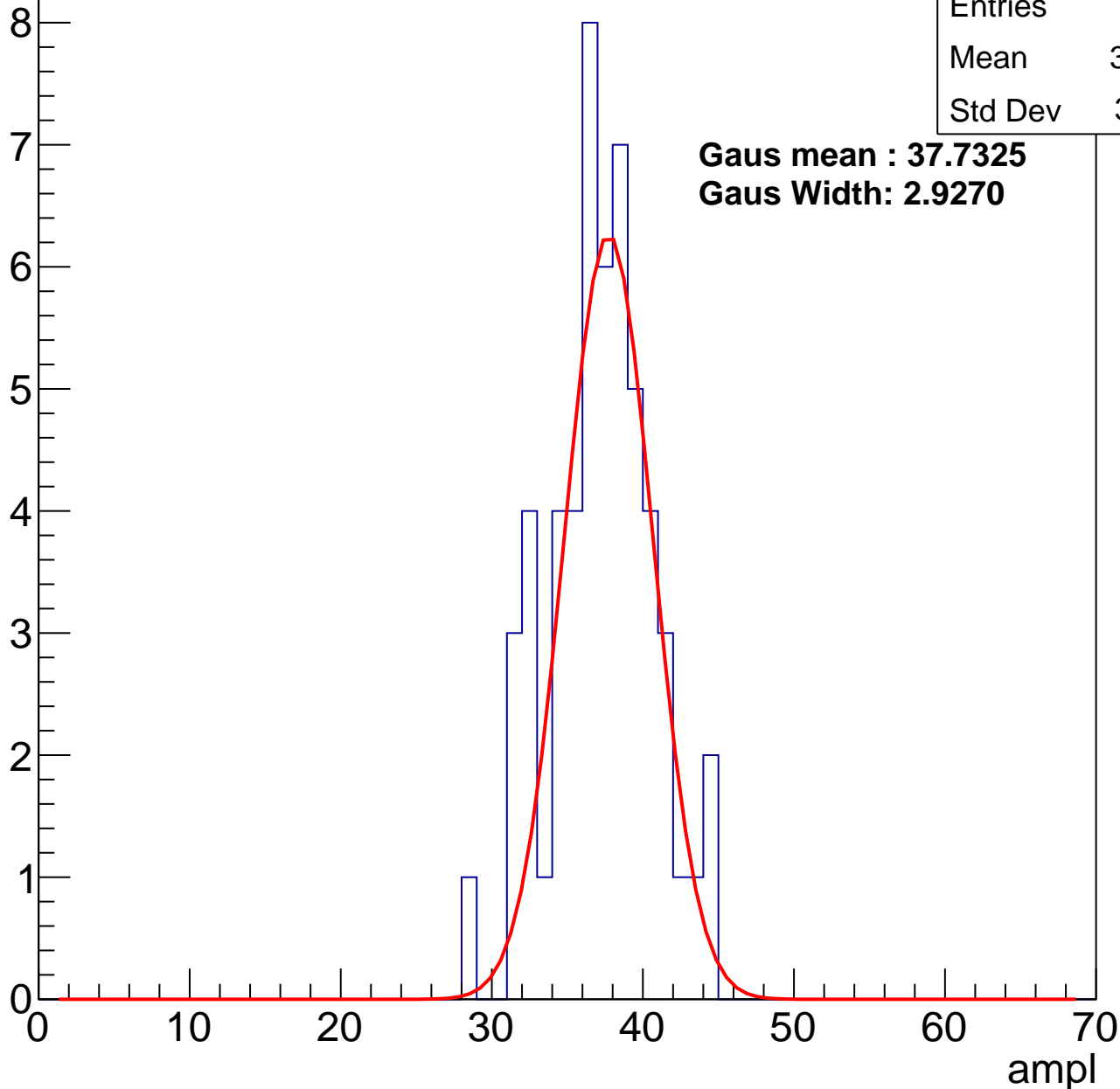
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	36.76
Std Dev	3.421

Gaus mean : 37.7325

Gaus Width: 2.9270



B0L002S, U4-ch117, adc2

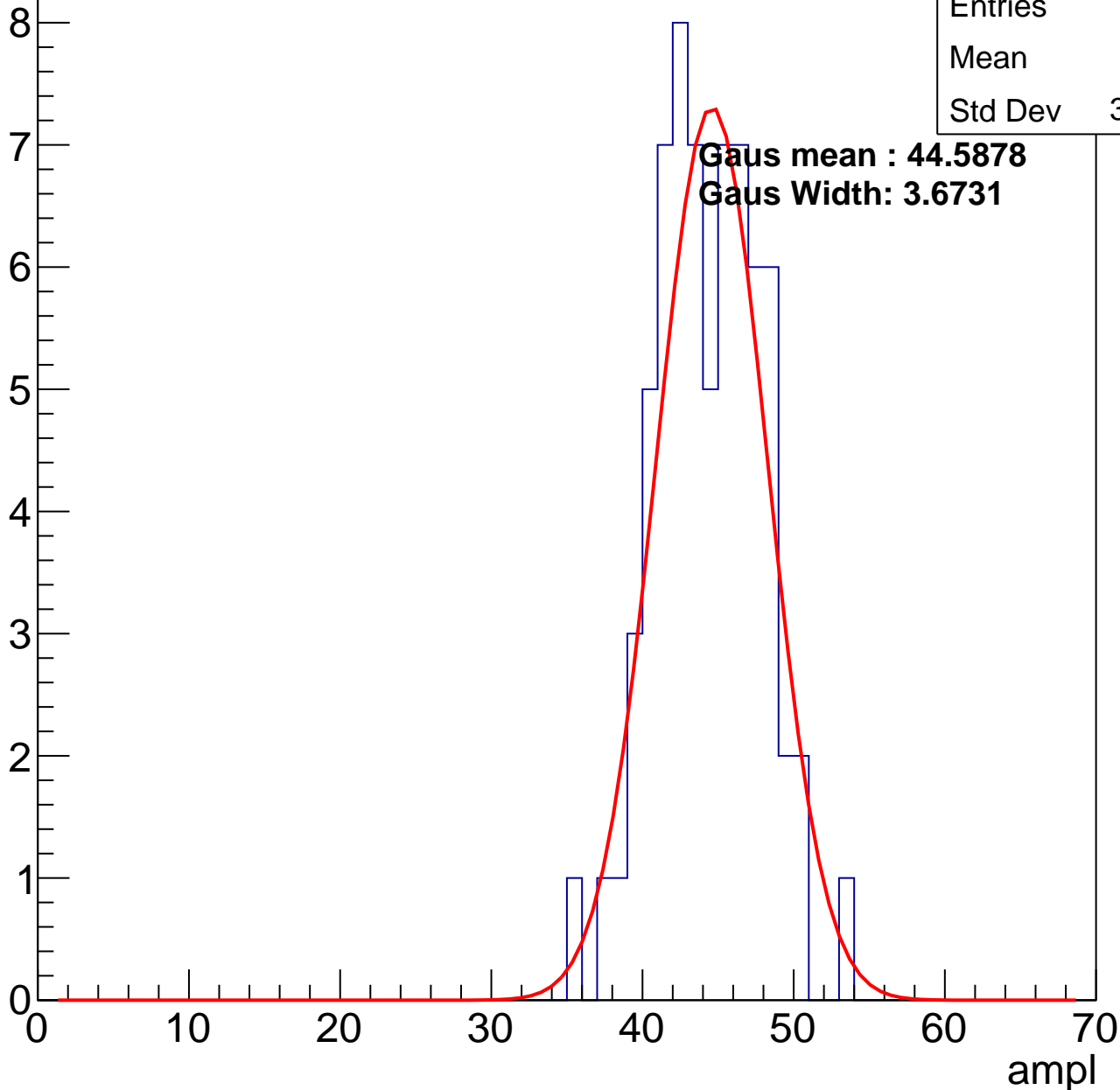
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.9
Std Dev	3.427

Gaus mean : 44.5878

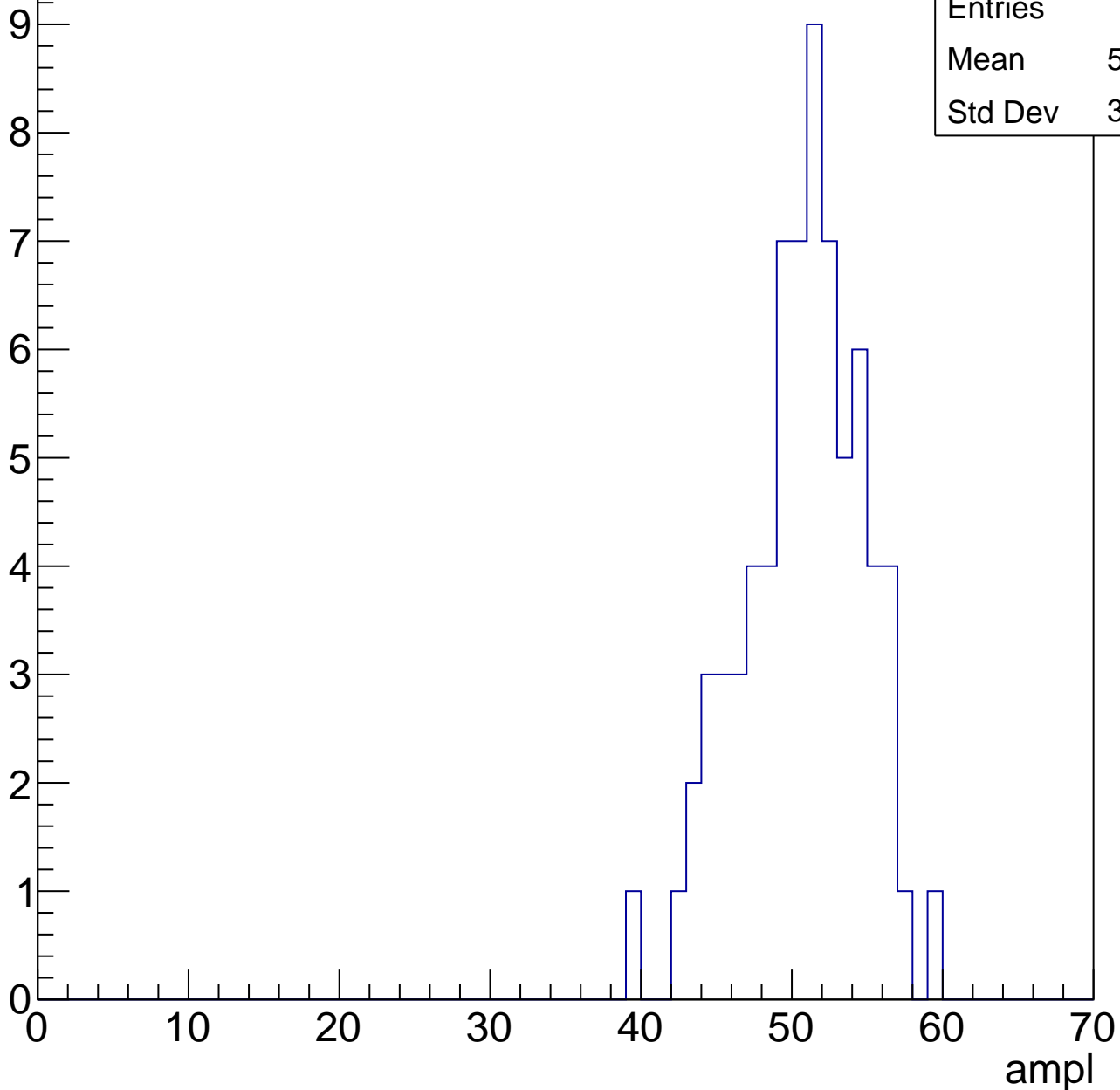
Gaus Width: 3.6731



B0L002S, U4-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch117, adc4

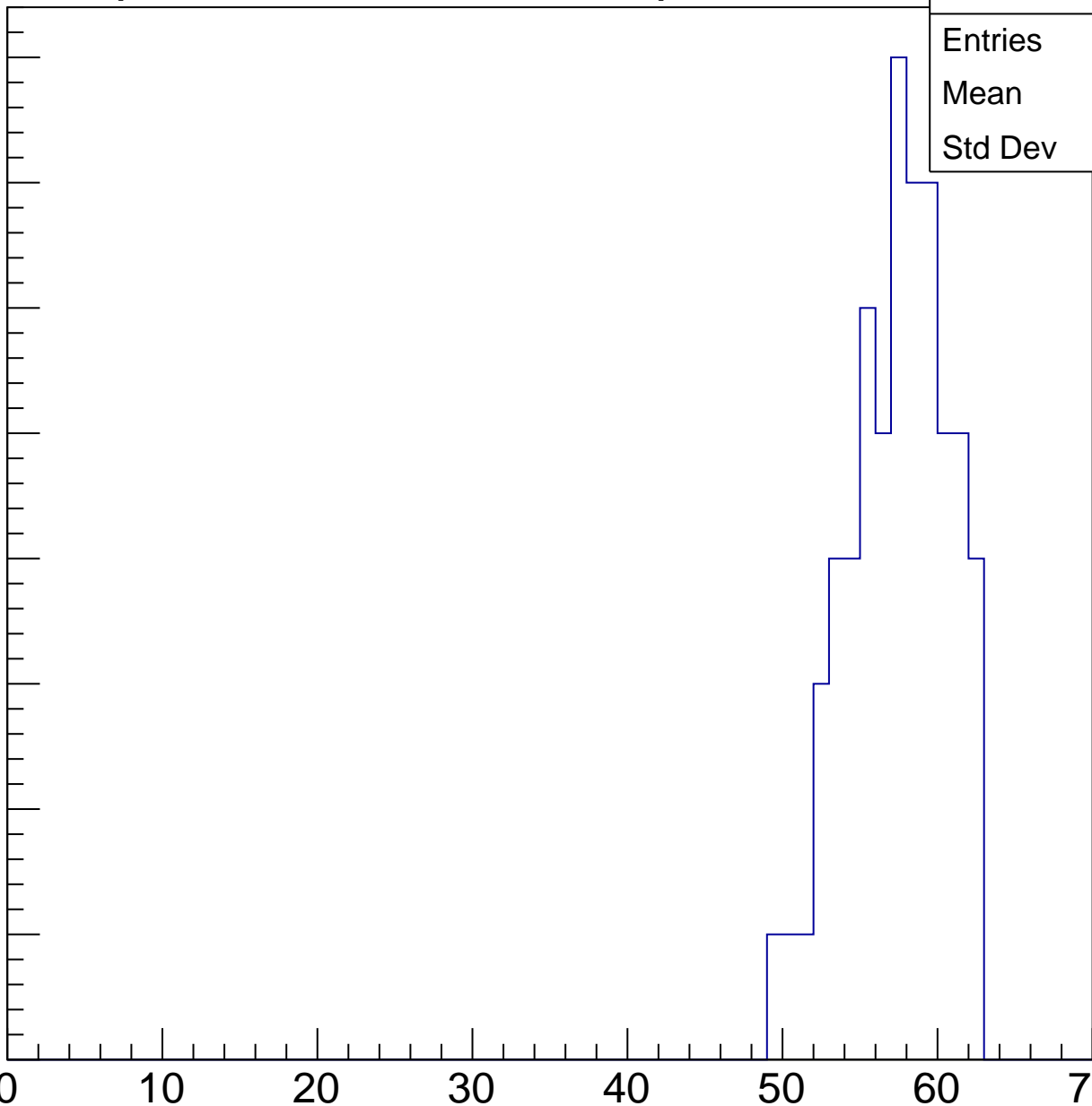
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	56.92
Std Dev	3.169

ampl

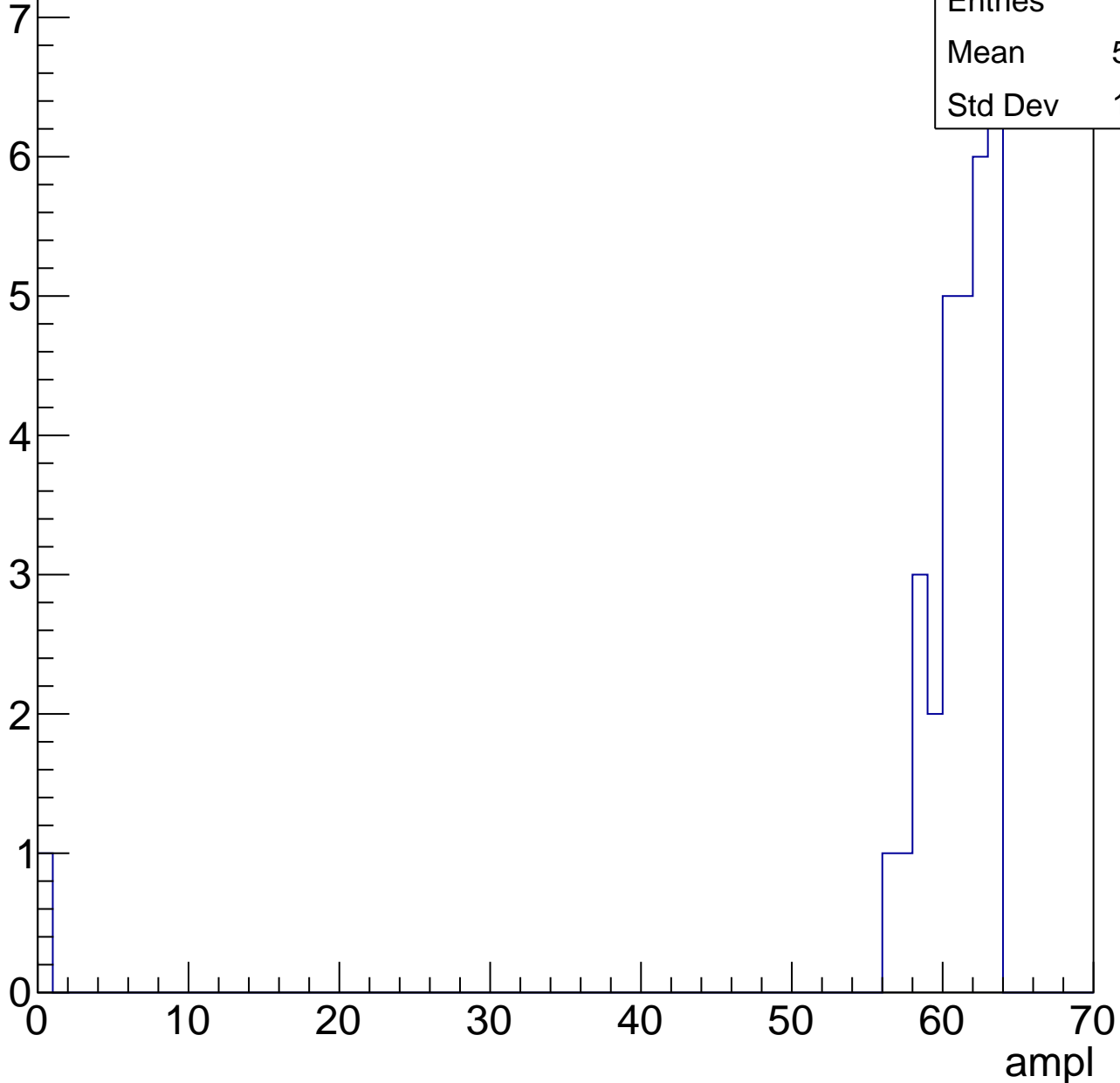


B0L002S, U4-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.81
Std Dev	10.91



B0L002S, U4-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch118, adc0

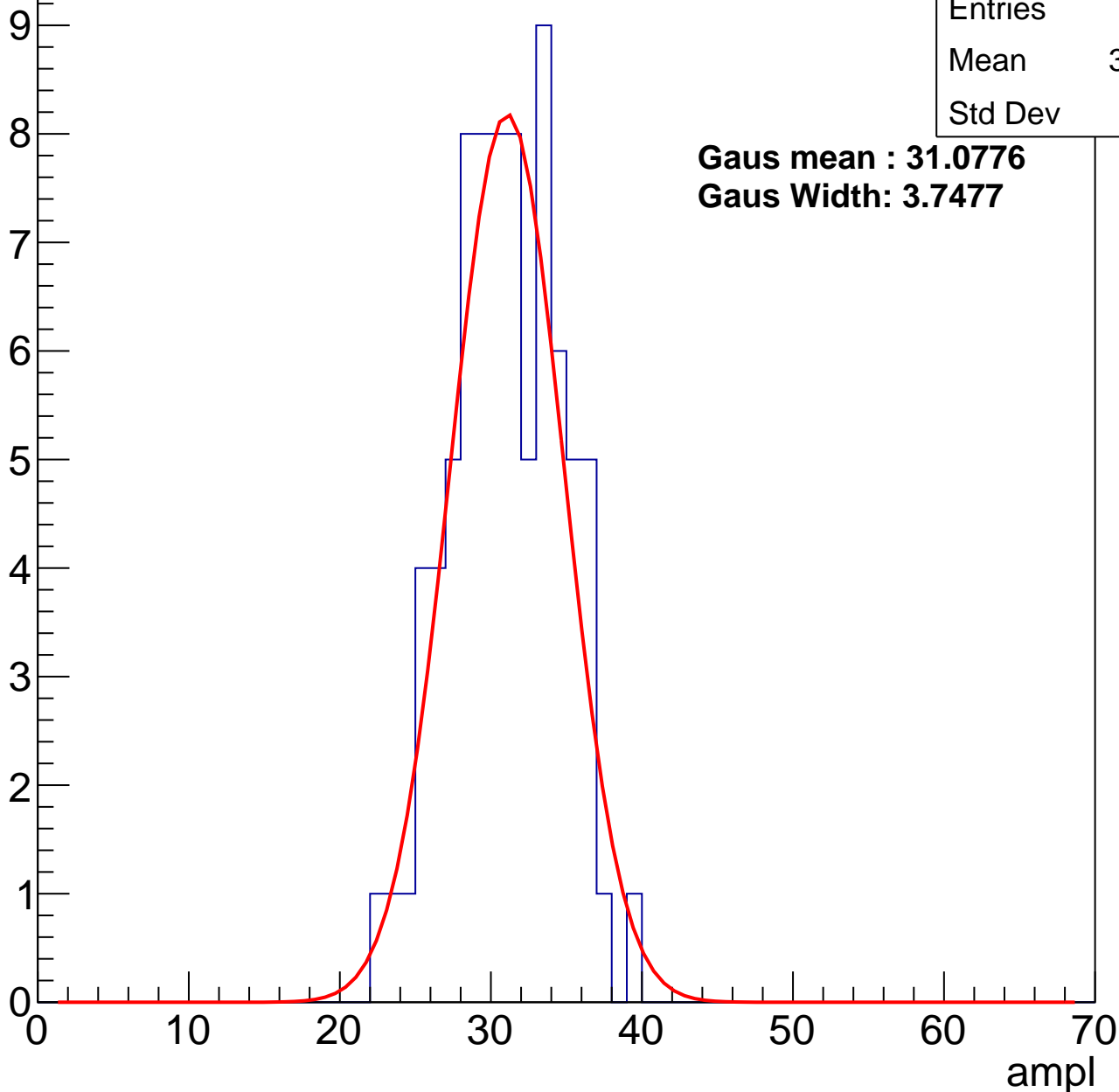
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	30.55
Std Dev	3.56

Gaus mean : 31.0776

Gaus Width: 3.7477



B0L002S, U4-ch118, adc1

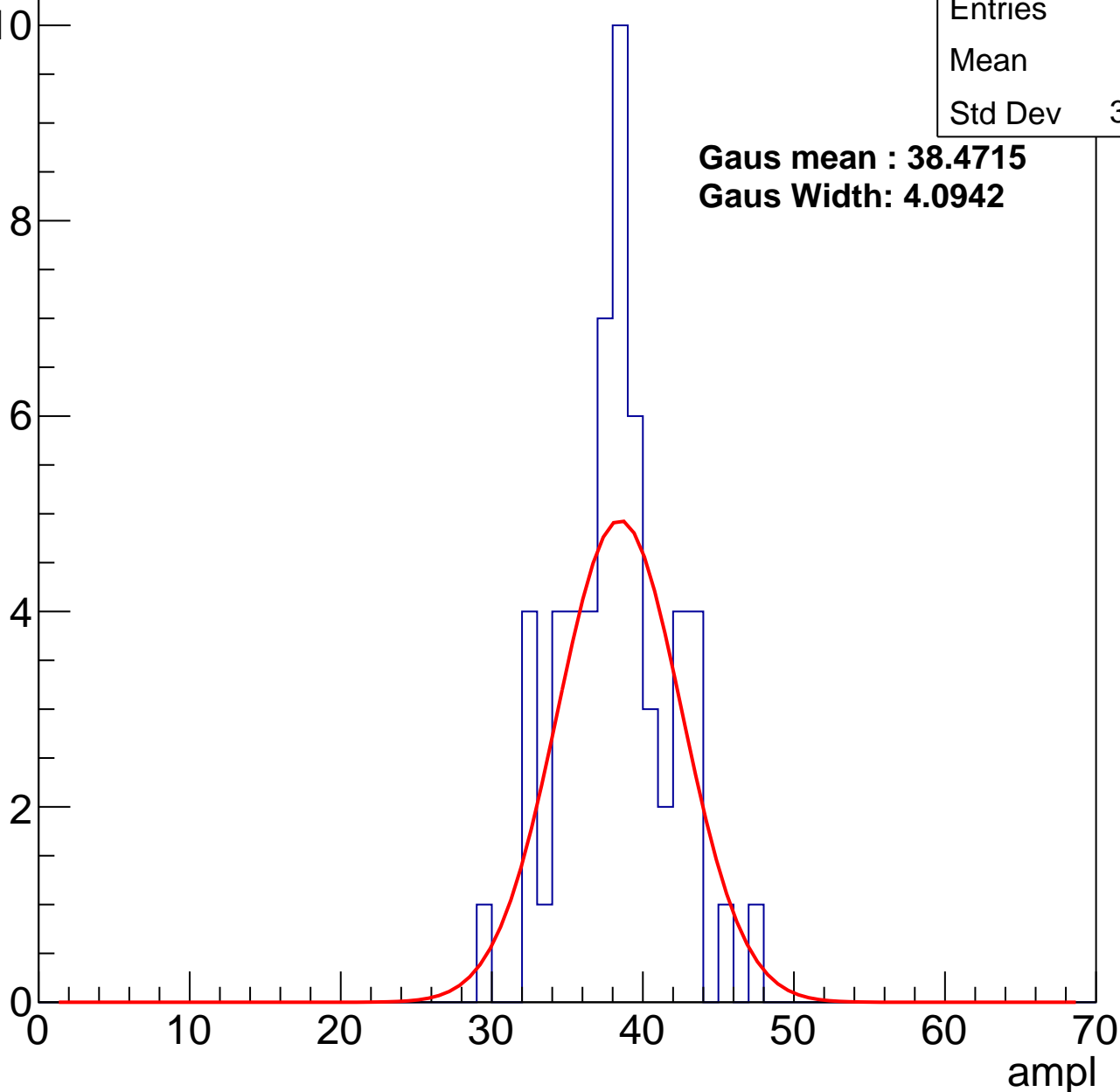
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.8
Std Dev	3.538

Gaus mean : 38.4715

Gaus Width: 4.0942



B0L002S, U4-ch118, adc2

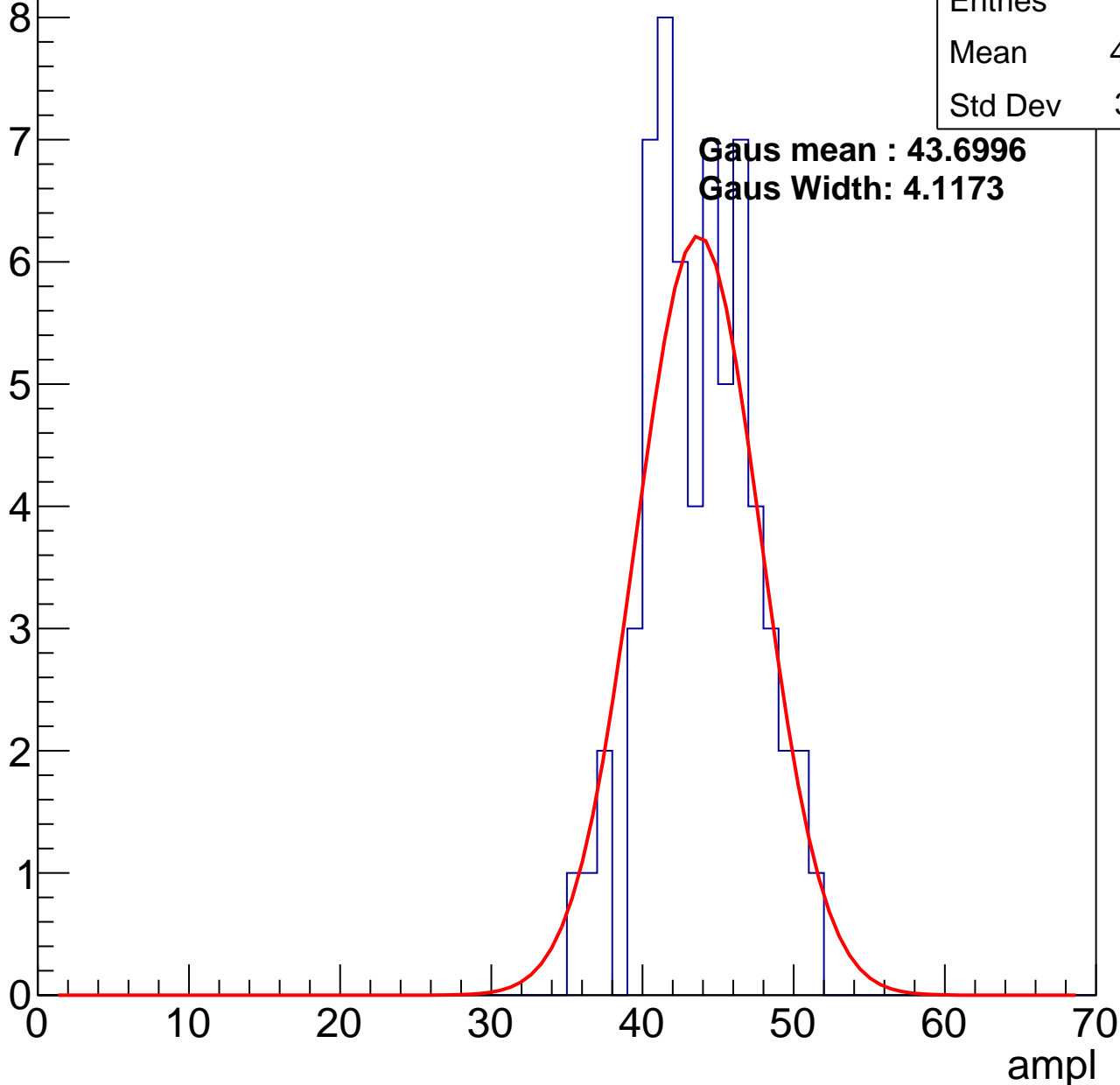
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.33
Std Dev	3.541

Gaus mean : 43.6996

Gaus Width: 4.1173

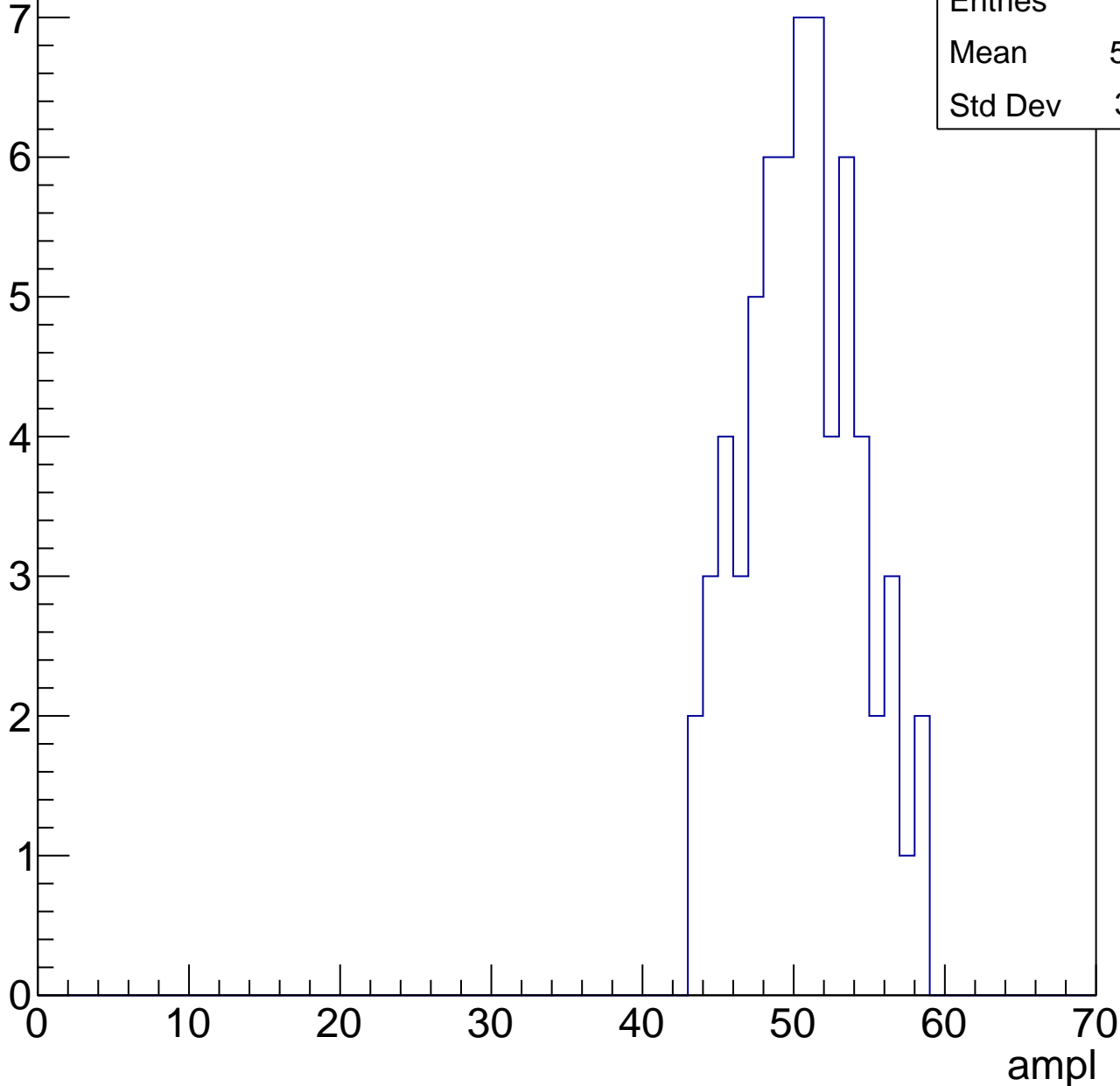


B0L002S, U4-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.05
Std Dev	3.731

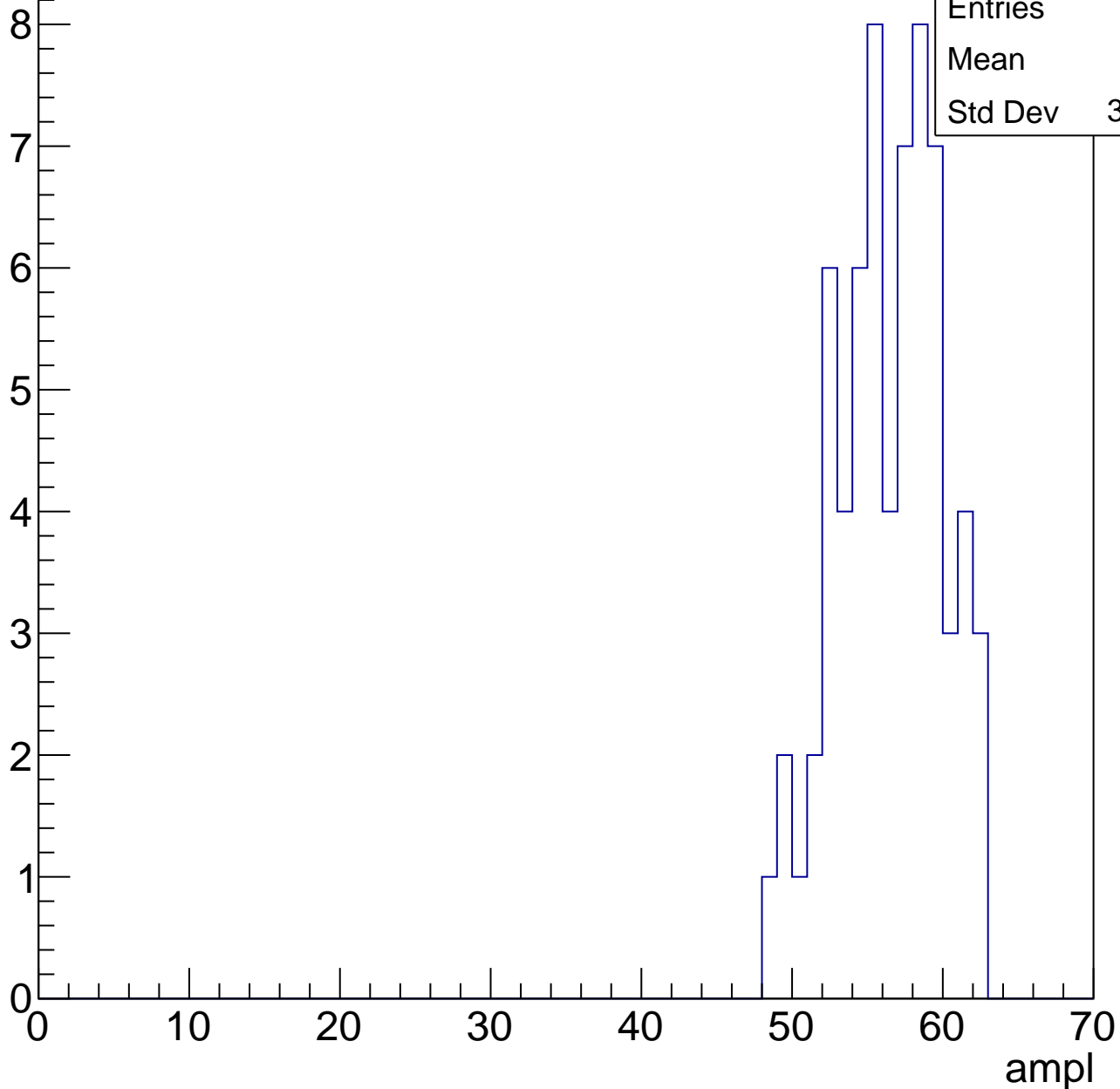


B0L002S, U4-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

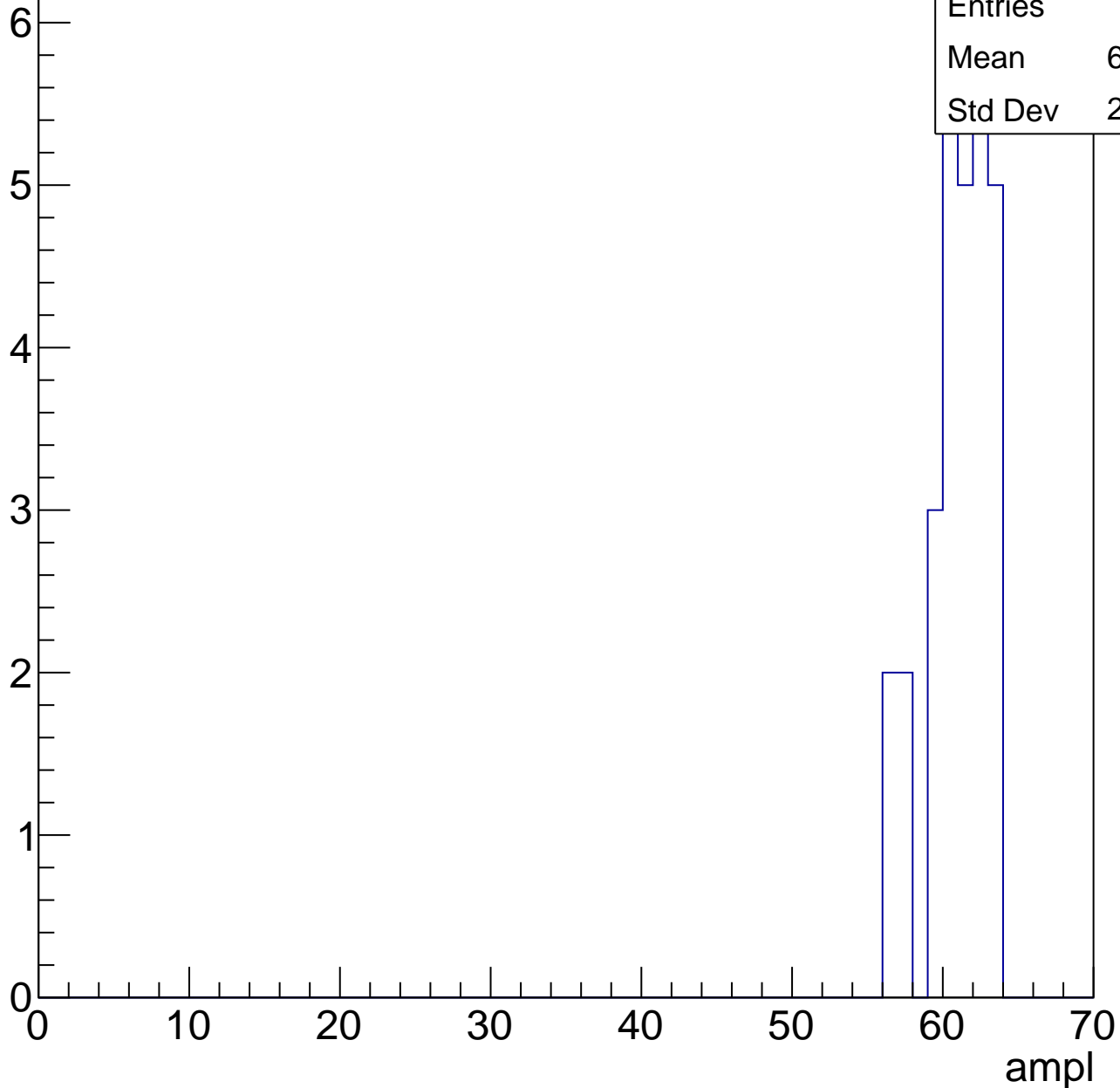
Entries	66
Mean	56
Std Dev	3.416



B0L002S, U4-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

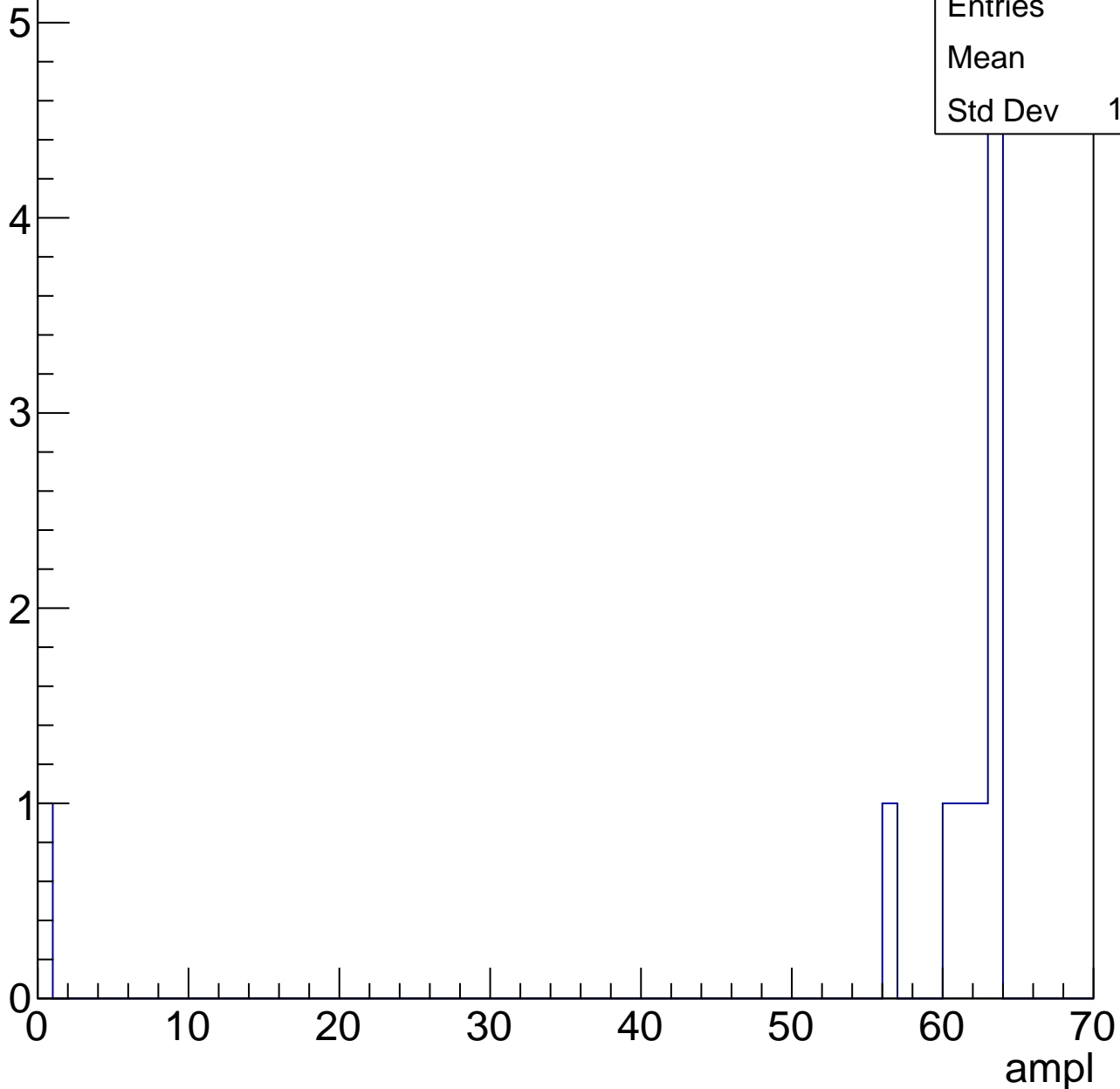


B0L002S, U4-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	55.4
Std Dev	18.59



B0L002S, U4-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch119, adc0

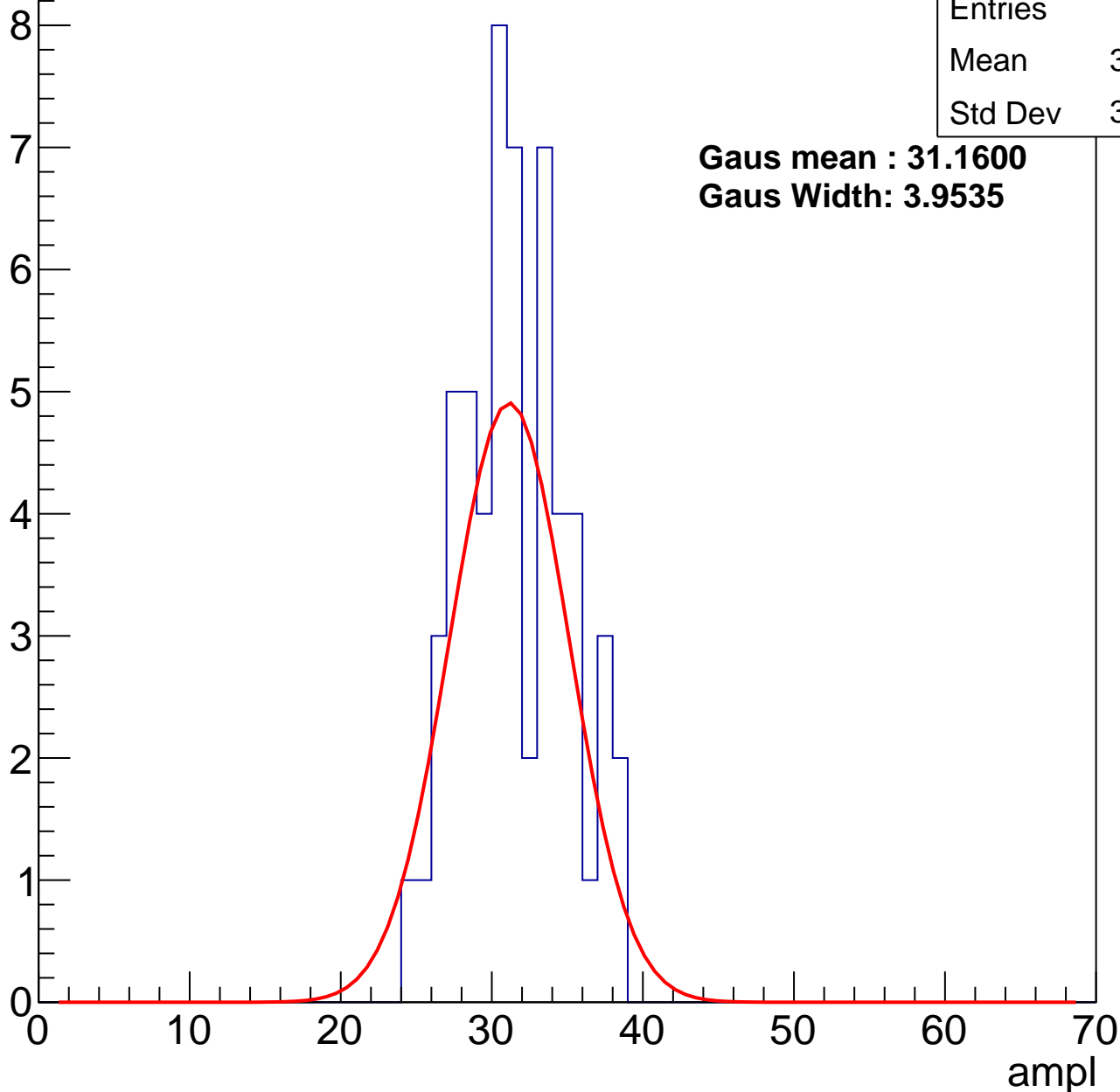
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	31.04
Std Dev	3.428

Gaus mean : 31.1600

Gaus Width: 3.9535



B0L002S, U4-ch119, adc1

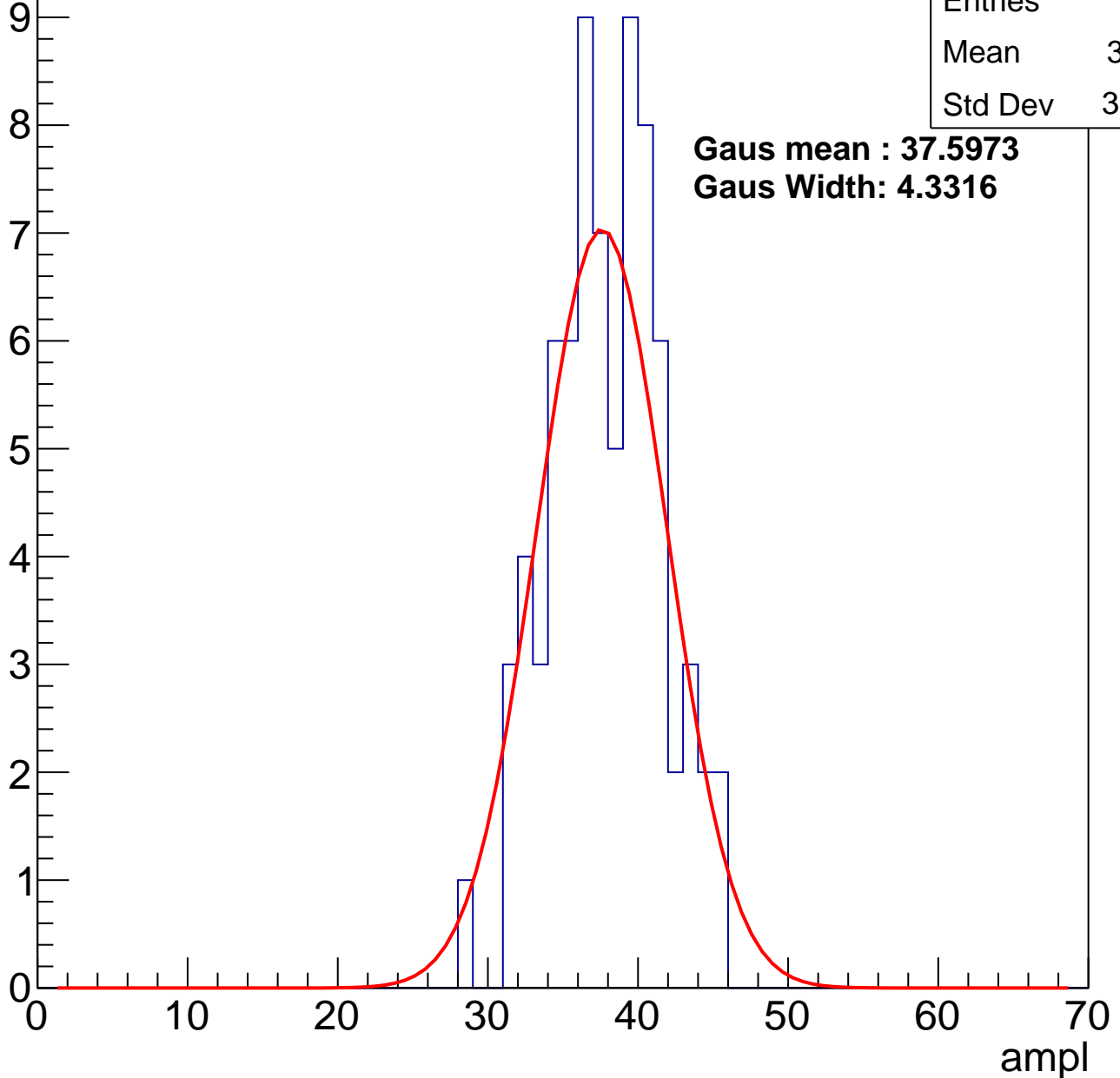
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	37.41
Std Dev	3.635

Gaus mean : 37.5973

Gaus Width: 4.3316



B0L002S, U4-ch119, adc2

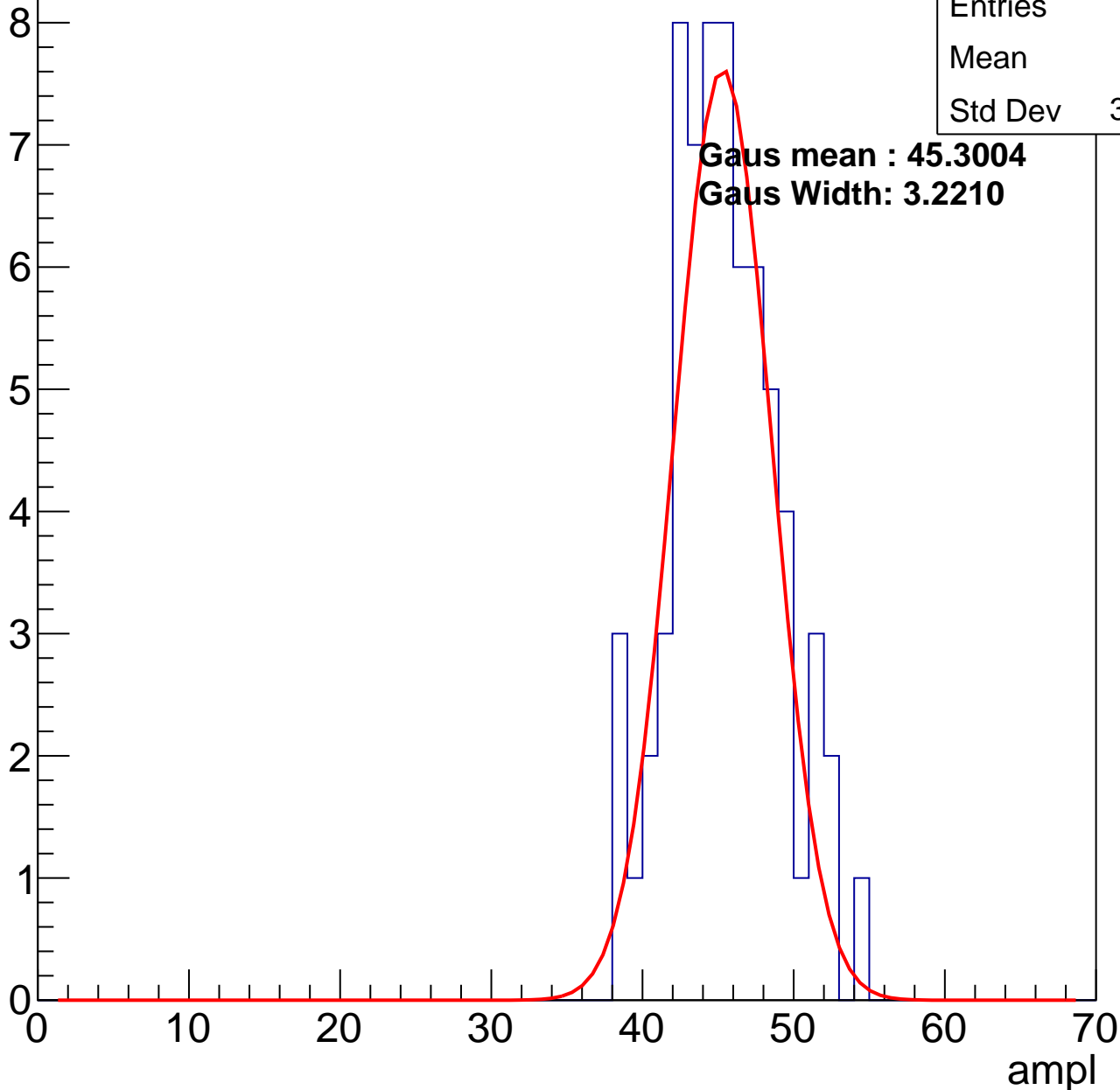
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	45
Std Dev	3.515

Gaus mean : 45.3004

Gaus Width: 3.2210

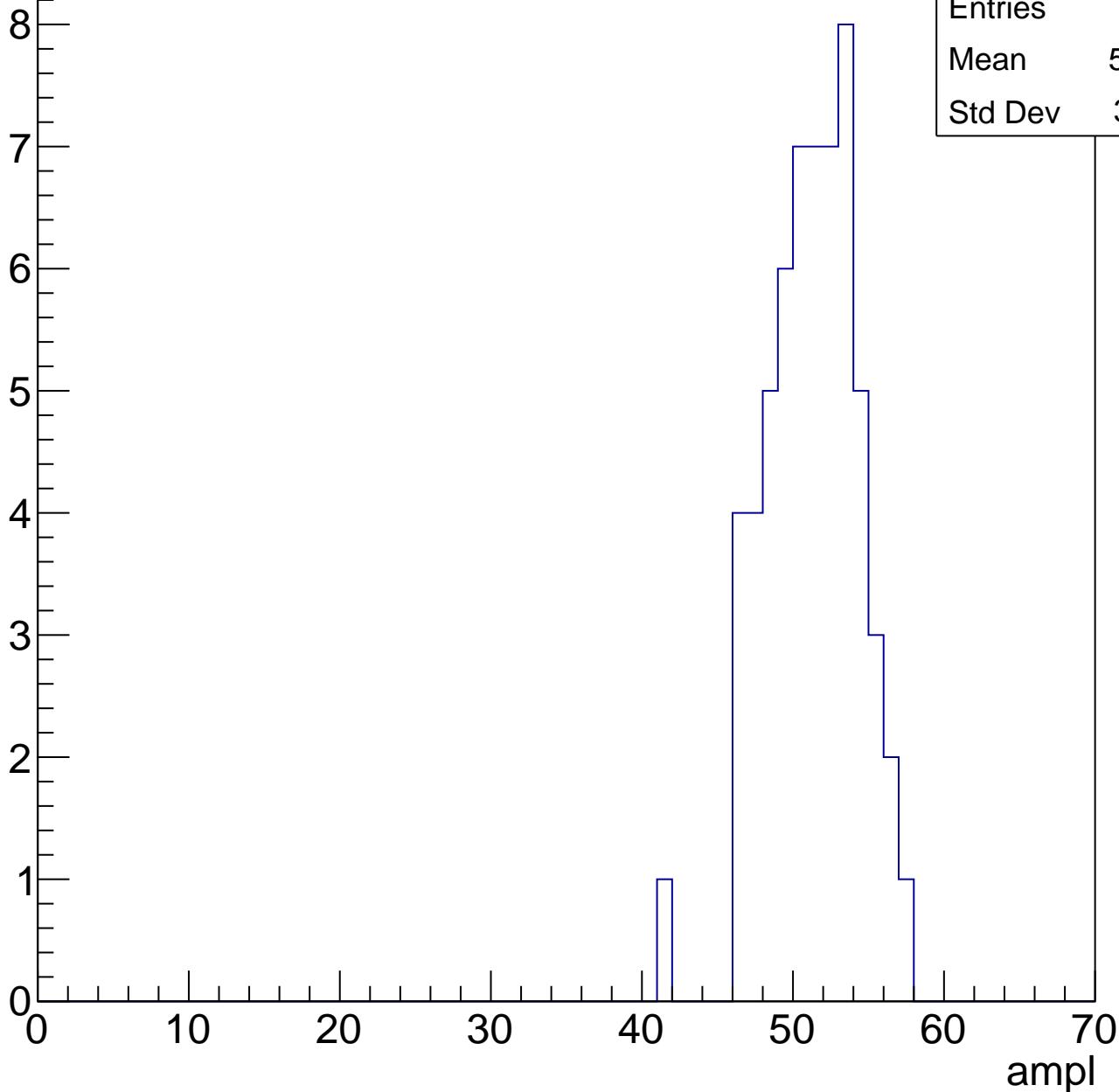


B0L002S, U4-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.77
Std Dev	3.041

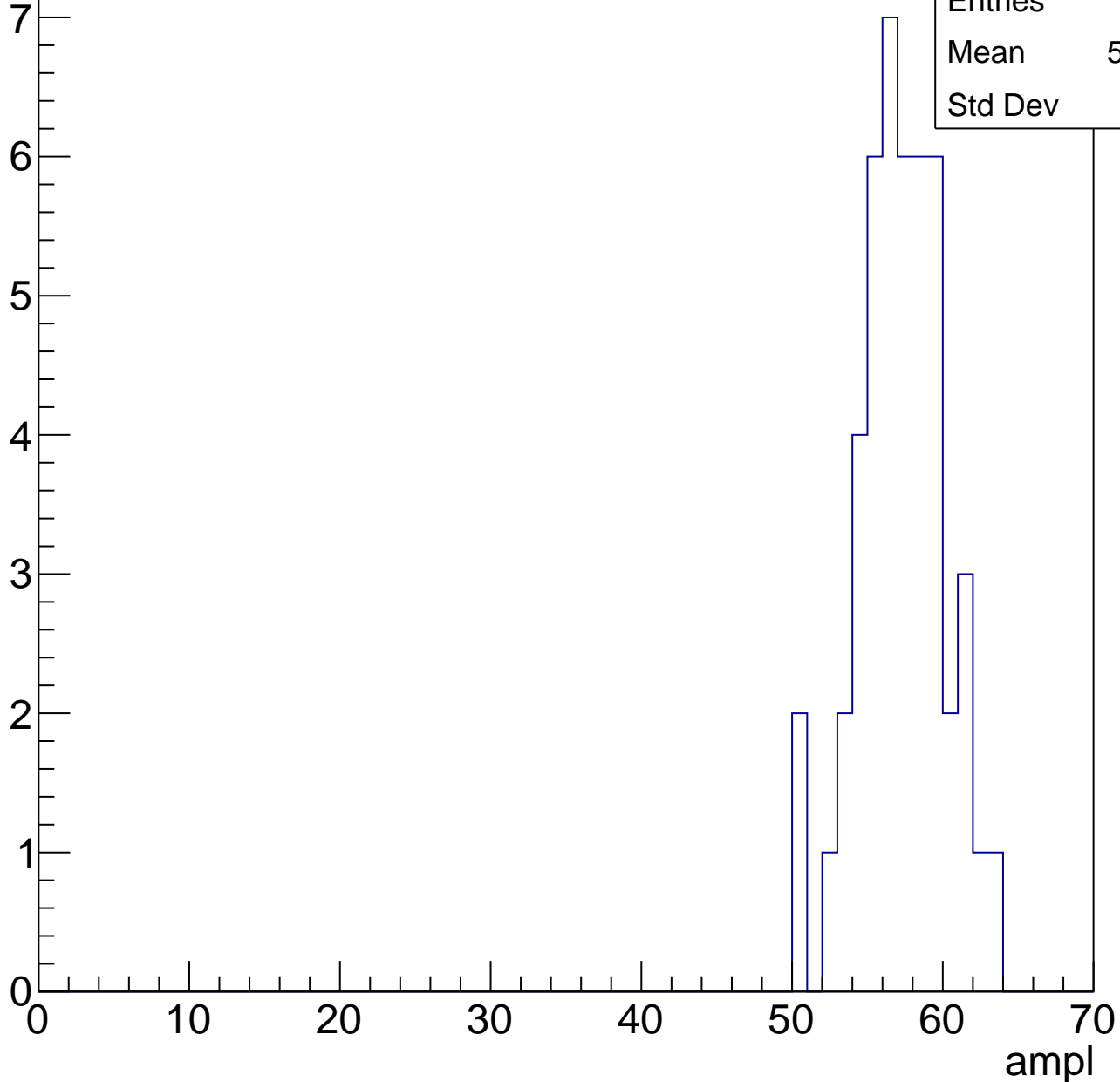


B0L002S, U4-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	56.77
Std Dev	2.83

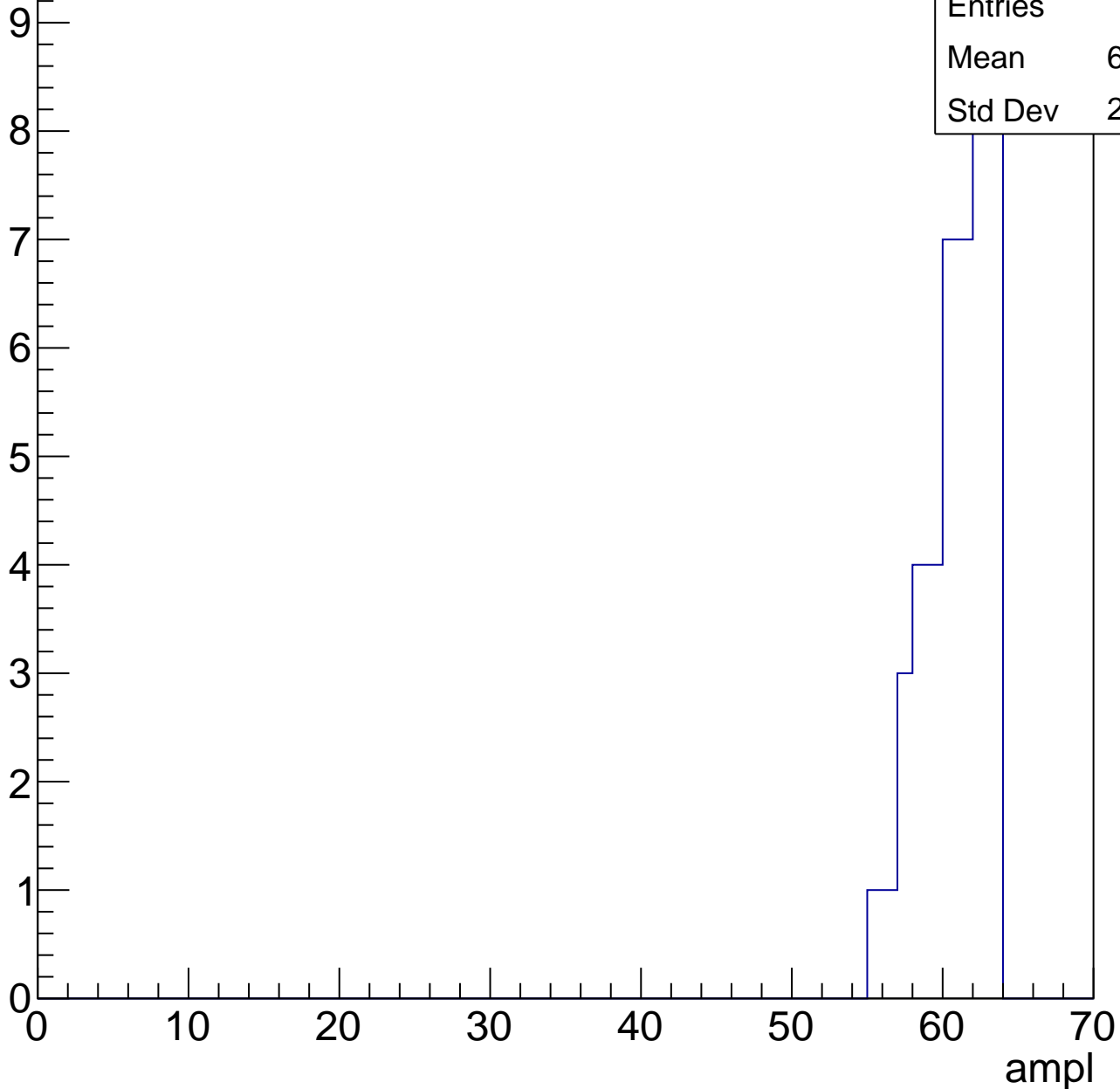


B0L002S, U4-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

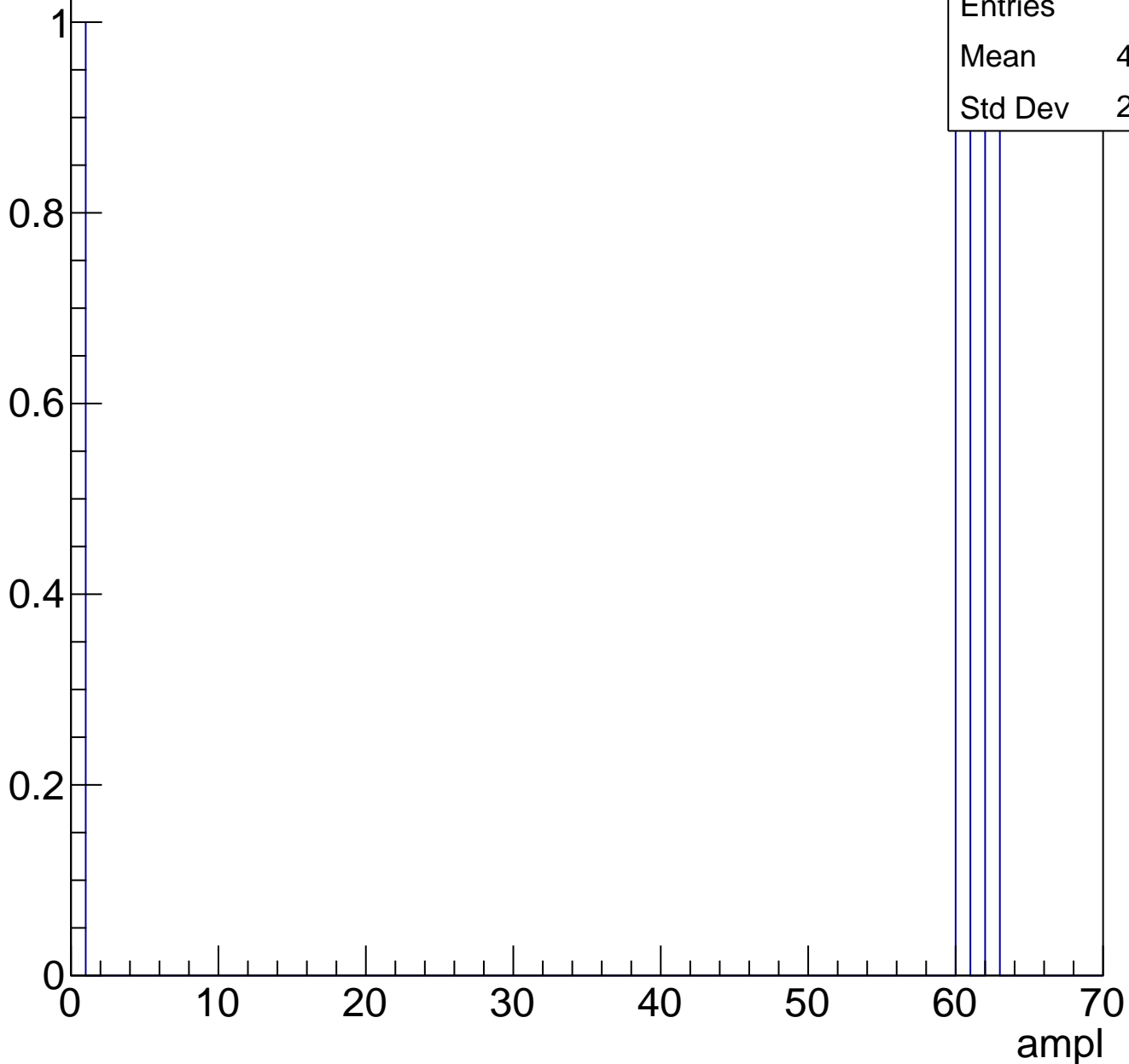
Entries	44
Mean	60.45
Std Dev	2.126



B0L002S, U4-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch120, adc0

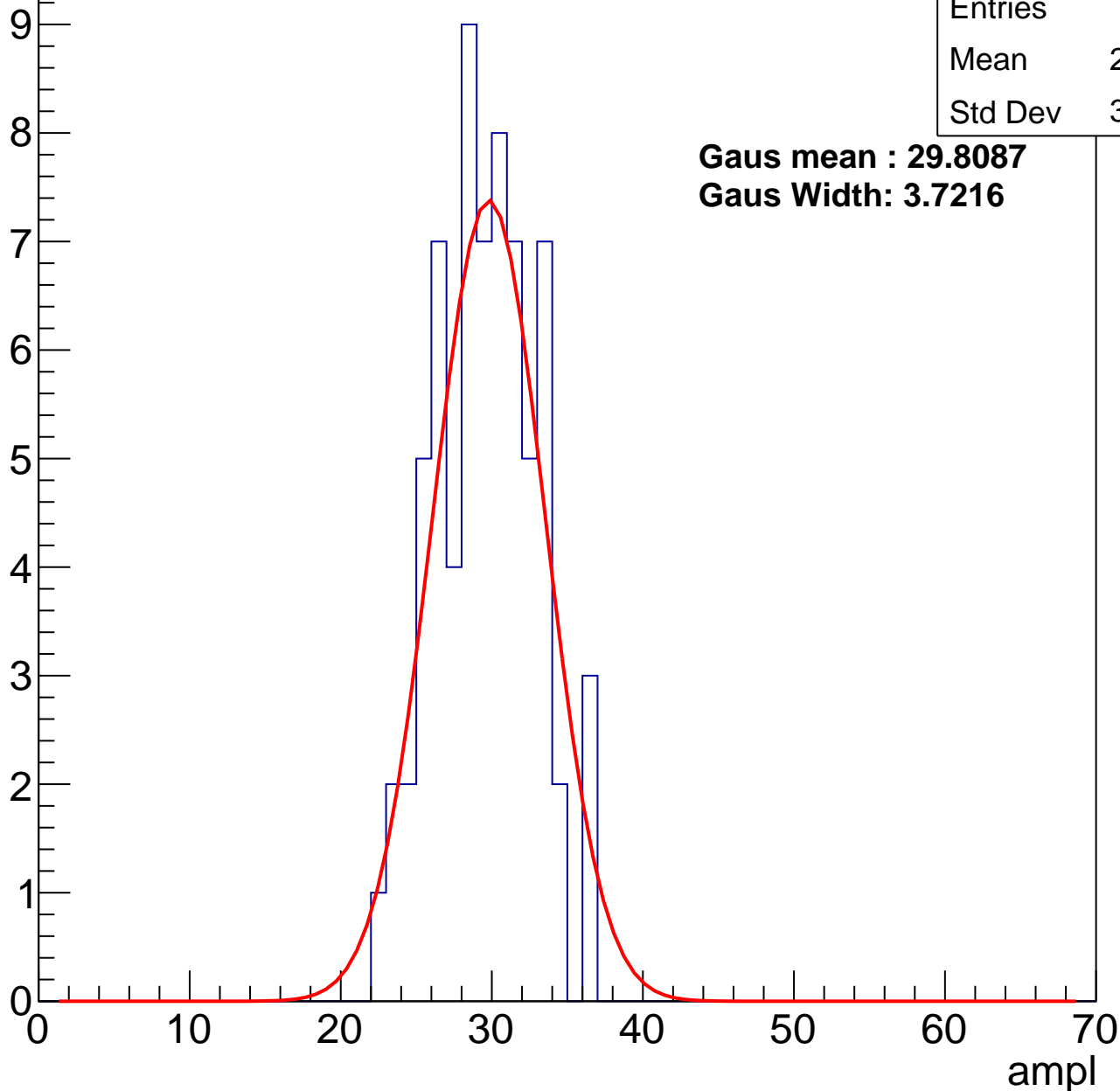
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	29.13
Std Dev	3.248

Gaus mean : 29.8087

Gaus Width: 3.7216



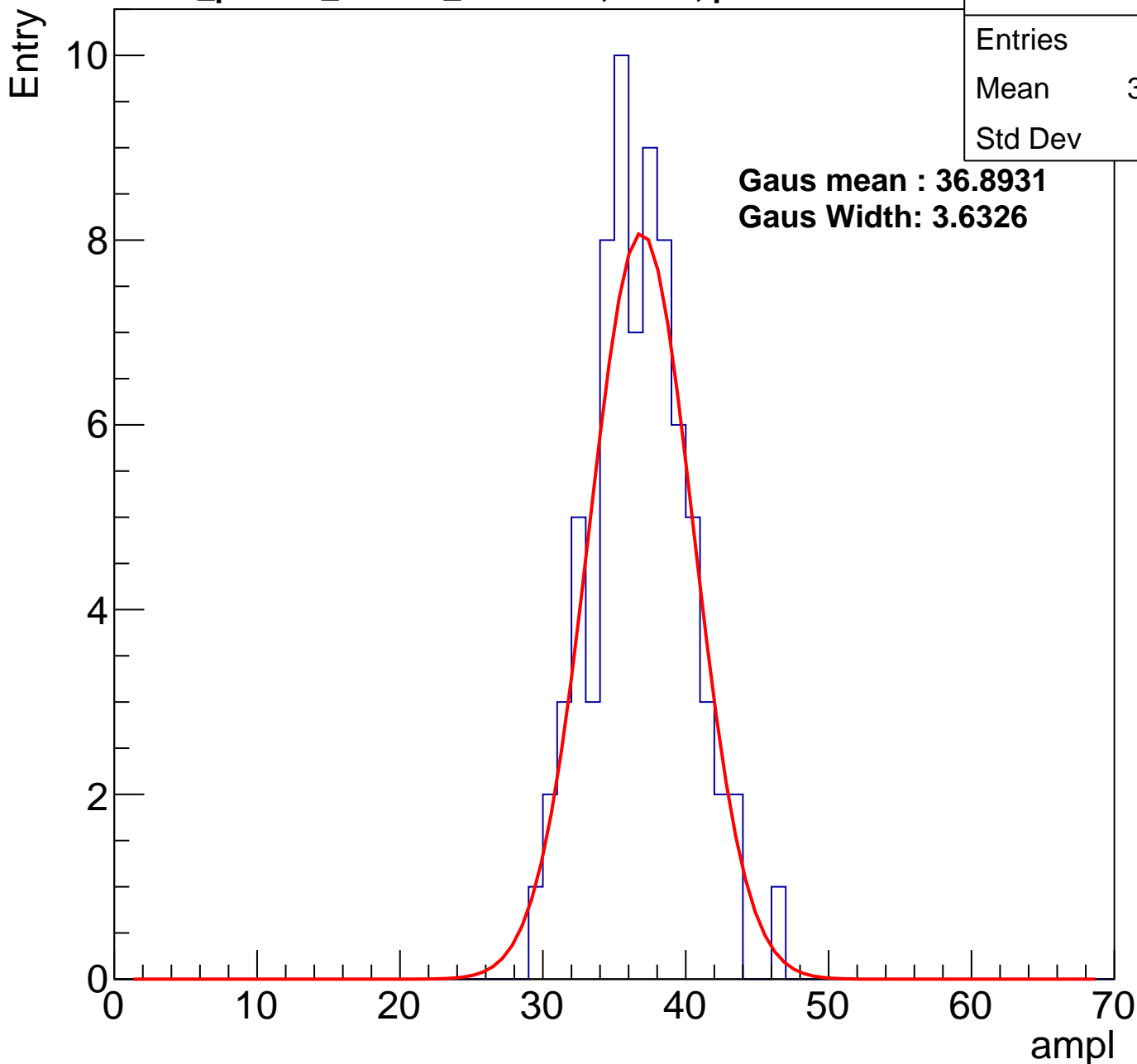
B0L002S, U4-ch120, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.33
Std Dev	3.38

Gaus mean : 36.8931

Gaus Width: 3.6326



B0L002S, U4-ch120, adc2

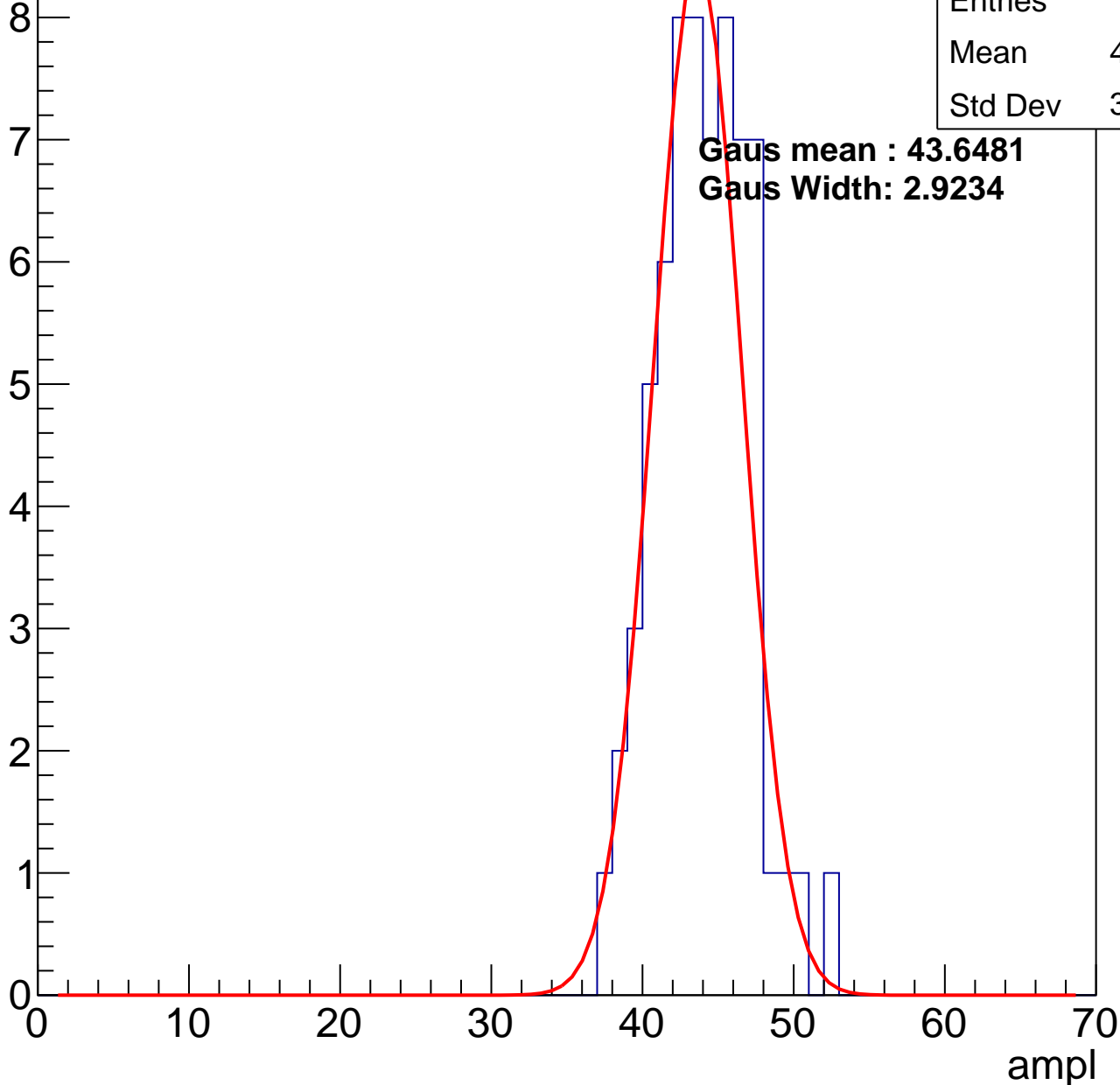
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.55
Std Dev	3.006

Gaus mean : 43.6481

Gaus Width: 2.9234

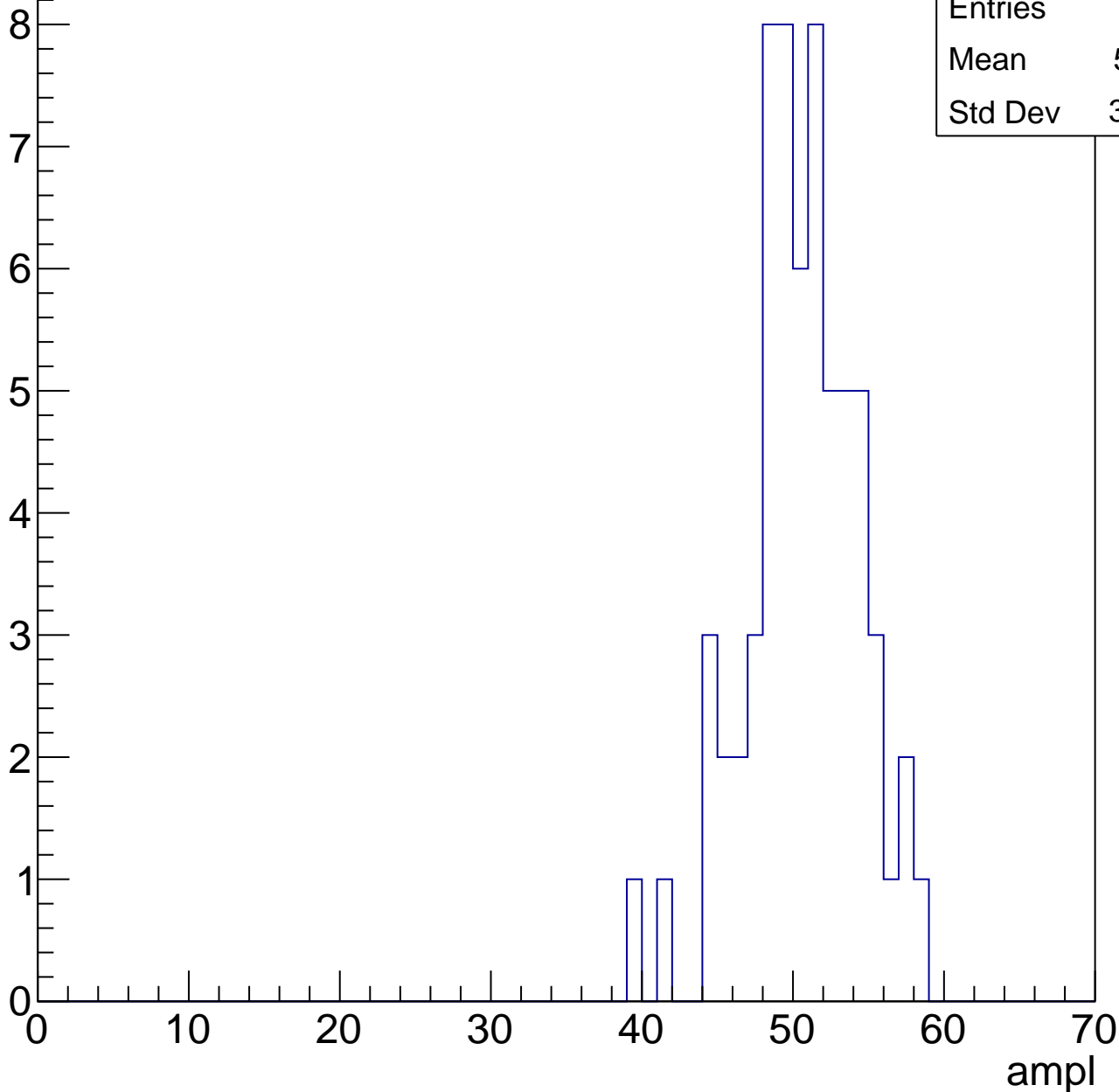


B0L002S, U4-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	50.11
Std Dev	3.717

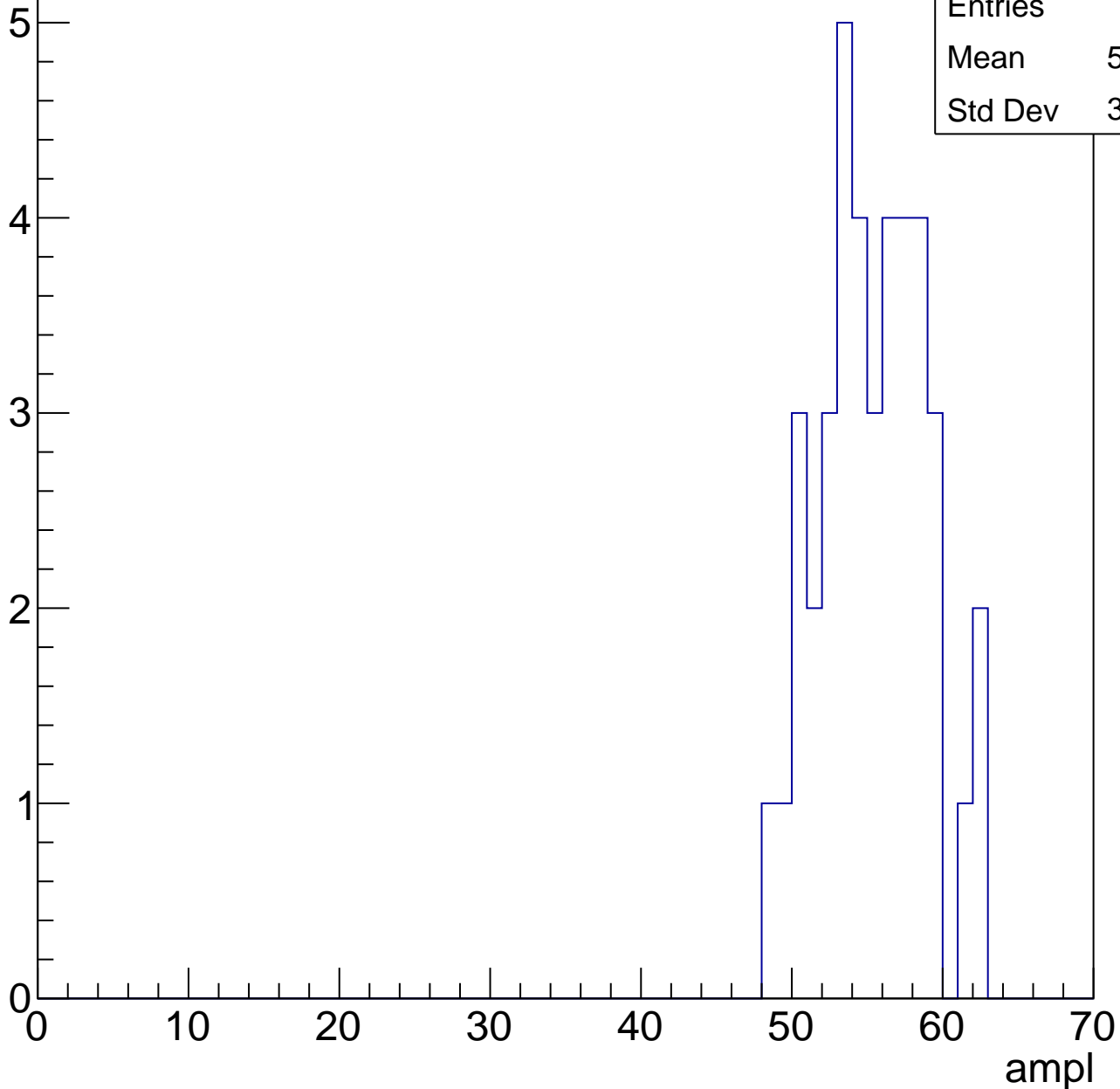


B0L002S, U4-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	54.92
Std Dev	3.467

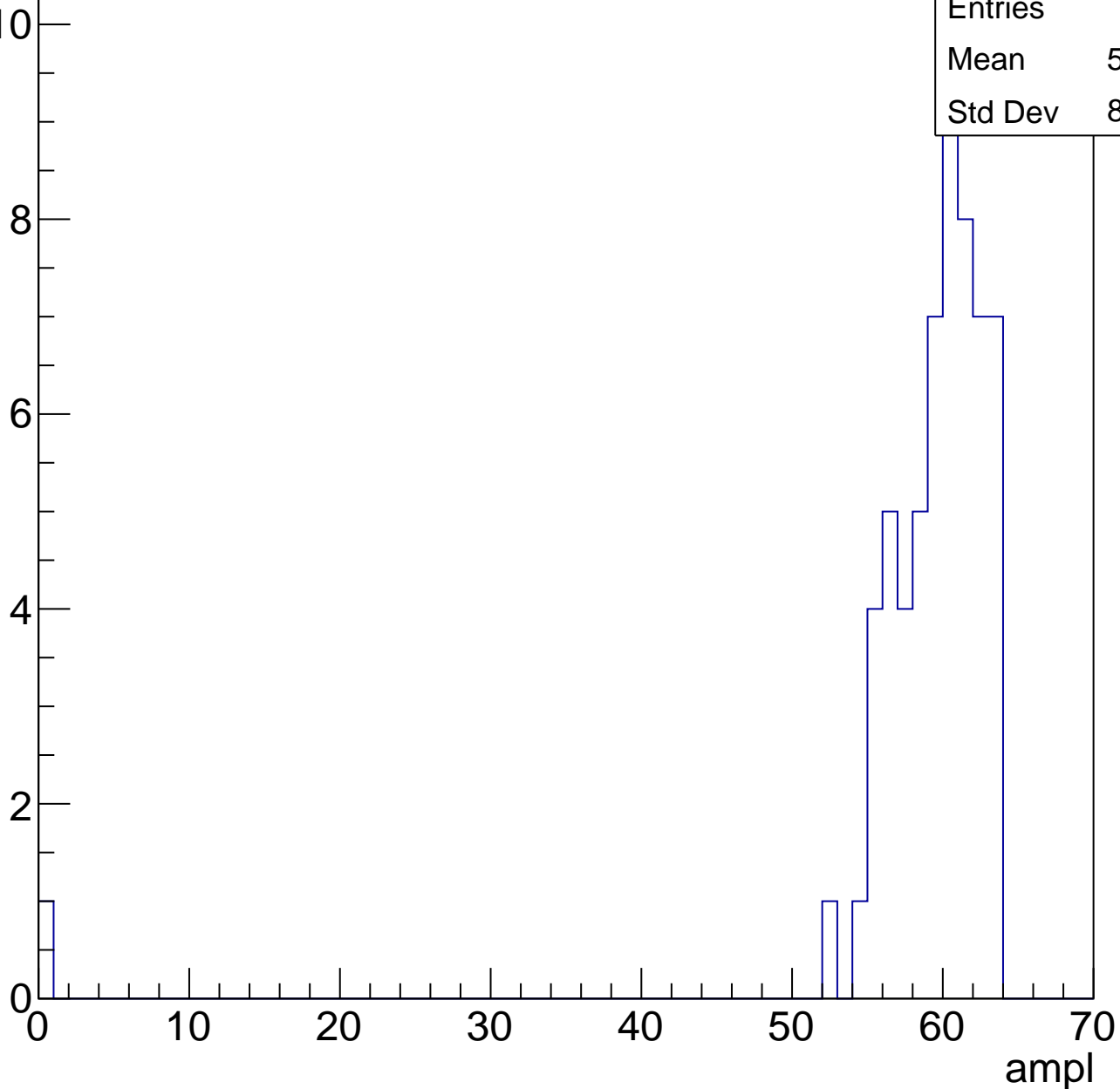


B0L002S, U4-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	58.33
Std Dev	8.037



B0L002S, U4-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch121, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	30.59
Std Dev	6.981

Gaus mean : 31.9113

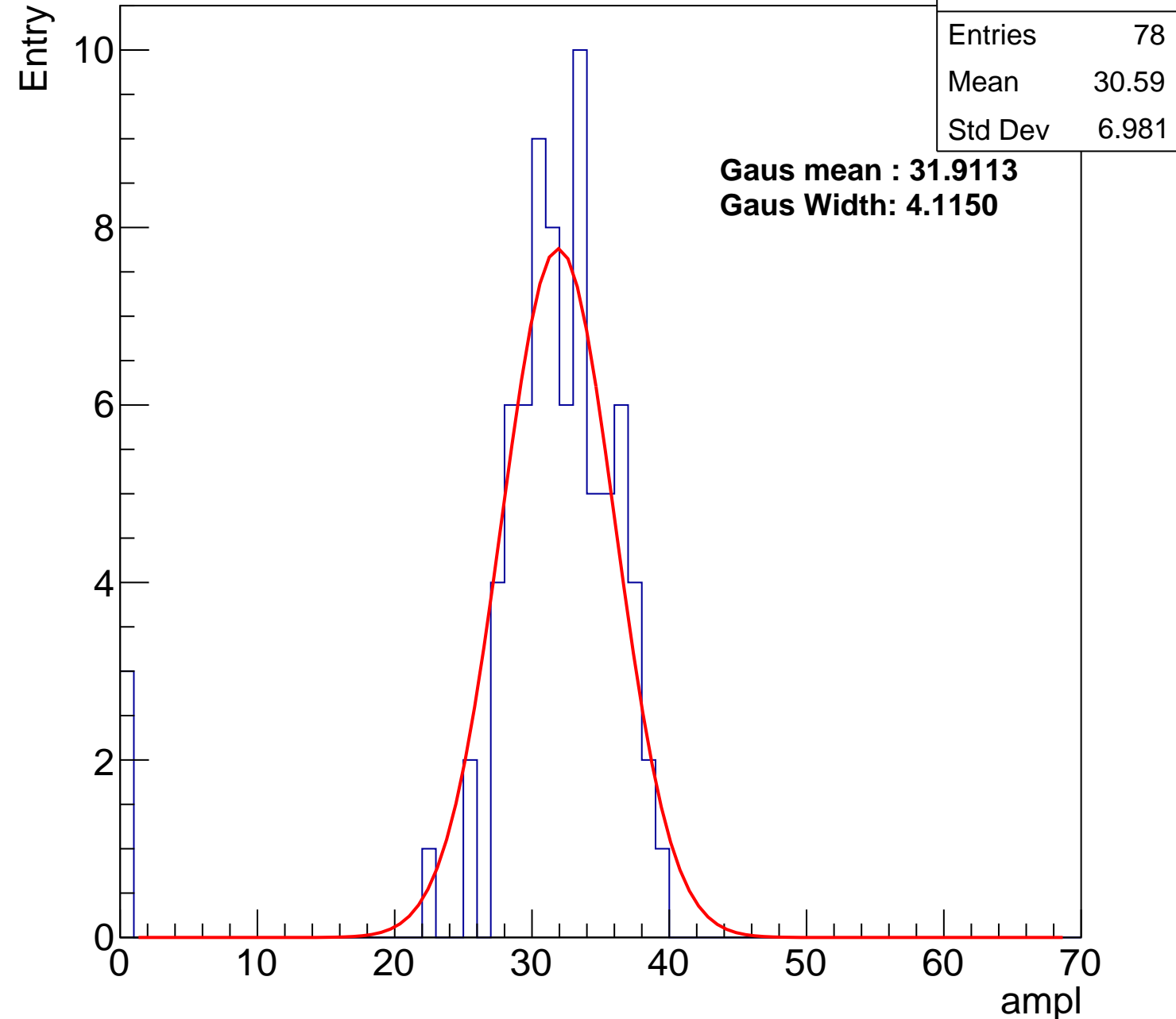
Gaus Width: 4.1150

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch121, adc1

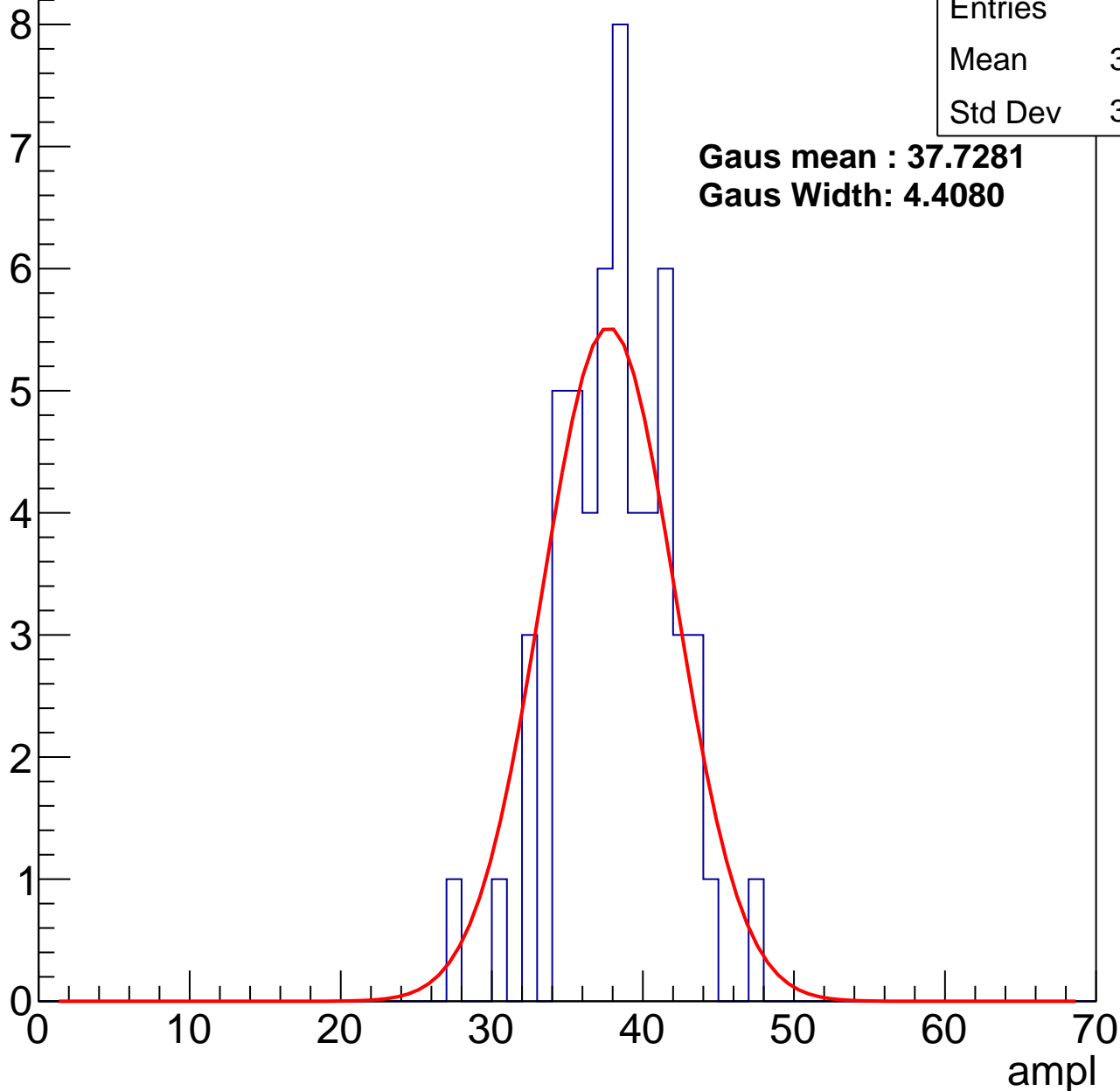
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	37.75
Std Dev	3.694

Gaus mean : 37.7281

Gaus Width: 4.4080



B0L002S, U4-ch121, adc2

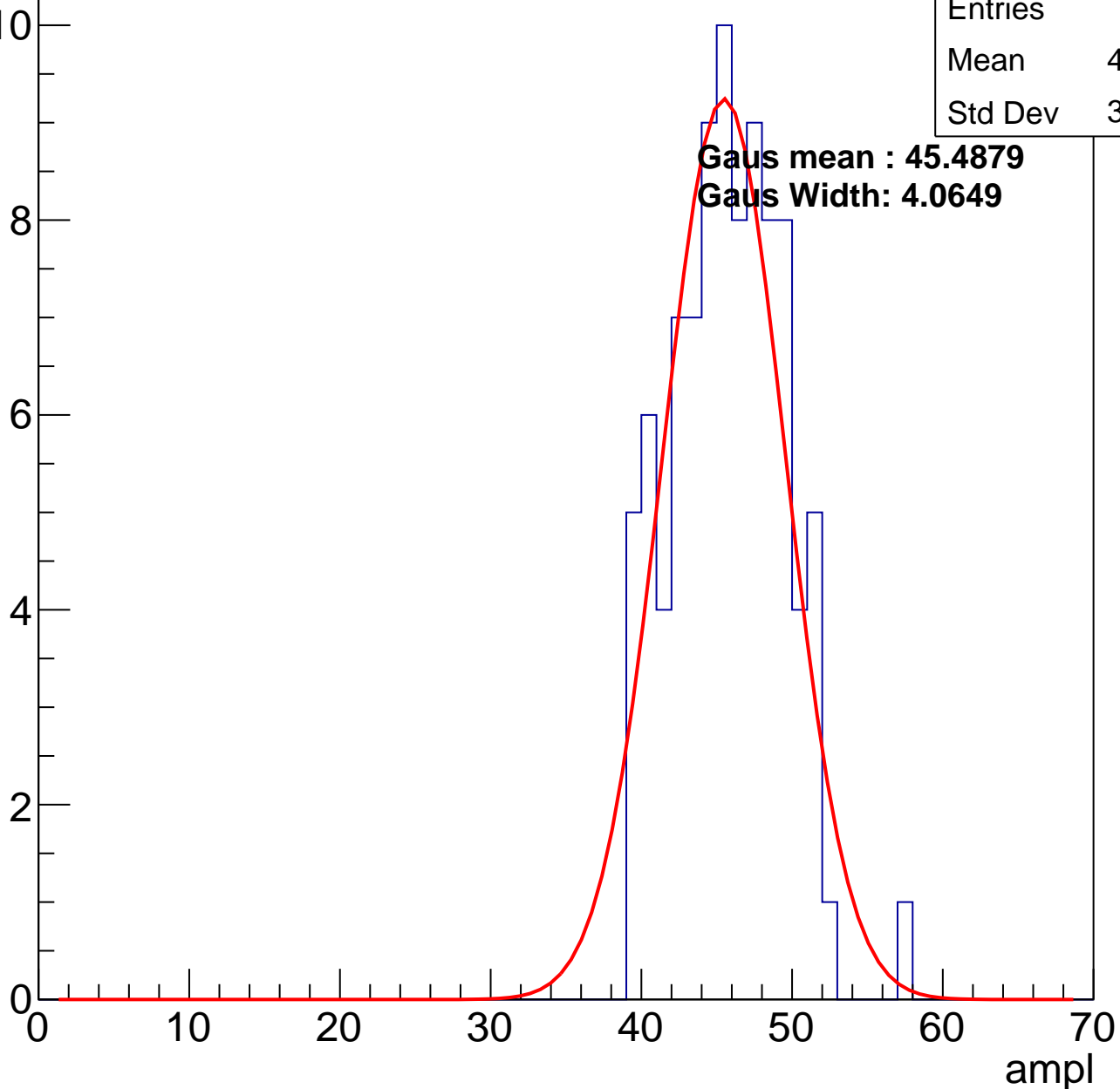
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	92
Mean	45.34
Std Dev	3.612

Gaus mean : 45.4879

Gaus Width: 4.0649

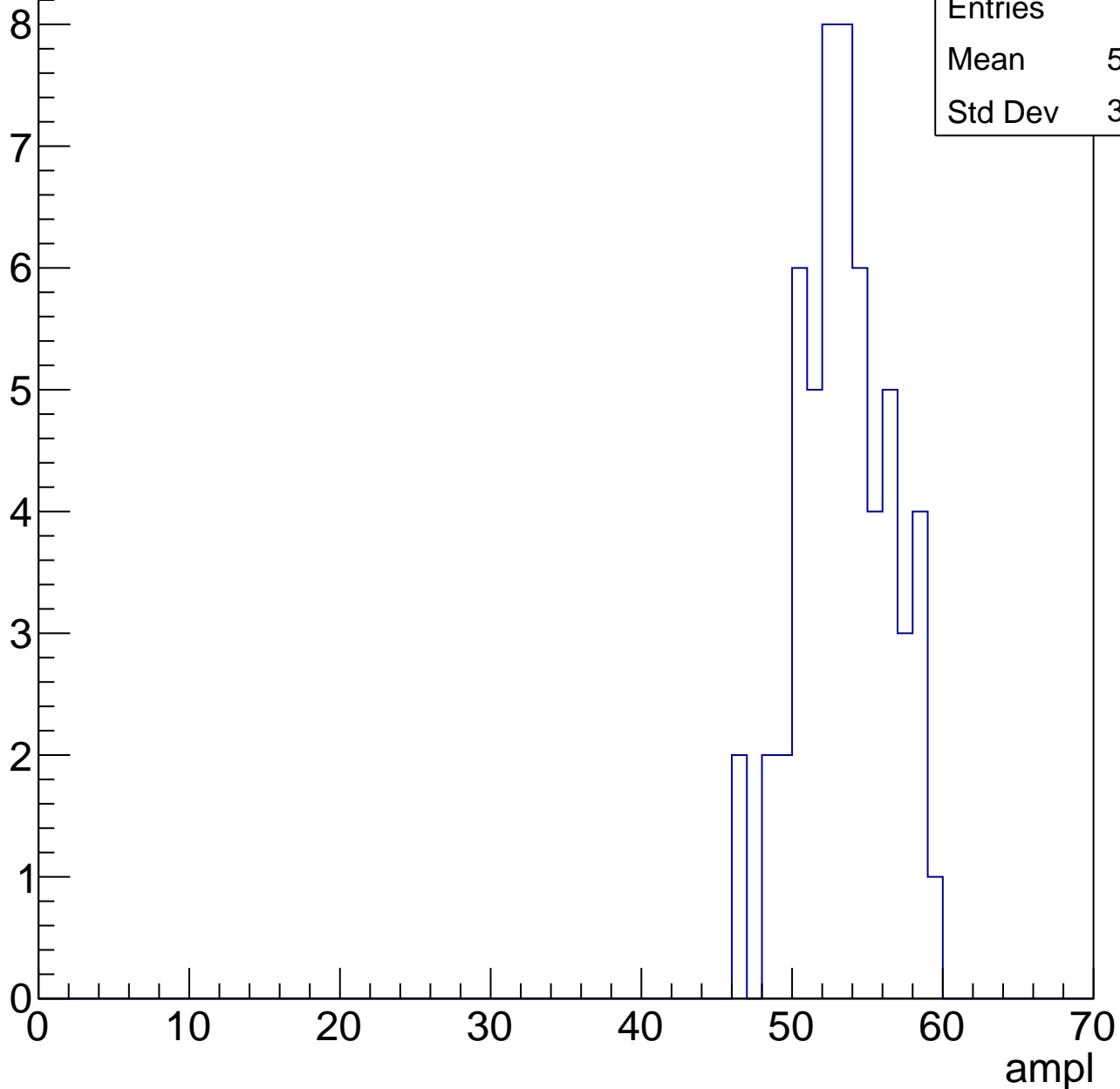


B0L002S, U4-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	52.98
Std Dev	3.027

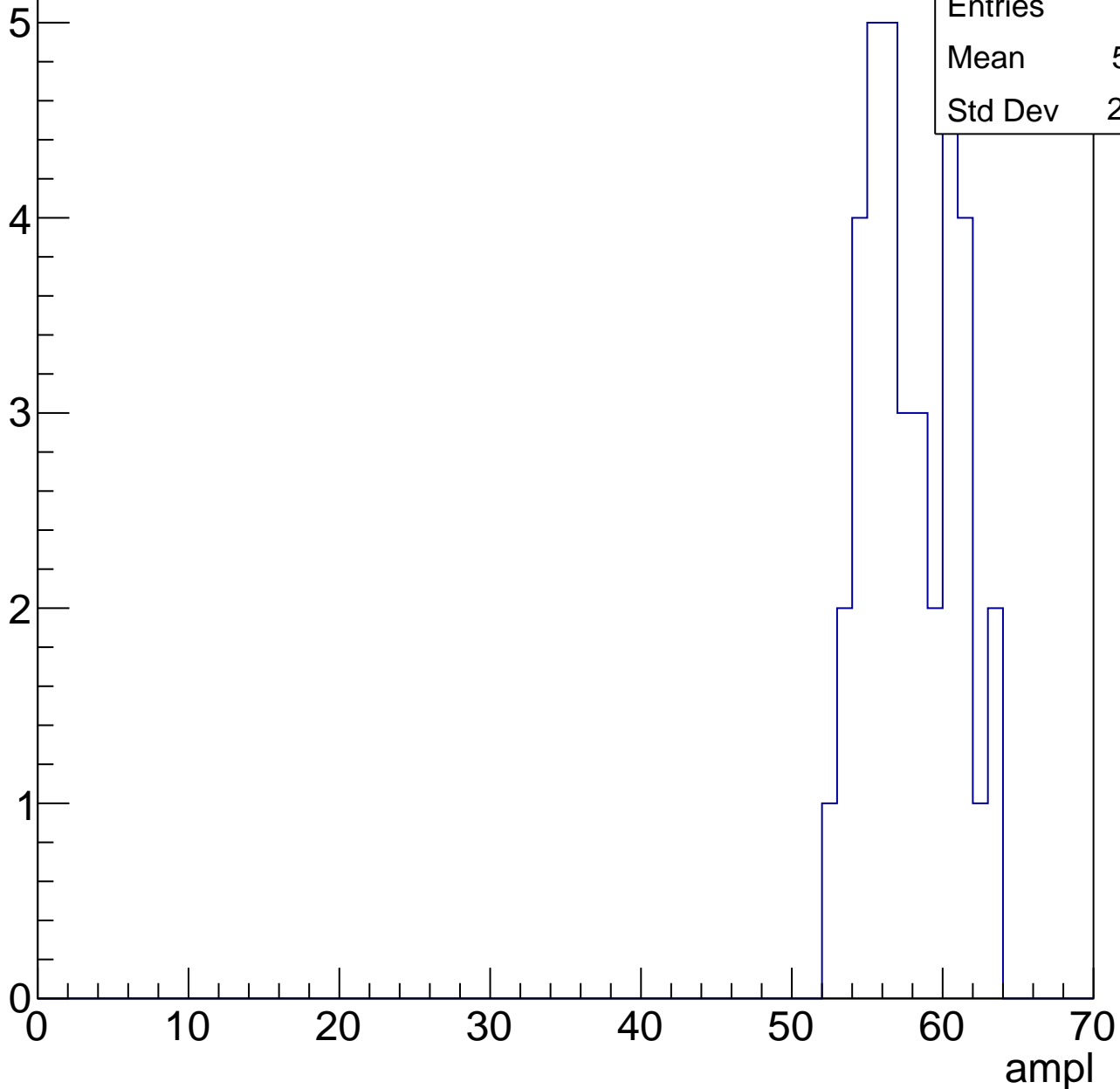


B0L002S, U4-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

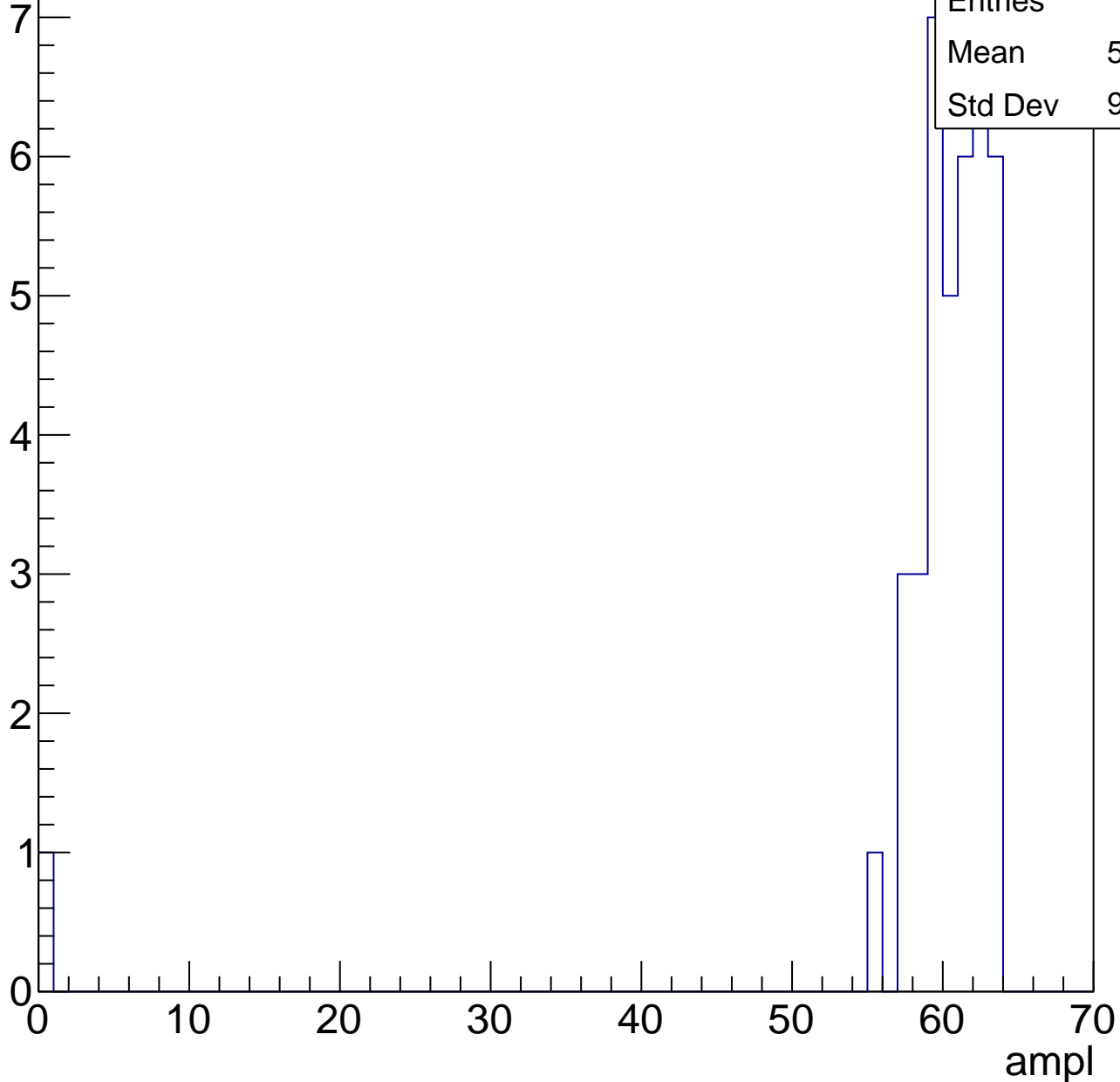
Entries	37
Mean	57.41
Std Dev	2.982



B0L002S, U4-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	39
Mean	58.74
Std Dev	9.737

B0L002S, U4-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch122, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	31.21
Std Dev	4.889

Gaus mean : 32.0056

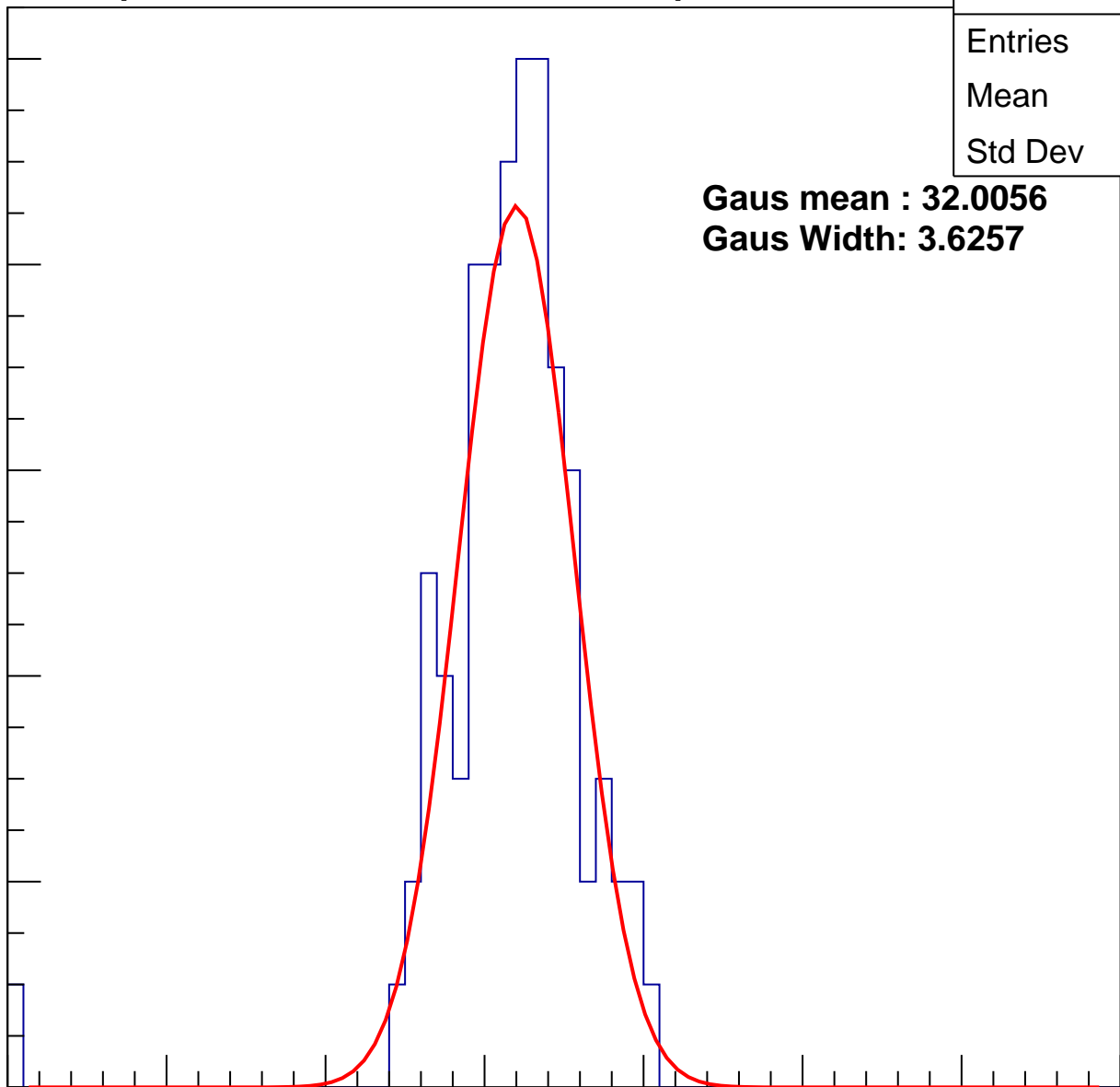
Gaus Width: 3.6257

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch122, adc1

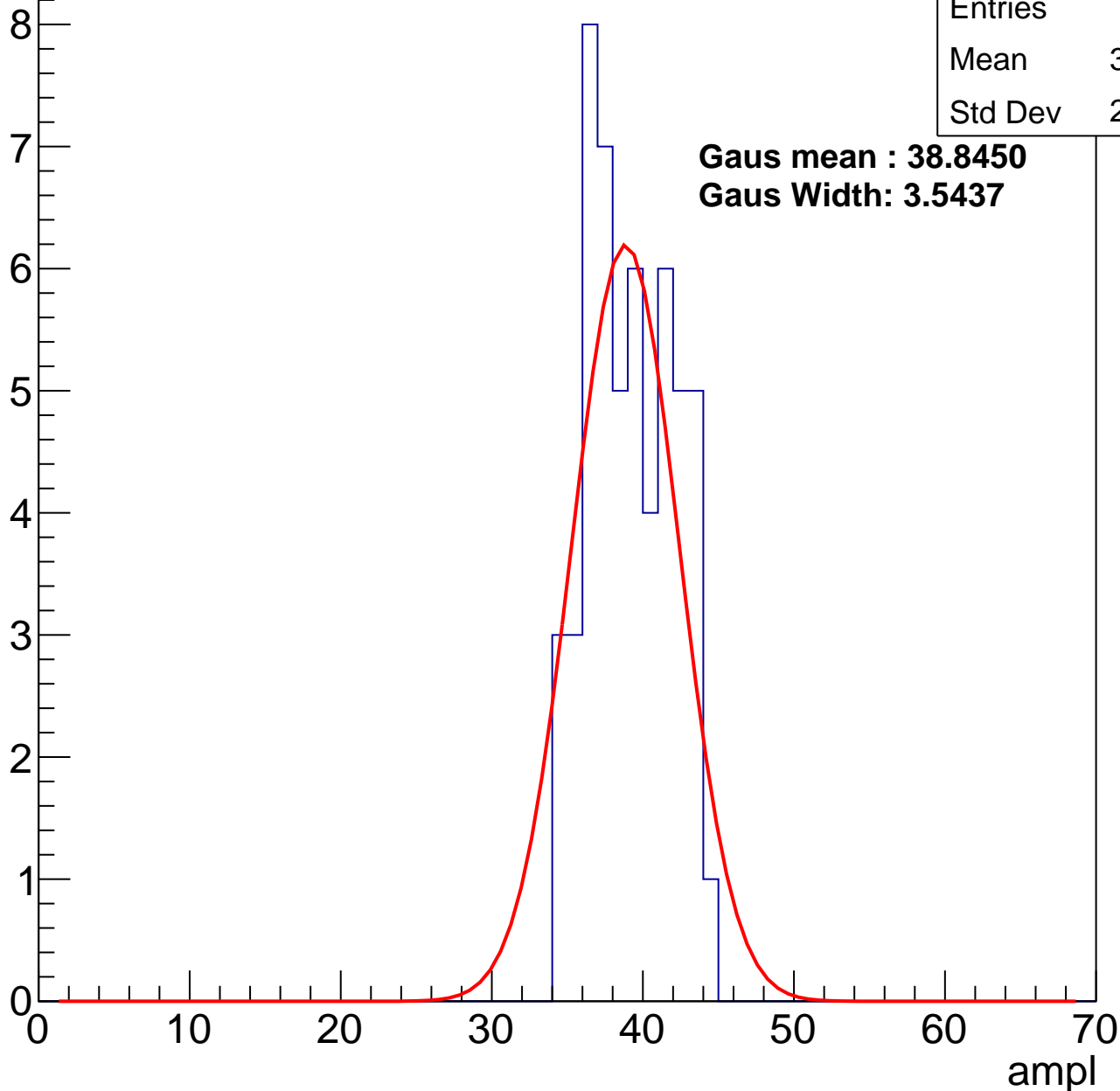
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	38.74
Std Dev	2.755

Gaus mean : 38.8450

Gaus Width: 3.5437



B0L002S, U4-ch122, adc2

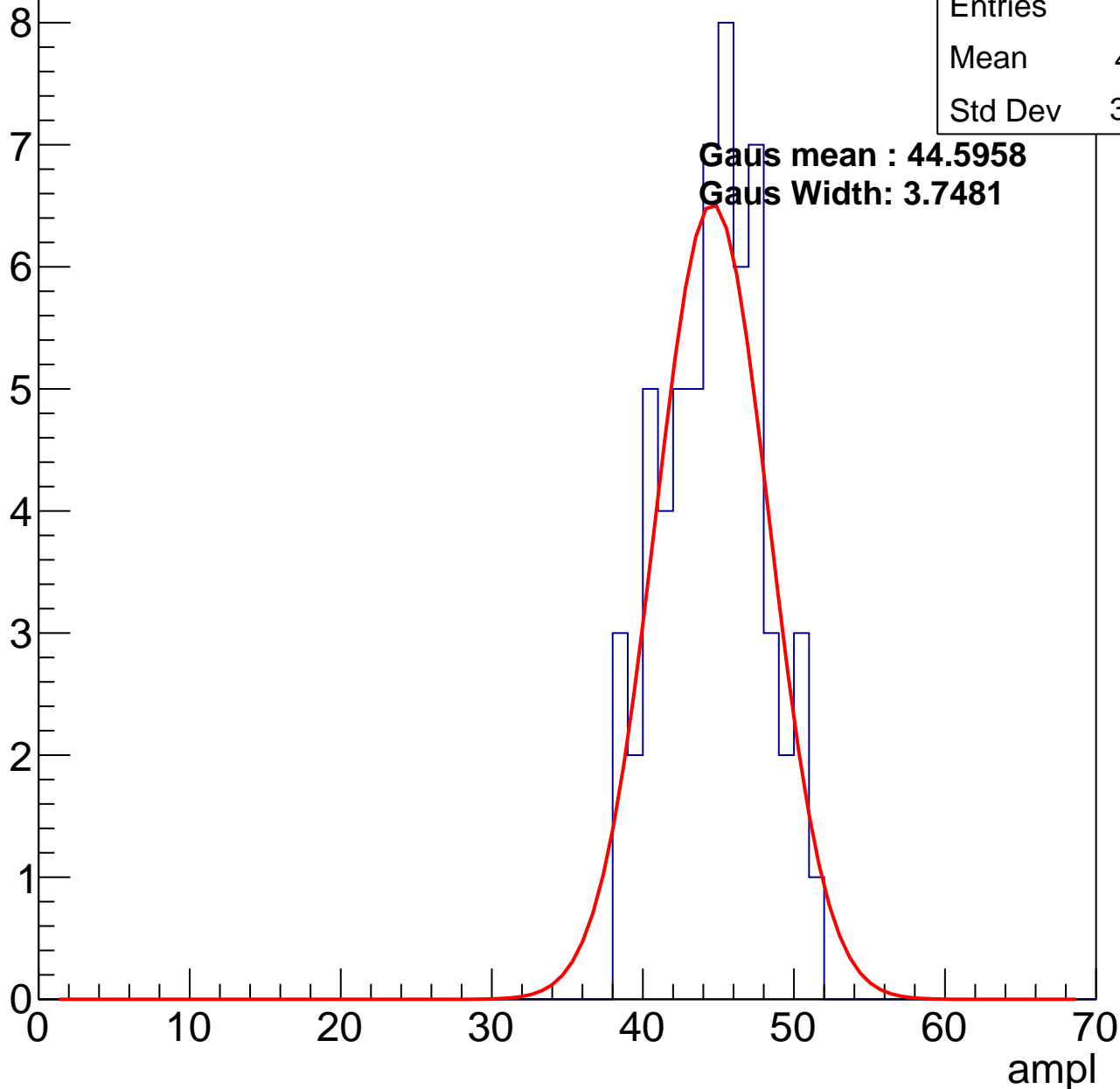
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.21
Std Dev	3.255

Gaus mean : 44.5958

Gaus Width: 3.7481

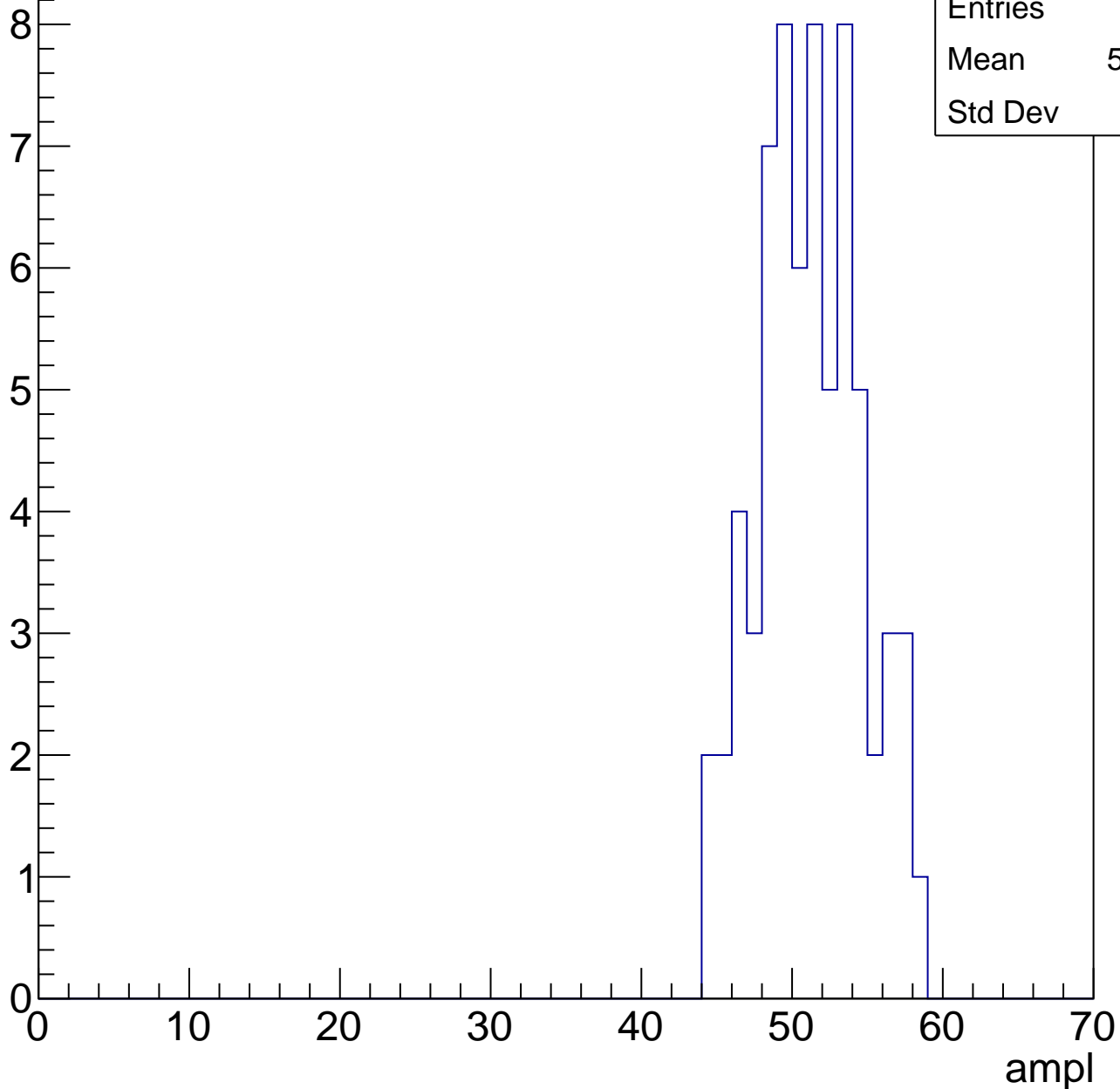


B0L002S, U4-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	50.75
Std Dev	3.37

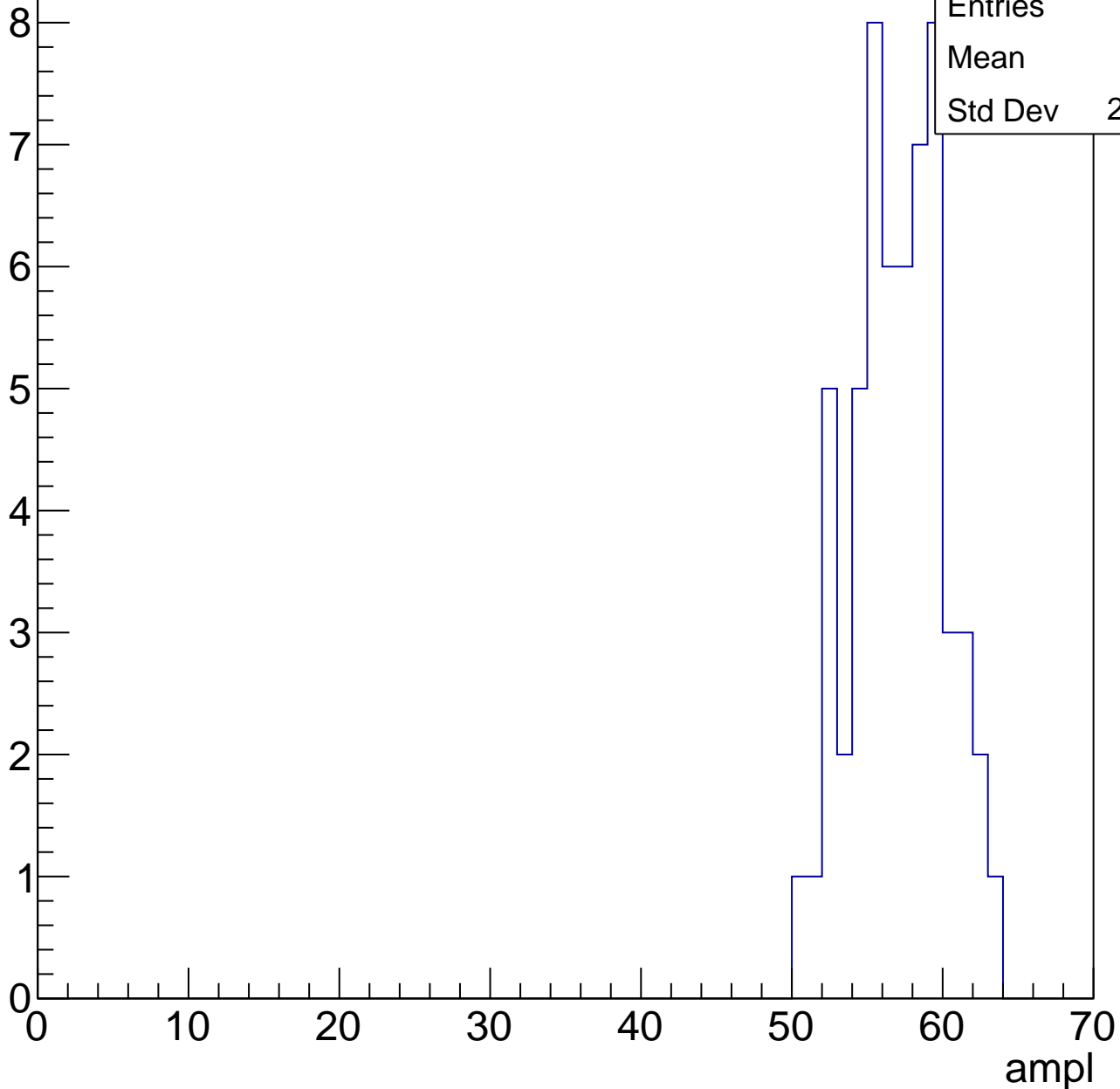


B0L002S, U4-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	56.6
Std Dev	2.982

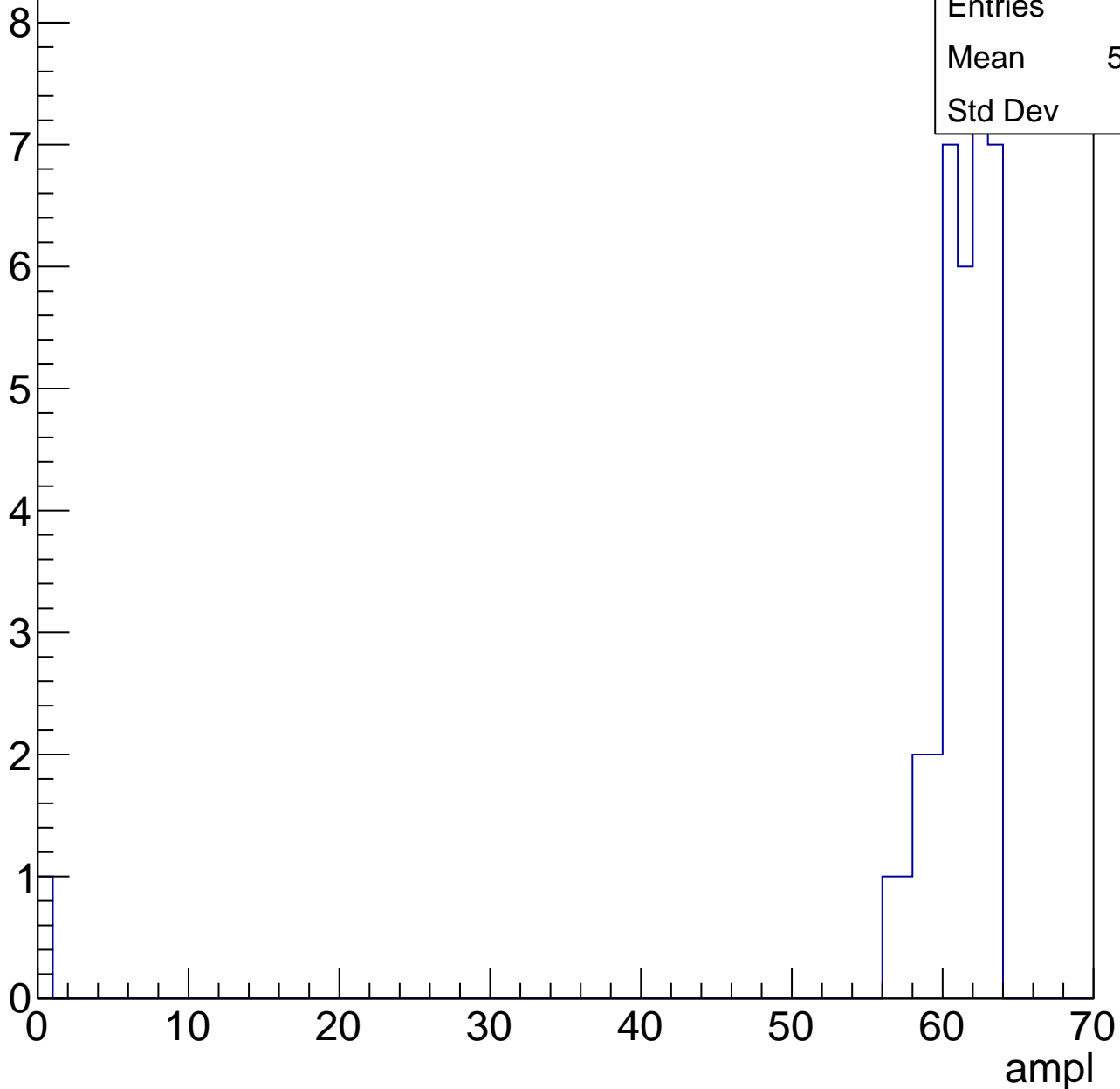


B0L002S, U4-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	59.14
Std Dev	10.3



B0L002S, U4-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch123, adc0

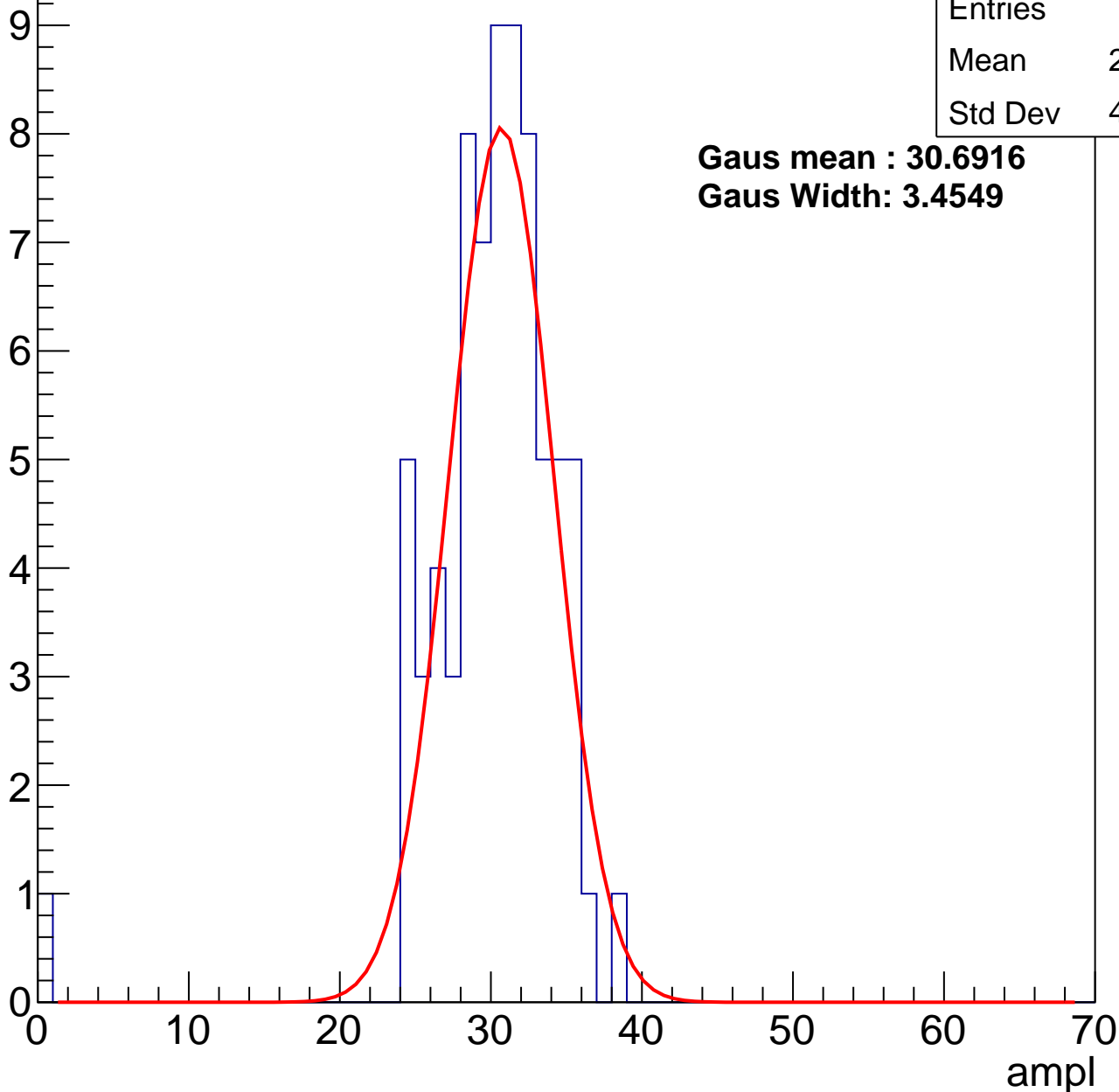
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.68
Std Dev	4.748

Gaus mean : 30.6916

Gaus Width: 3.4549



B0L002S, U4-ch123, adc1

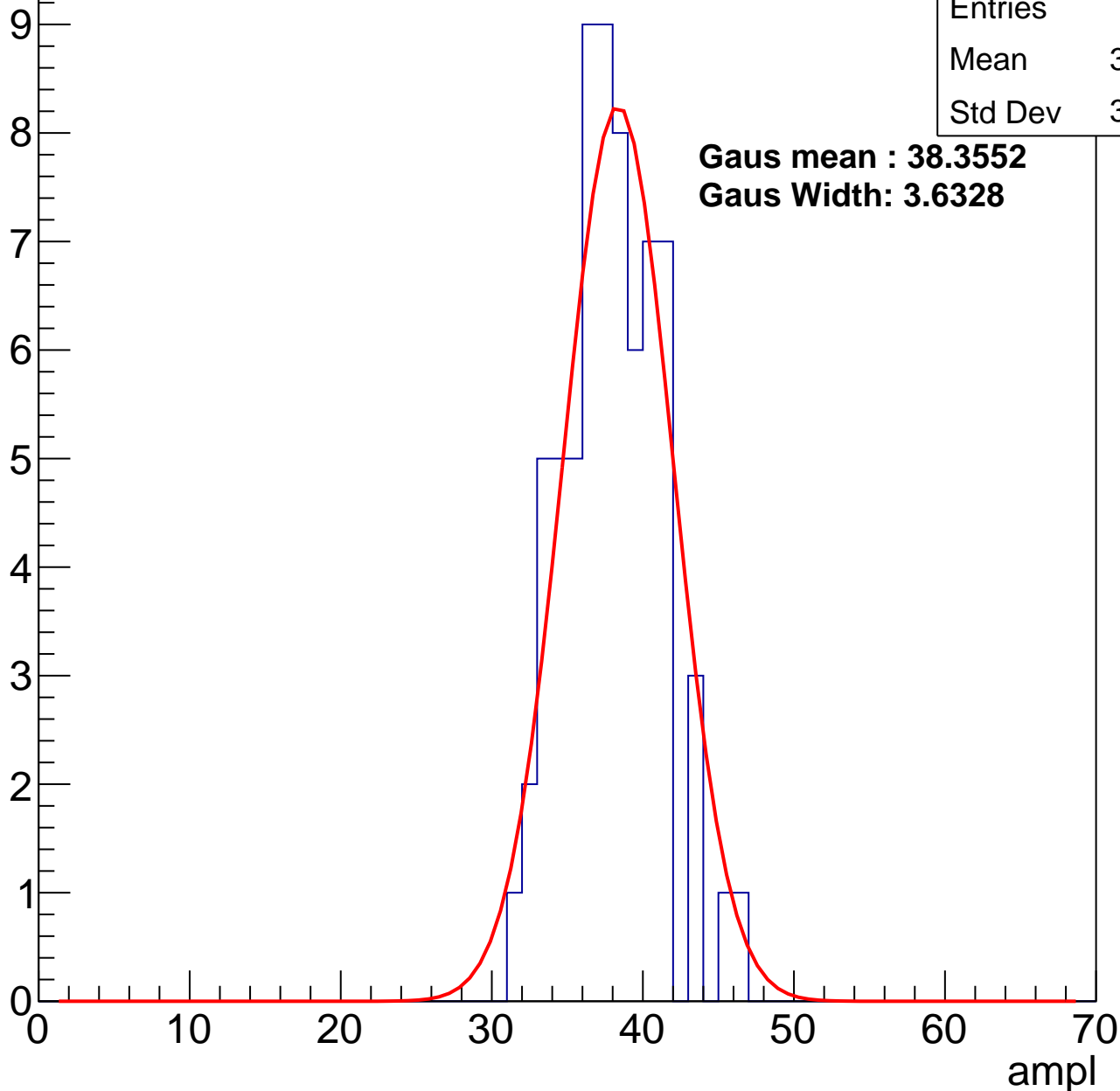
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.49
Std Dev	3.147

Gaus mean : 38.3552

Gaus Width: 3.6328



B0L002S, U4-ch123, adc2

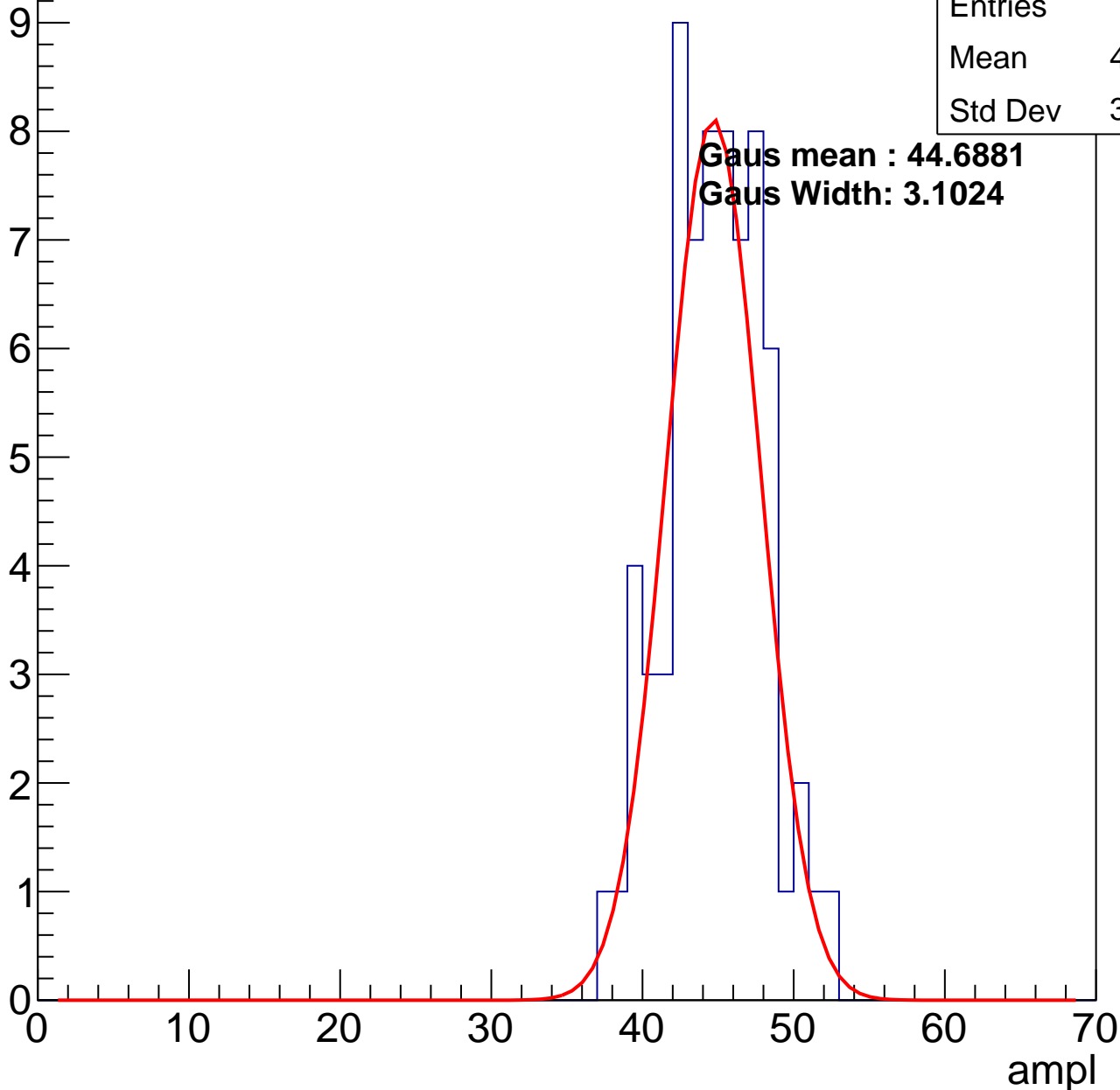
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.33
Std Dev	3.184

Gaus mean : 44.6881

Gaus Width: 3.1024

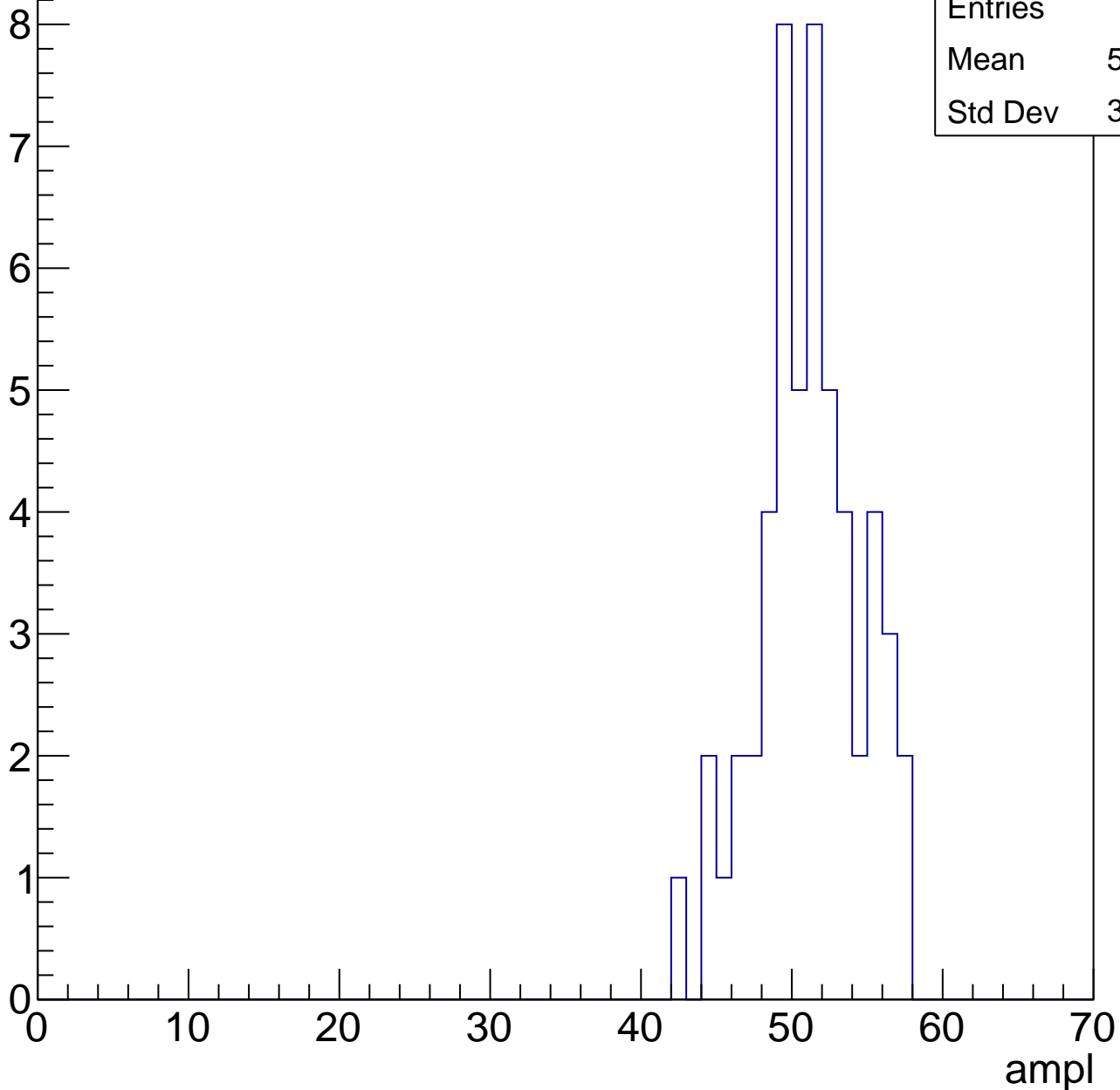


B0L002S, U4-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

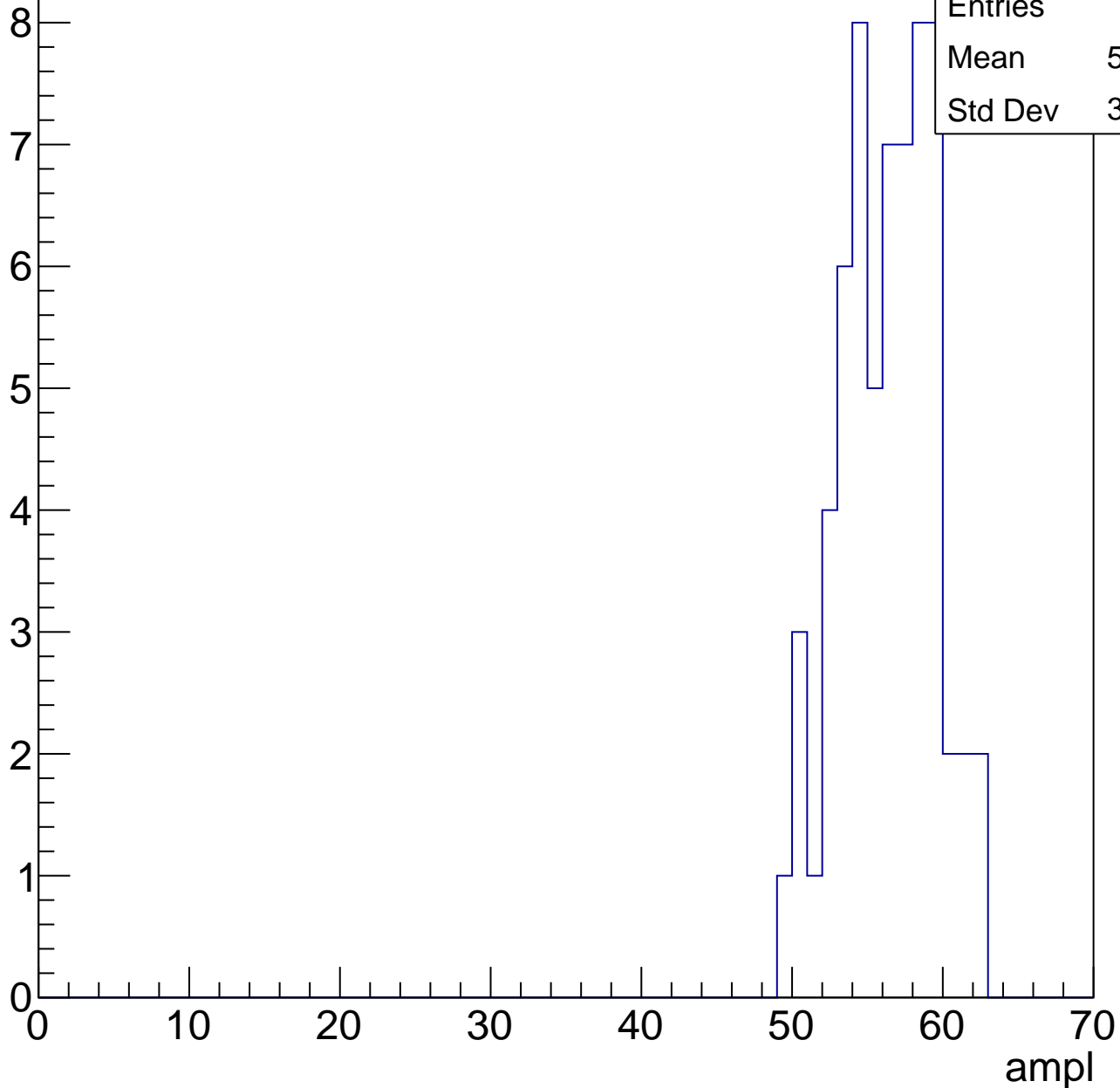
Entries	53
Mean	50.66
Std Dev	3.403



B0L002S, U4-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



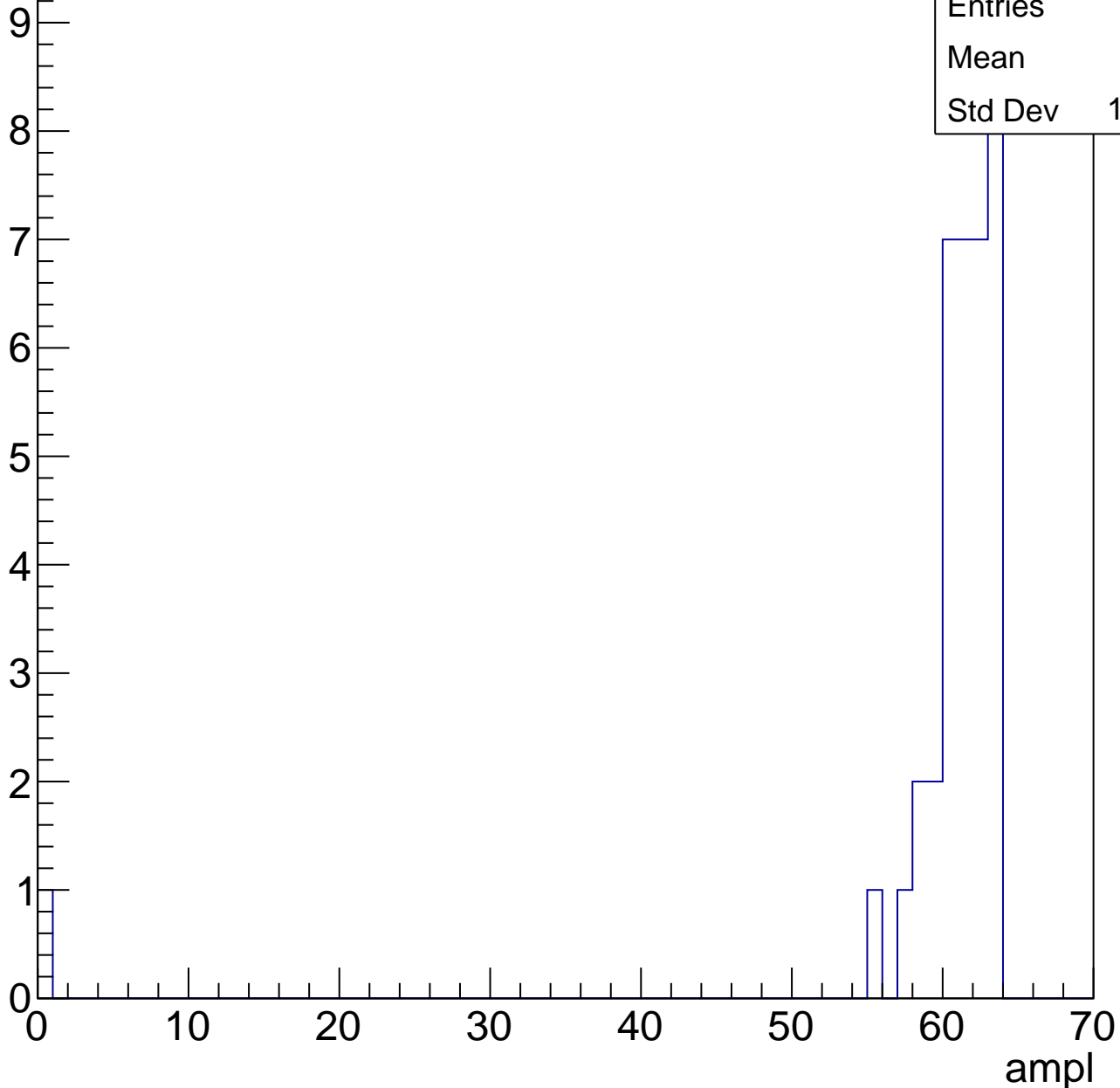
Entries	64
Mean	55.88
Std Dev	3.064

B0L002S, U4-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.3
Std Dev	10.06



B0L002S, U4-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U4-ch124, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	30.43
Std Dev	5.081

Gaus mean : 31.0314

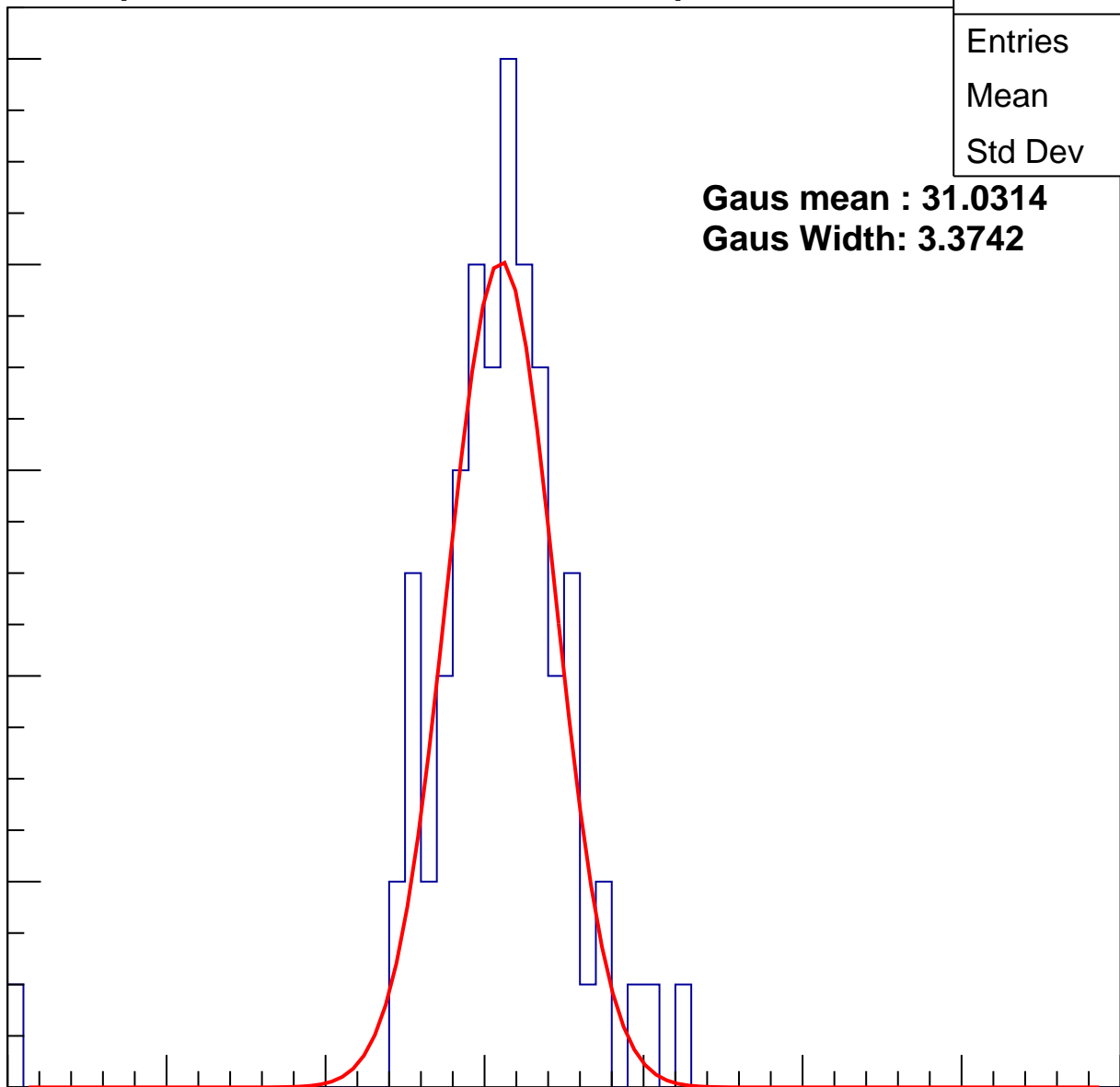
Gaus Width: 3.3742

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U4-ch124, adc1

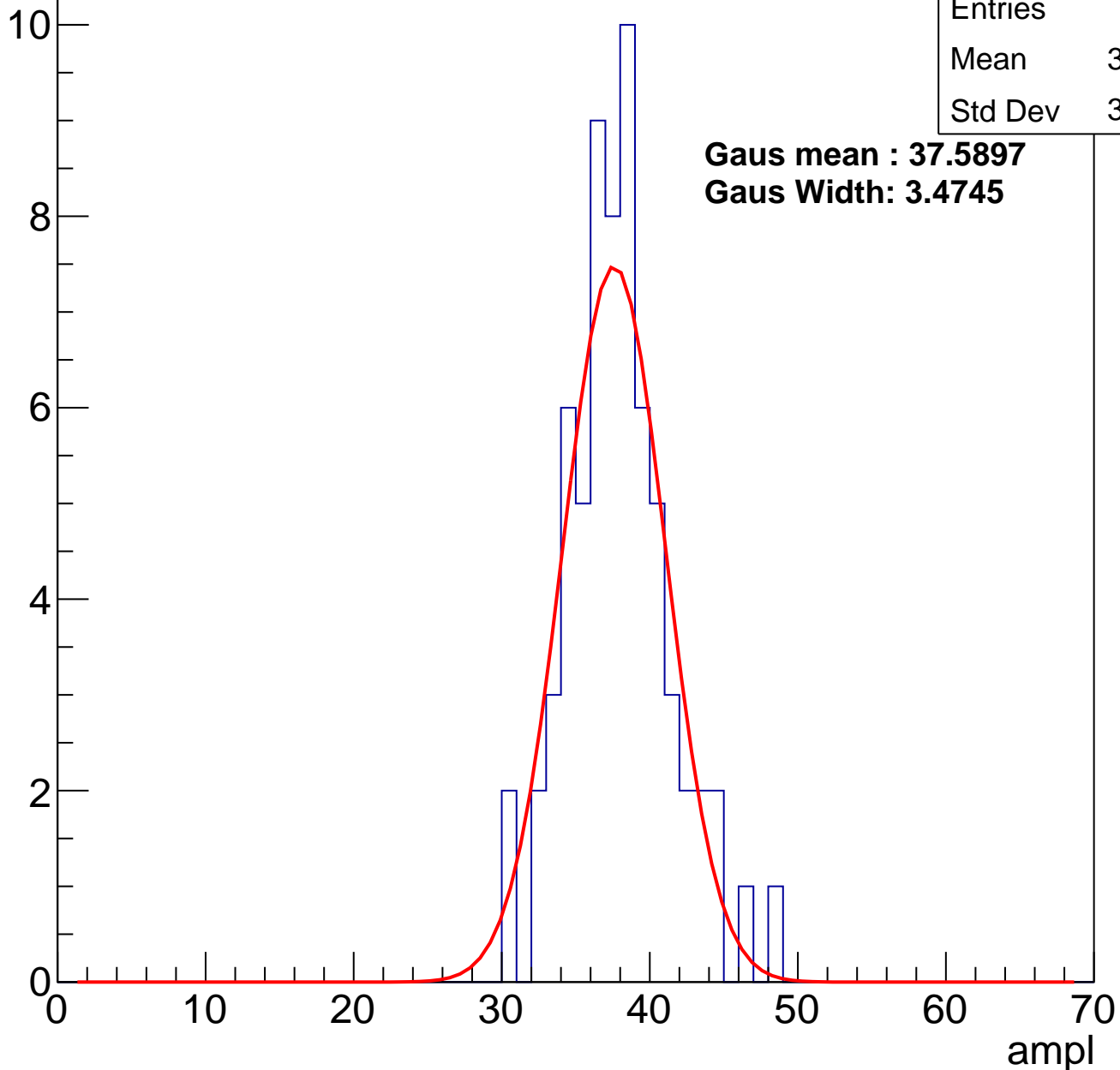
calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	37.48
Std Dev	3.483

Gaus mean : 37.5897

Gaus Width: 3.4745

Entry

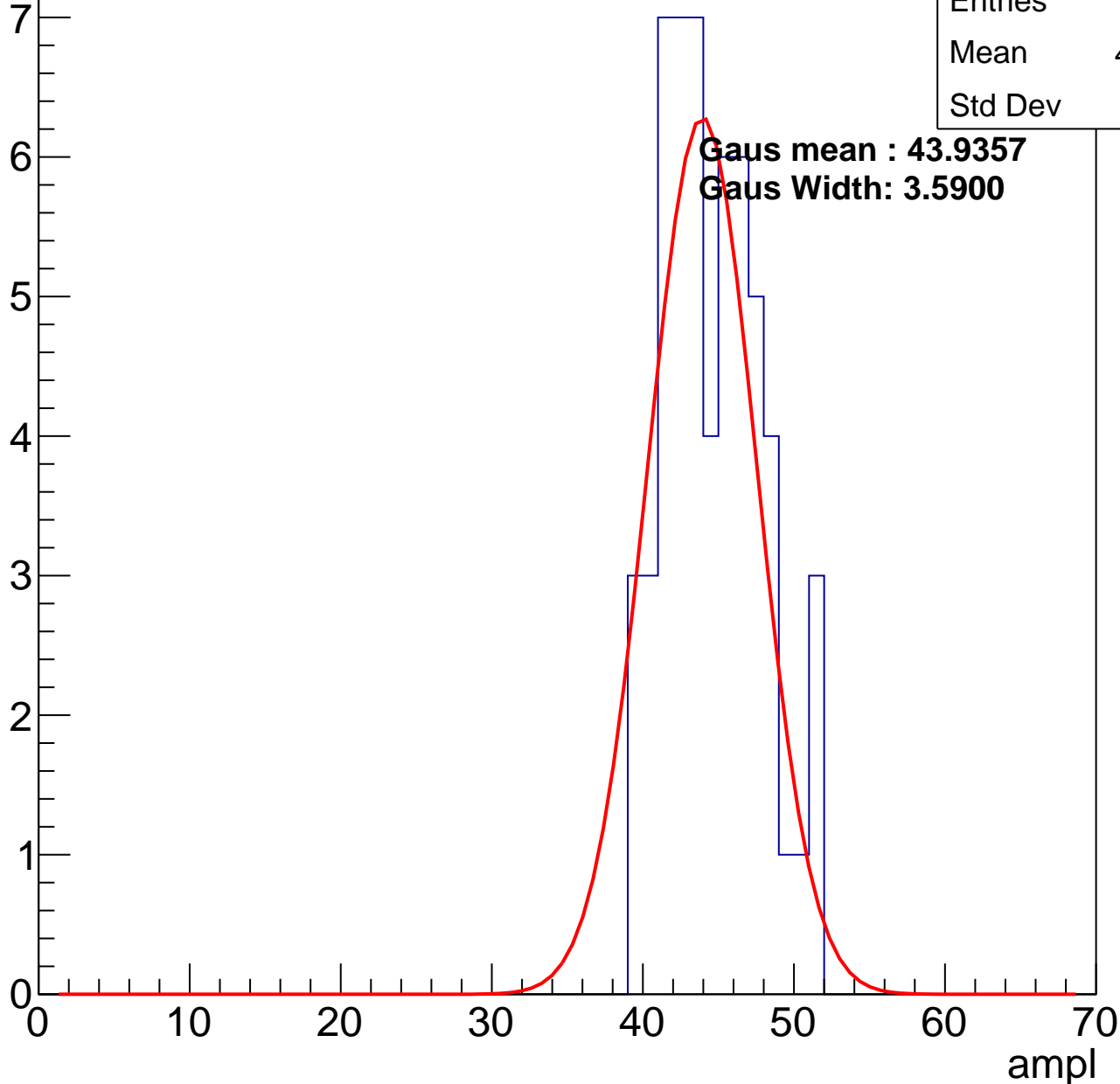


B0L002S, U4-ch124, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.21
Std Dev	3.15

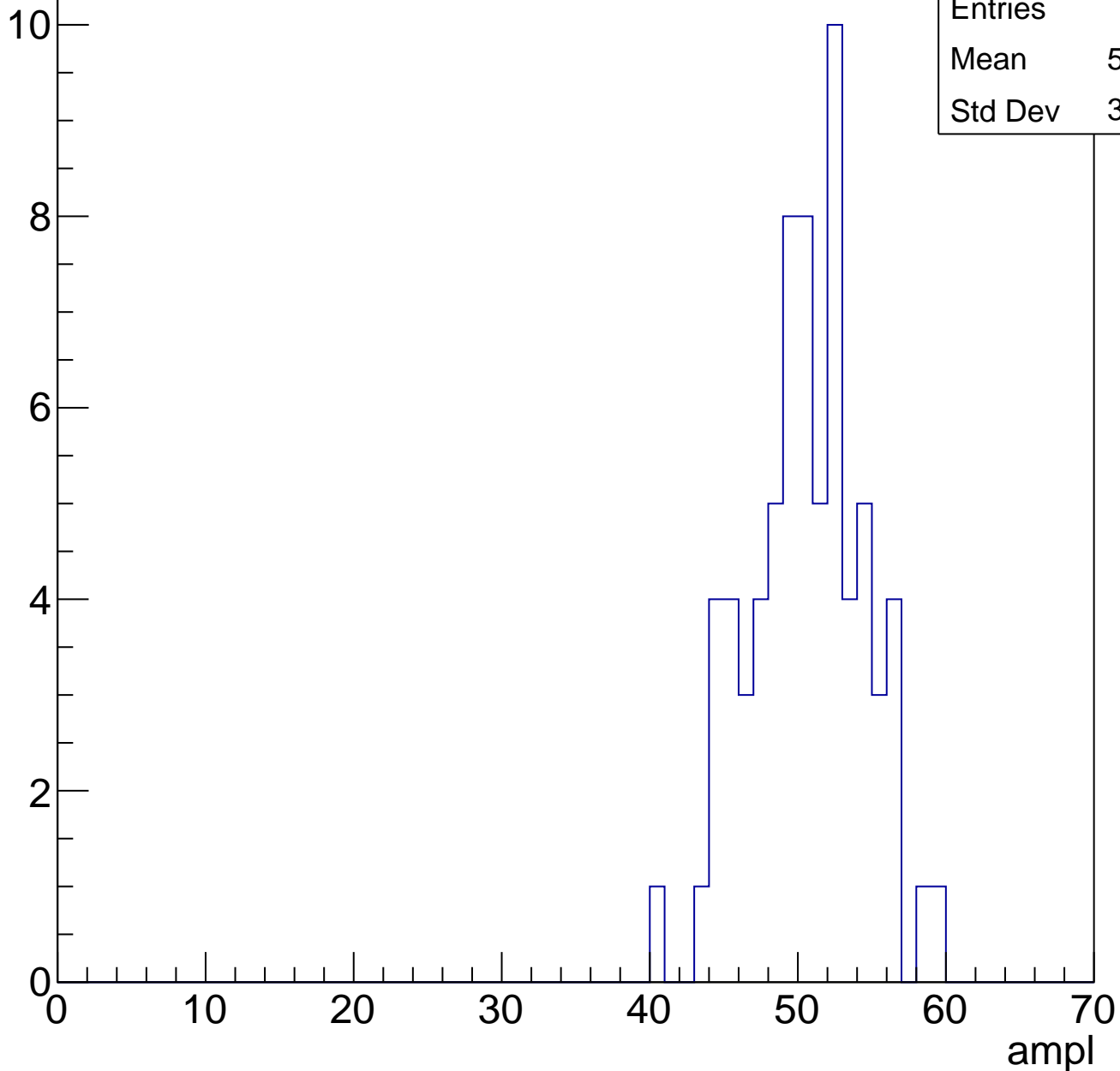


B0L002S, U4-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	50.14
Std Dev	3.806

Entry

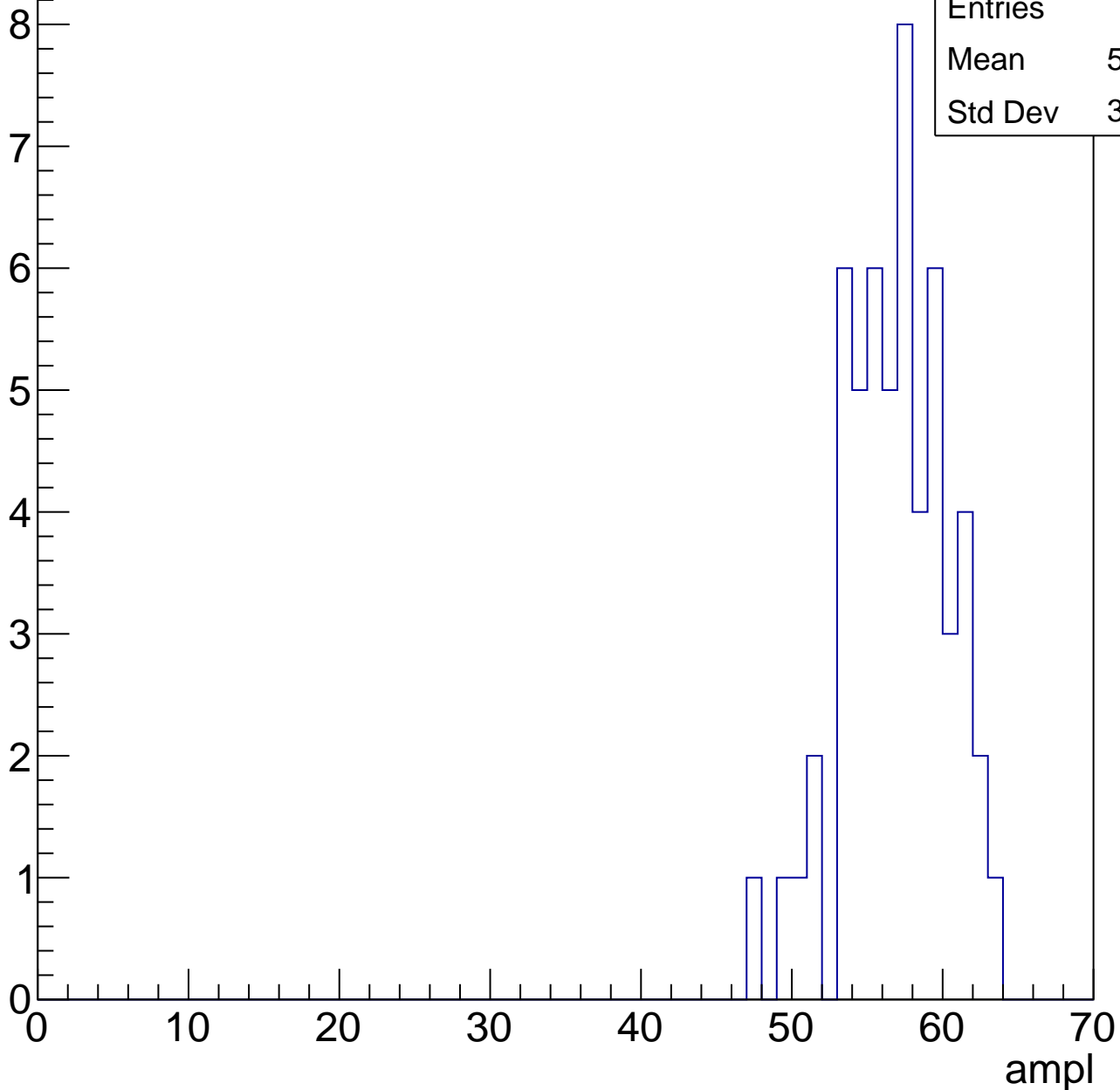


B0L002S, U4-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	56.35
Std Dev	3.402

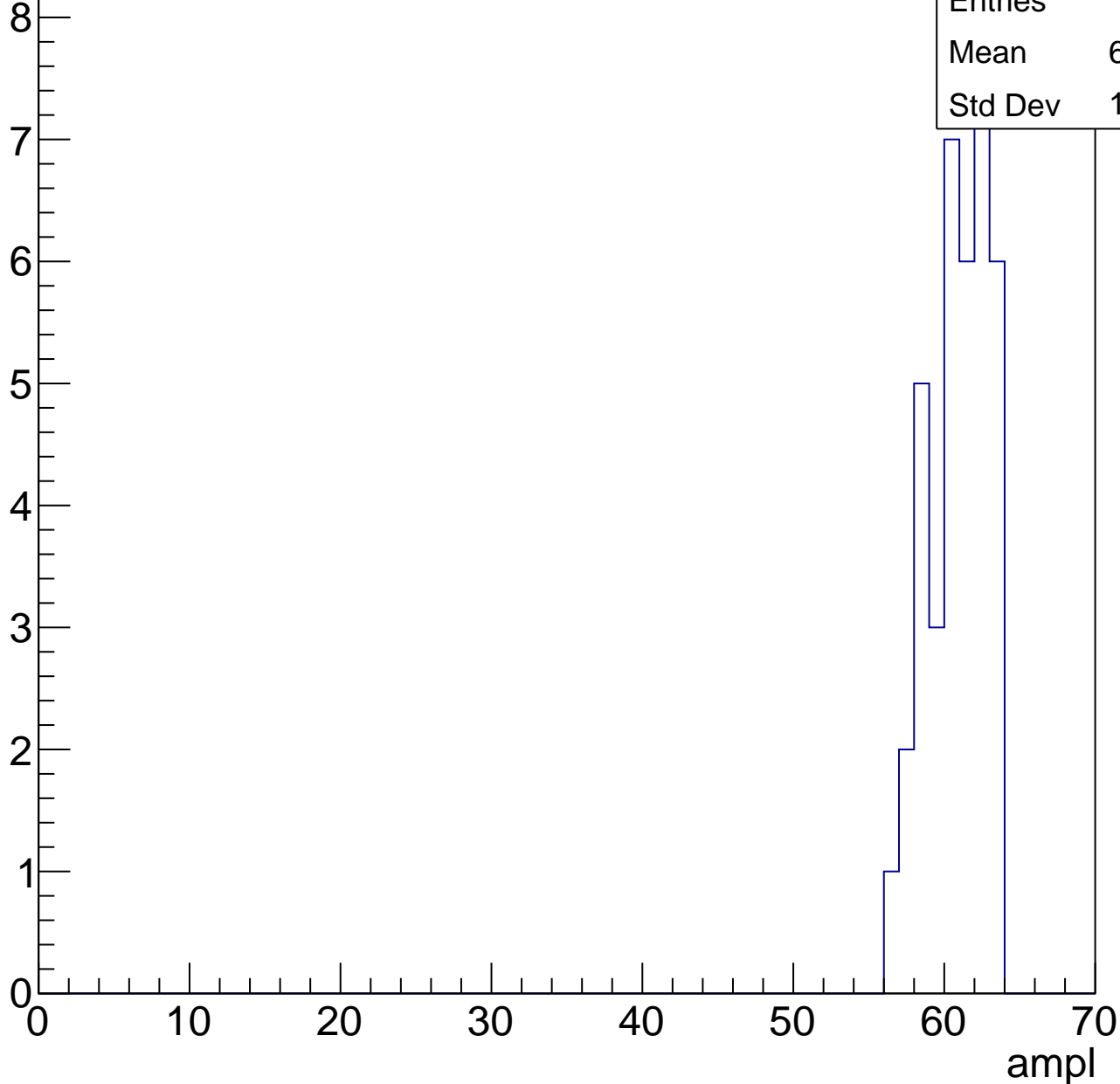


B0L002S, U4-ch124, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

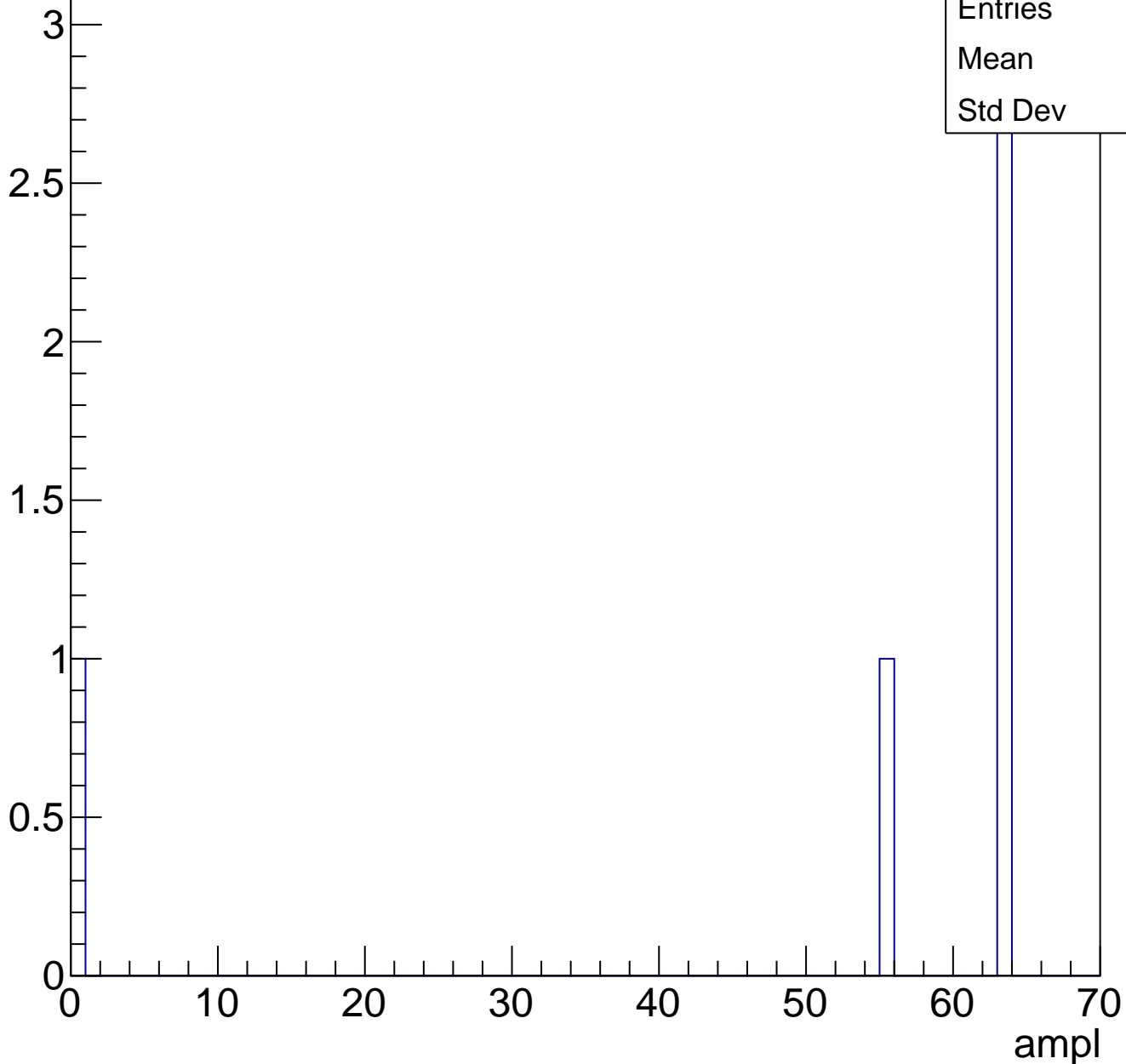
Entries	38
Mean	60.45
Std Dev	1.929



B0L002S, U4-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch125, adc0

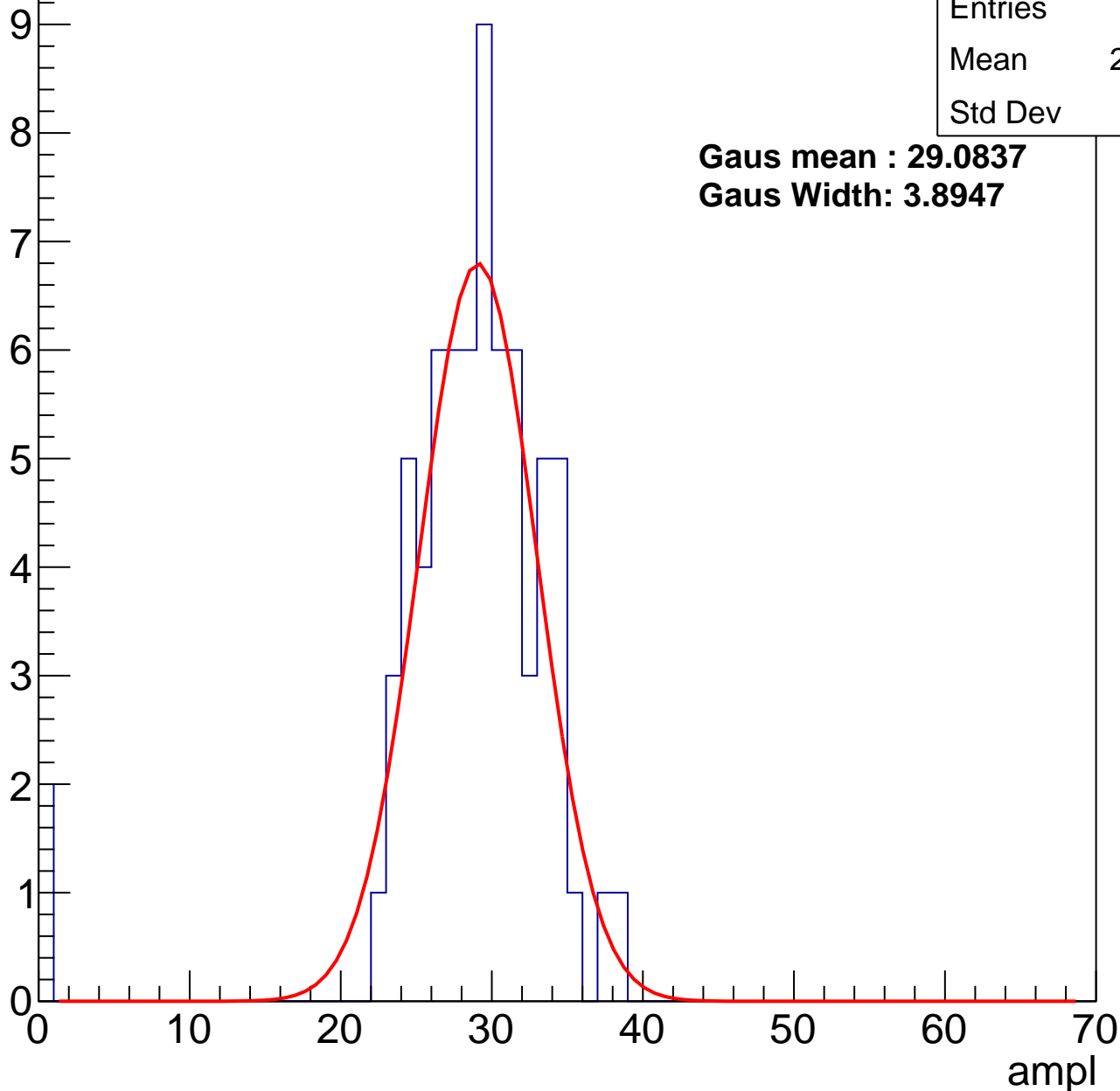
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.07
Std Dev	5.97

Gaus mean : 29.0837

Gaus Width: 3.8947



B0L002S, U4-ch125, adc1

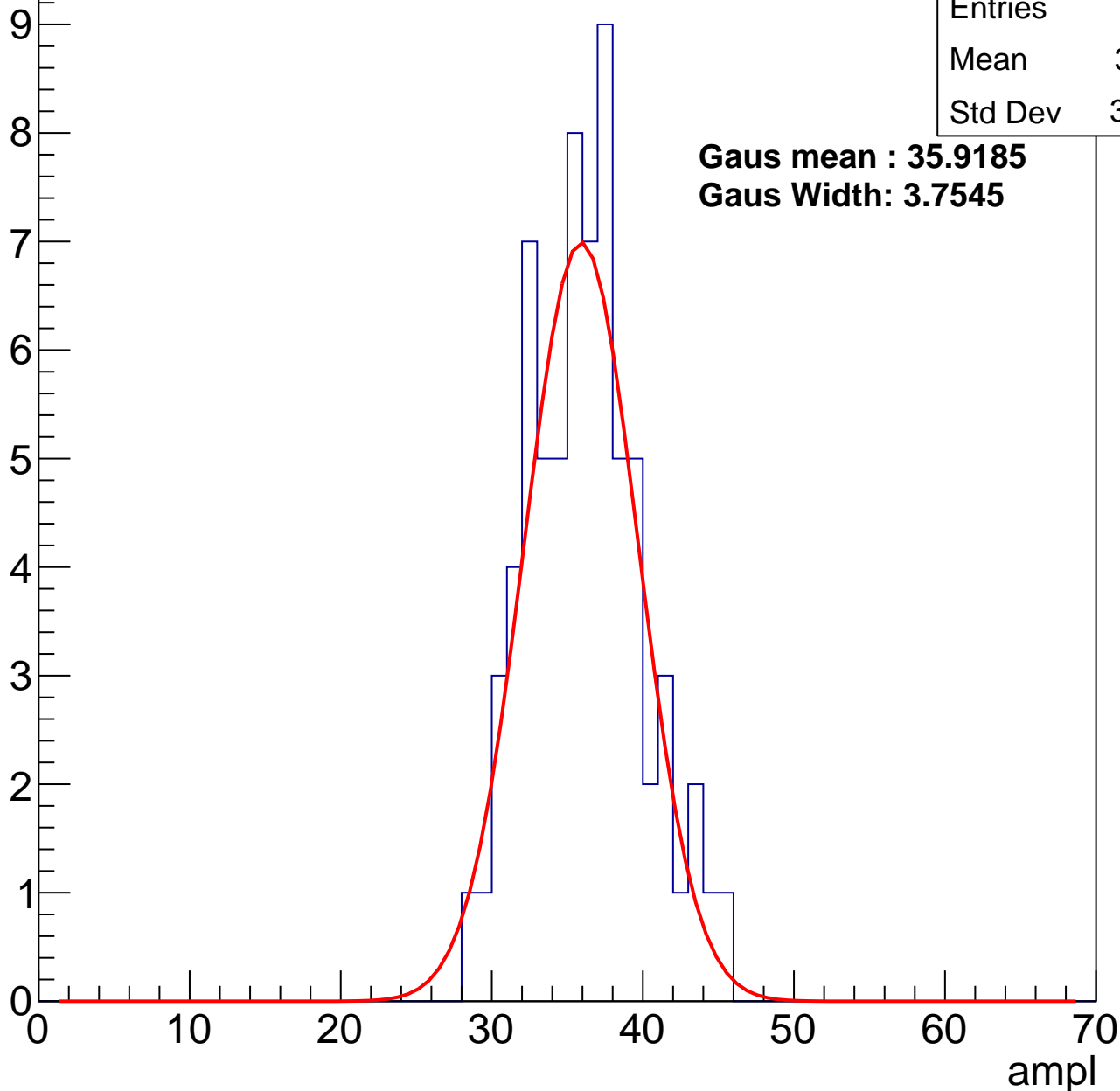
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	35.71
Std Dev	3.704

Gaus mean : 35.9185

Gaus Width: 3.7545



B0L002S, U4-ch125, adc2

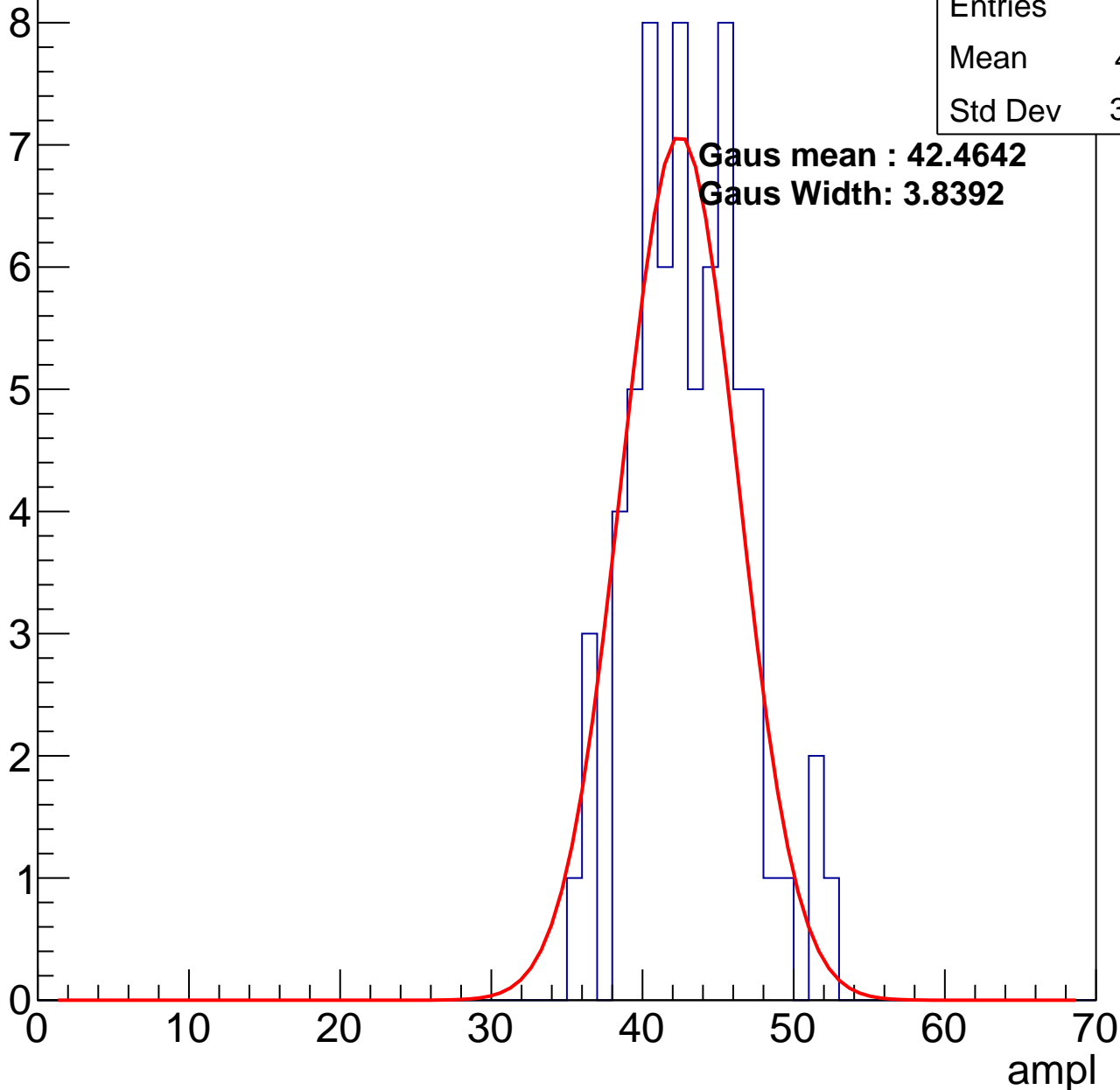
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	42.71
Std Dev	3.656

Gaus mean : 42.4642

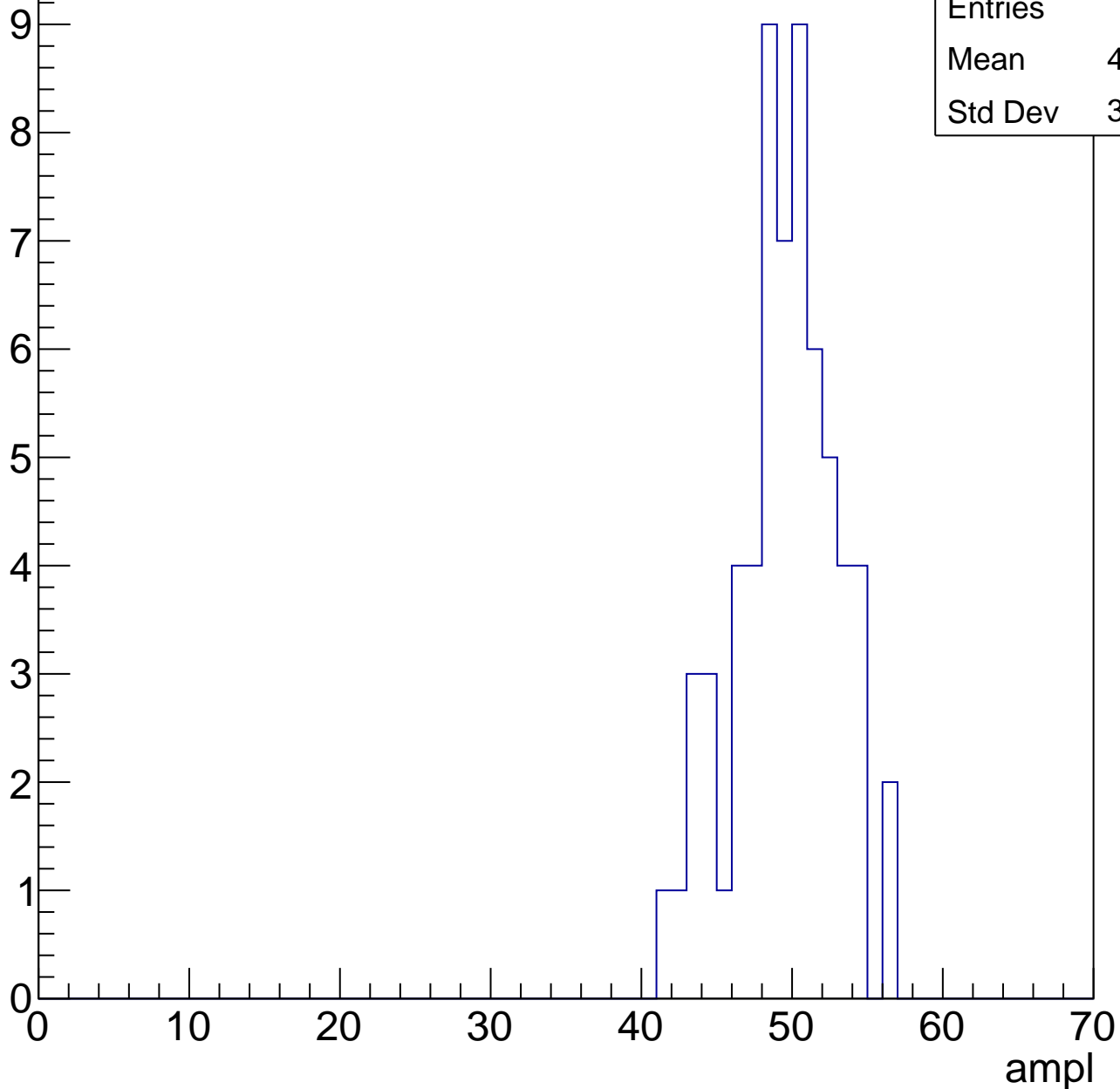
Gaus Width: 3.8392



B0L002S, U4-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

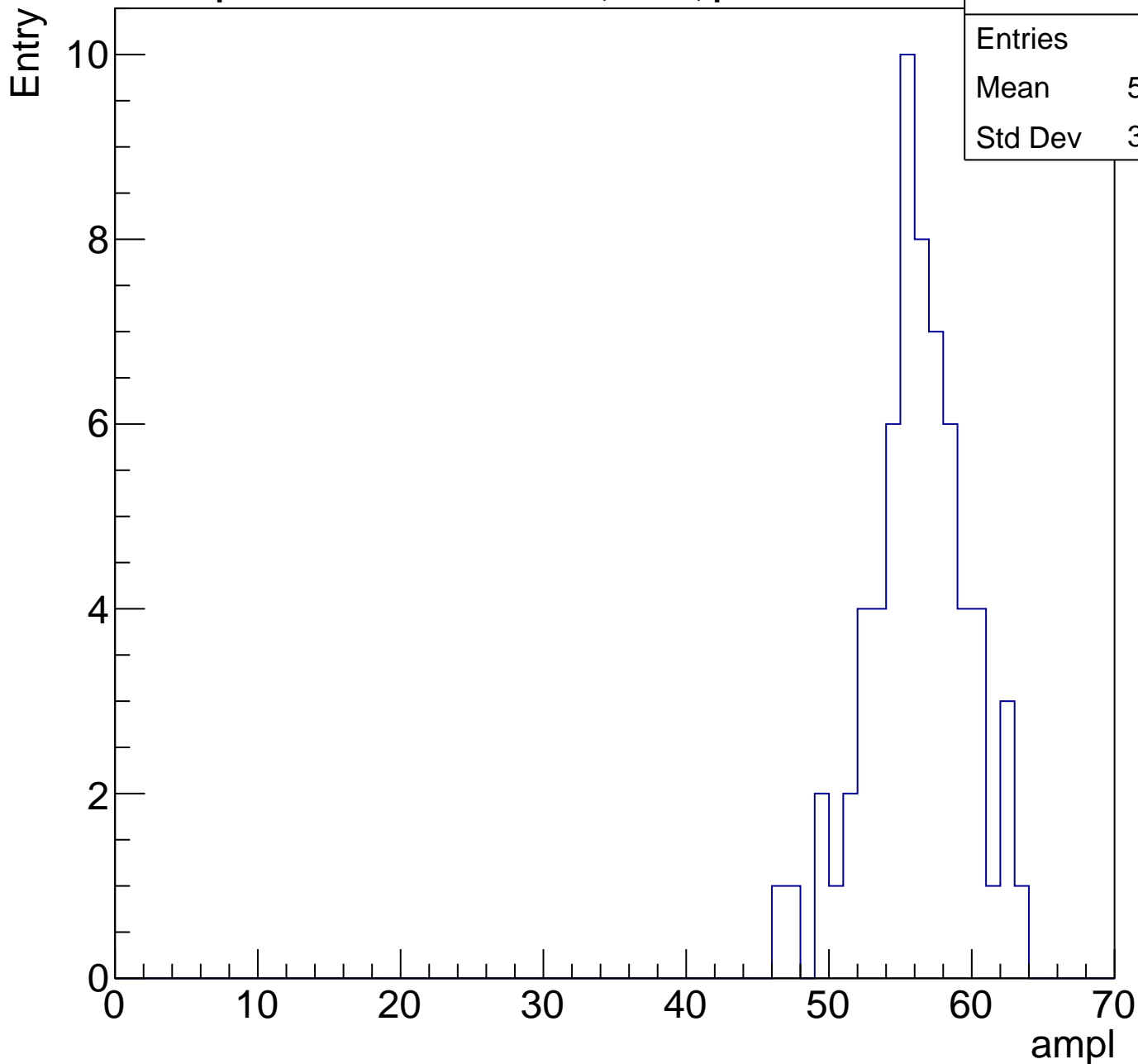


Entries	63
Mean	49.08
Std Dev	3.363

B0L002S, U4-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	55.66
Std Dev	3.518

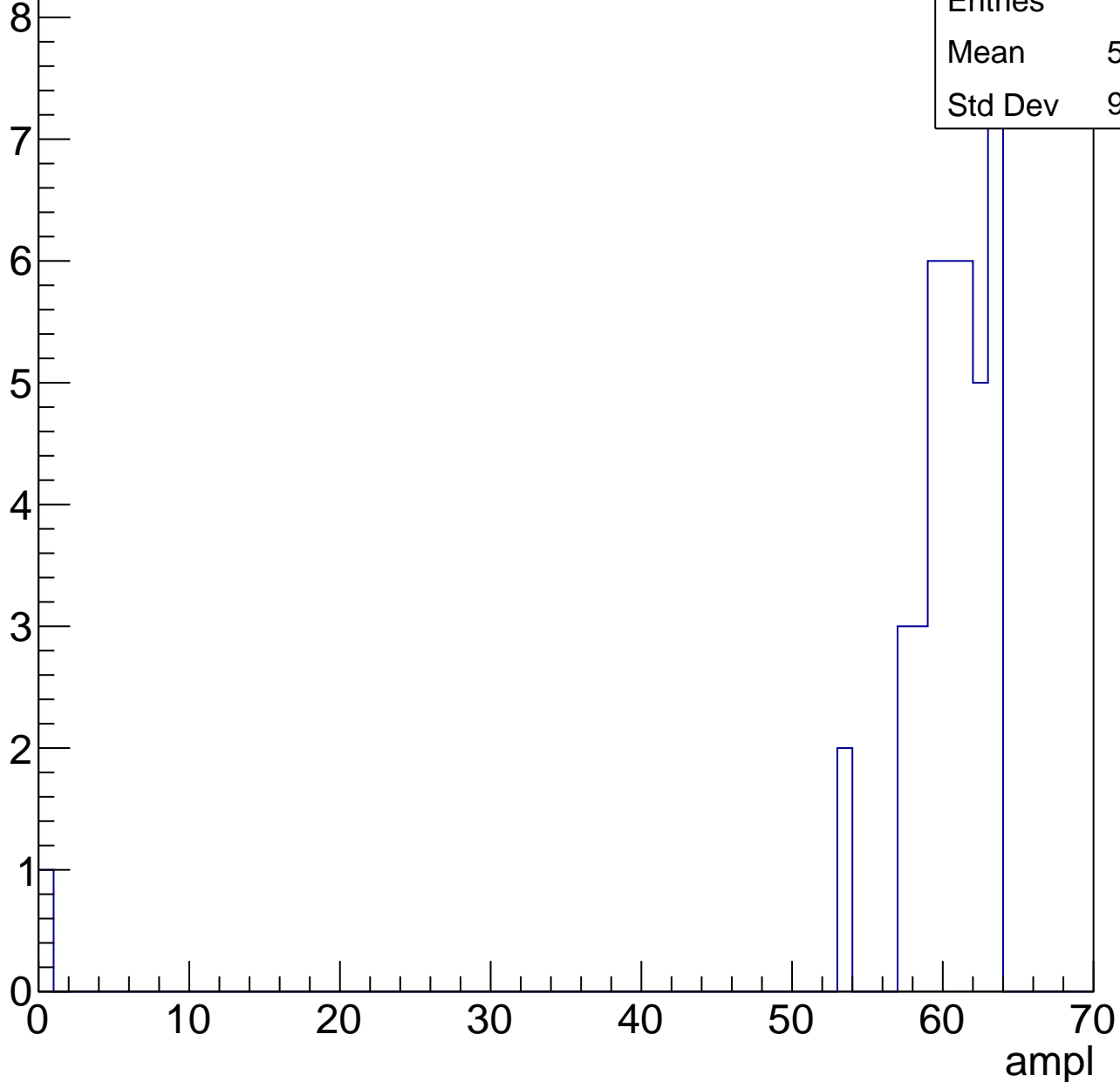


B0L002S, U4-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

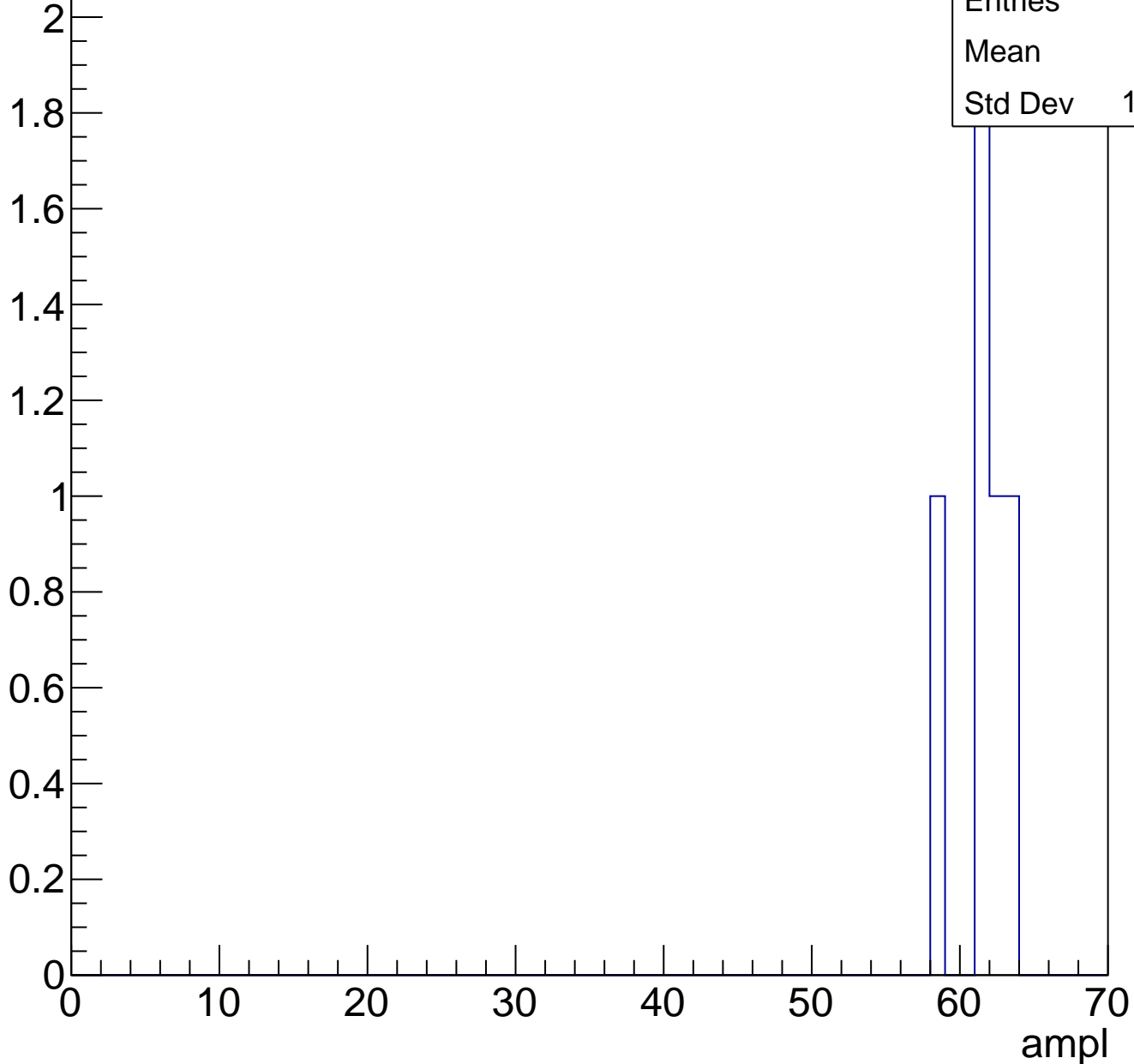
Entries	40
Mean	58.62
Std Dev	9.702



B0L002S, U4-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

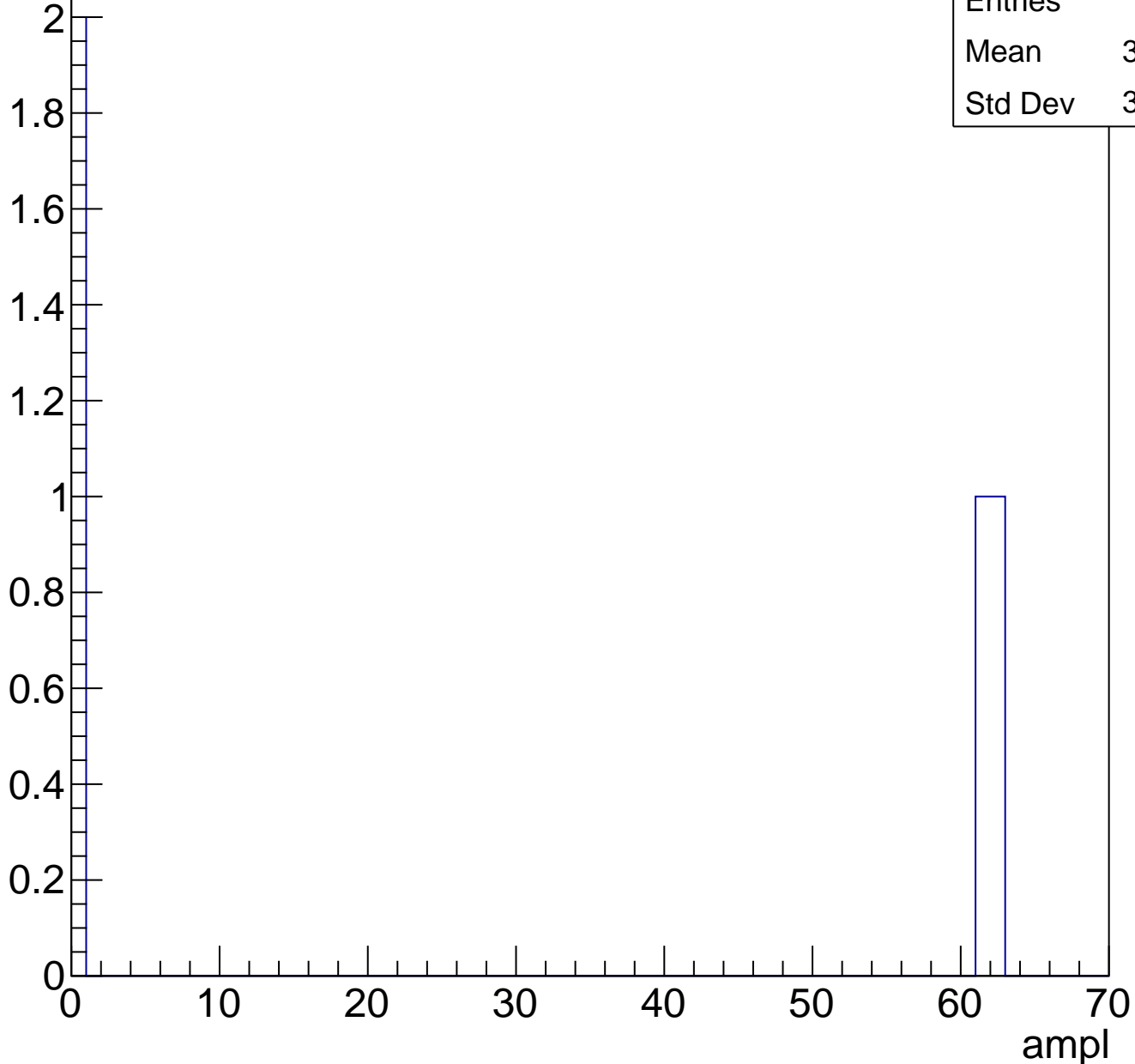
Entry



B0L002S, U4-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	30.75
Std Dev	30.75

B0L002S, U4-ch126, adc0

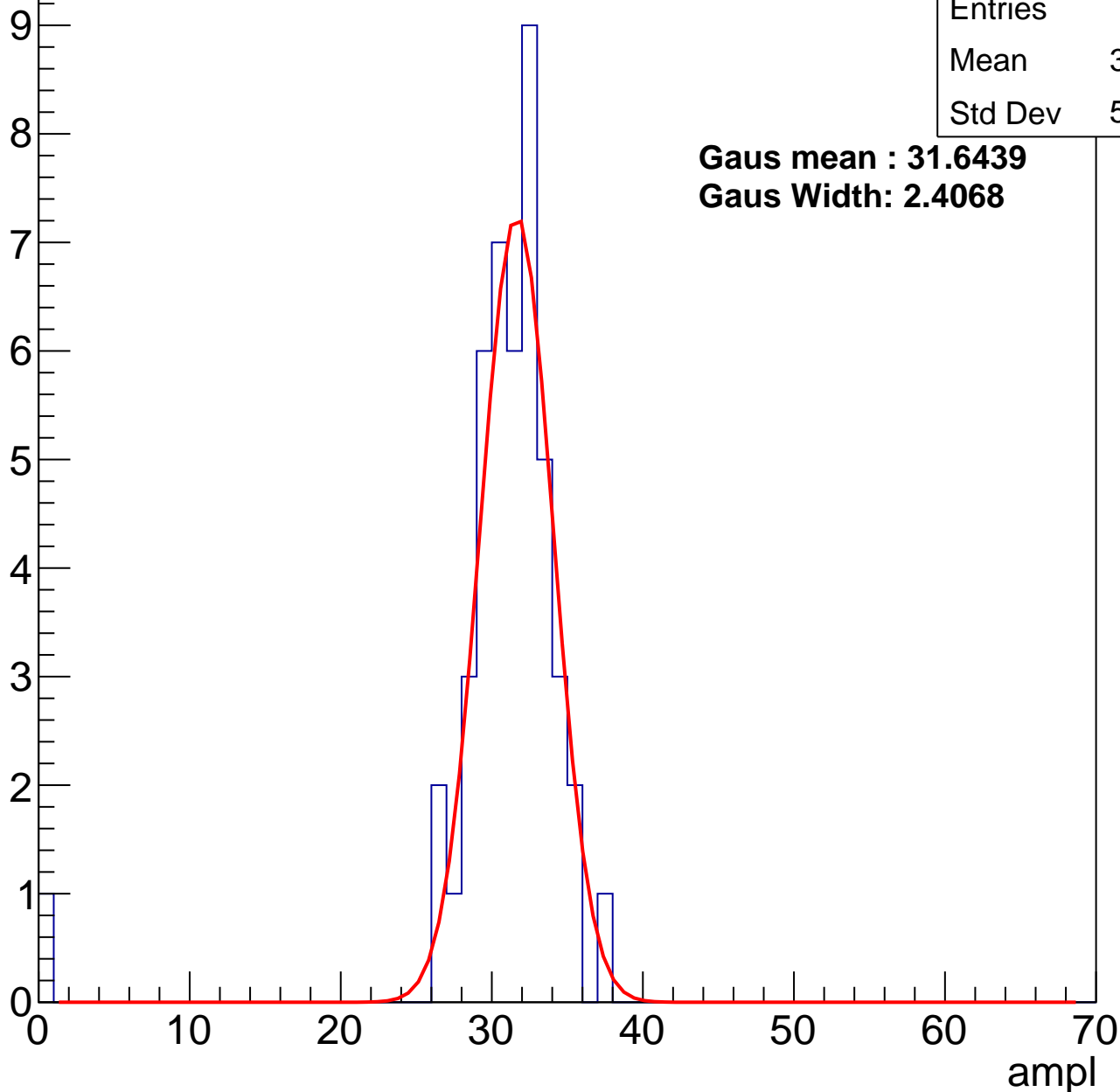
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	30.33
Std Dev	5.082

Gaus mean : 31.6439

Gaus Width: 2.4068

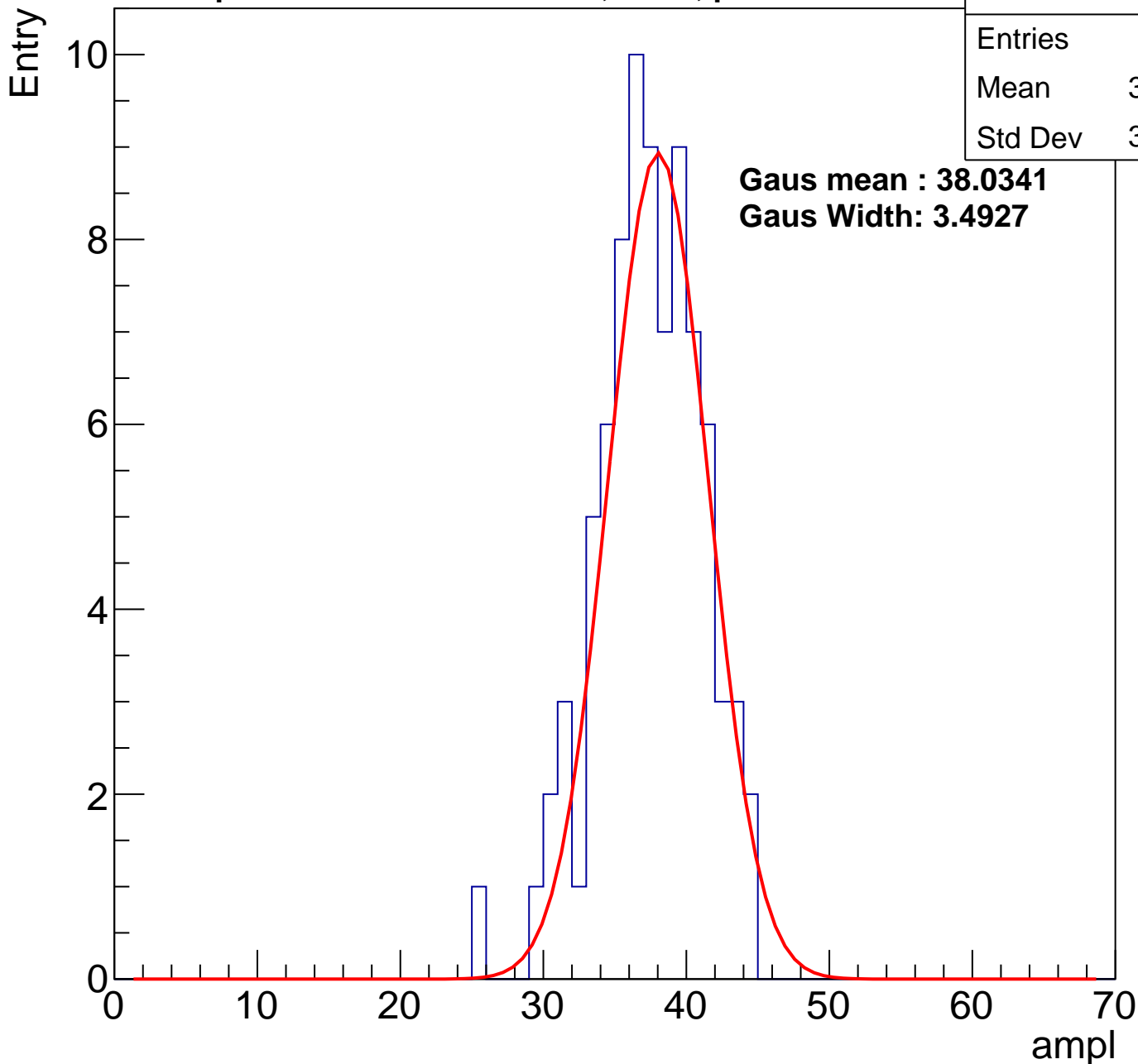


B0L002S, U4-ch126, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	36.95
Std Dev	3.637

Gaus mean : 38.0341
Gaus Width: 3.4927



B0L002S, U4-ch126, adc2

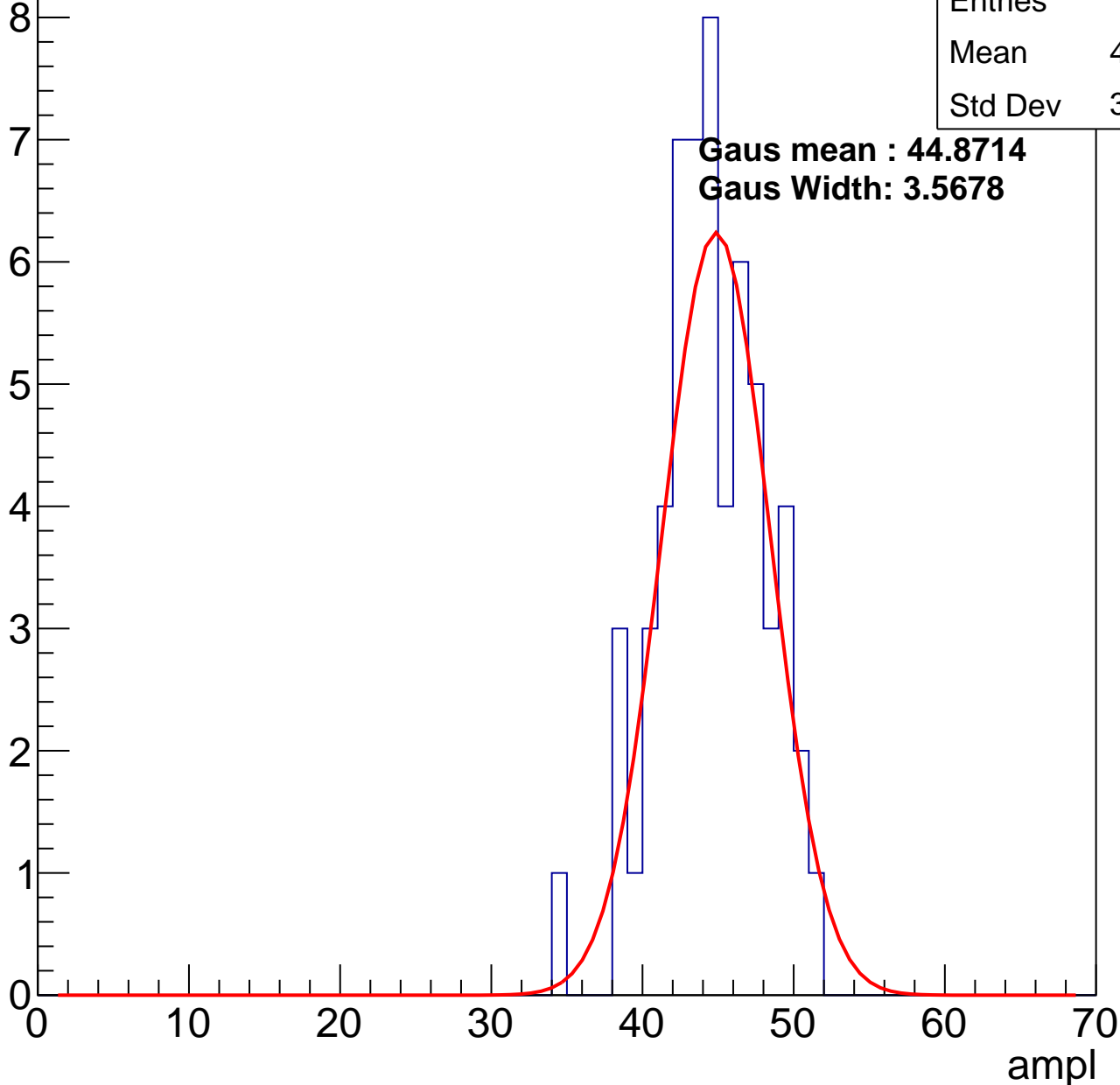
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	44.07
Std Dev	3.429

Gaus mean : 44.8714

Gaus Width: 3.5678

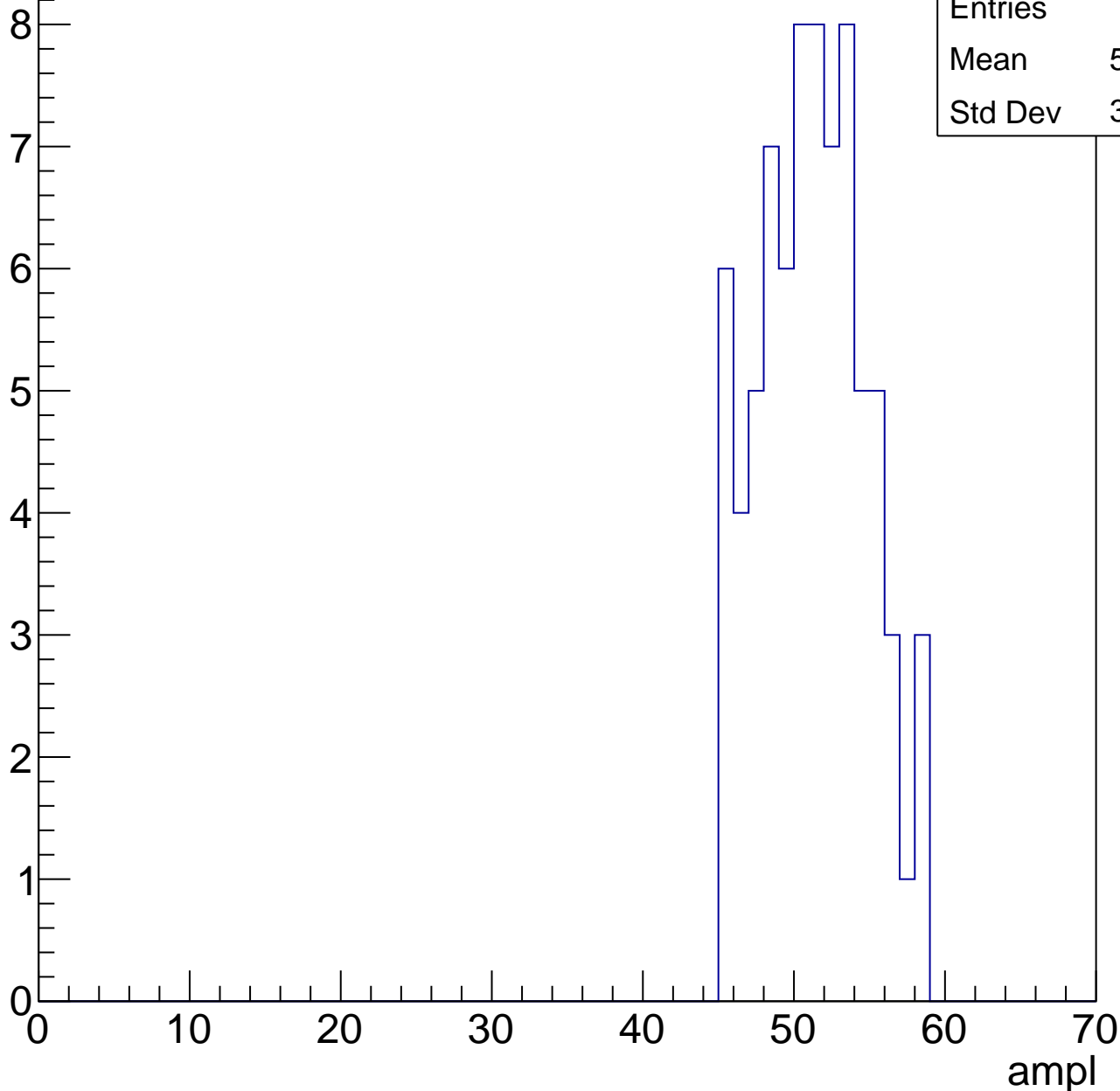


B0L002S, U4-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

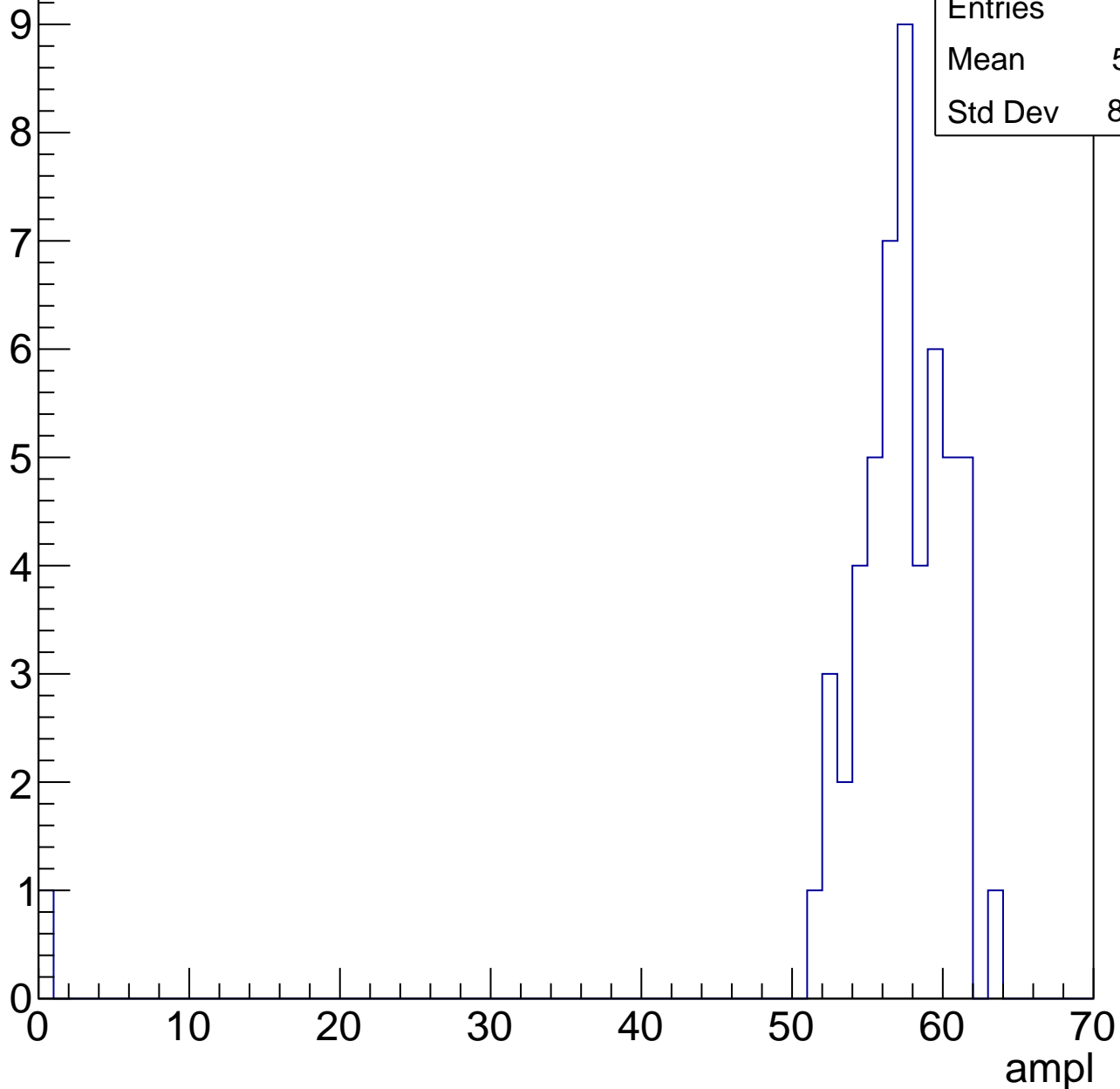
Entries	76
Mean	50.78
Std Dev	3.459



B0L002S, U4-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

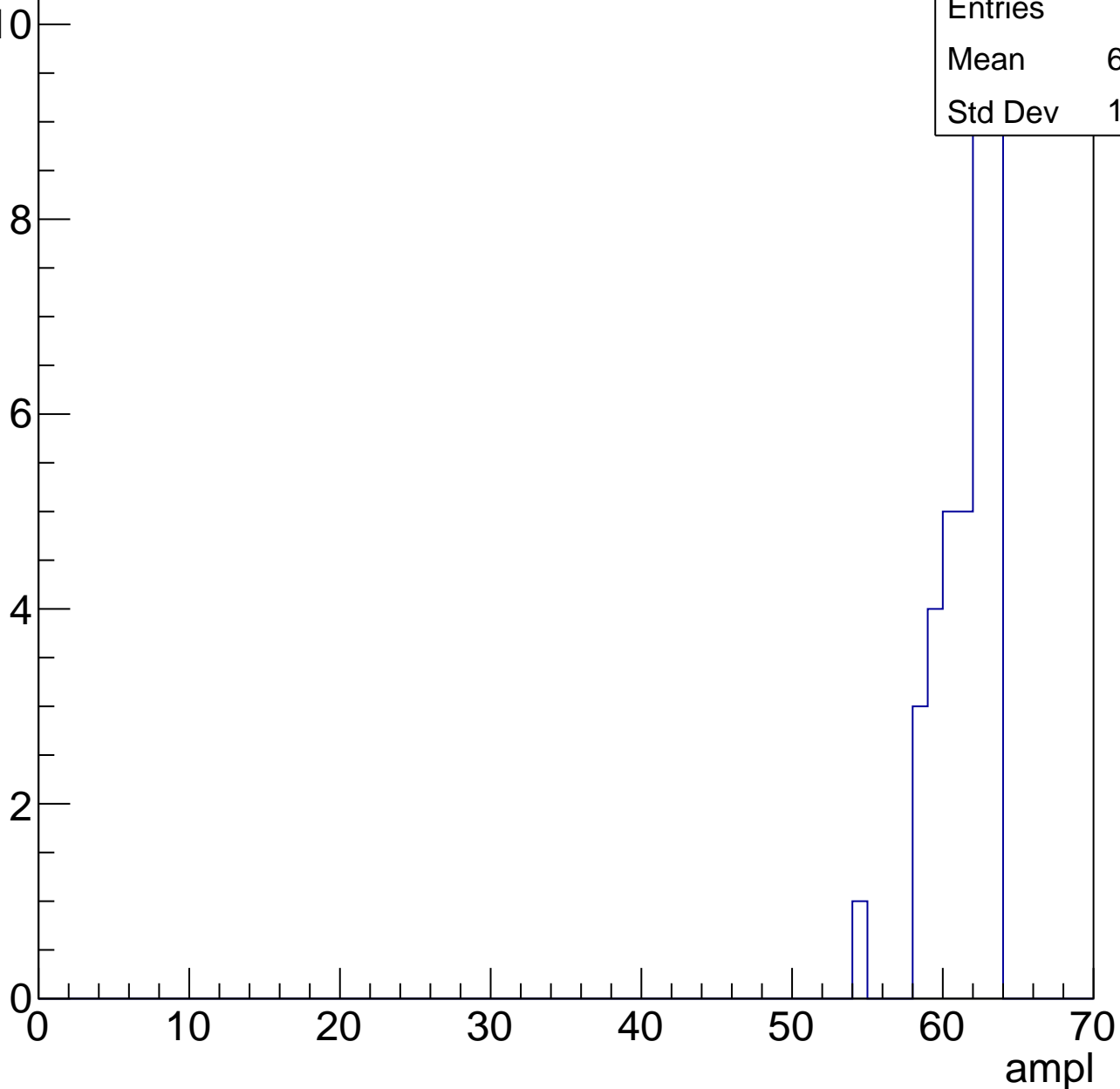


B0L002S, U4-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	60.97
Std Dev	1.966



B0L002S, U4-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U4-ch127, adc0

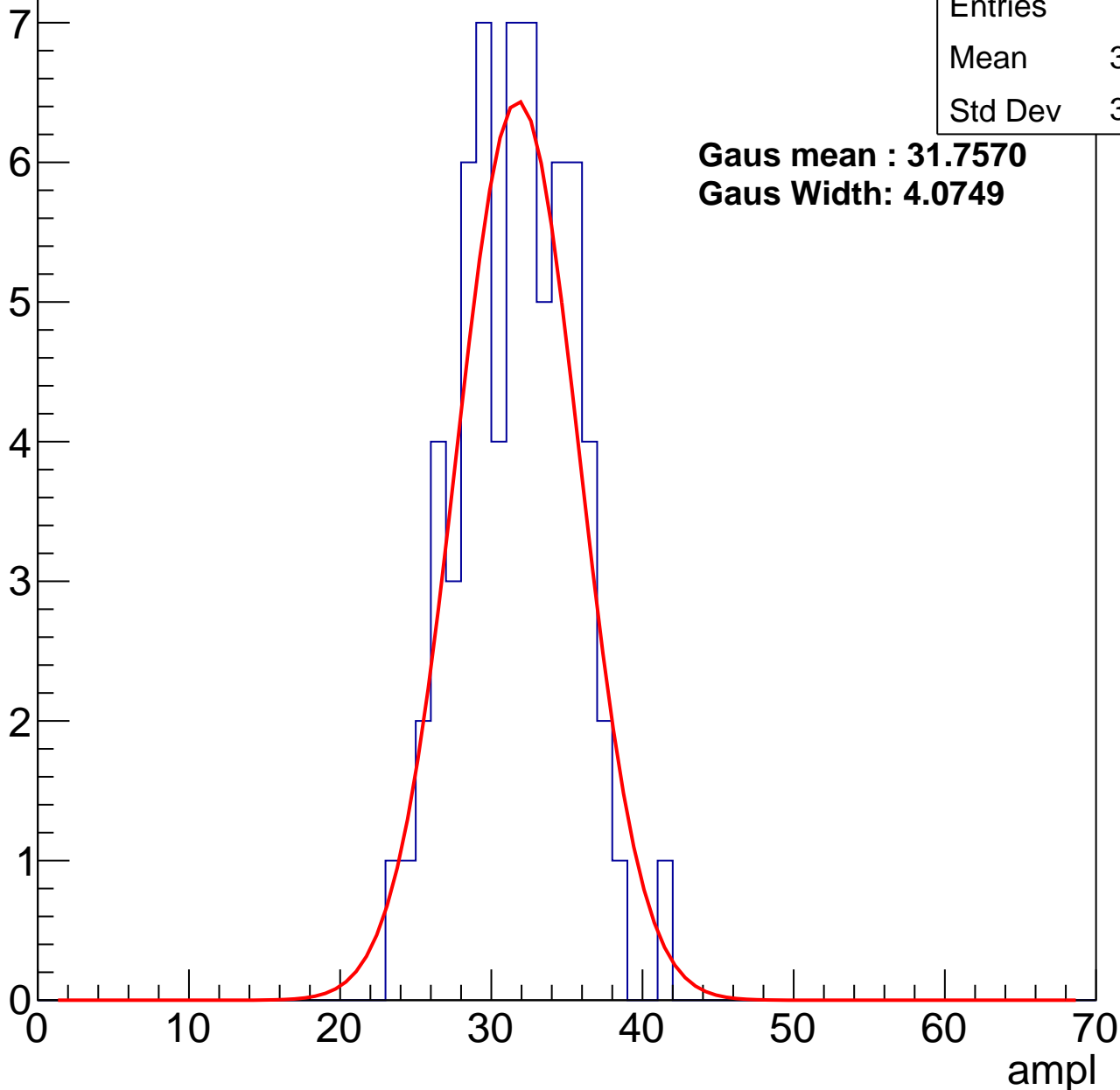
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	31.19
Std Dev	3.694

Gaus mean : 31.7570

Gaus Width: 4.0749



B0L002S, U4-ch127, adc1

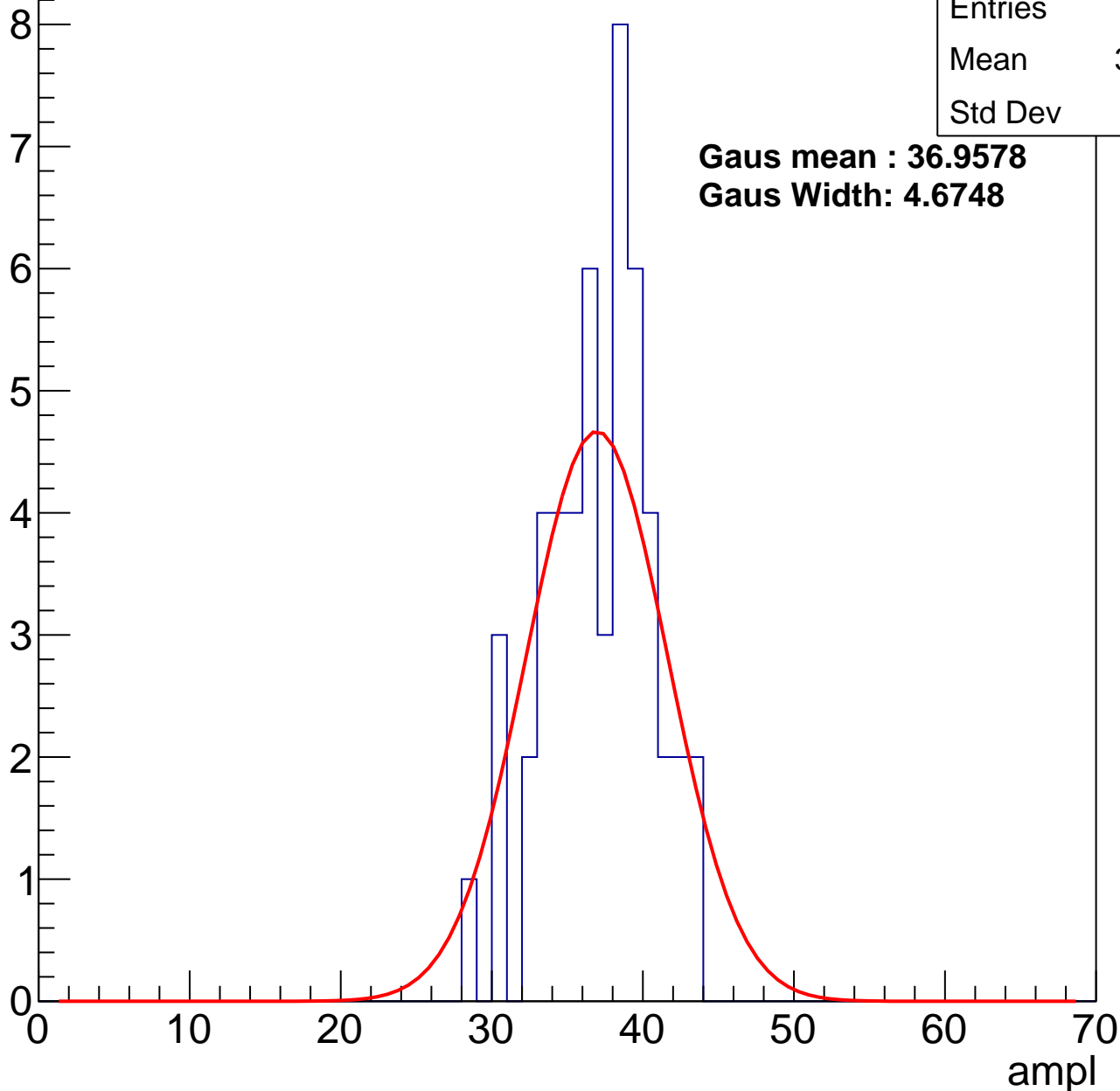
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	36.61
Std Dev	3.47

Gaus mean : 36.9578

Gaus Width: 4.6748



B0L002S, U4-ch127, adc2

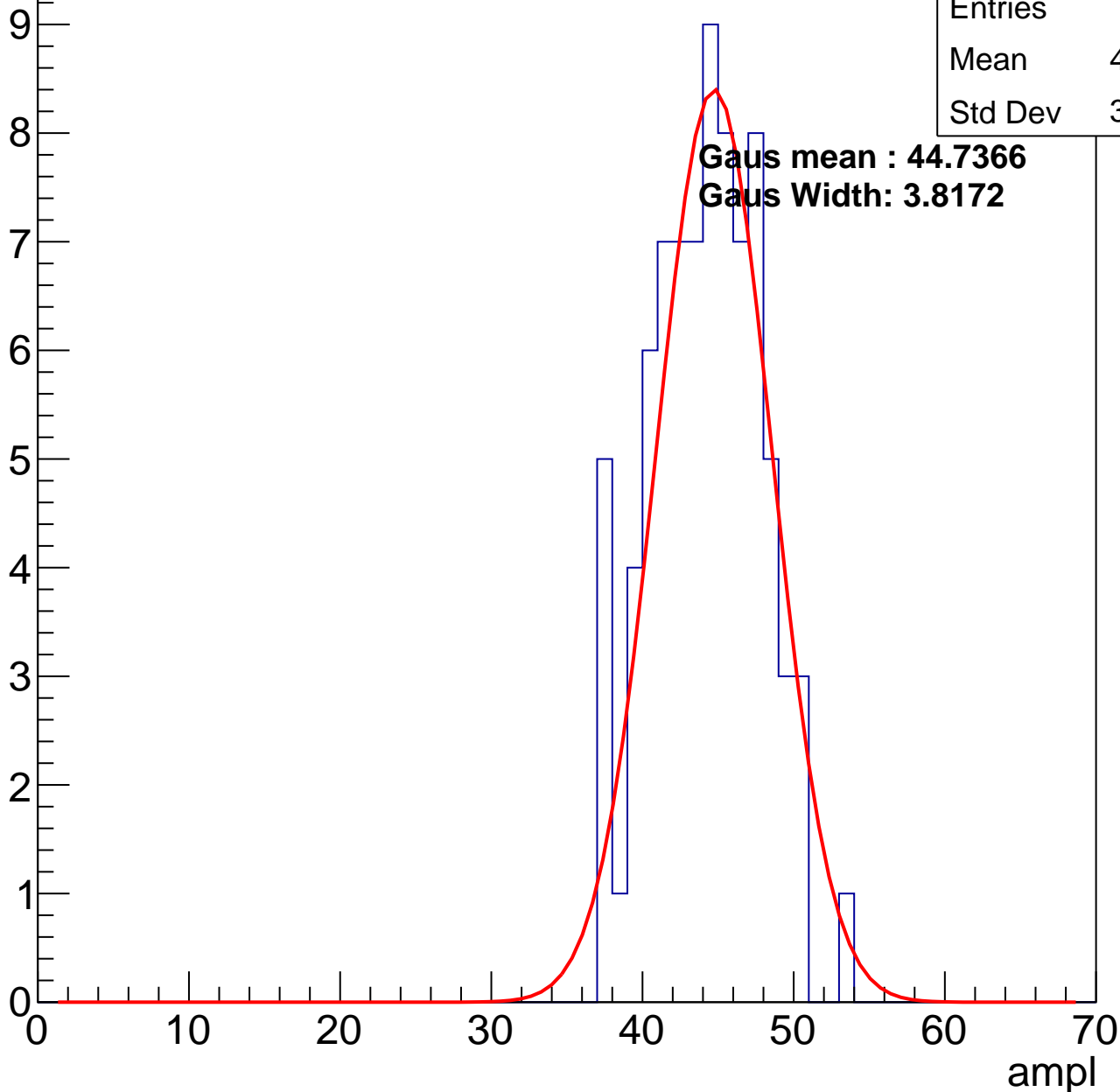
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	43.77
Std Dev	3.553

Gaus mean : 44.7366

Gaus Width: 3.8172

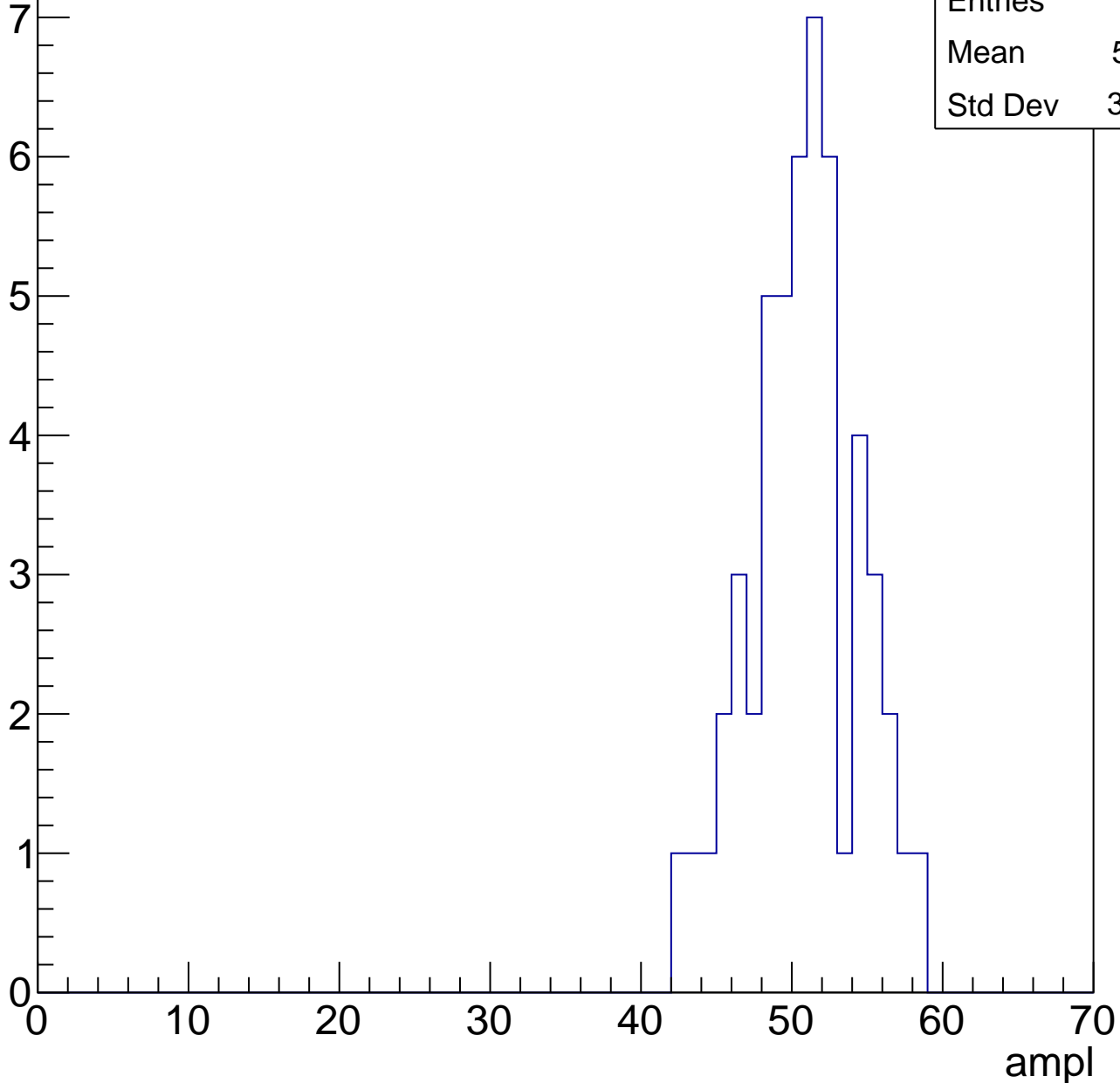


B0L002S, U4-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	50.31
Std Dev	3.567

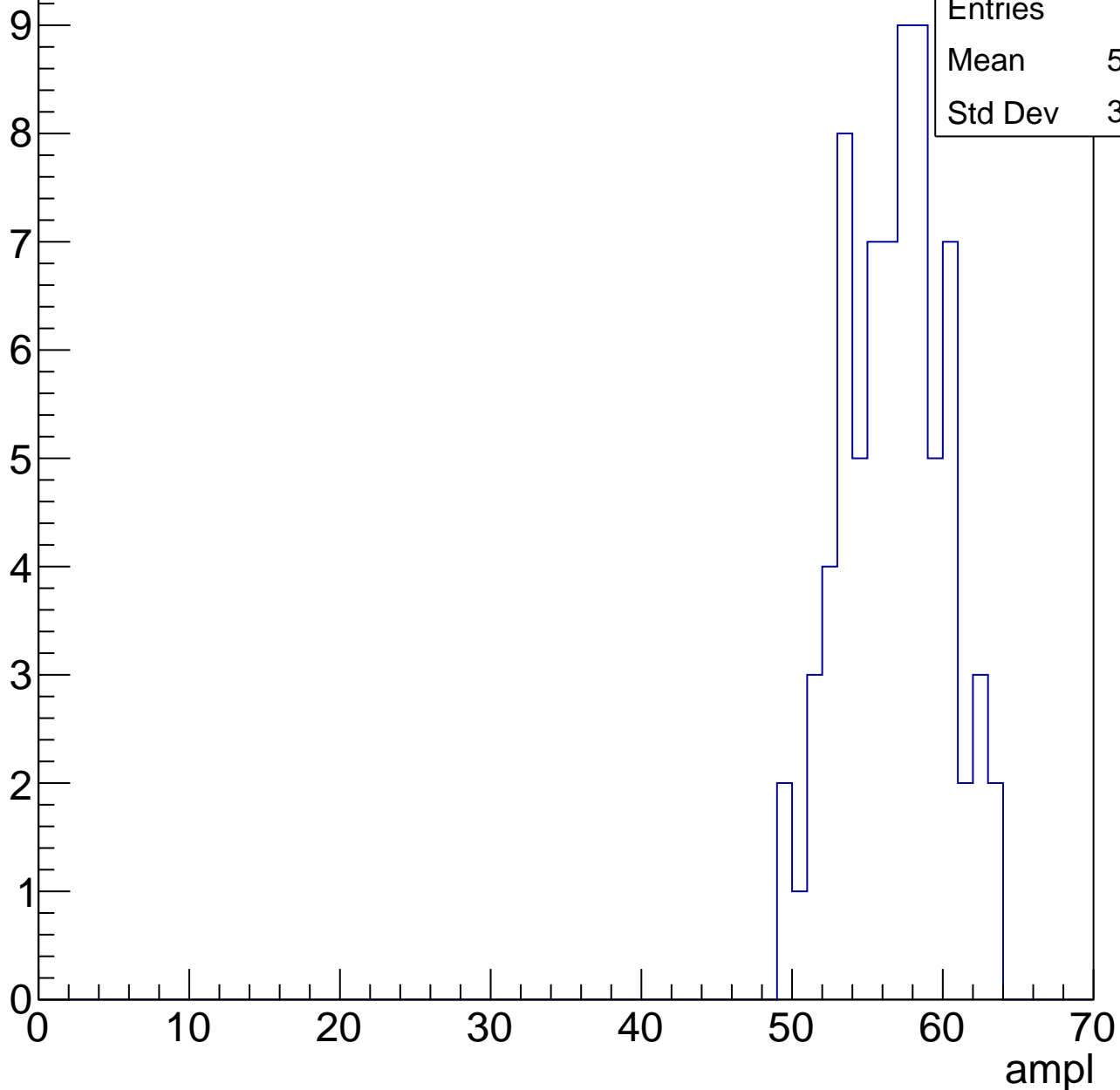


B0L002S, U4-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	56.27
Std Dev	3.338

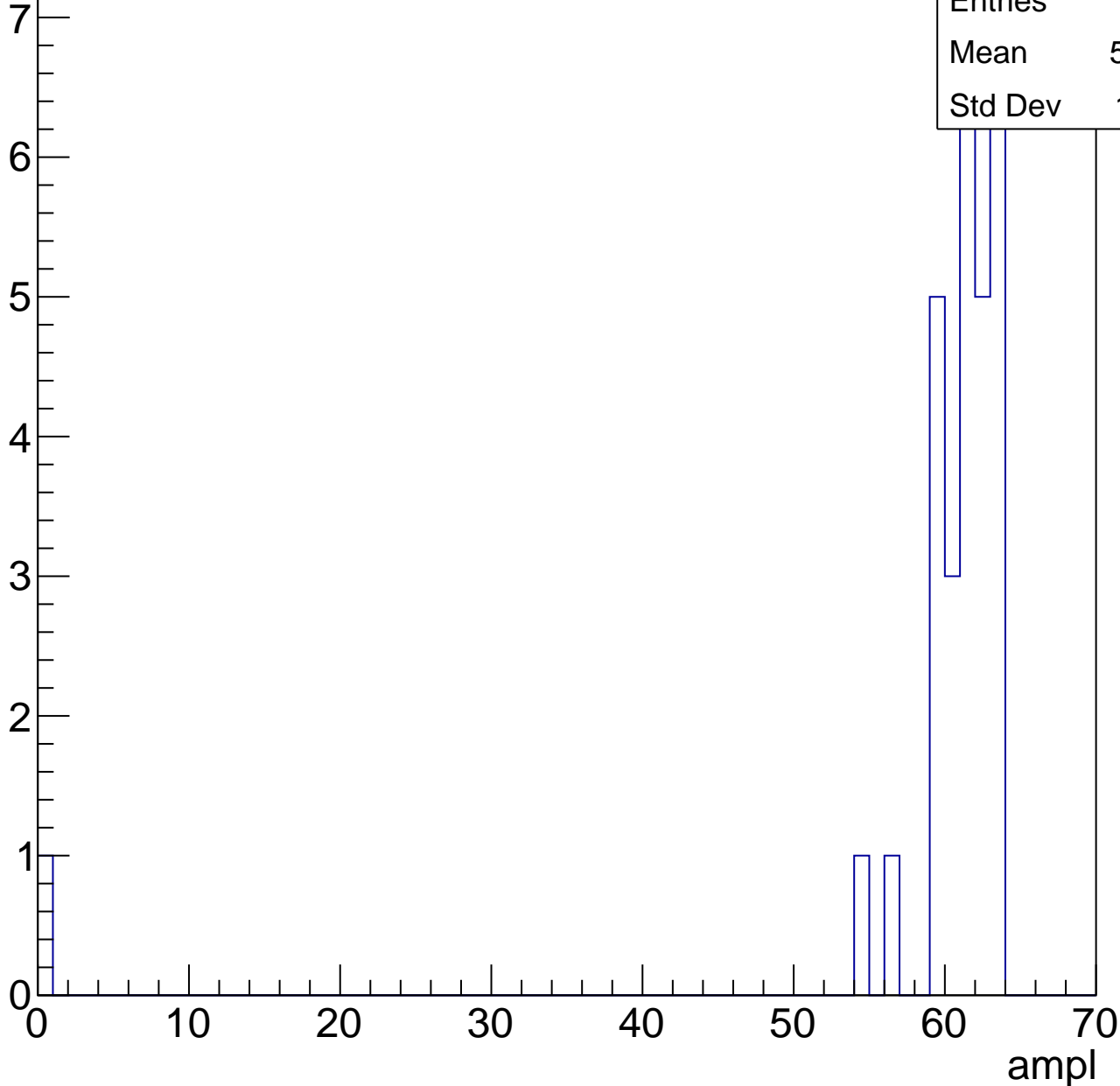


B0L002S, U4-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.77
Std Dev	11.11



B0L002S, U4-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

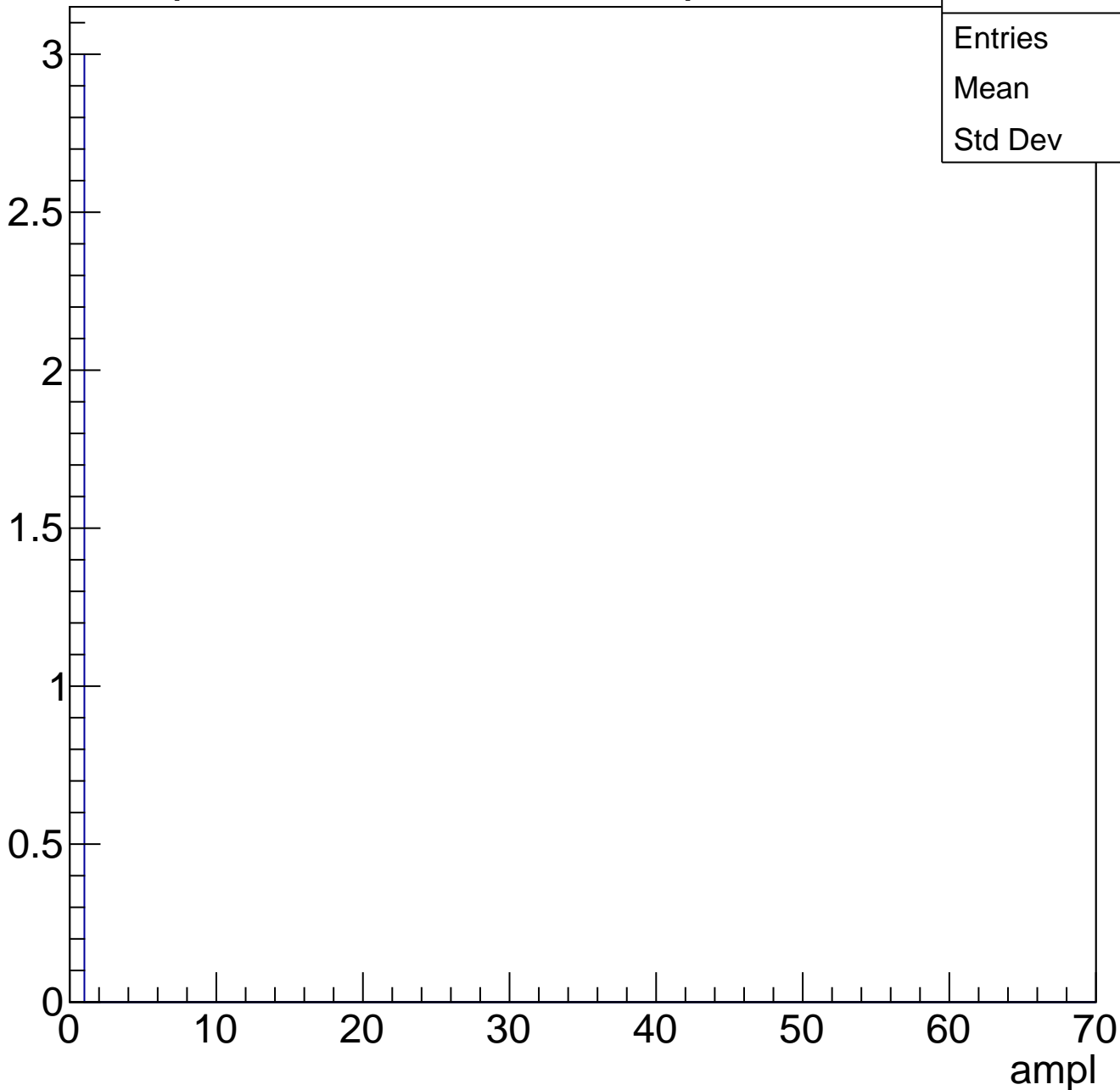
Entry



B0L002S, U4-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0