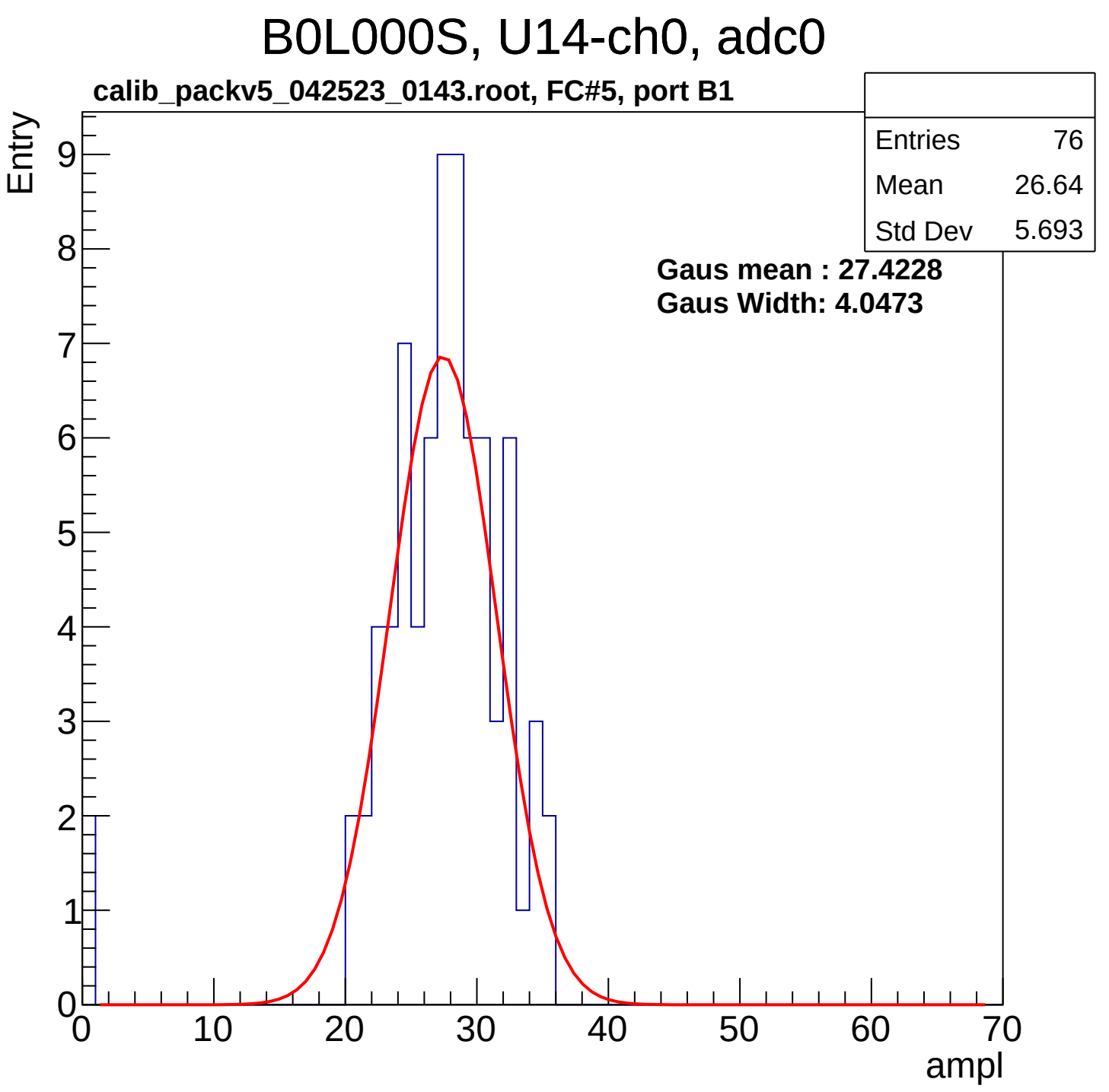
B0L000S, U14-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 26.64
Std Dev 5.693
Gaus mean : 27.4228
Gaus Width: 4.0473
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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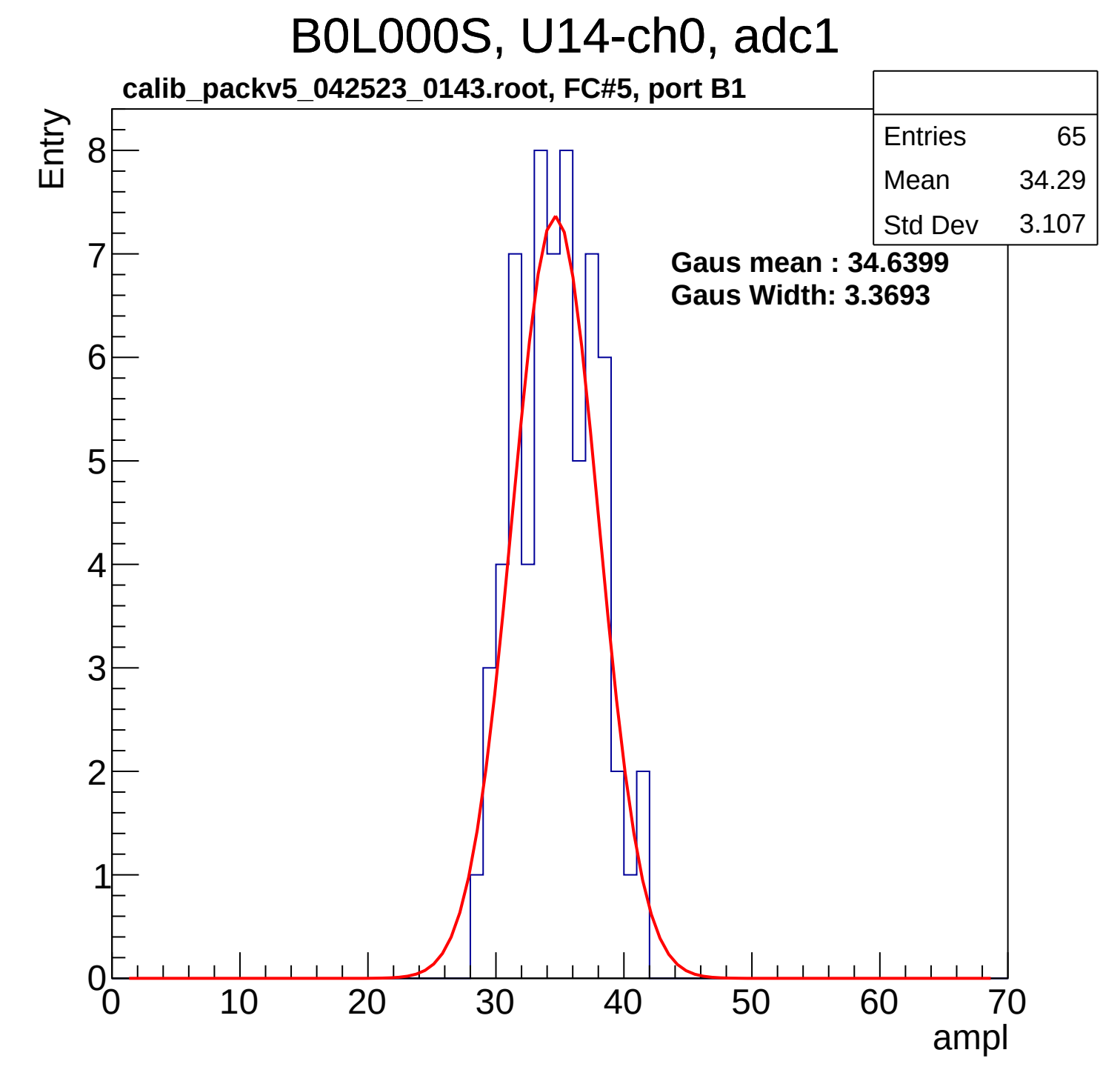
B0L000S, U14-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 34.29
Std Dev 3.107
Gaus mean : 34.6399
Gaus Width: 3.3693
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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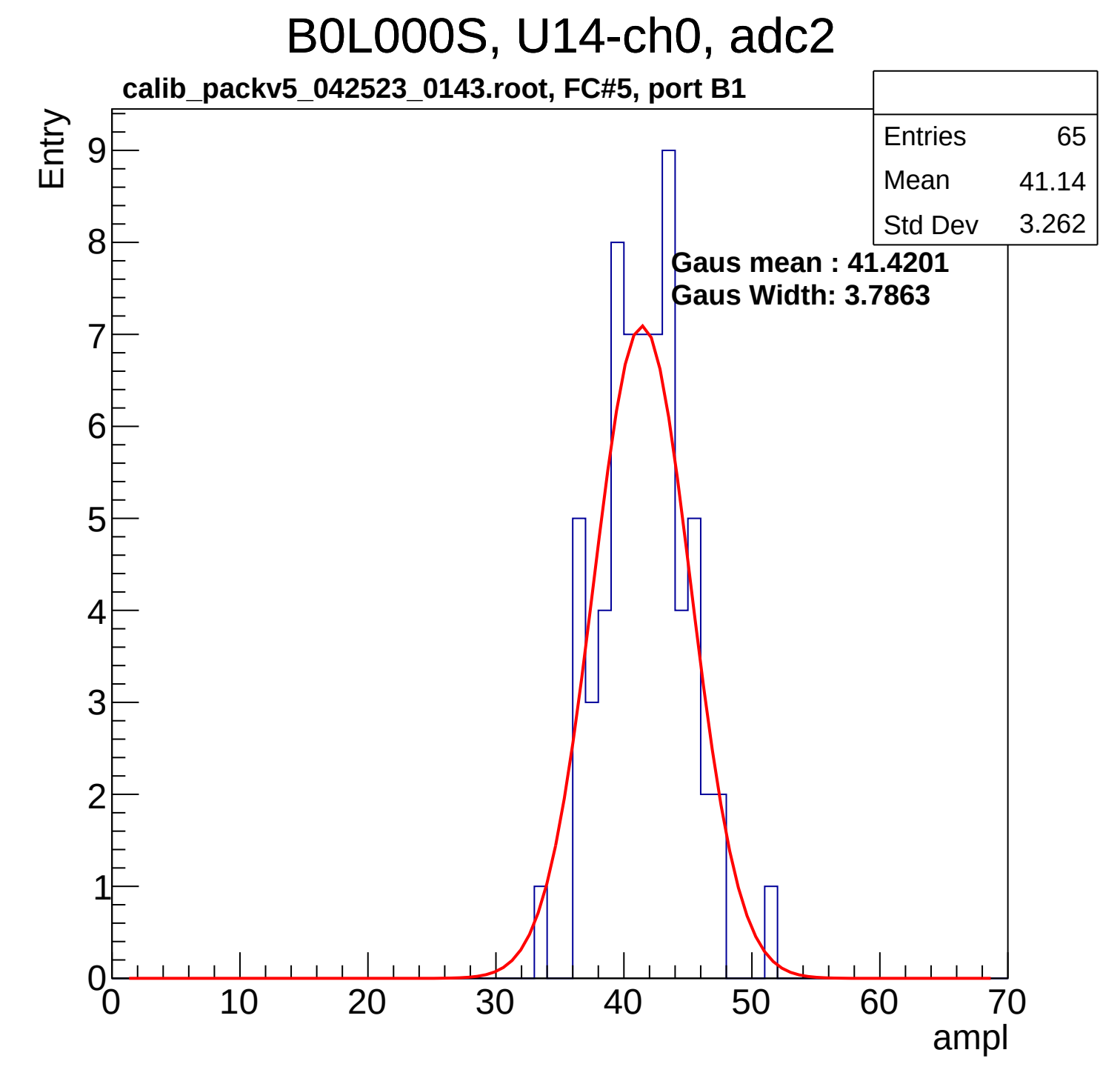
B0L000S, U14-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.14
Std Dev 3.262
Gaus mean : 41.4201
Gaus Width: 3.7863
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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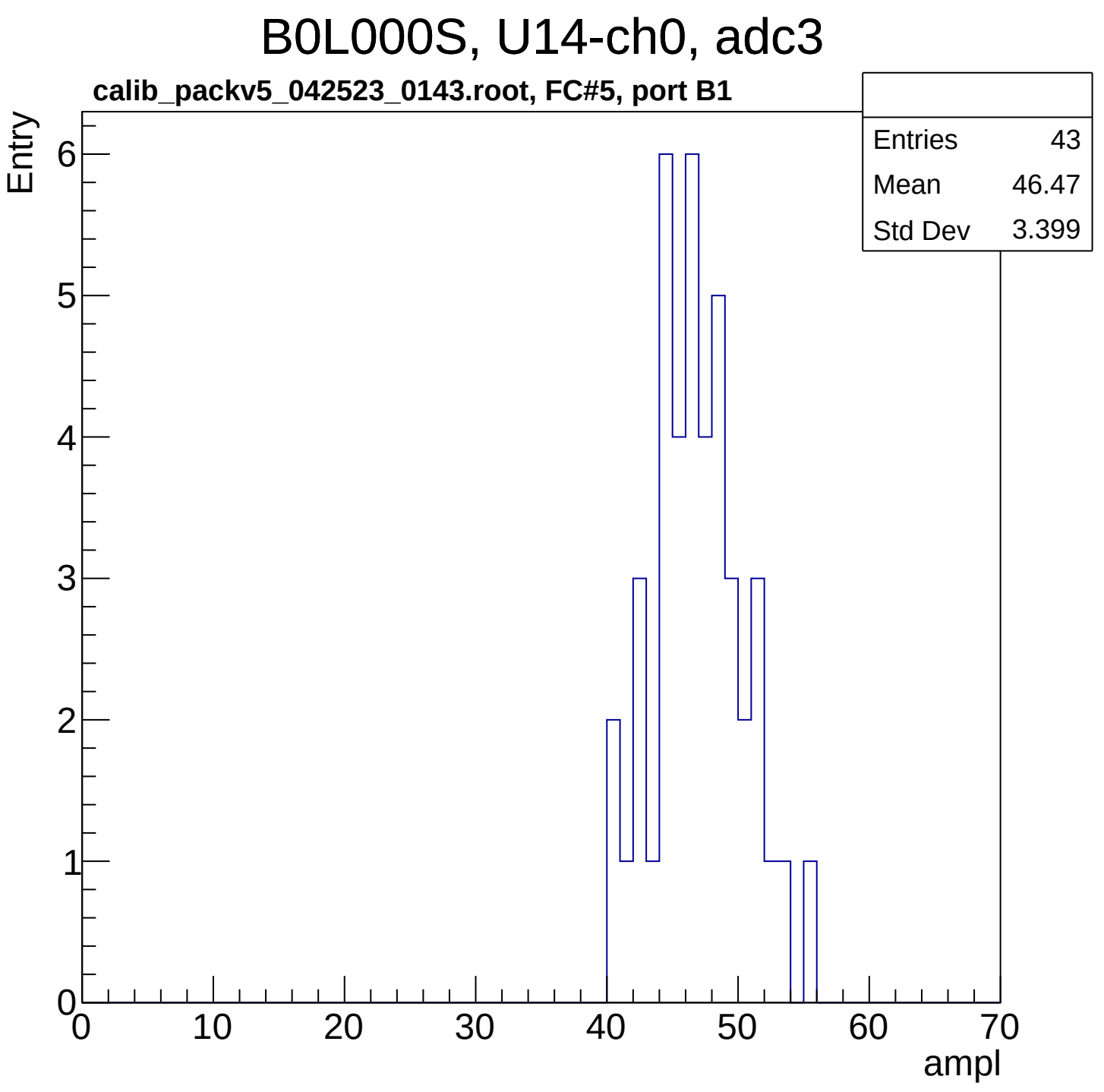
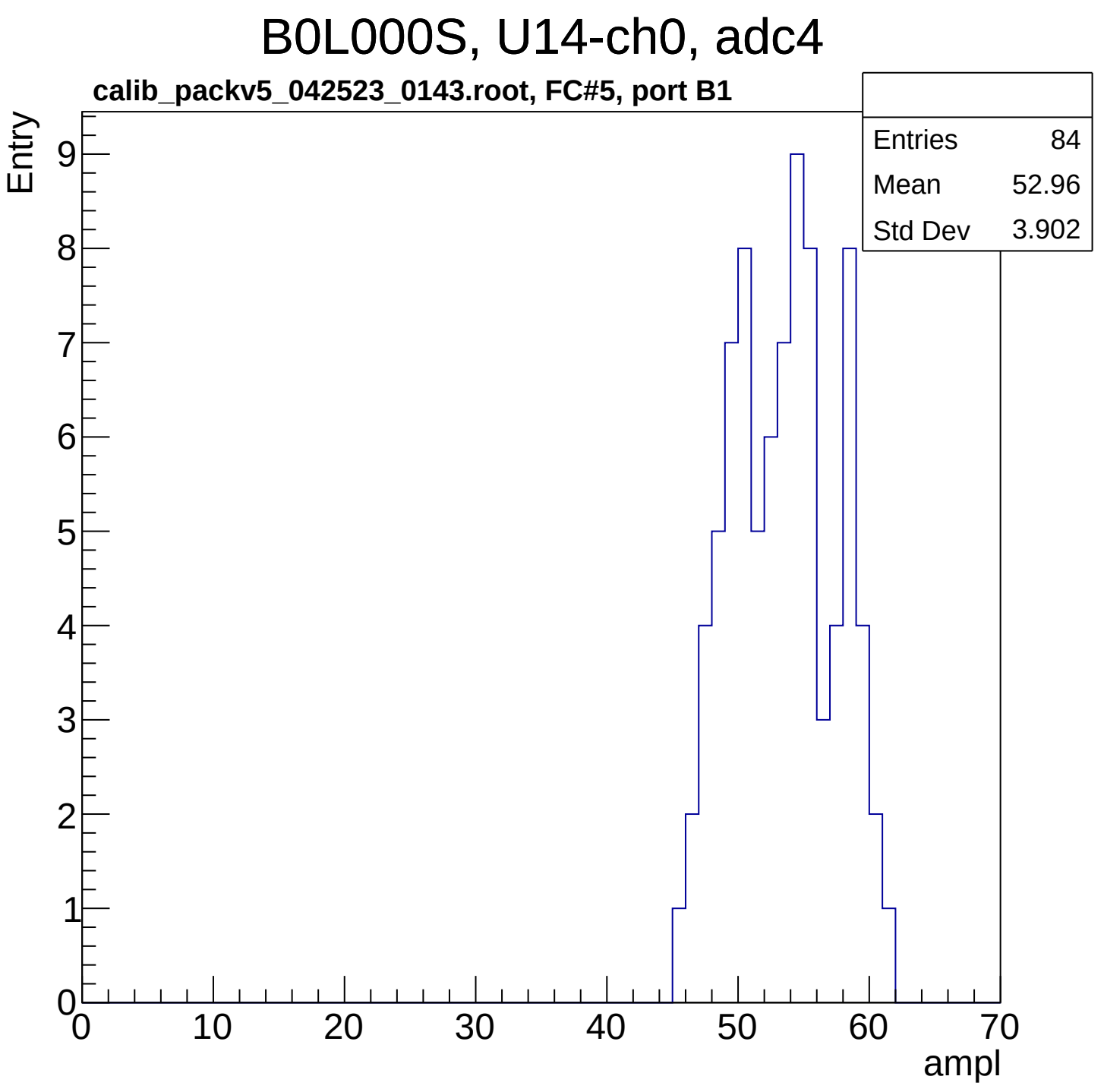

B0L000S, U14-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 46.47
Std Dev 3.399
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 52.96
Std Dev 3.902
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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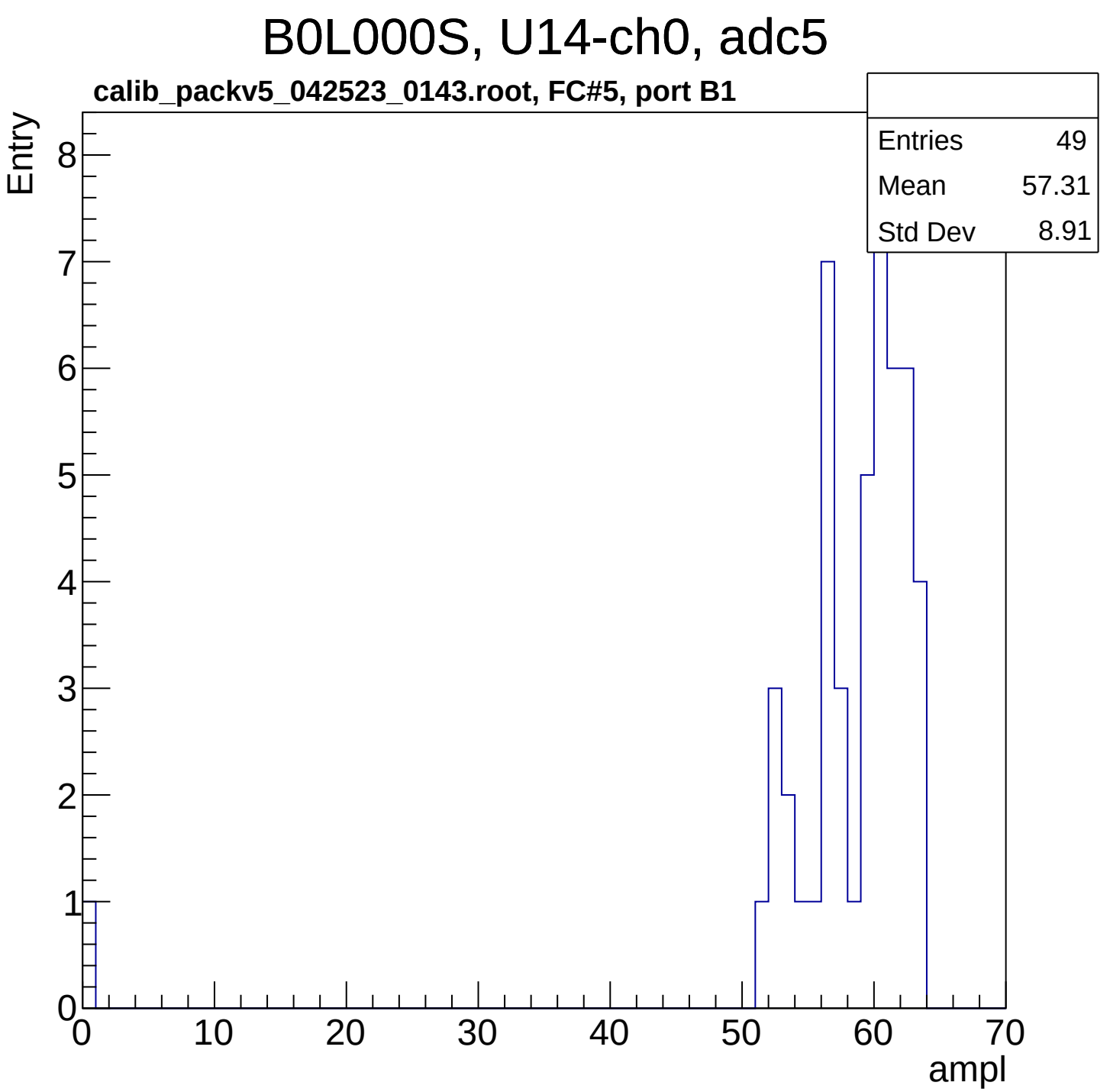
B0L000S, U14-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.31
Std Dev 8.91
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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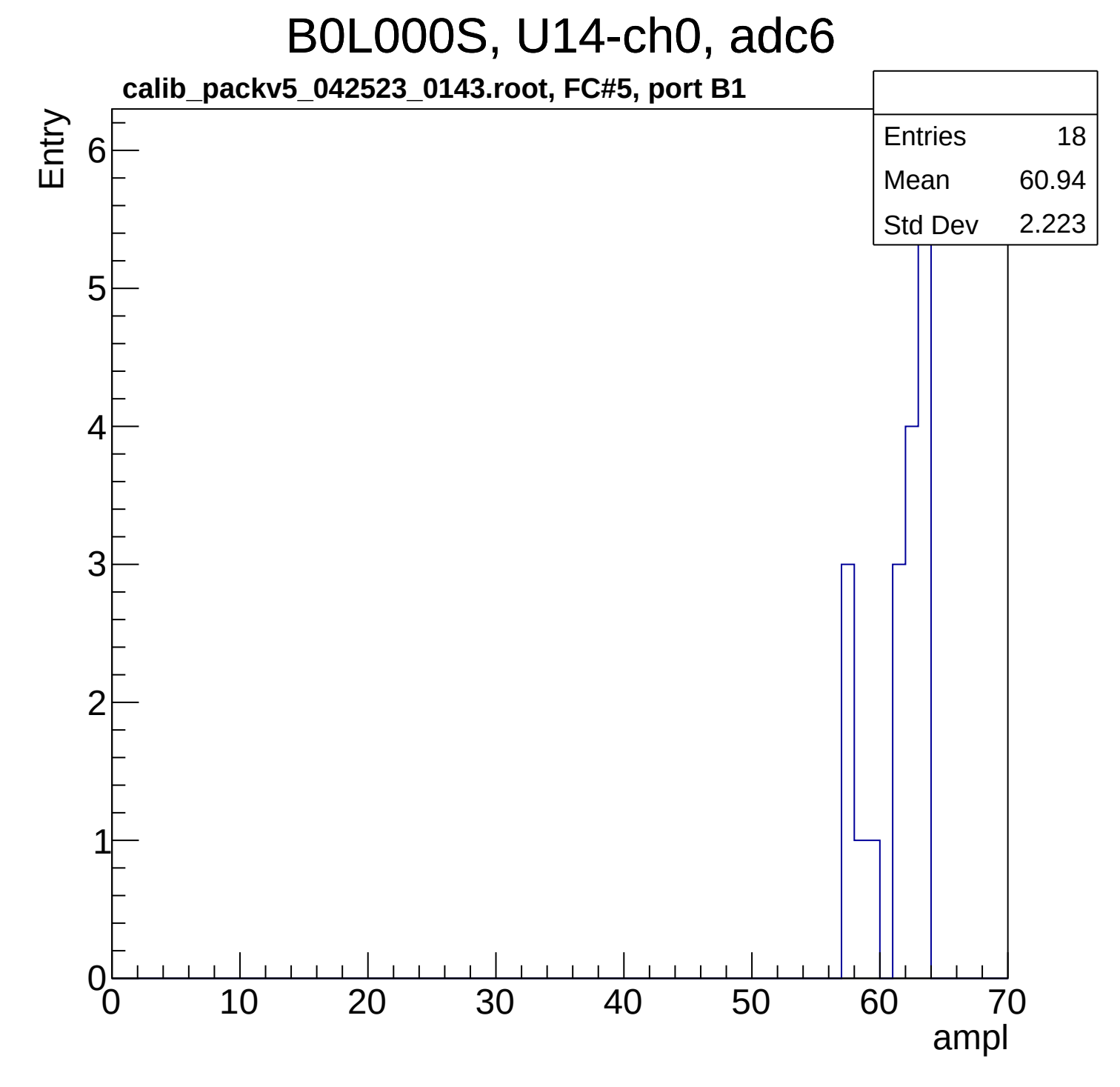
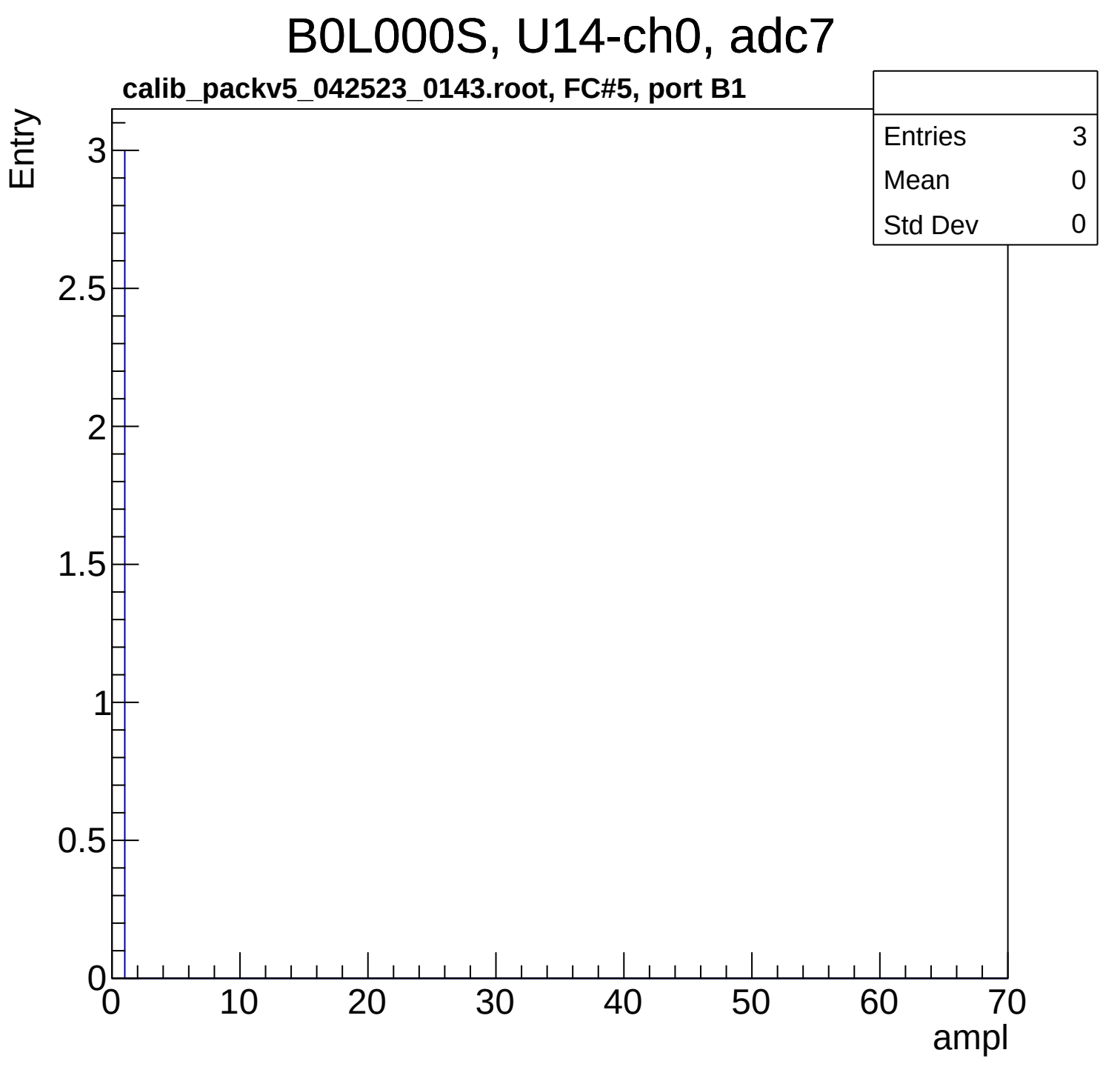
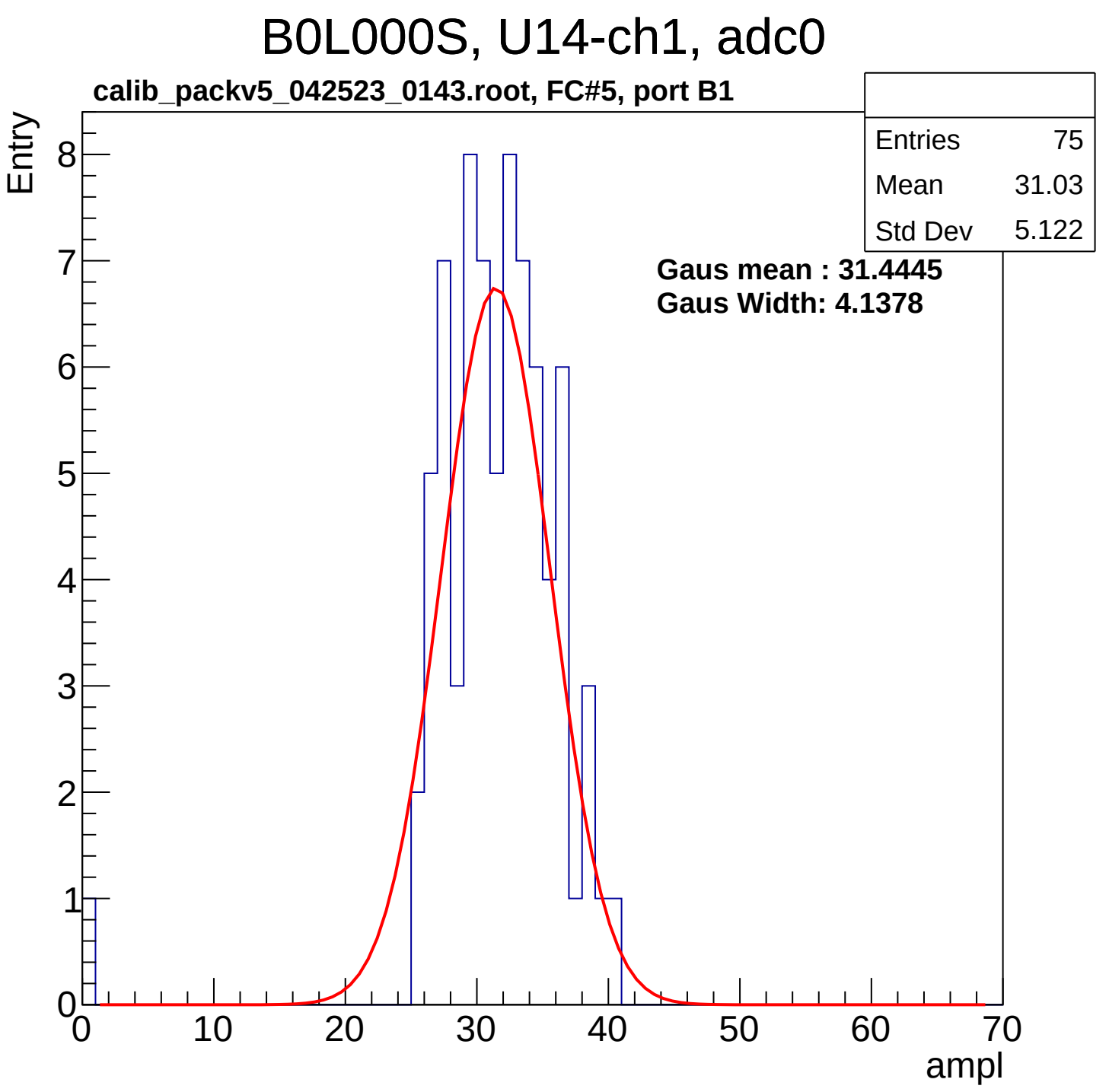

B0L000S, U14-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 60.94
Std Dev 2.223
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 31.03
Std Dev 5.122
Gaus mean : 31.4445
Gaus Width: 4.1378
Entry
ampl
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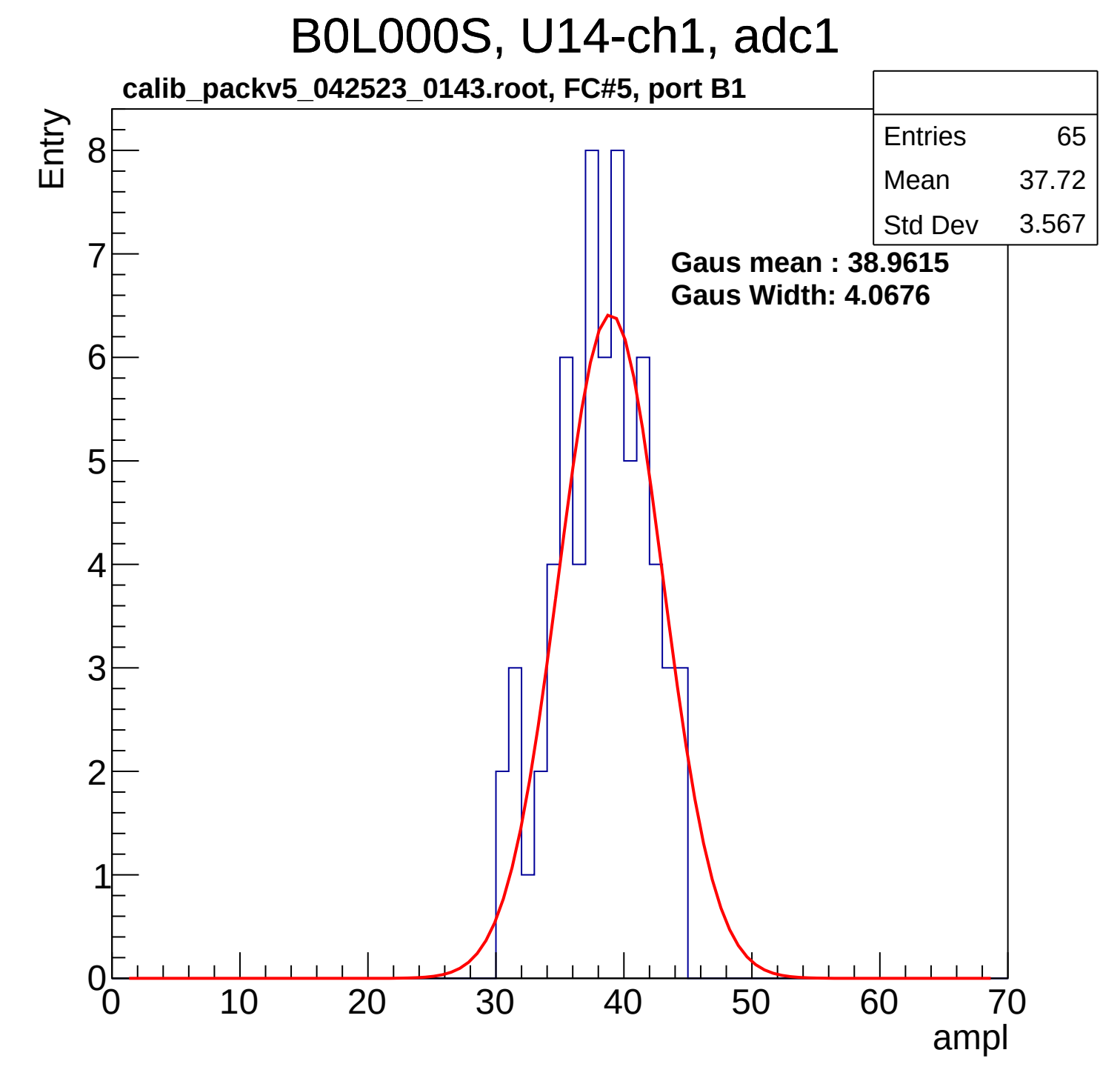
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 37.72
Std Dev 3.567
Gaus mean : 38.9615
Gaus Width: 4.0676
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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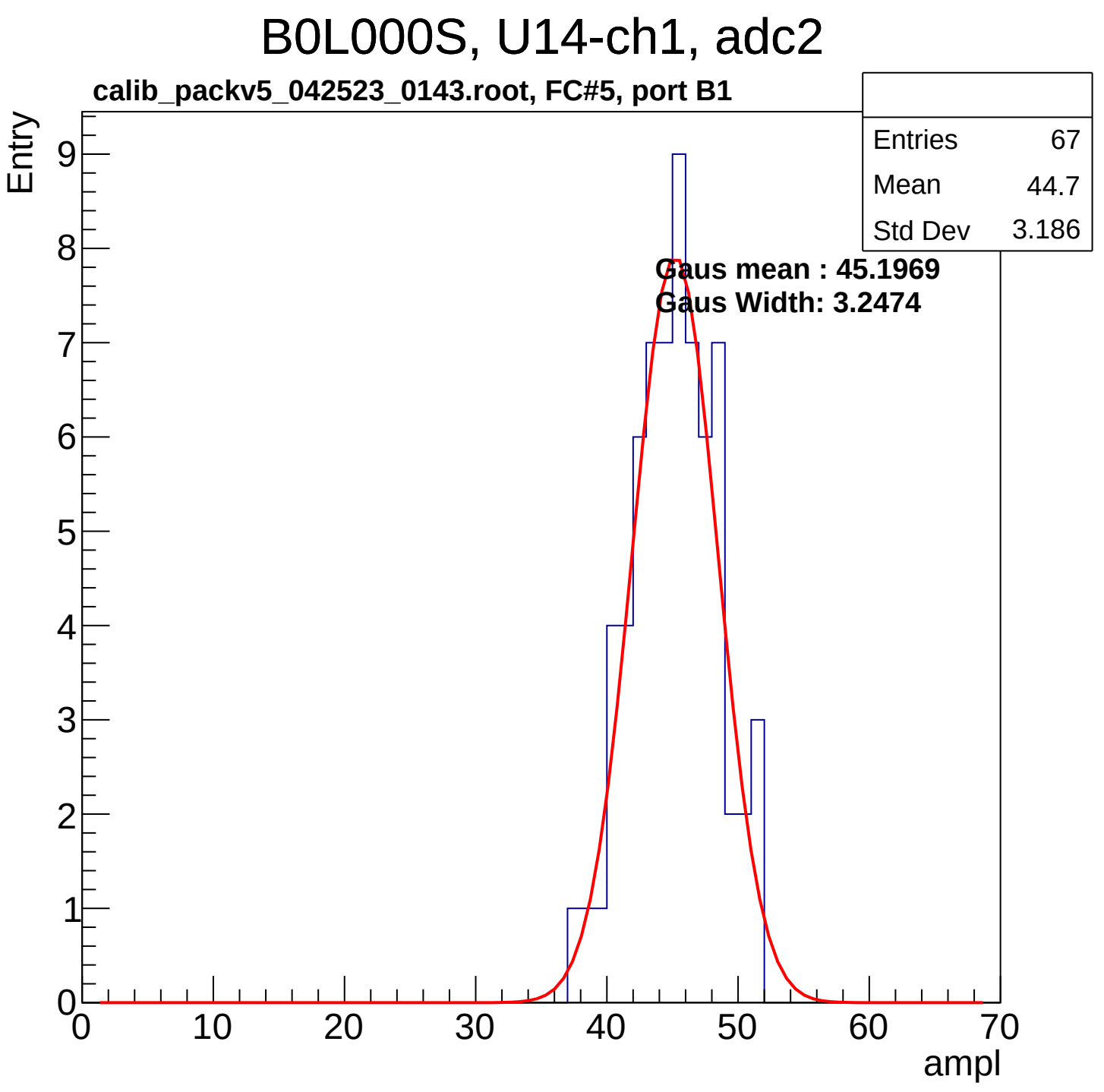
B0L000S, U14-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.7
Std Dev 3.186
Gaus mean : 45.1969
Gaus Width: 3.2474
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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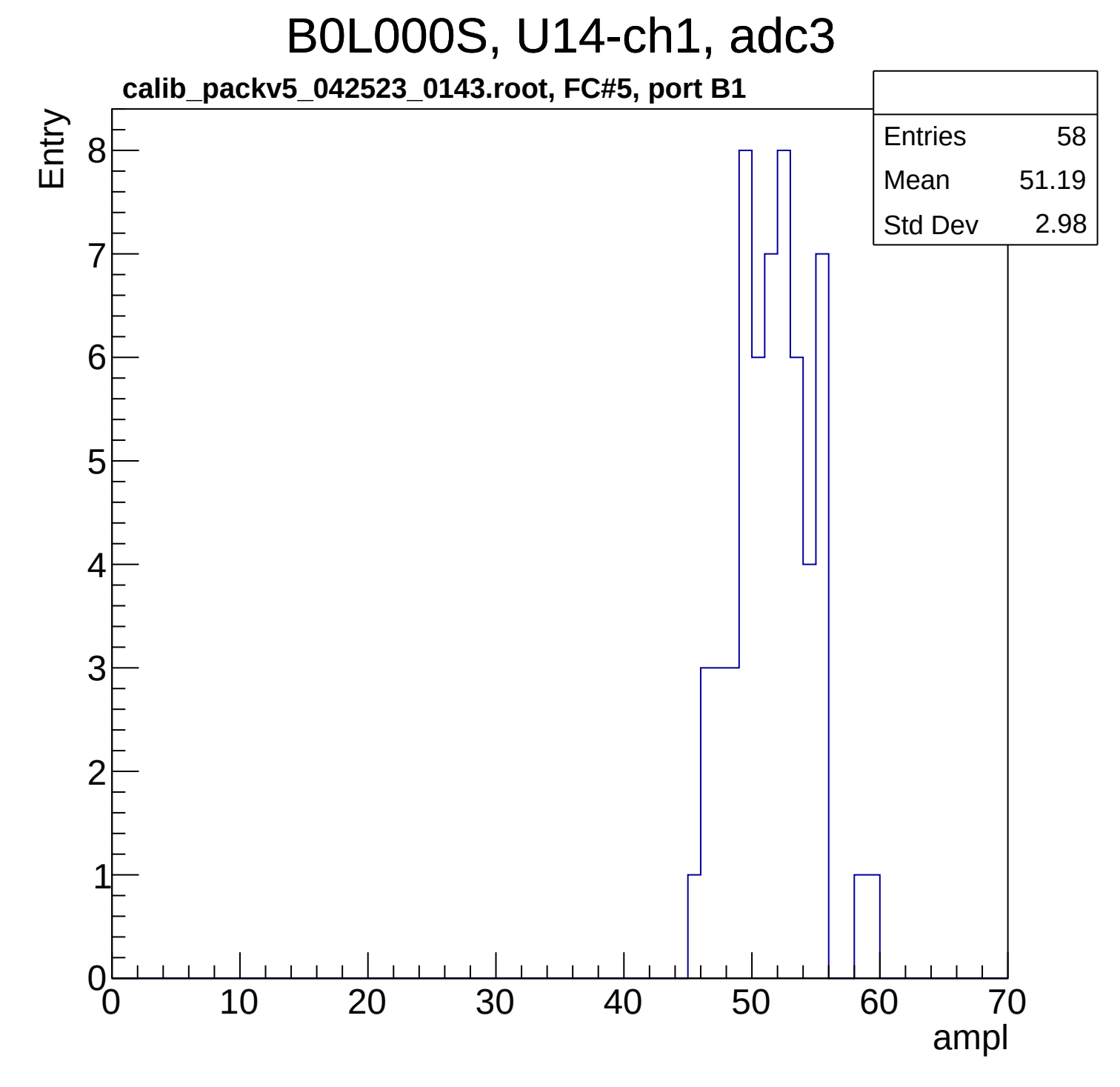
B0L000S, U14-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.19
Std Dev 2.98
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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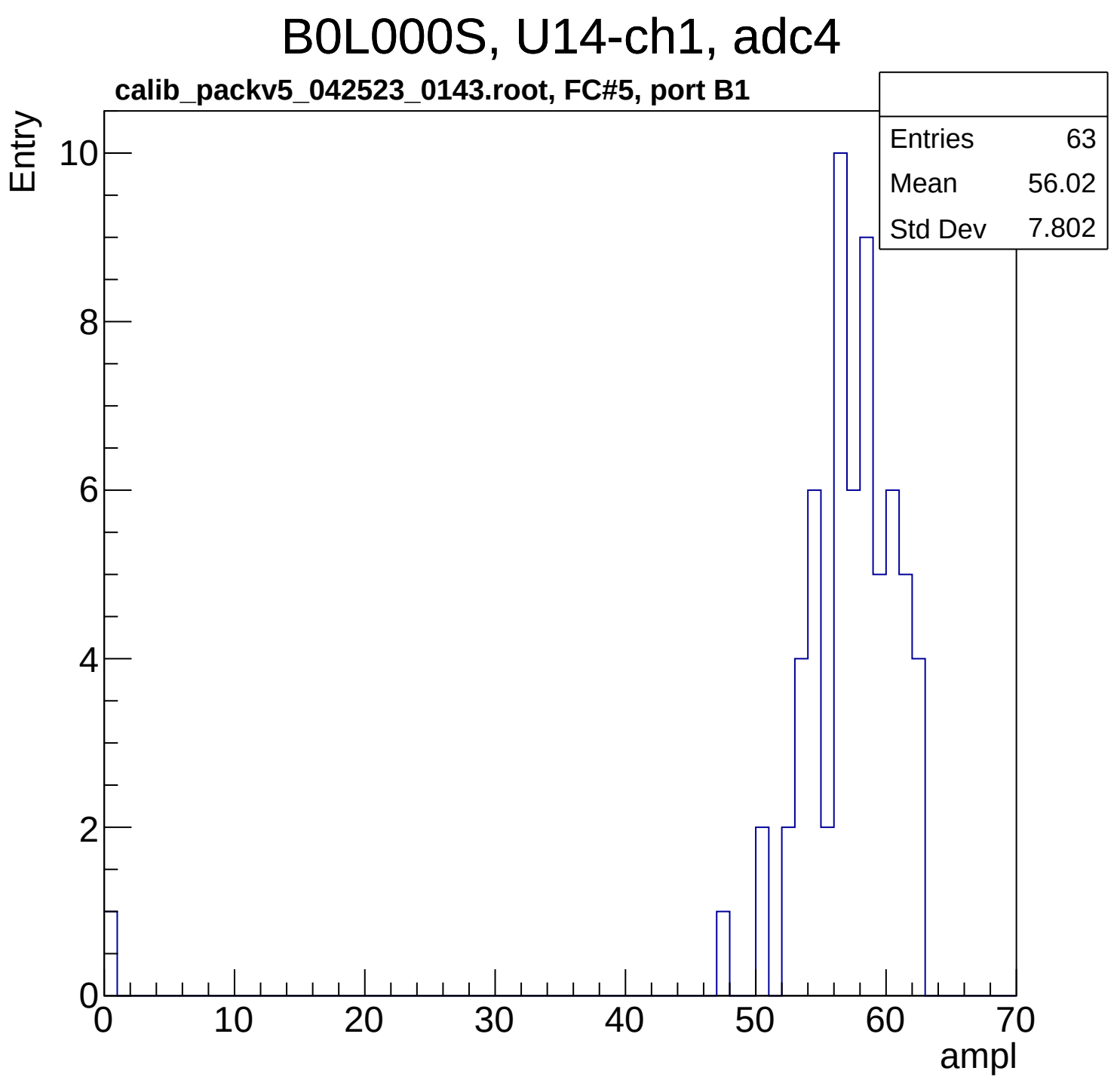
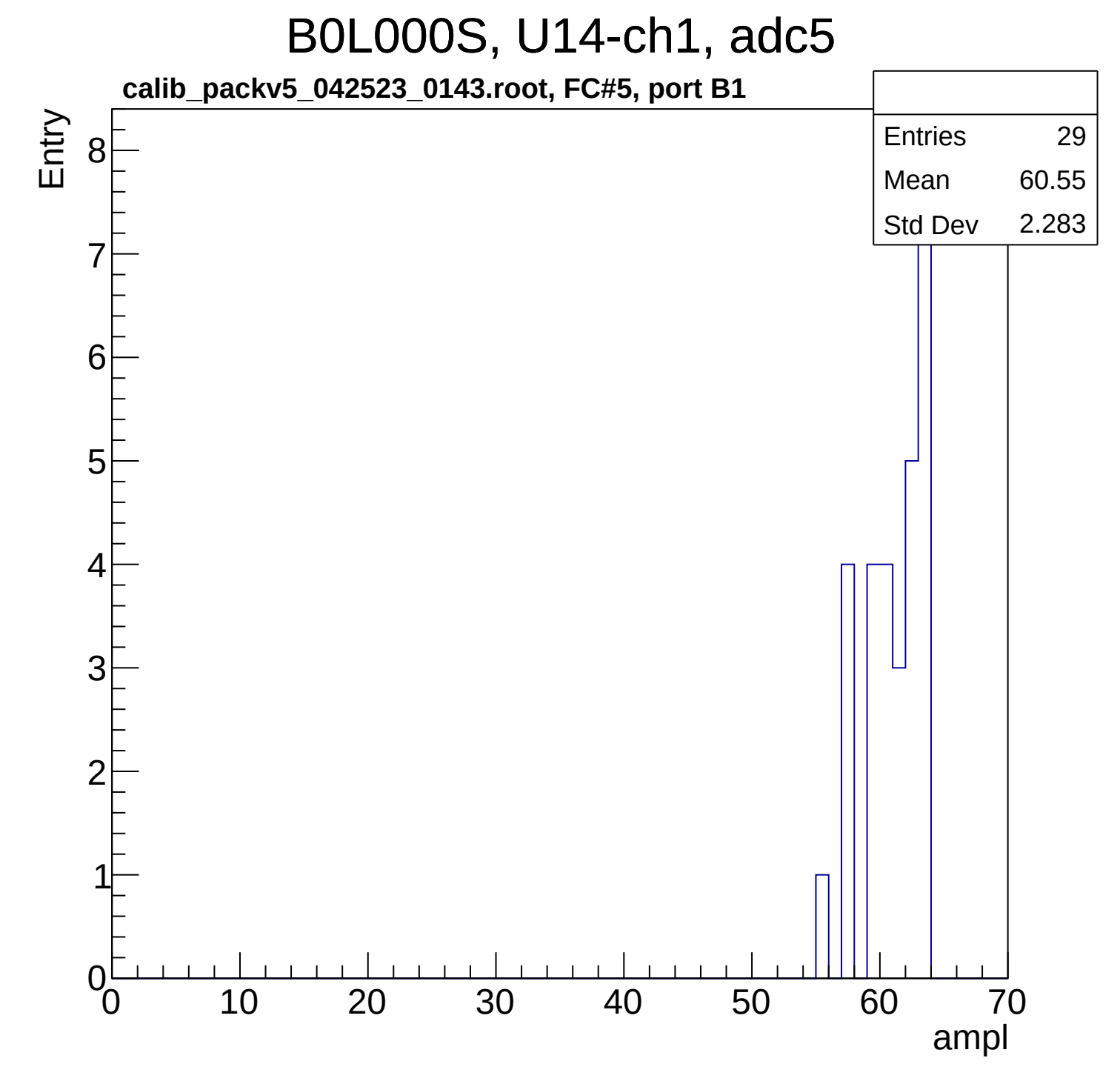

B0L000S, U14-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.02
Std Dev 7.802
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 60.55
Std Dev 2.283
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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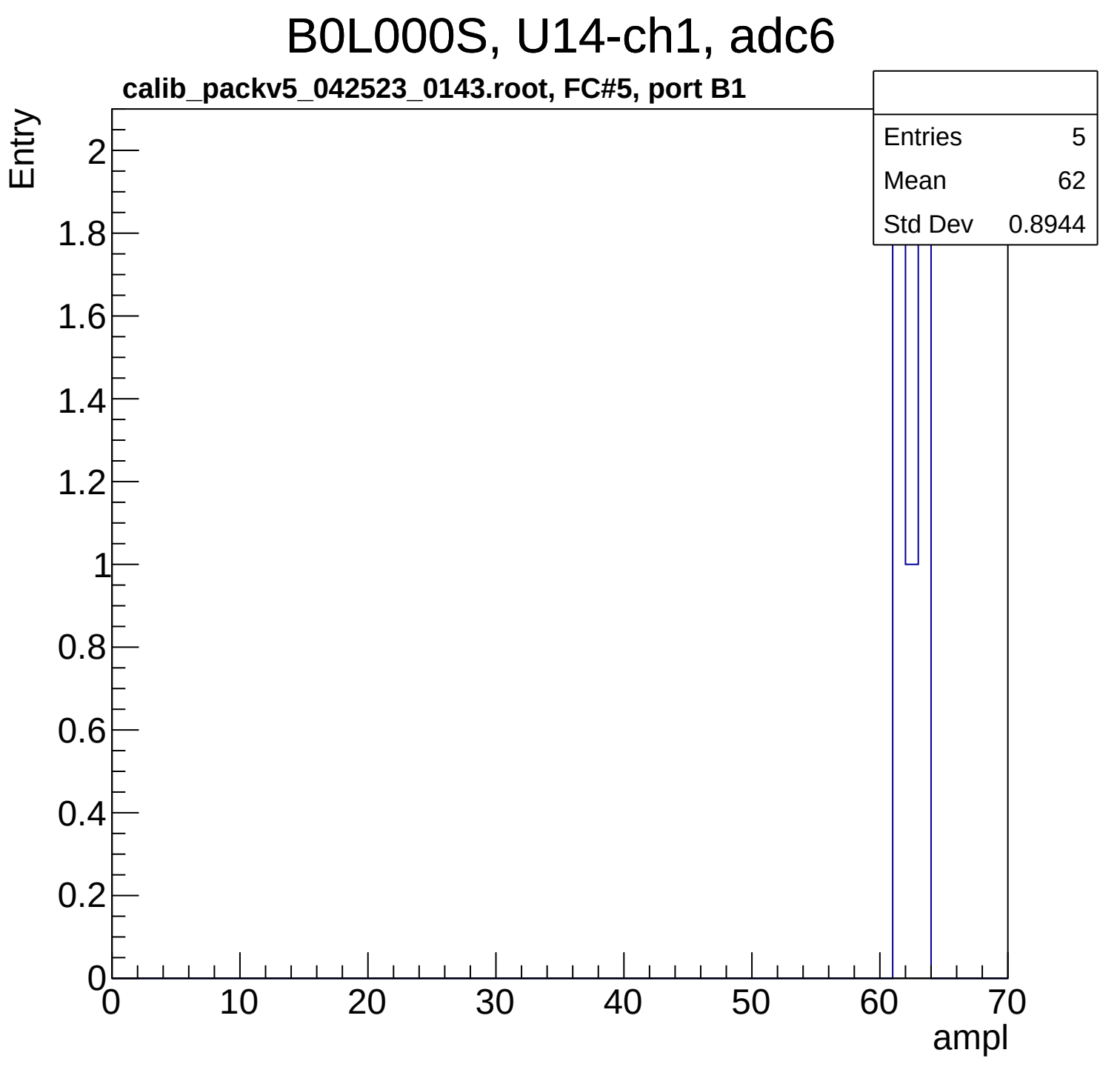
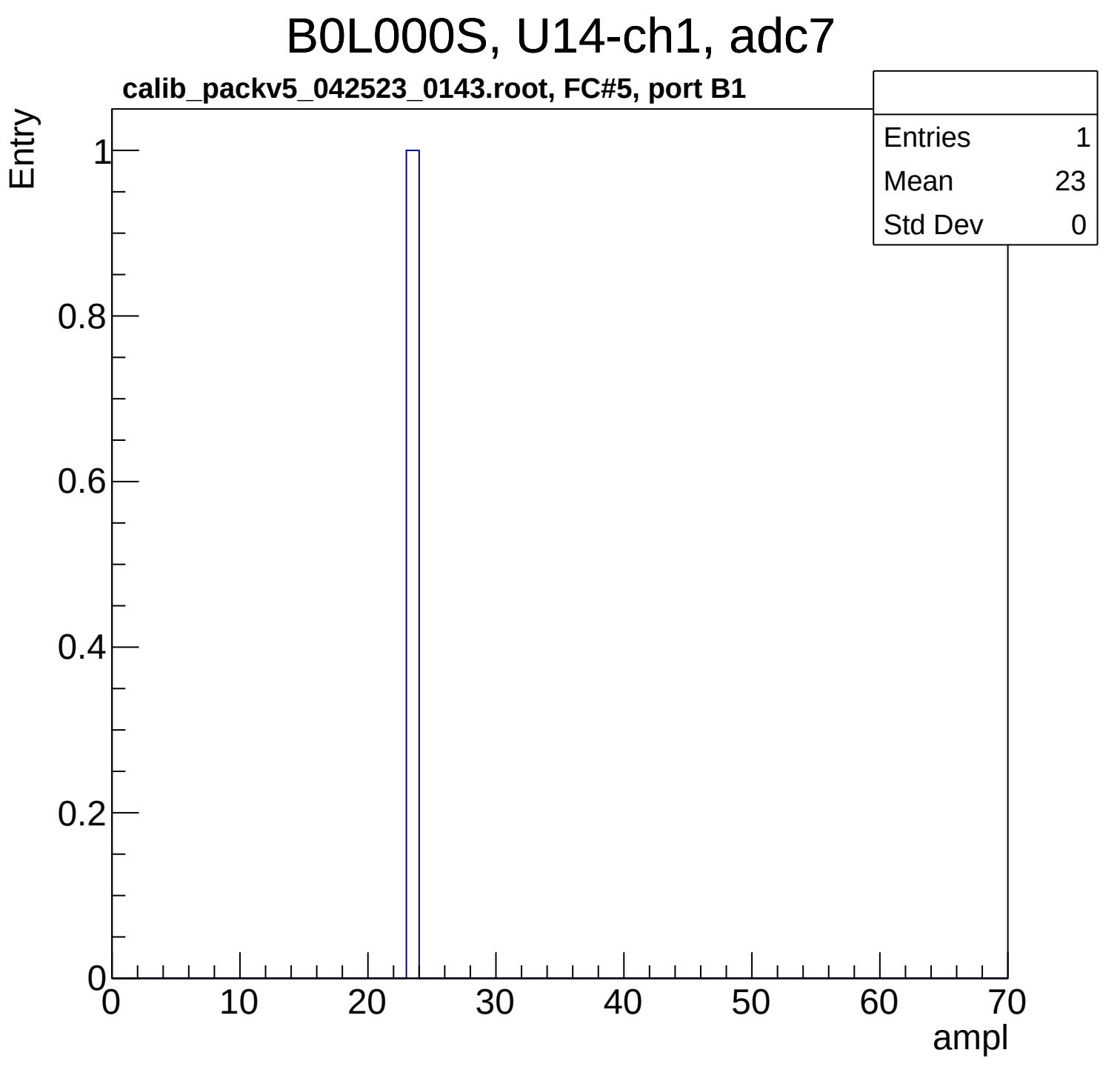

B0L000S, U14-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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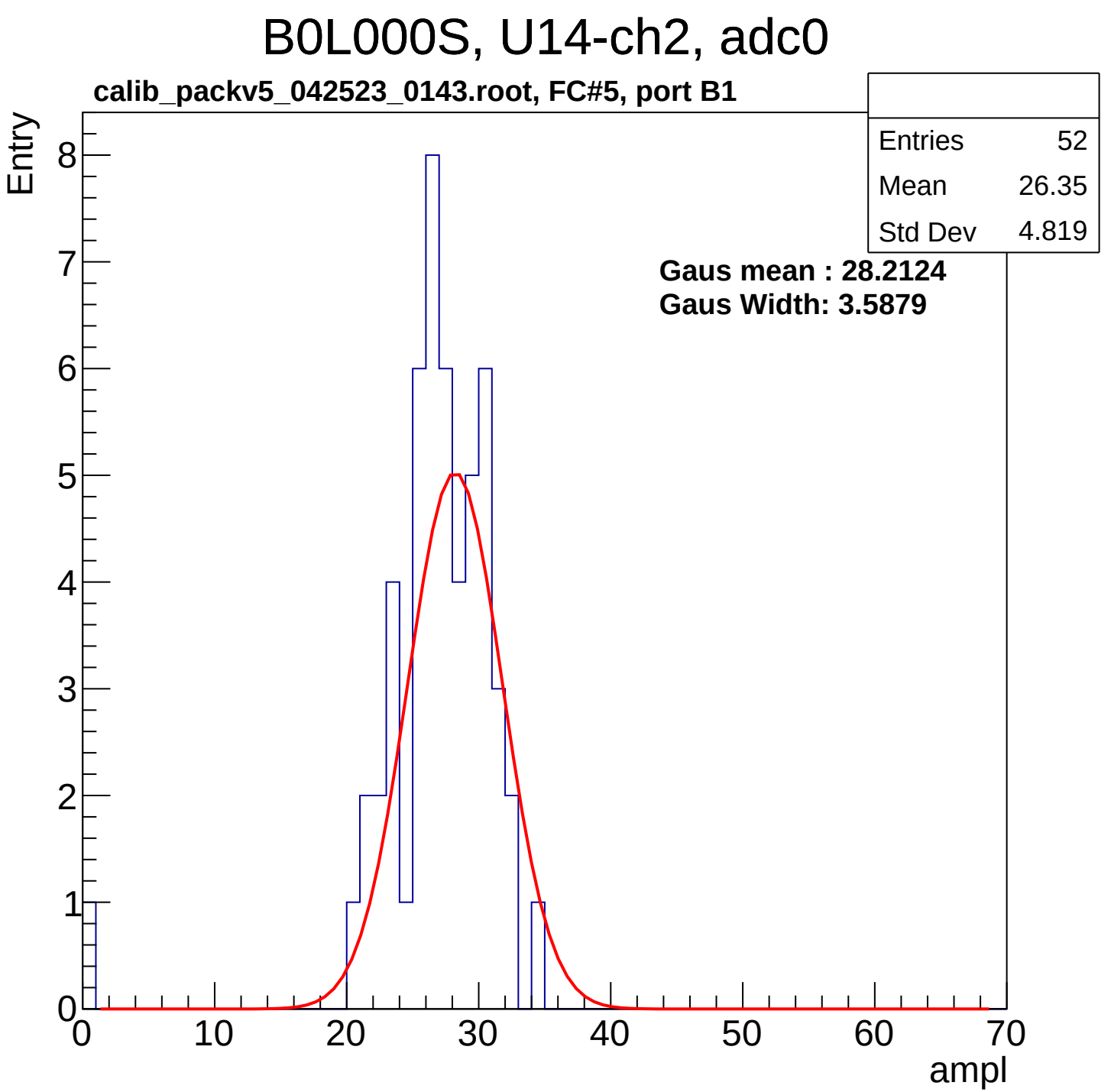
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 26.35
Std Dev 4.819
Gaus mean : 28.2124
Gaus Width: 3.5879
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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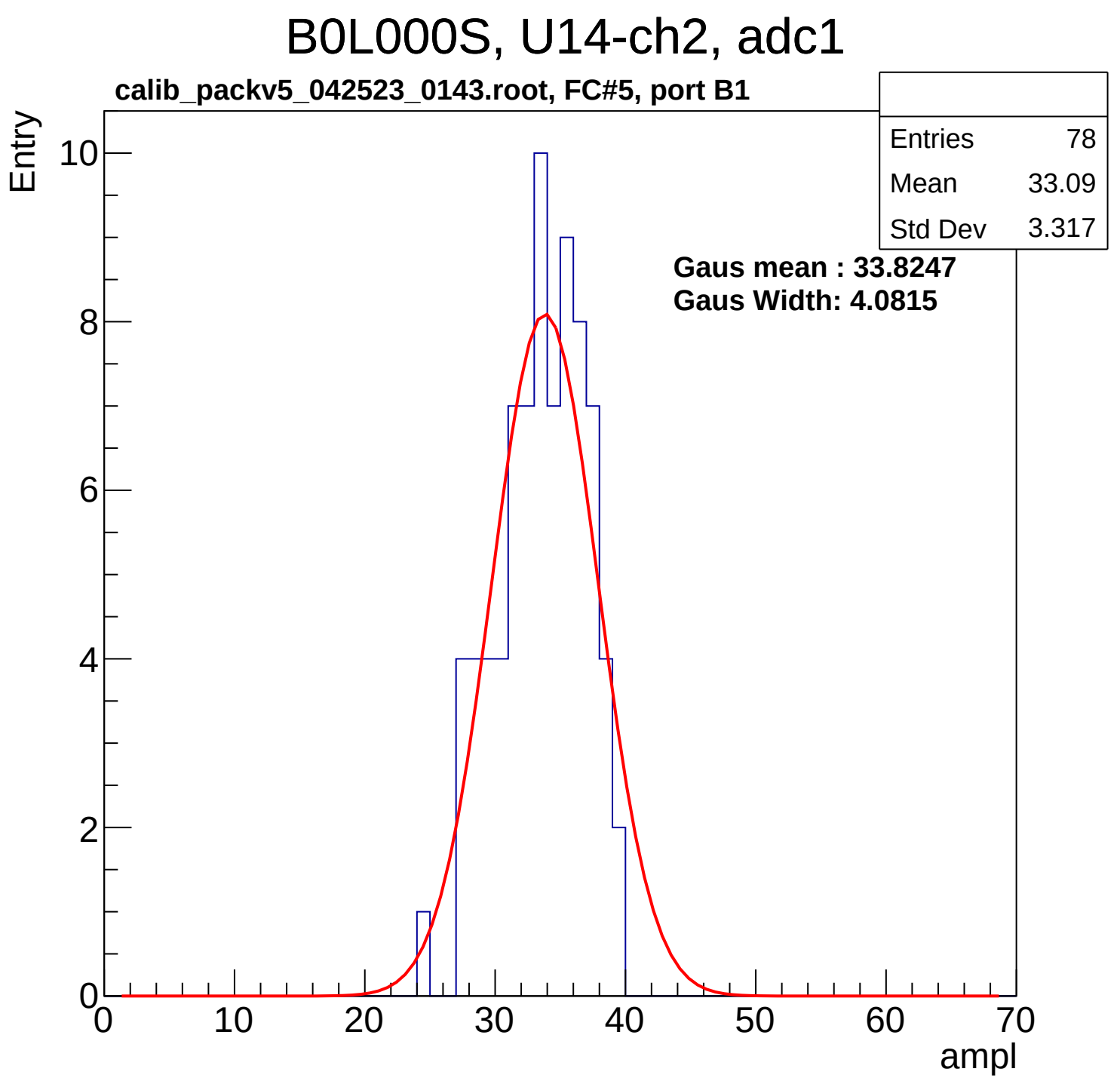
B0L000S, U14-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 33.09
Std Dev 3.317
Gaus mean : 33.8247
Gaus Width: 4.0815
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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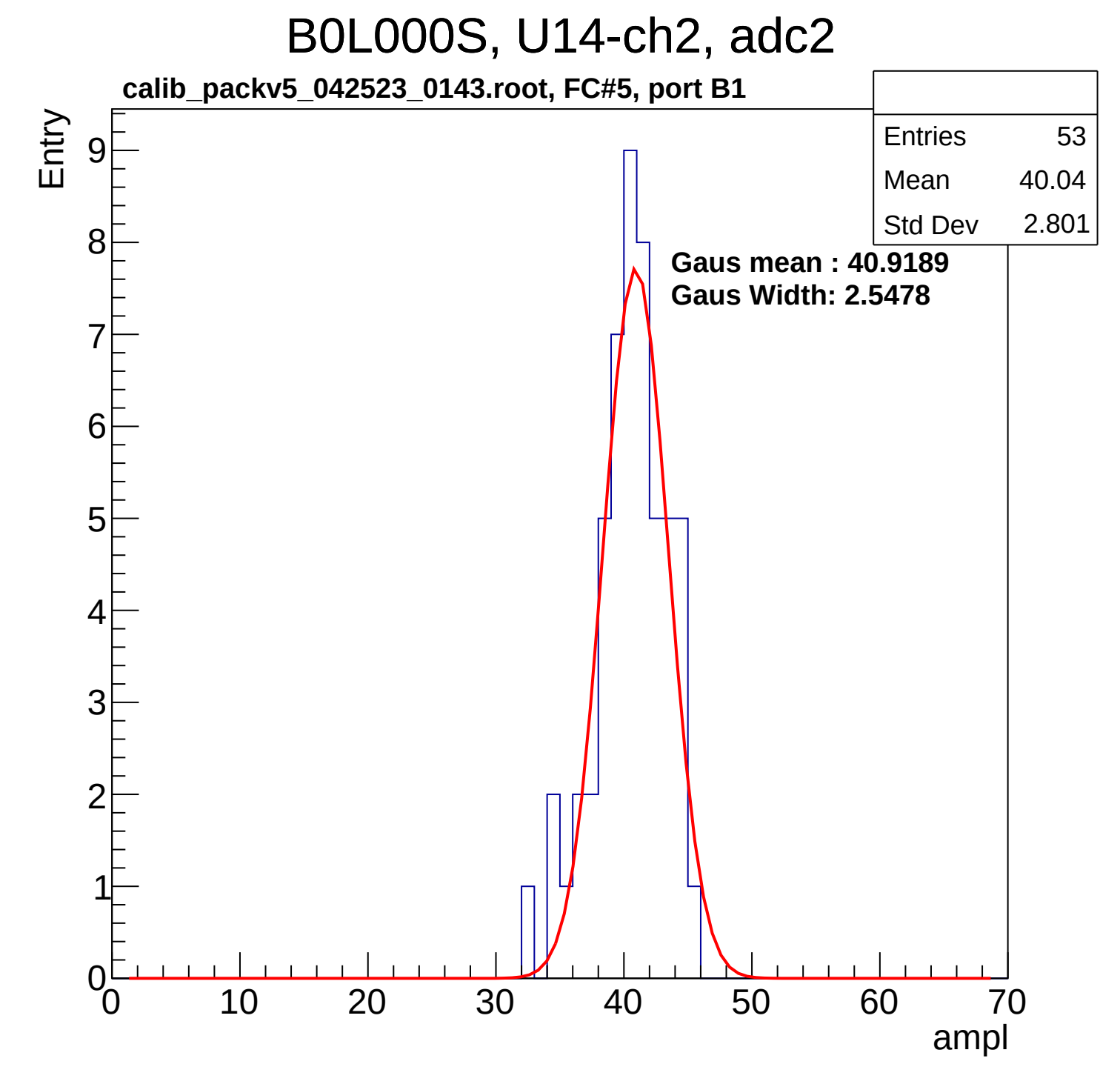
B0L000S, U14-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 40.04
Std Dev 2.801
Gaus mean : 40.9189
Gaus Width: 2.5478
Entry
ampl
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3
4
5
6
7
8
9
0
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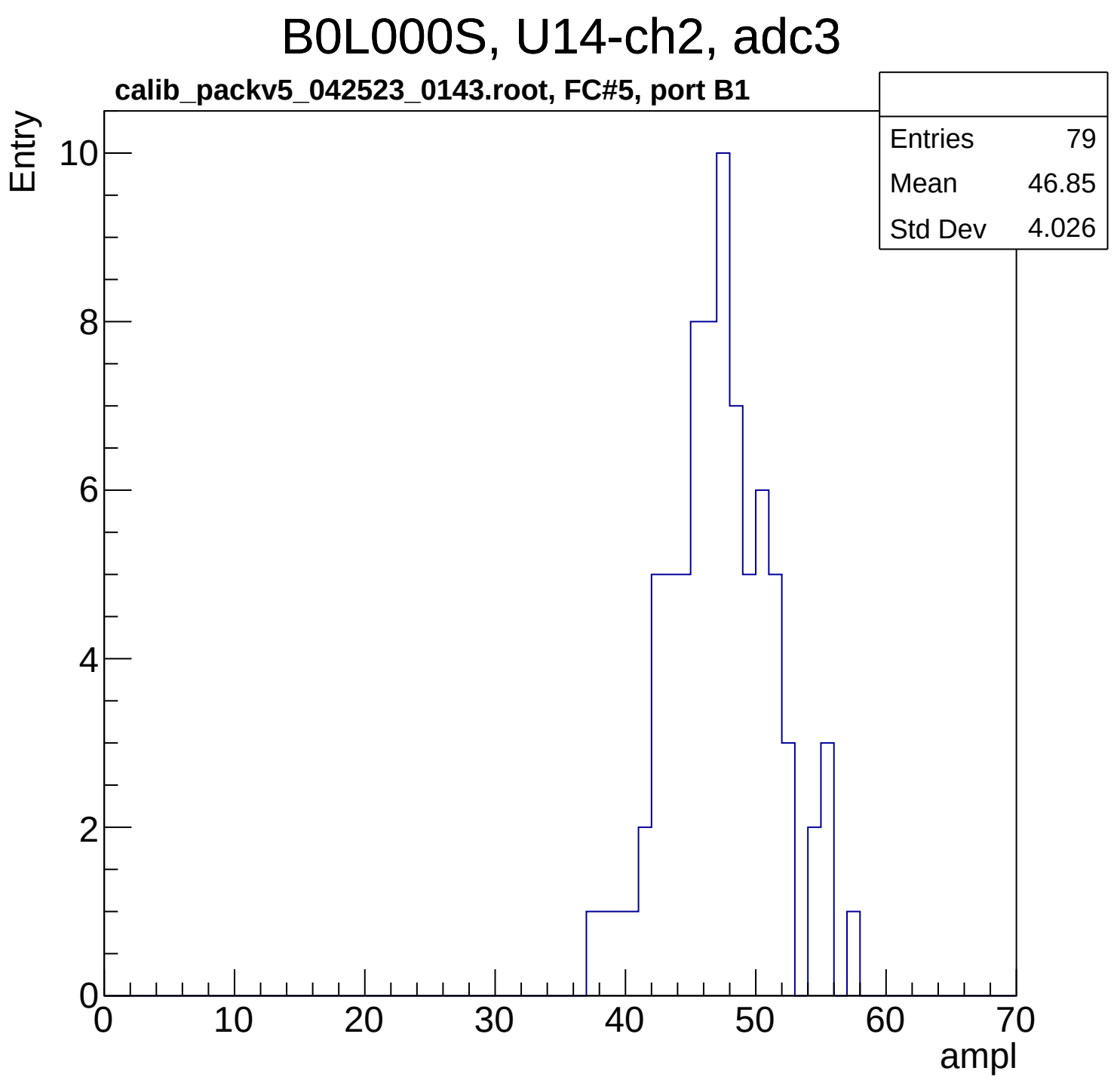
B0L000S, U14-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 46.85
Std Dev 4.026
Entry
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10
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10
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30
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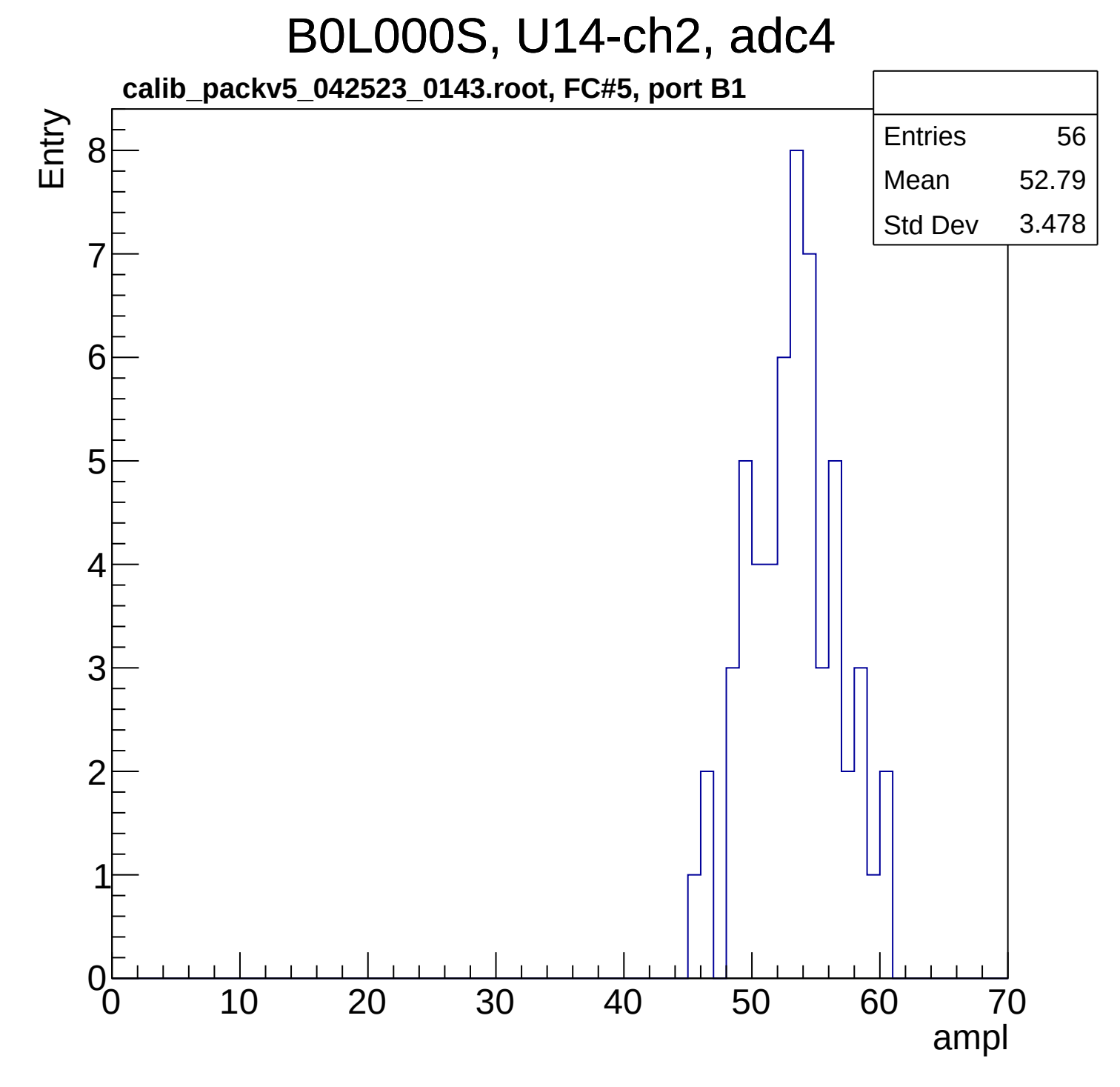
50
60
70
ampl


B0L000S, U14-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 52.79
Std Dev 3.478
Entry
ampl
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0
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60
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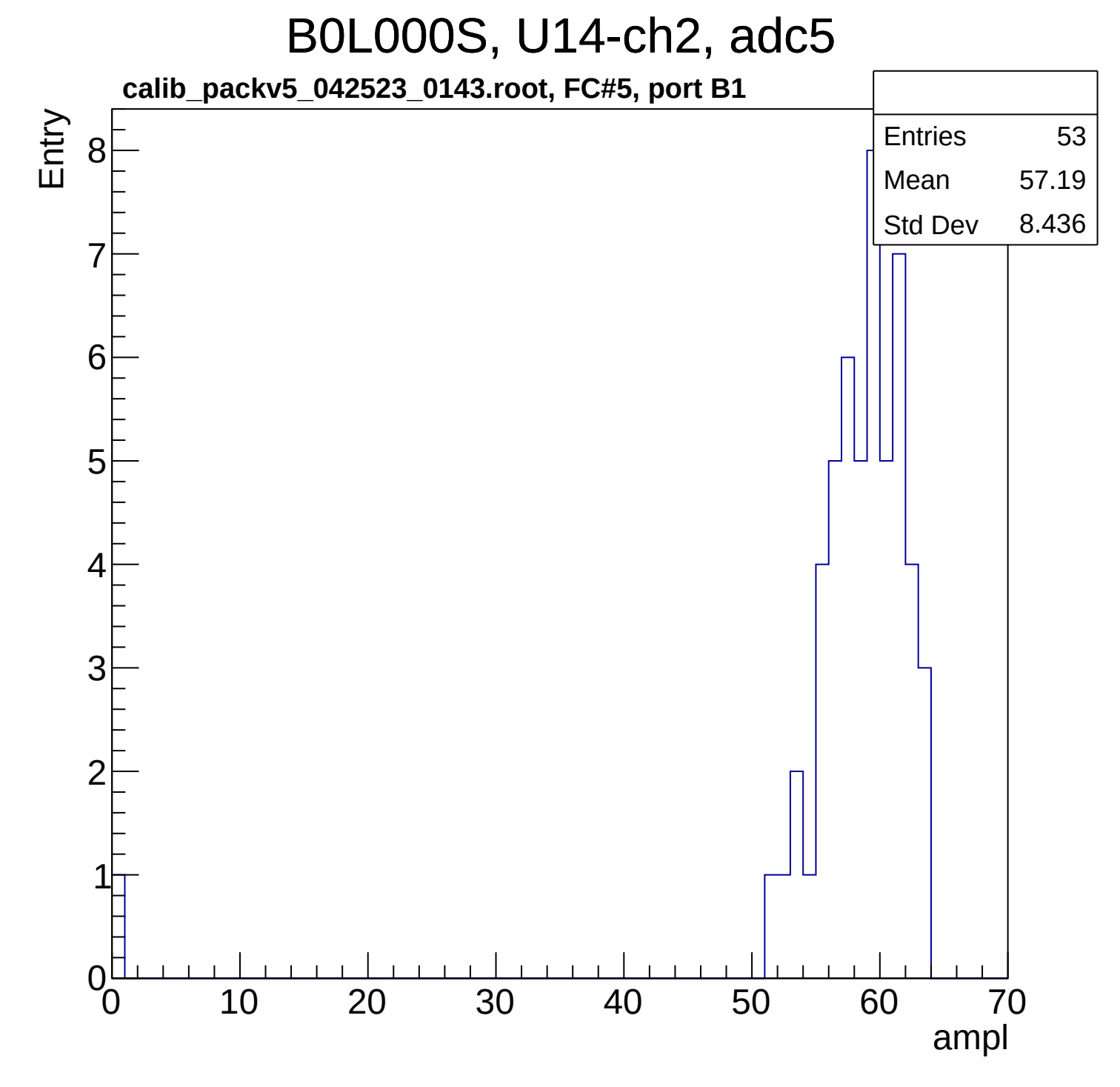
B0L000S, U14-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.19
Std Dev 8.436
Entry
ampl
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4
5
6
7
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0
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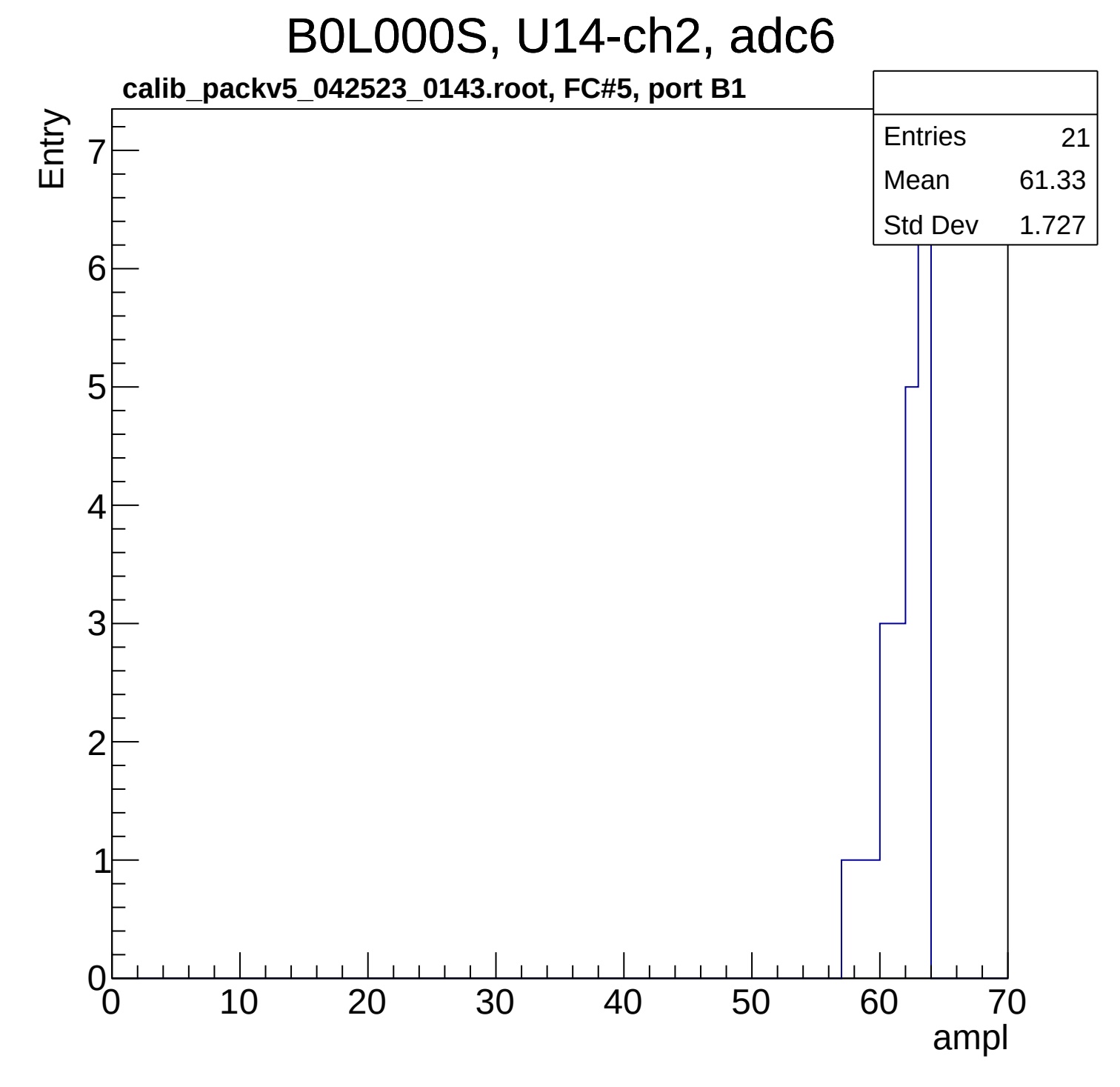
B0L000S, U14-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.33
Std Dev 1.727
Entry
ampl
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1
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3
4
5
6
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0
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60
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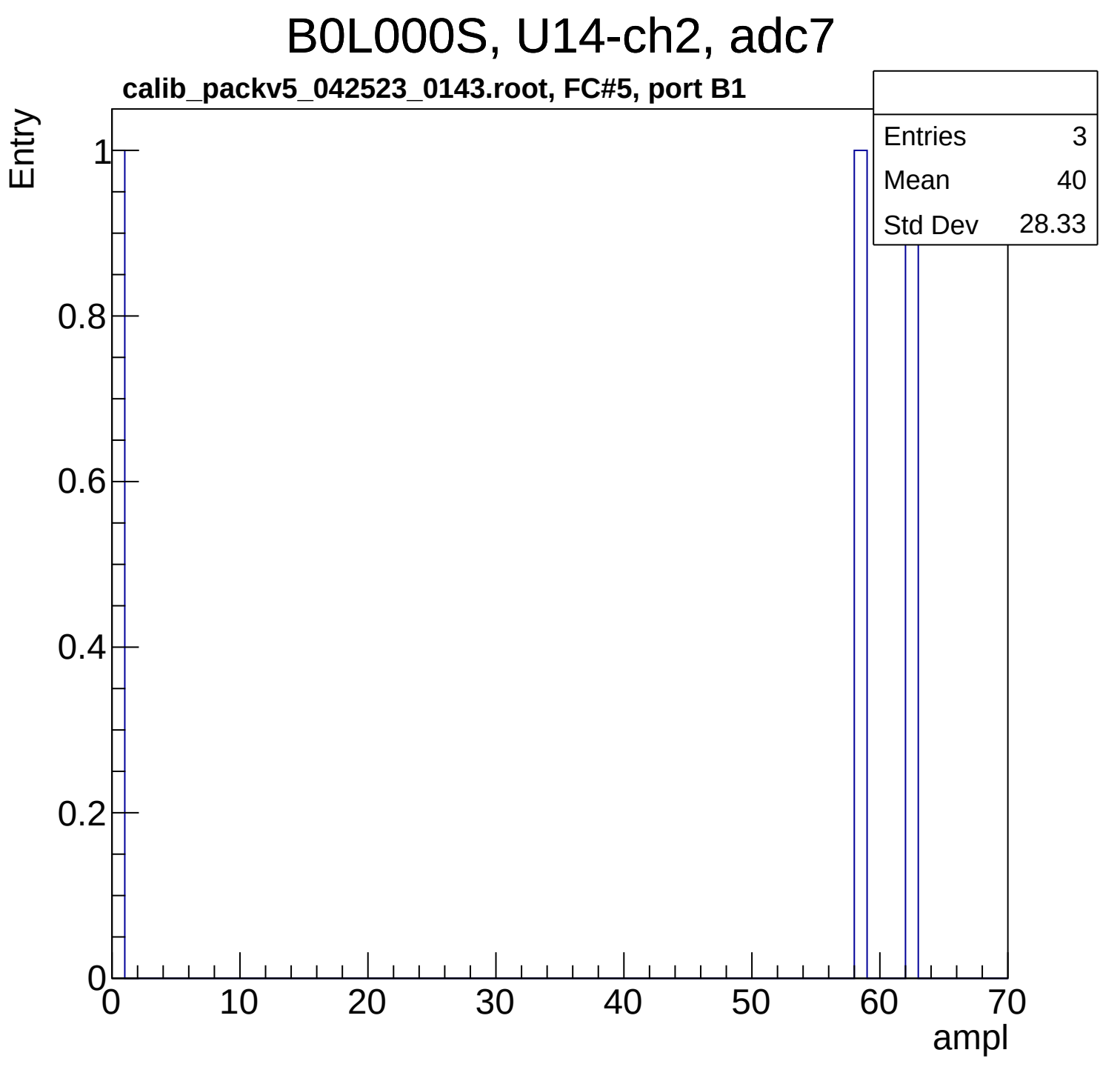
B0L000S, U14-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 40
Std Dev 28.33
Entry
ampl
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0.4
0.6
0.8
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0
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40
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60
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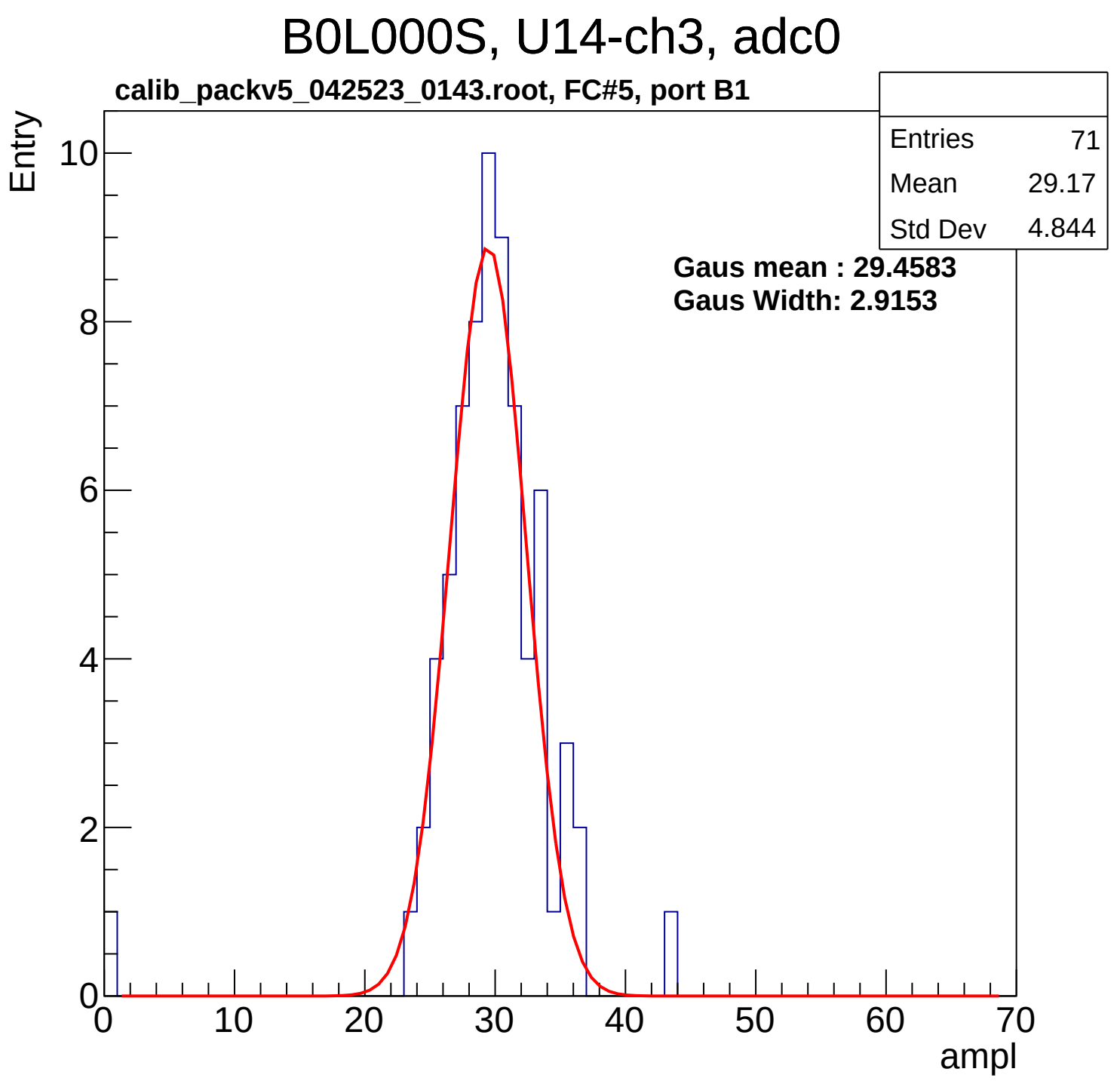
B0L000S, U14-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.17
Std Dev 4.844
Gaus mean : 29.4583
Gaus Width: 2.9153
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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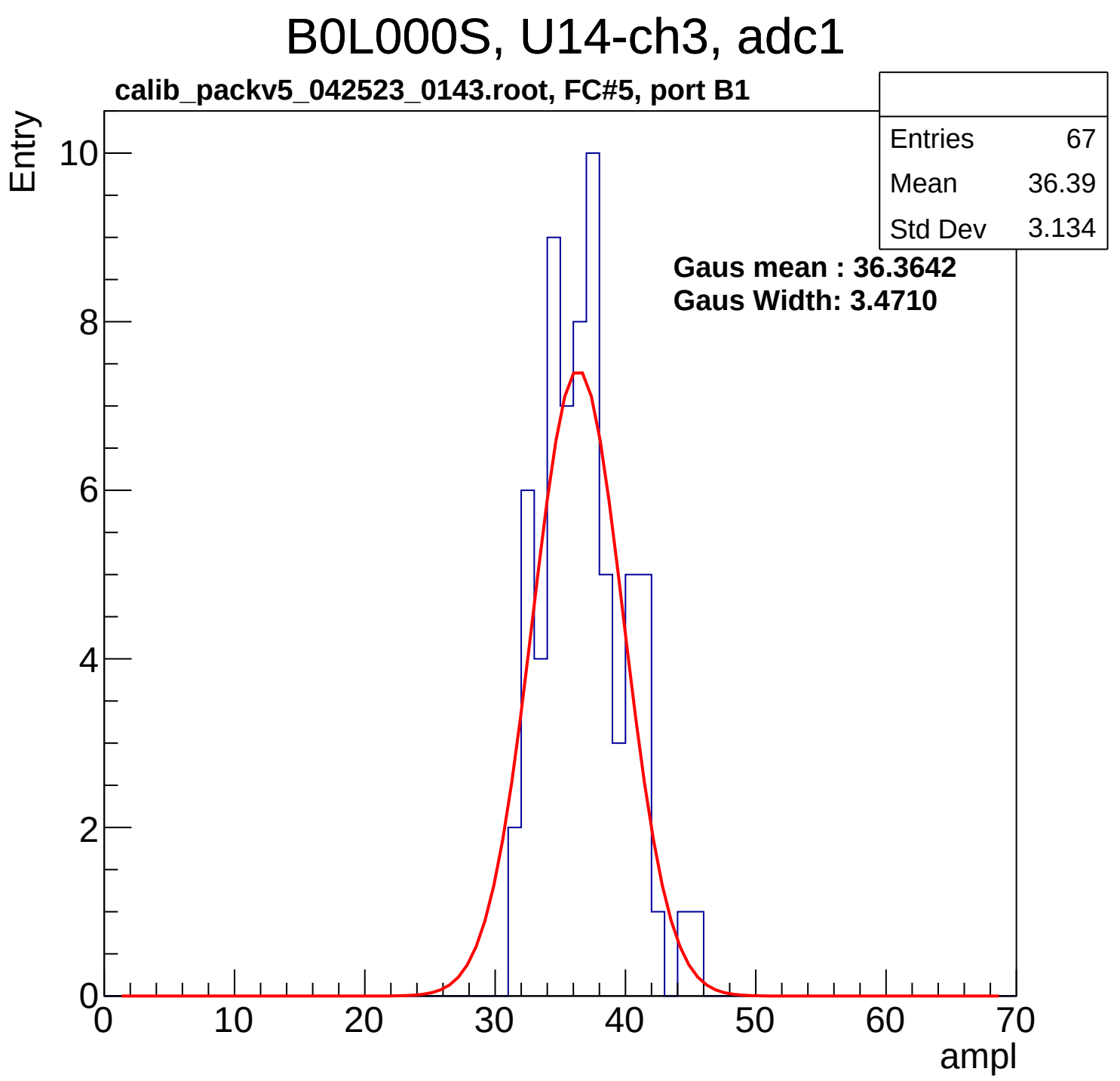
B0L000S, U14-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 36.39
Std Dev 3.134
Gaus mean : 36.3642
Gaus Width: 3.4710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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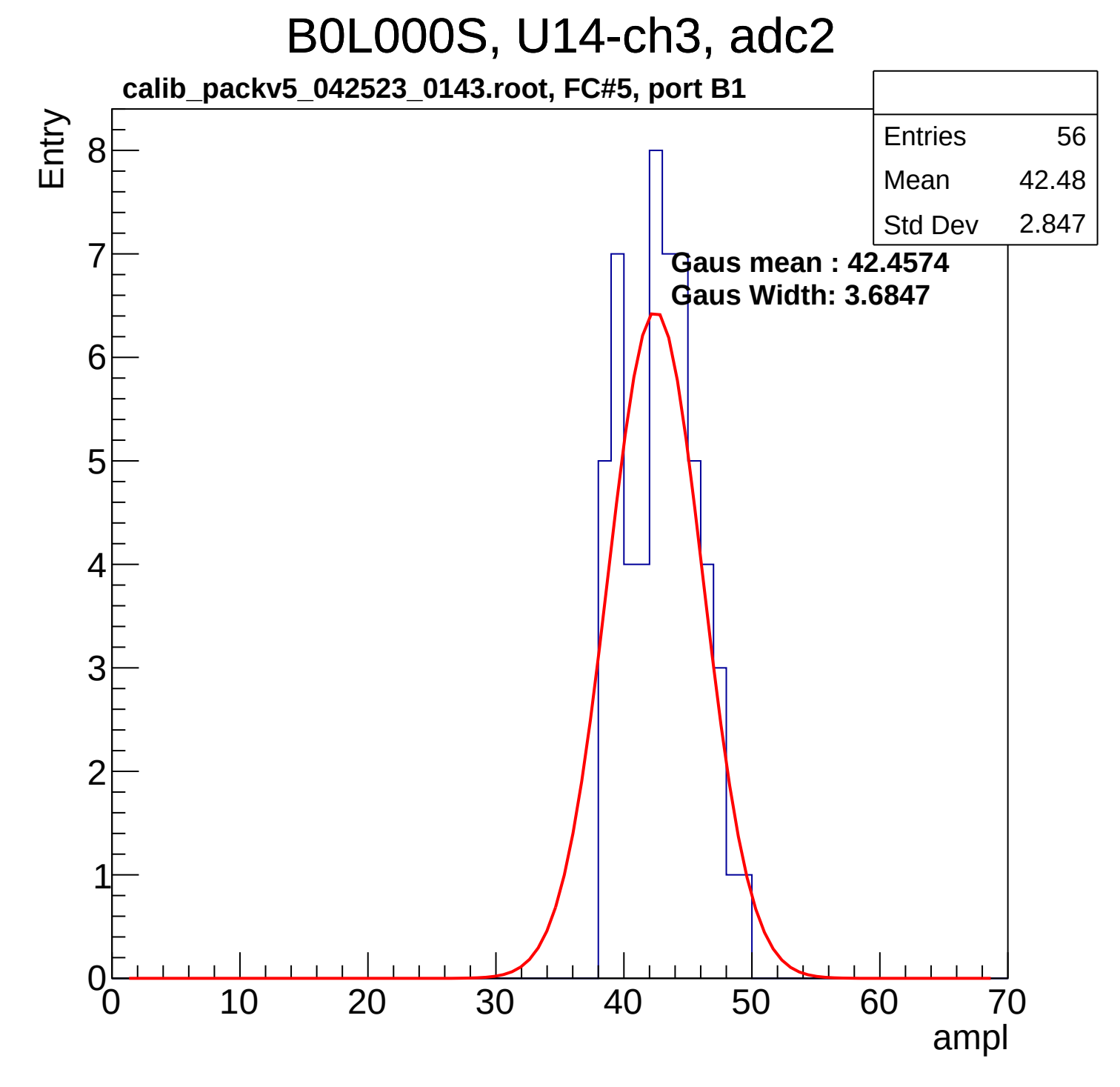
B0L000S, U14-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 42.48
Std Dev 2.847
Gaus mean : 42.4574
Gaus Width: 3.6847
Entry
ampl
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3
4
5
6
7
8
0
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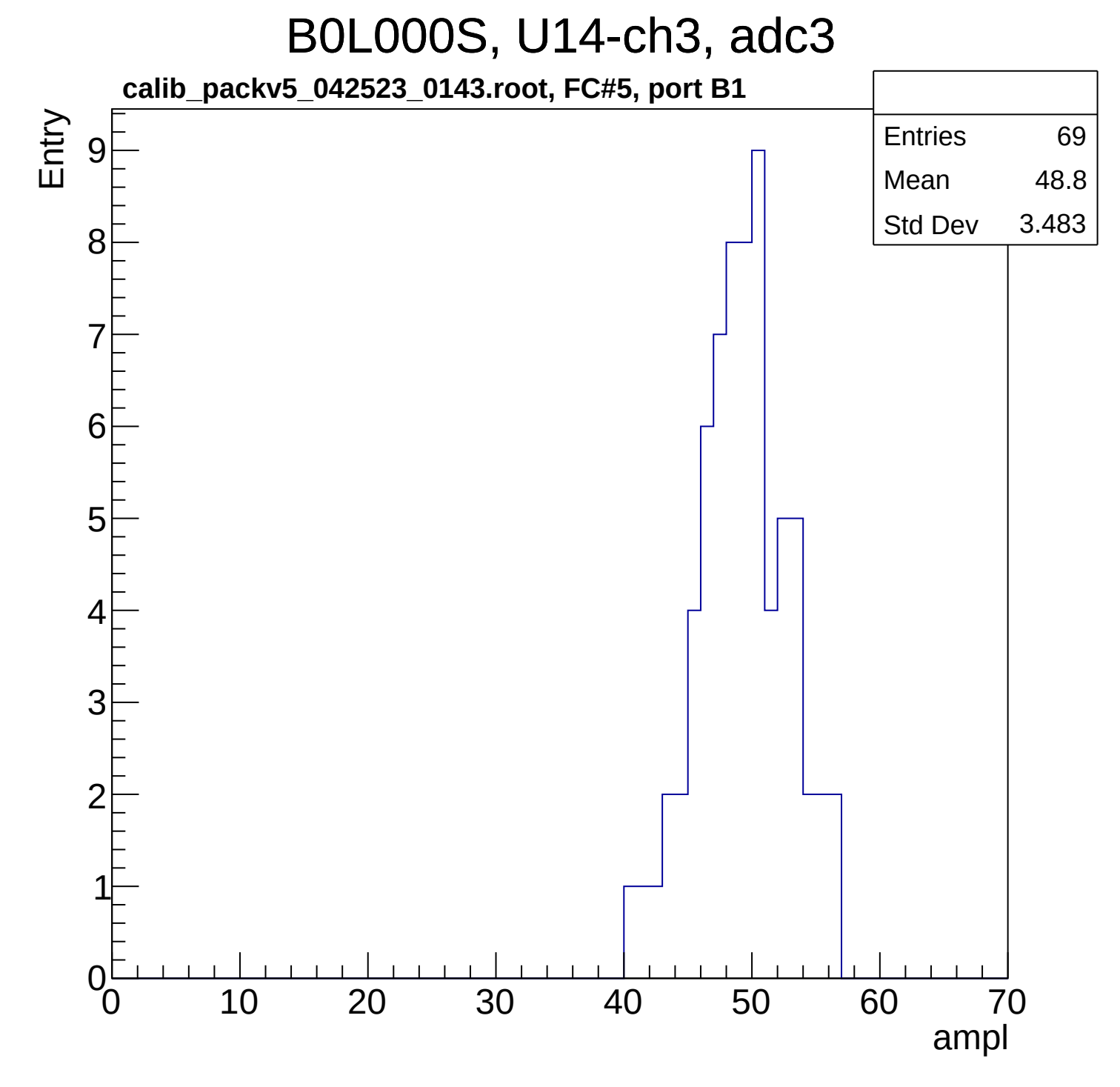
B0L000S, U14-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48.8
Std Dev 3.483
Entry
ampl
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4
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6
7
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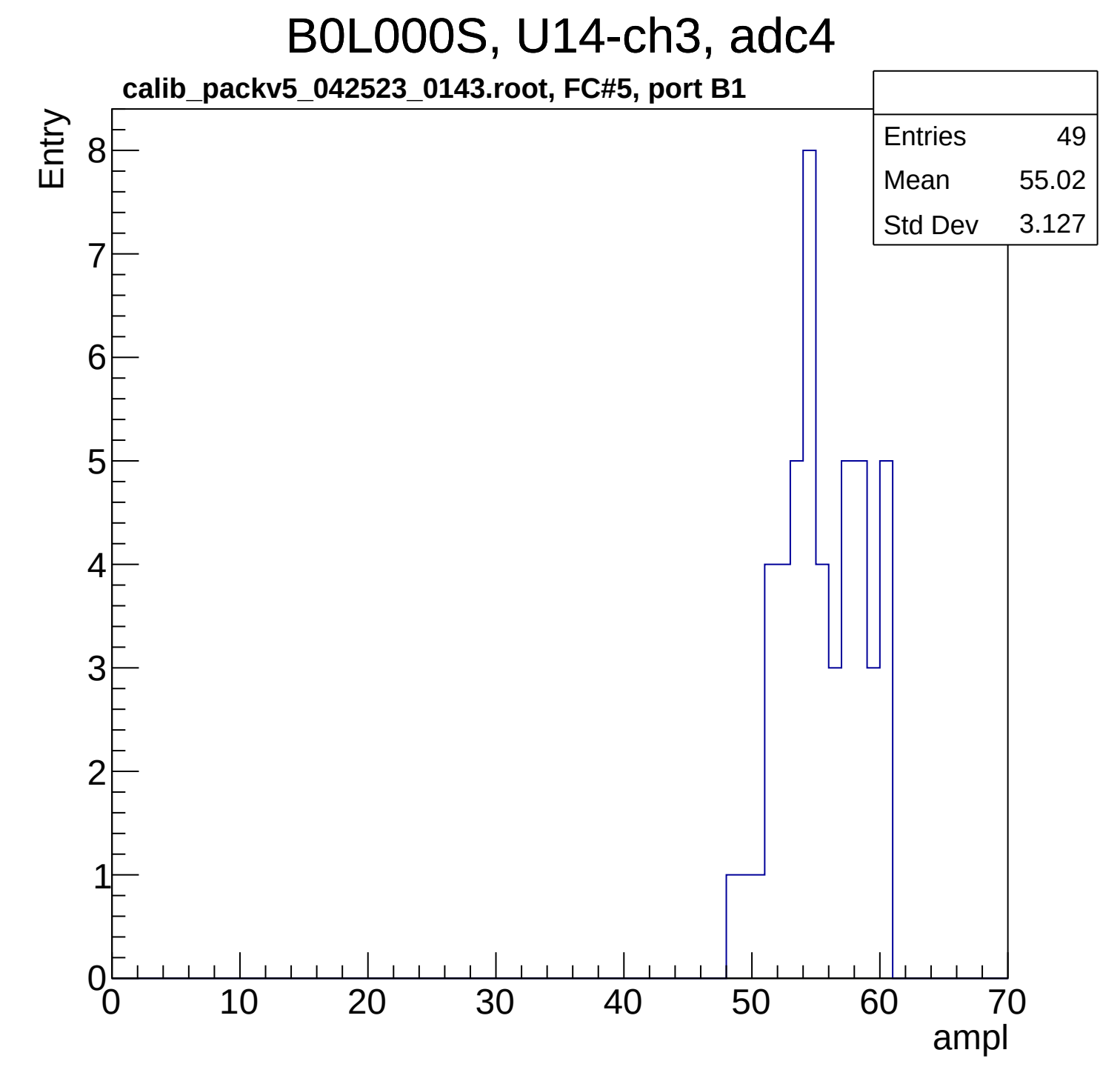
B0L000S, U14-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.02
Std Dev 3.127
Entry
ampl
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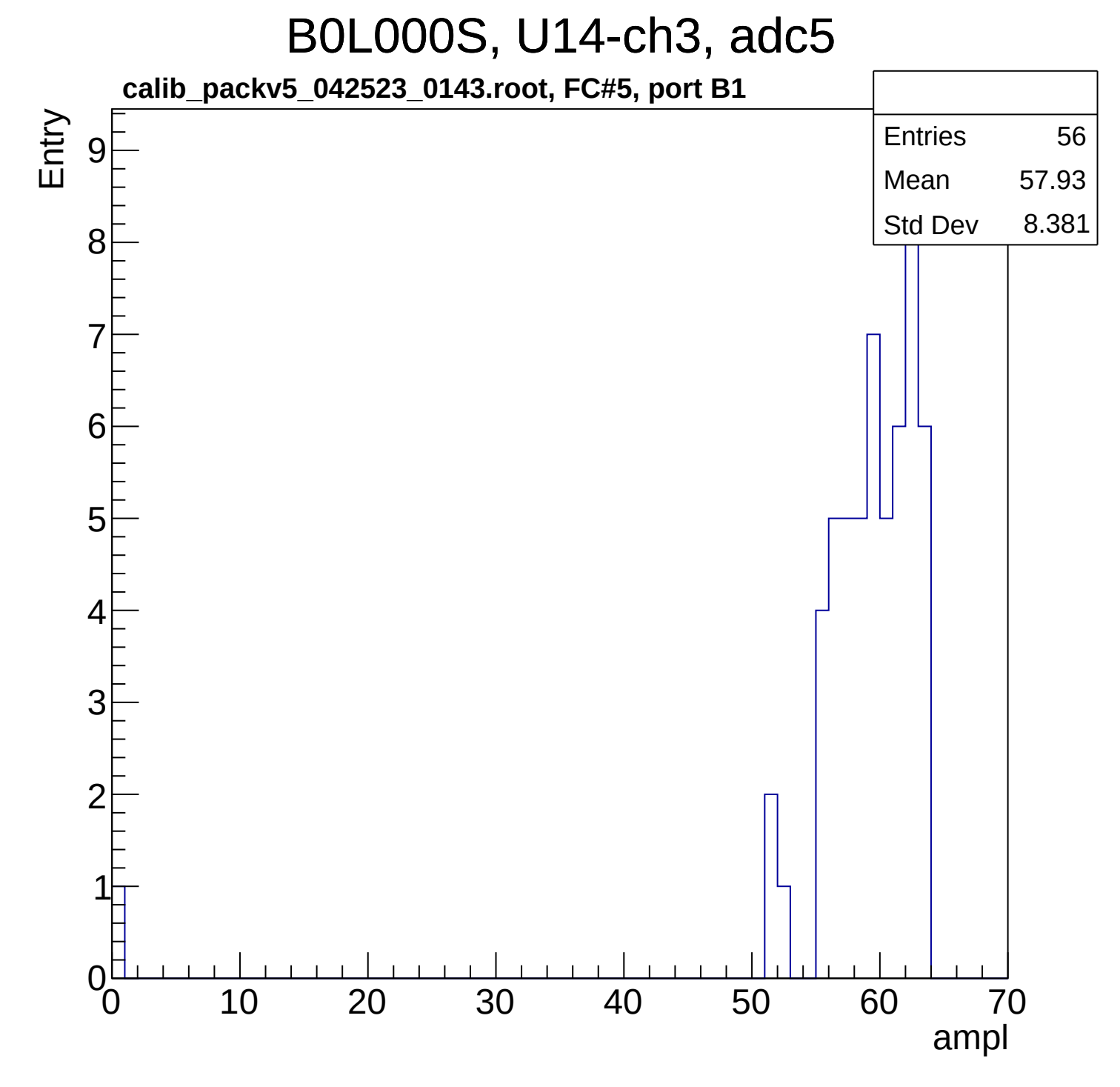
B0L000S, U14-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.93
Std Dev 8.381
Entry
ampl
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4
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6
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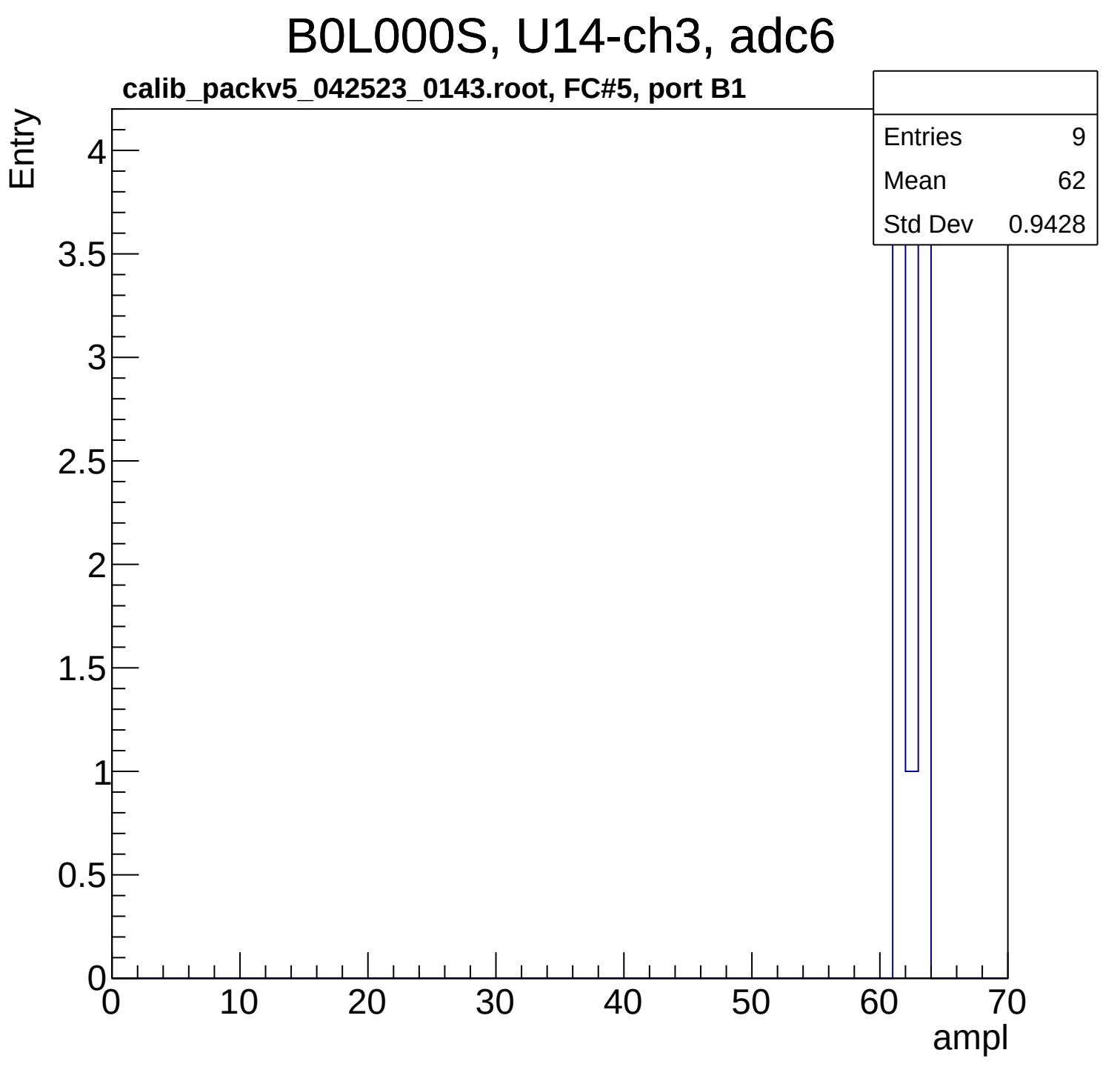
B0L000S, U14-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
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60
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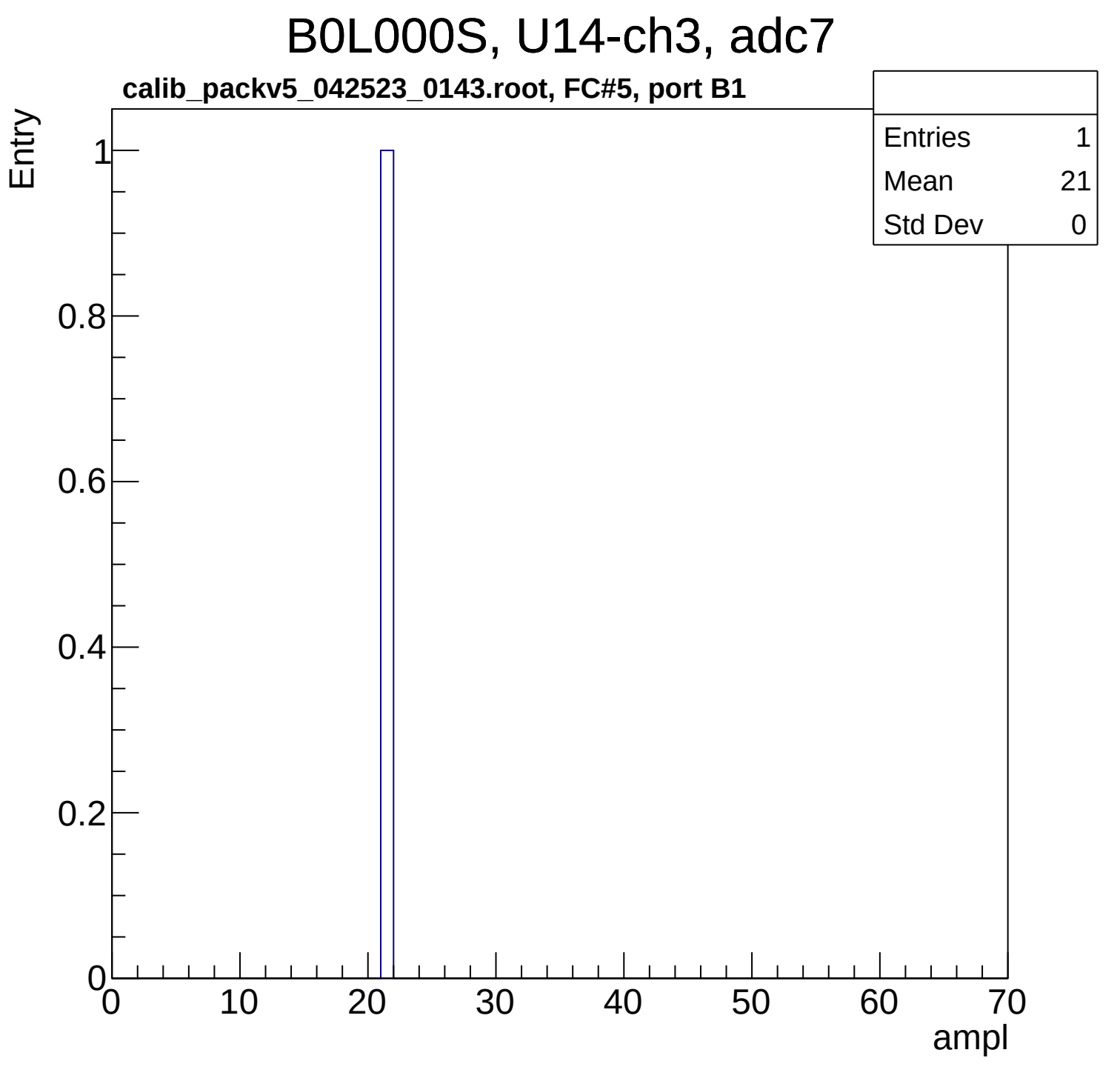
B0L000S, U14-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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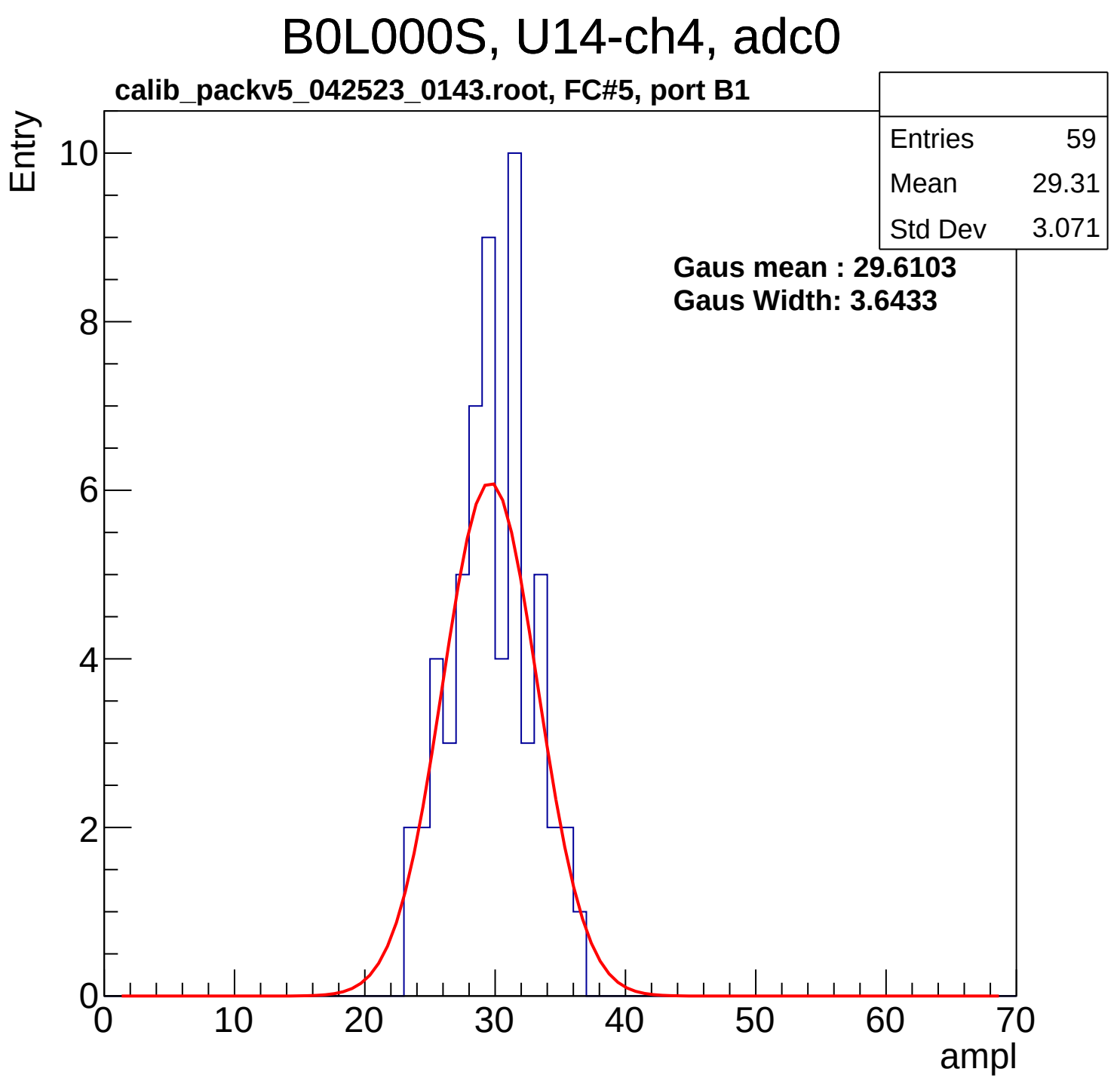
50
60
70
ampl


B0L000S, U14-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 29.31
Std Dev 3.071
Gaus mean : 29.6103
Gaus Width: 3.6433
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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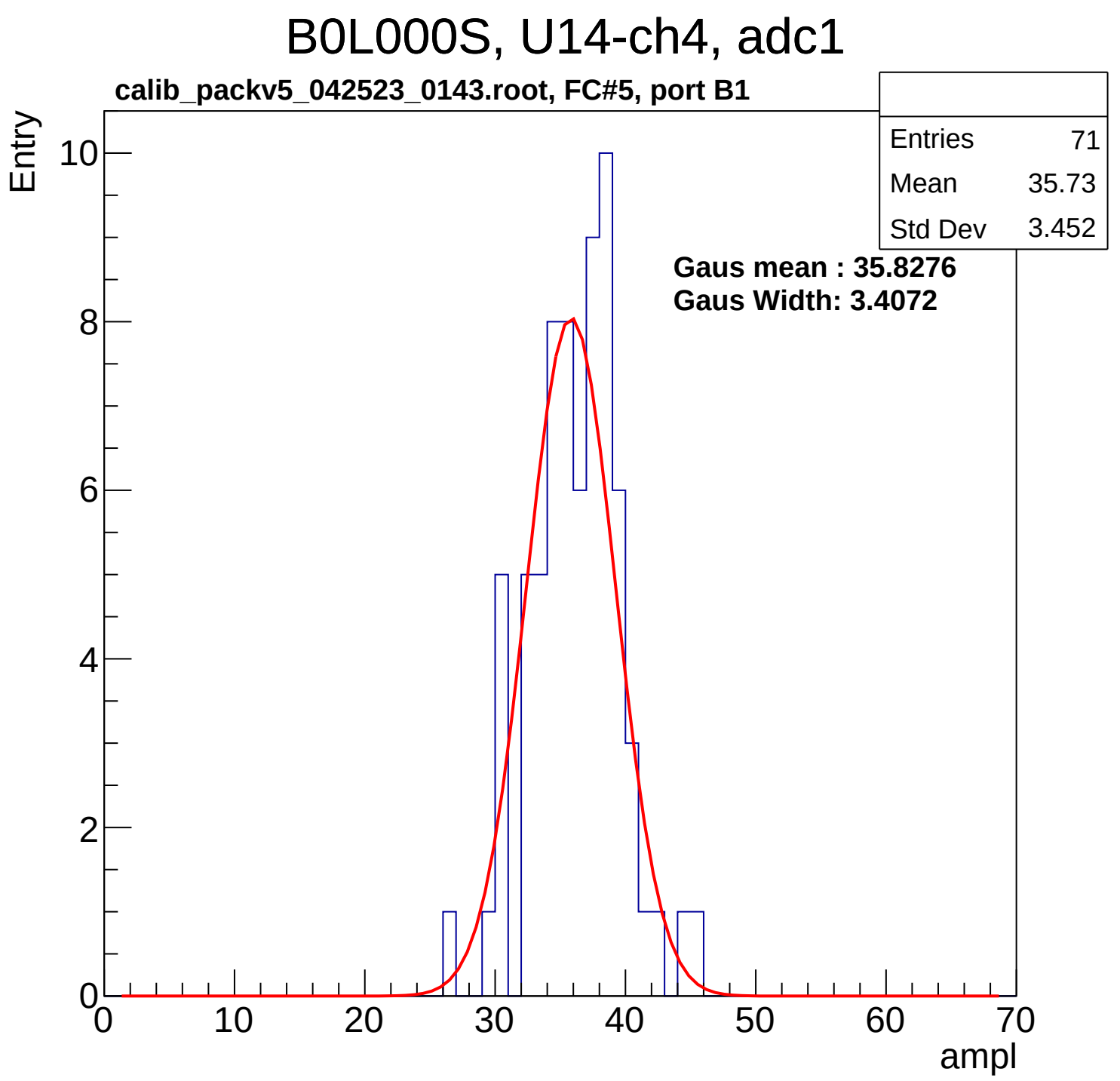
B0L000S, U14-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 35.73
Std Dev 3.452
Gaus mean : 35.8276
Gaus Width: 3.4072
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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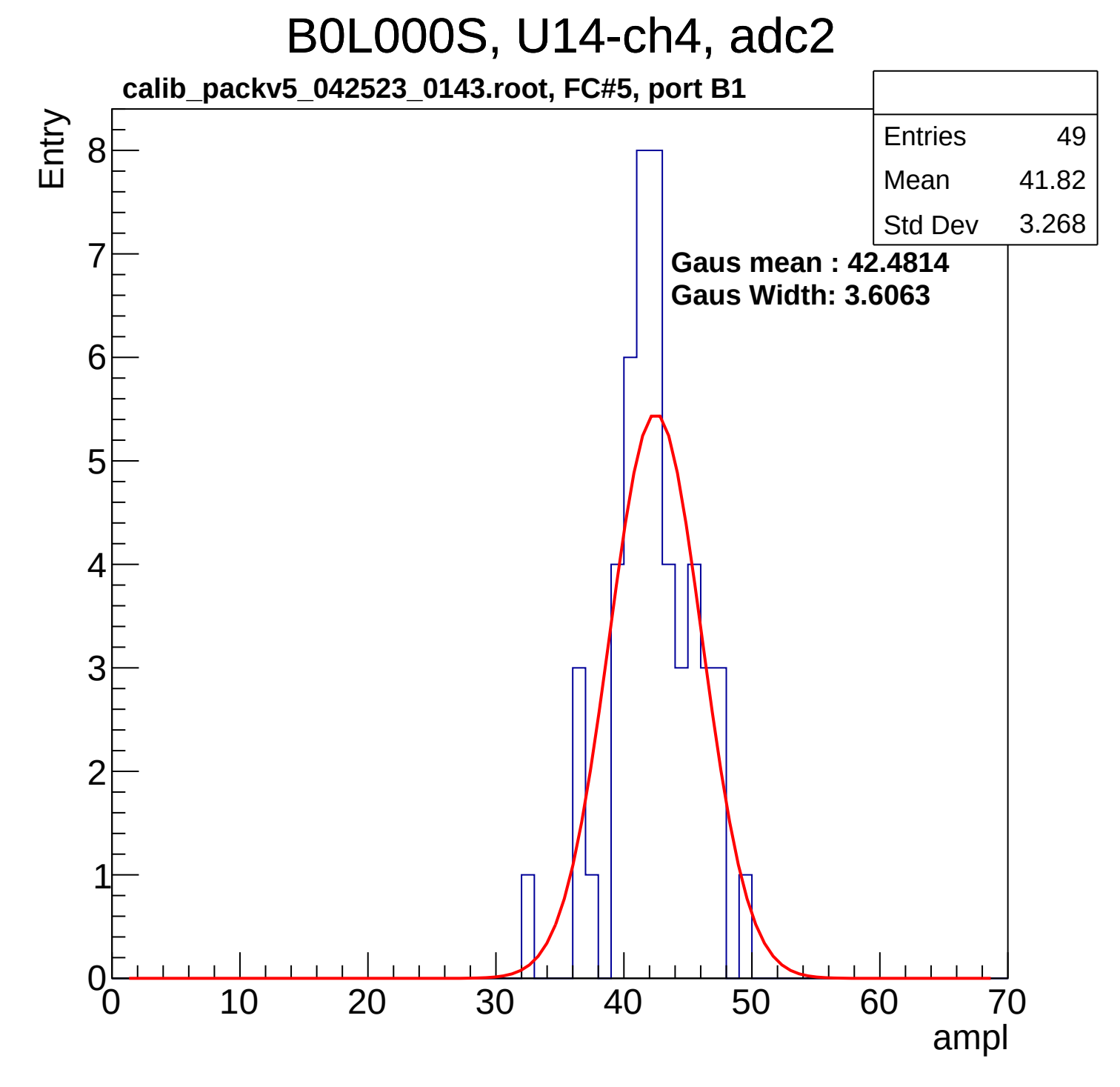
B0L000S, U14-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 41.82
Std Dev 3.268
Gaus mean : 42.4814
Gaus Width: 3.6063
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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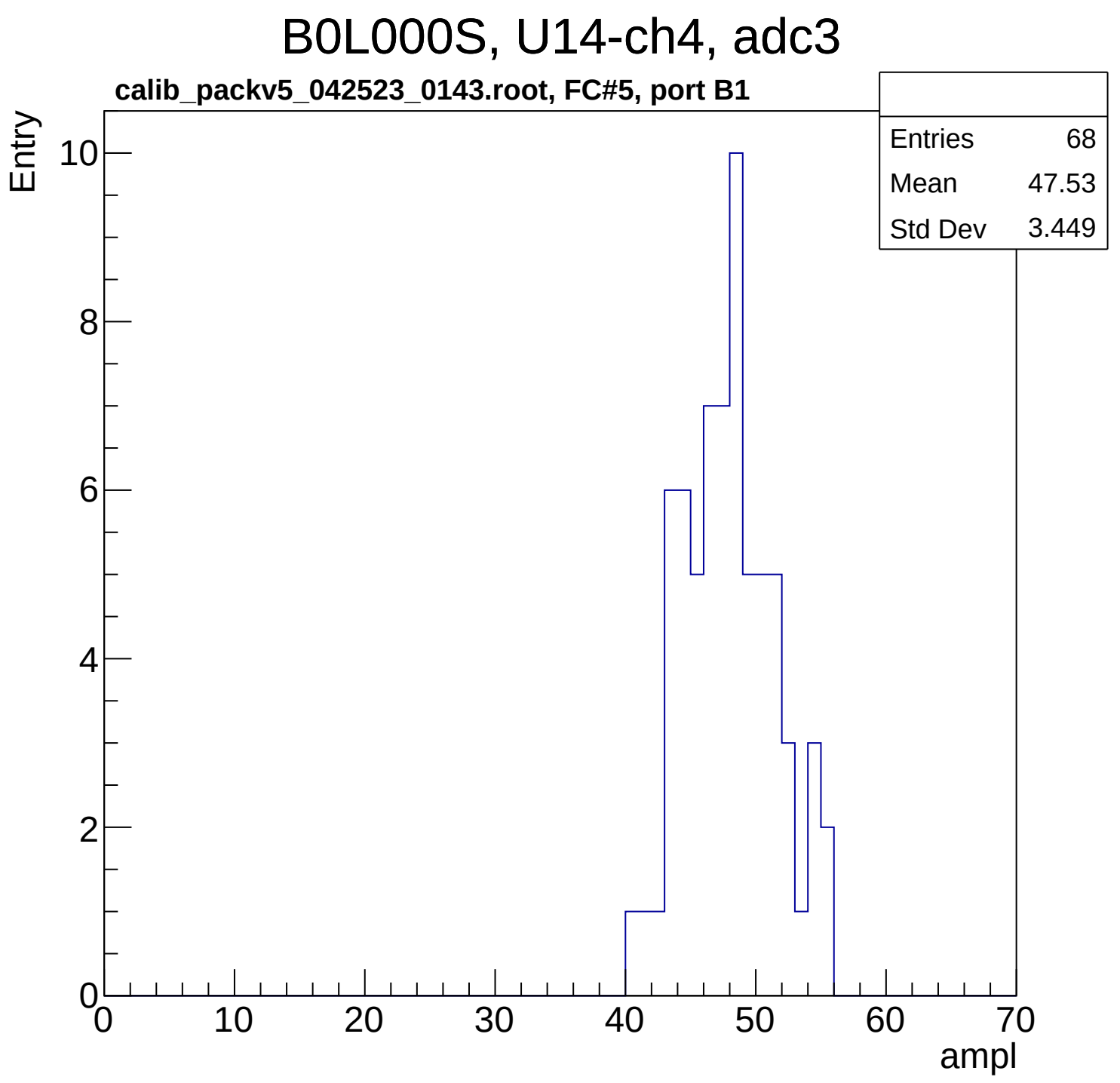
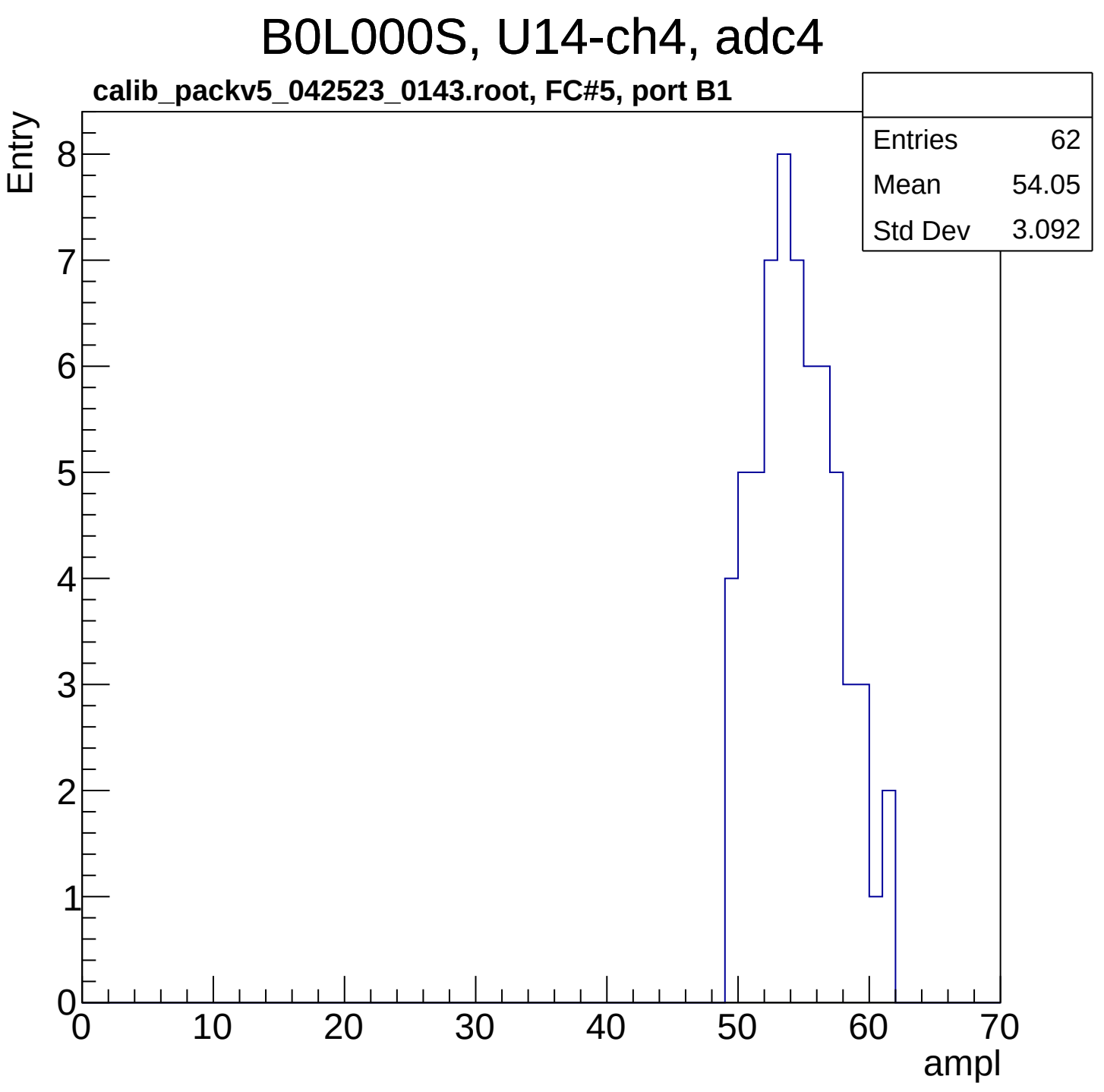

B0L000S, U14-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 47.53
Std Dev 3.449
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 54.05
Std Dev 3.092
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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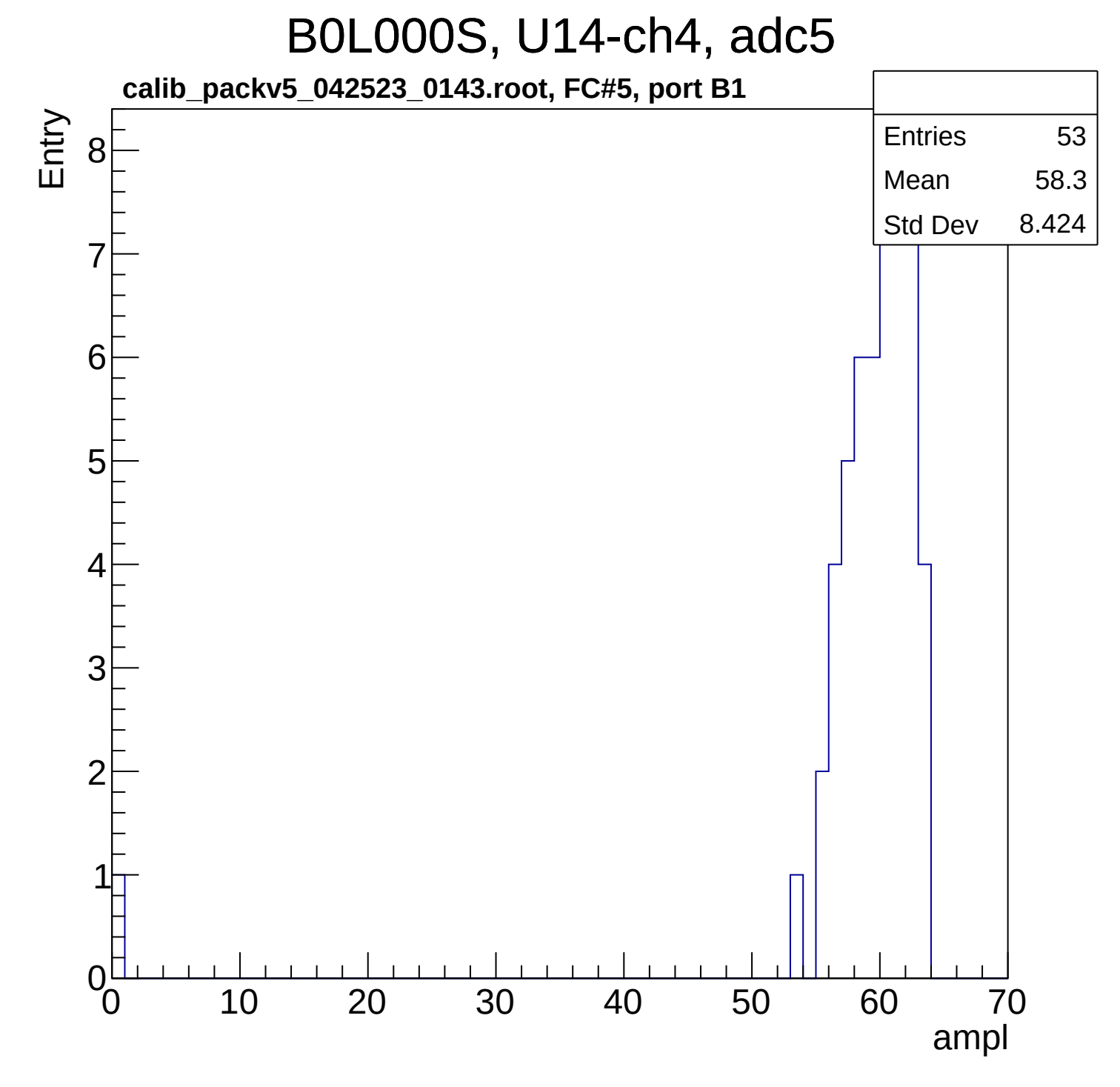
B0L000S, U14-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.3
Std Dev 8.424
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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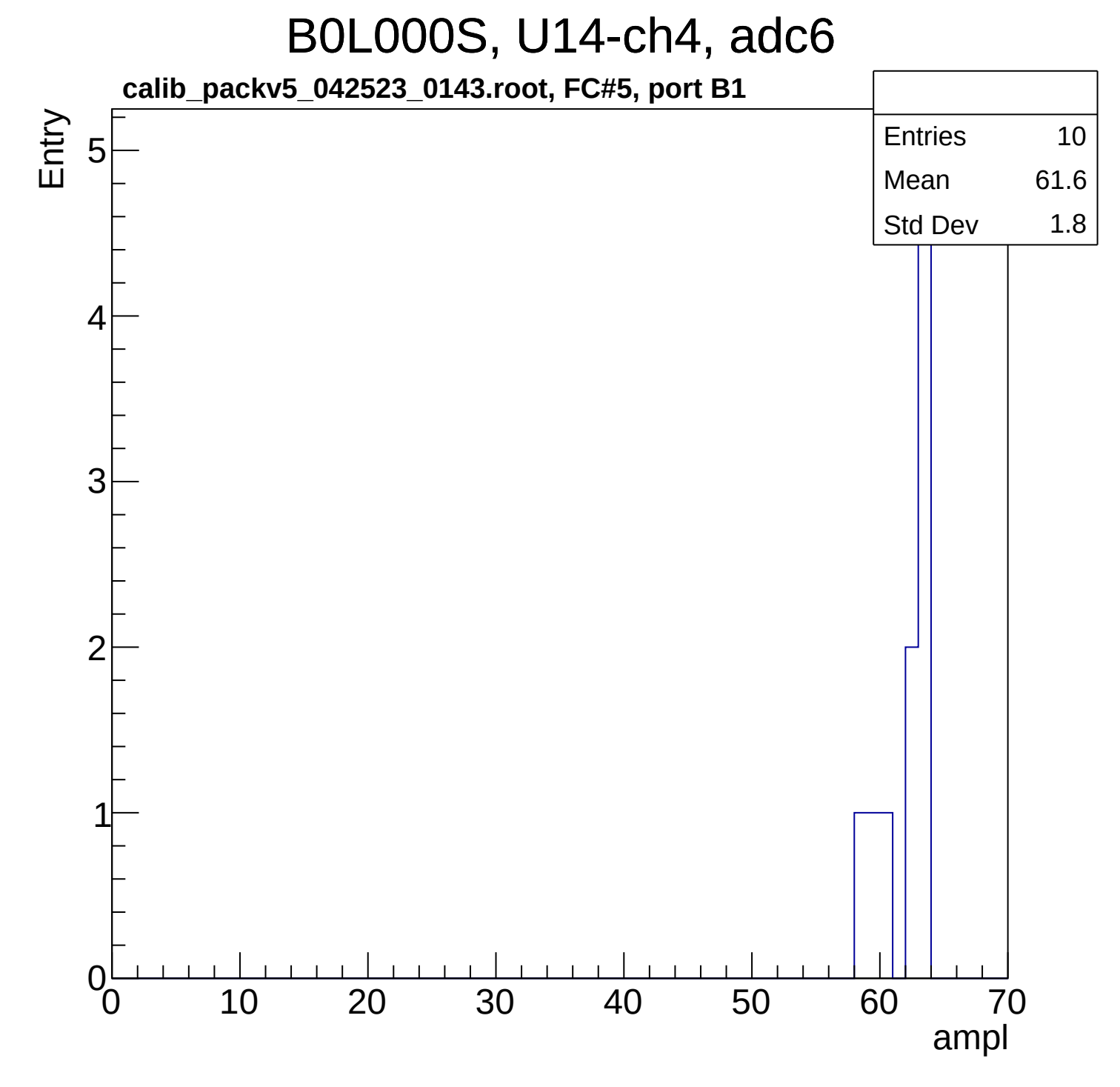
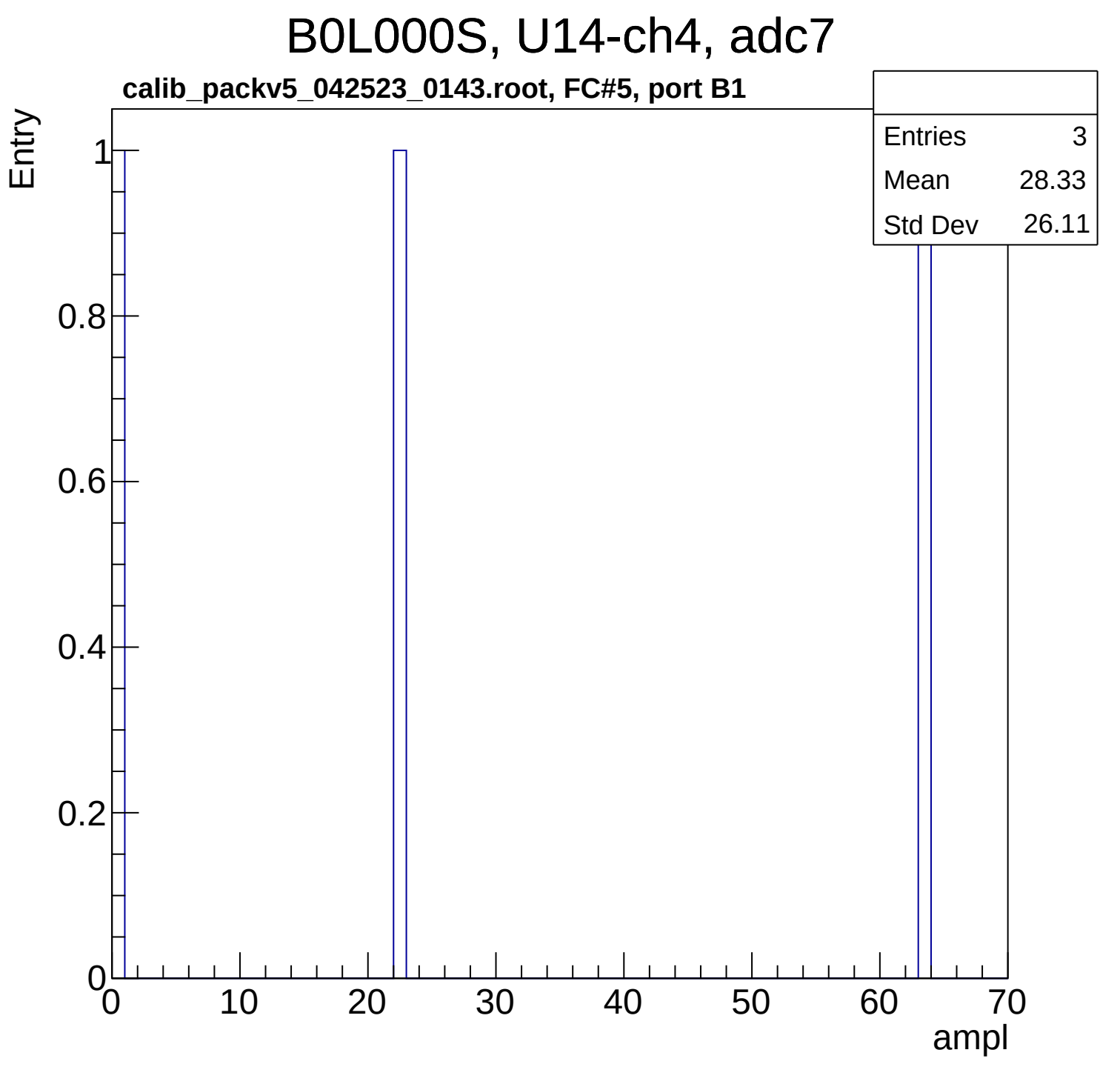
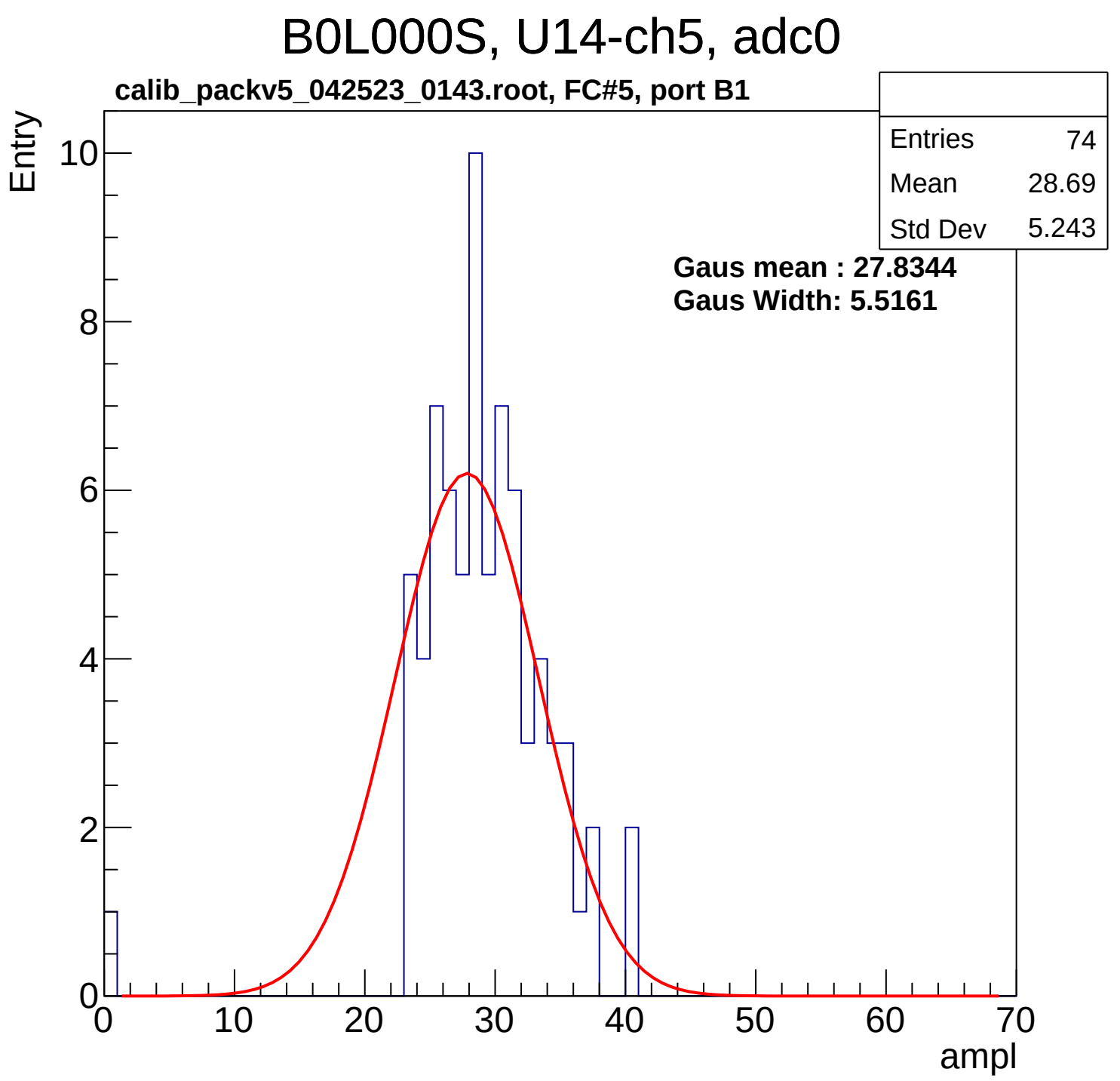
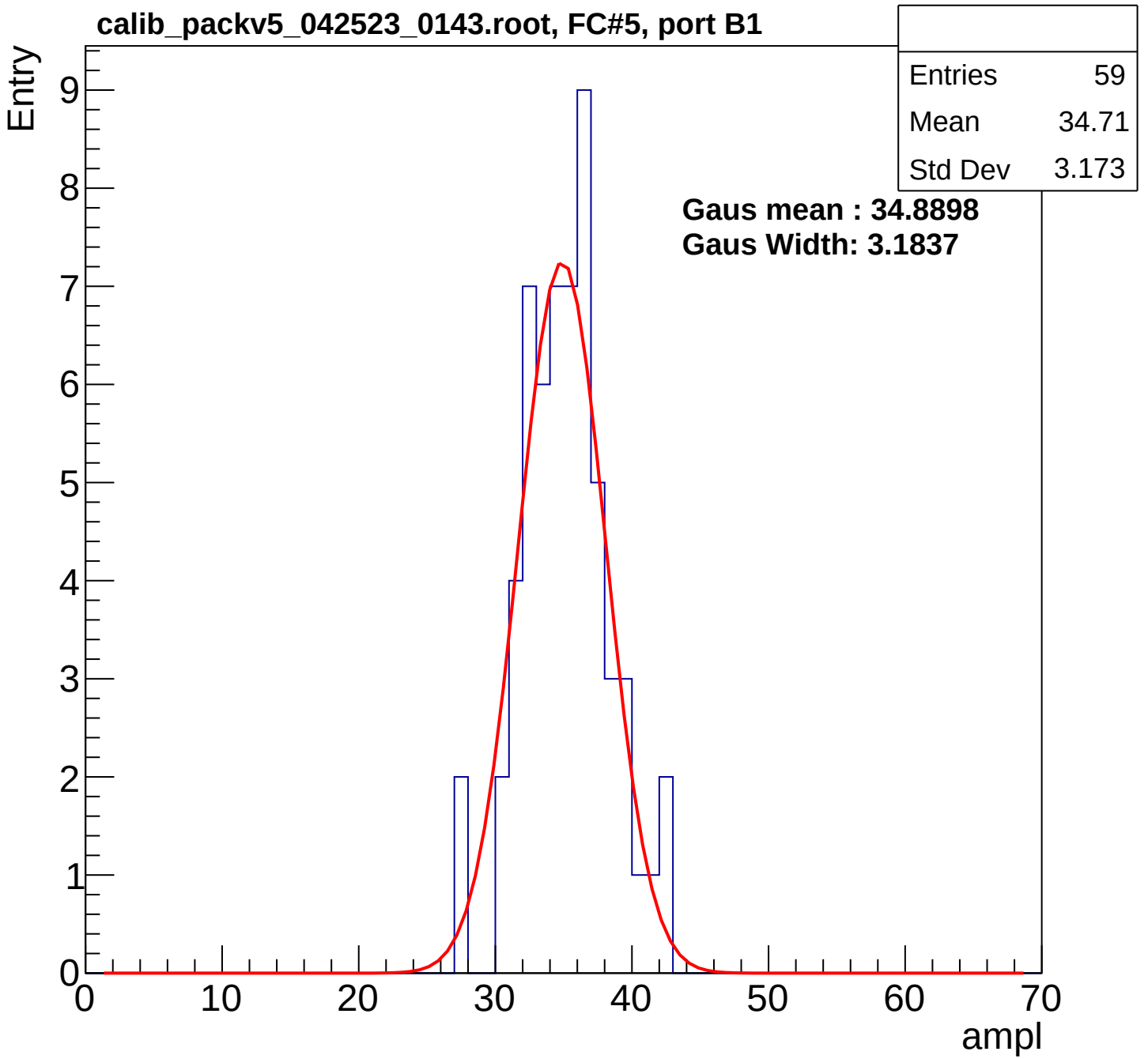

B0L000S, U14-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.6
Std Dev 1.8
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28.33
Std Dev 26.11
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U14-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.69
Std Dev 5.243
Gaus mean : 27.8344
Gaus Width: 5.5161
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 34.71
Std Dev 3.173
Gaus mean : 34.8898
Gaus Width: 3.1837
Entry
ampl
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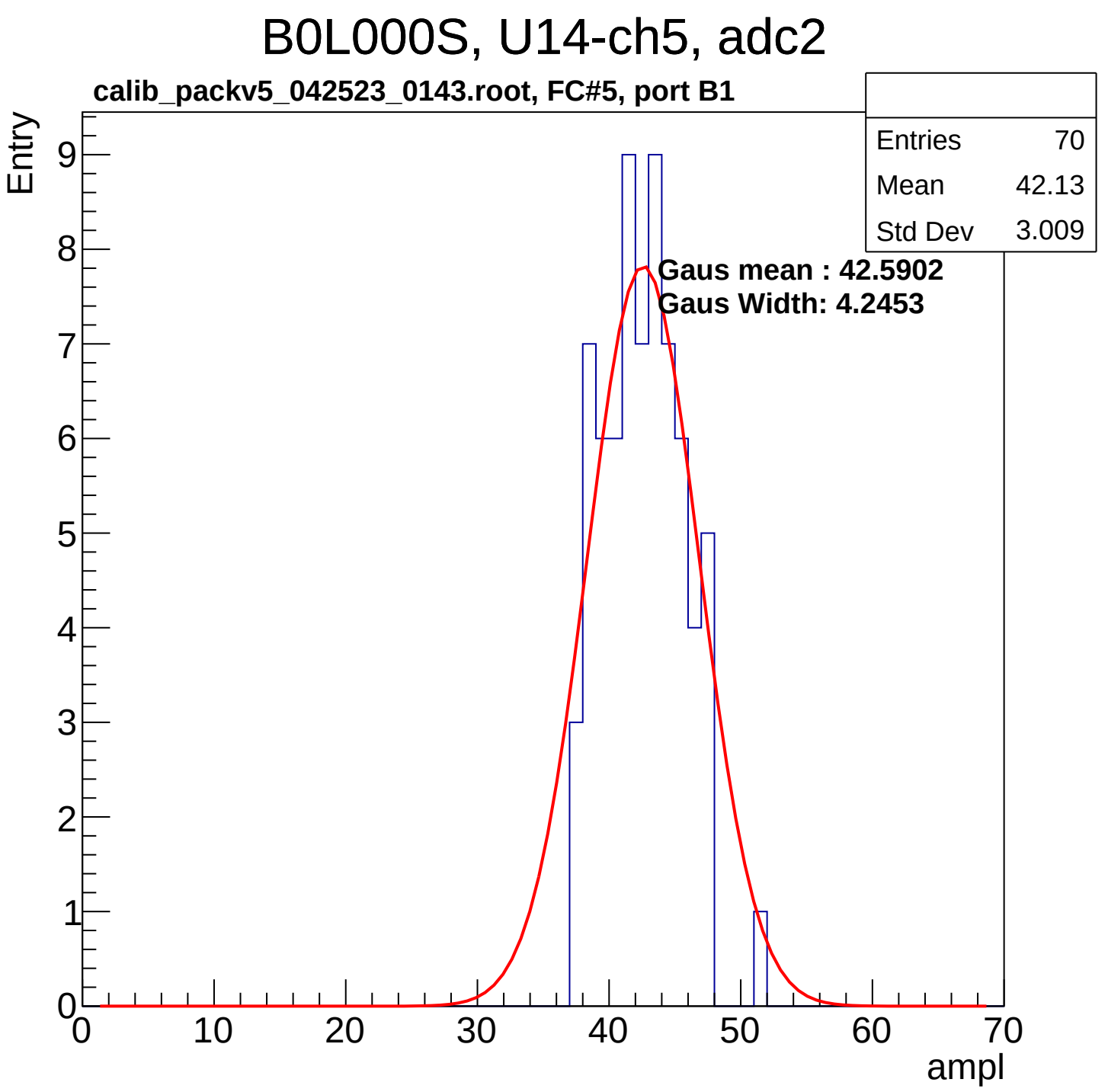
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.13
Std Dev 3.009
Gaus mean : 42.5902
Gaus Width: 4.2453
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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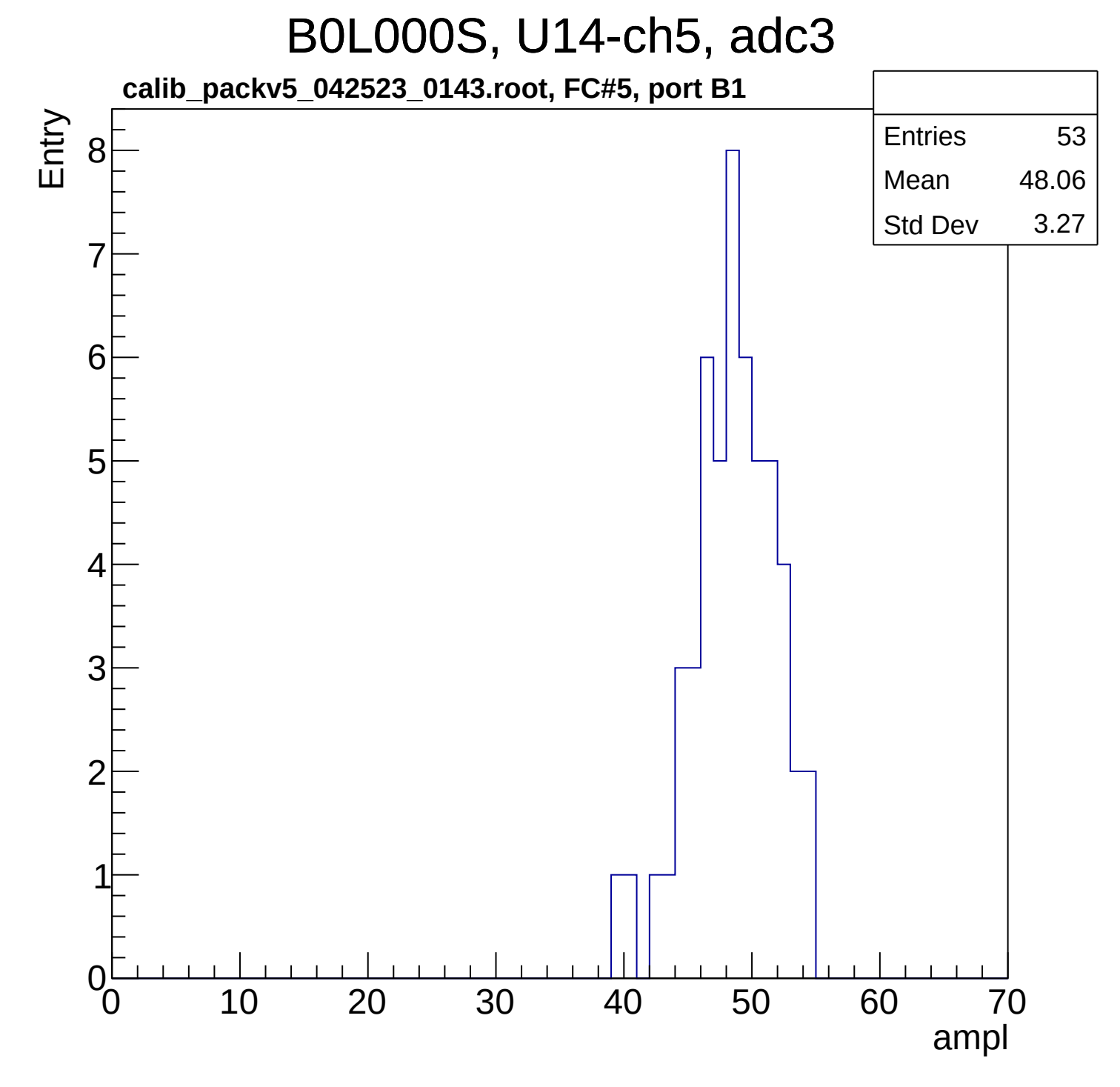
B0L000S, U14-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 48.06
Std Dev 3.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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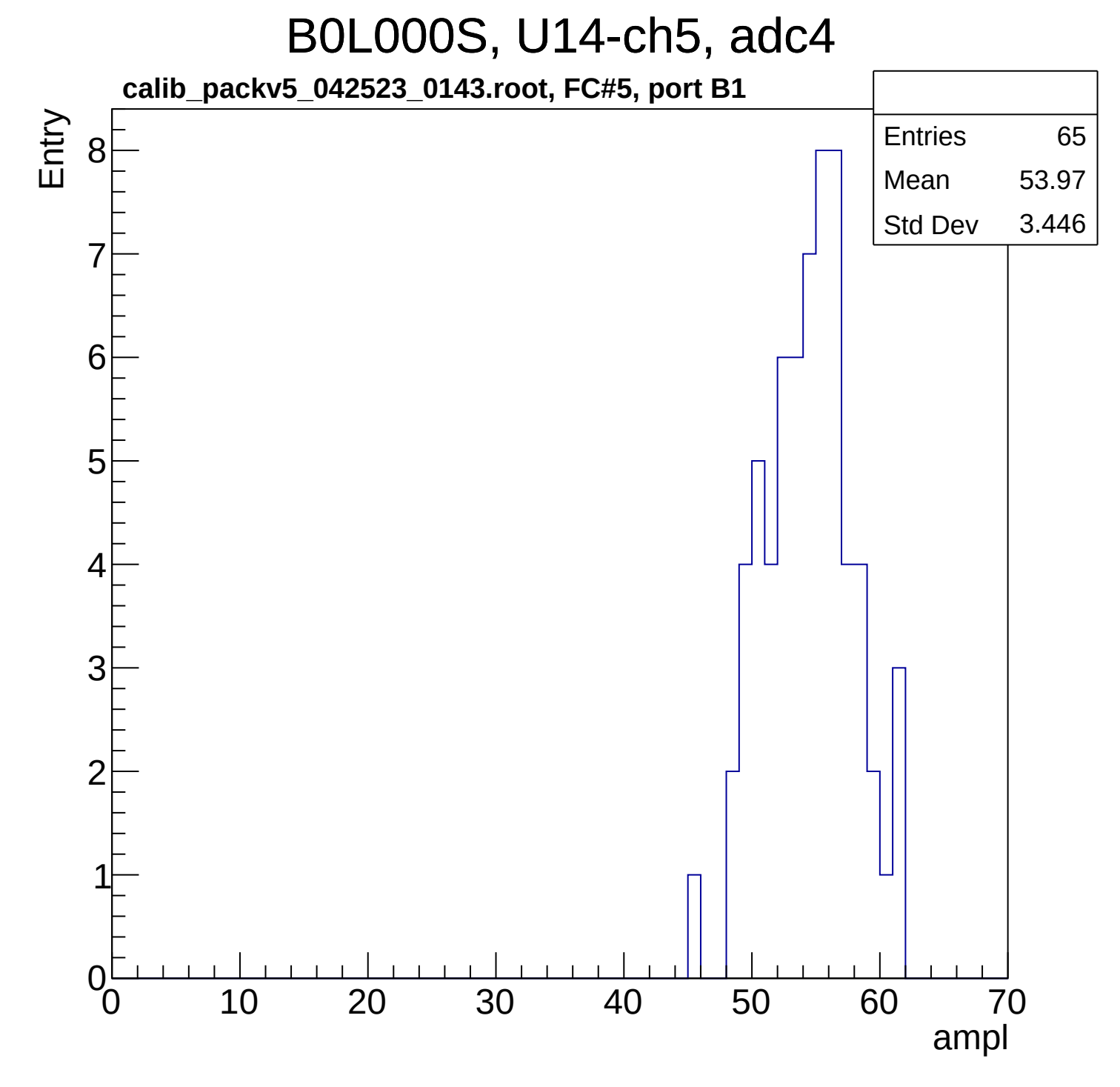
B0L000S, U14-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 53.97
Std Dev 3.446
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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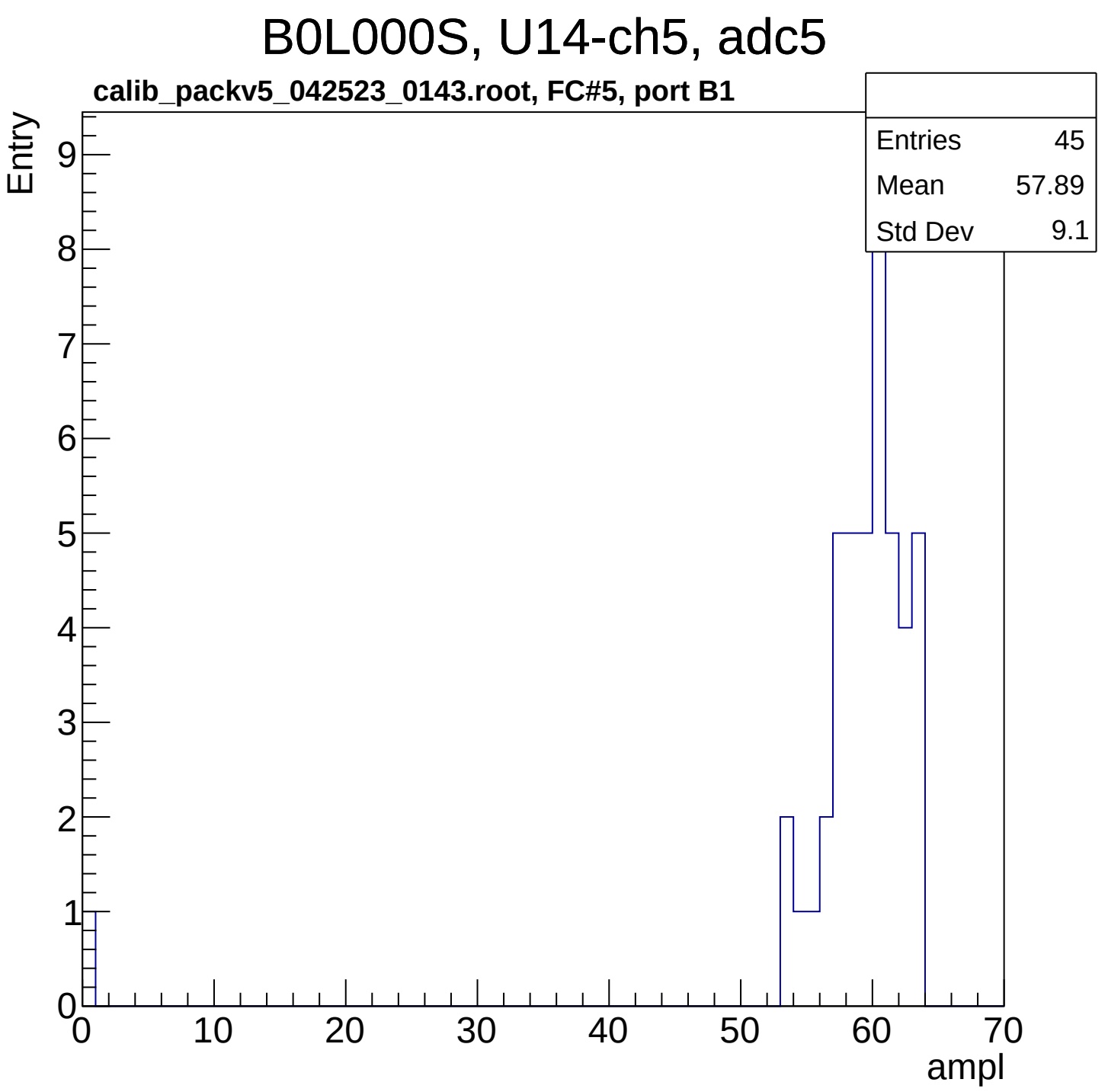
B0L000S, U14-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 57.89
Std Dev 9.1
Entry
ampl
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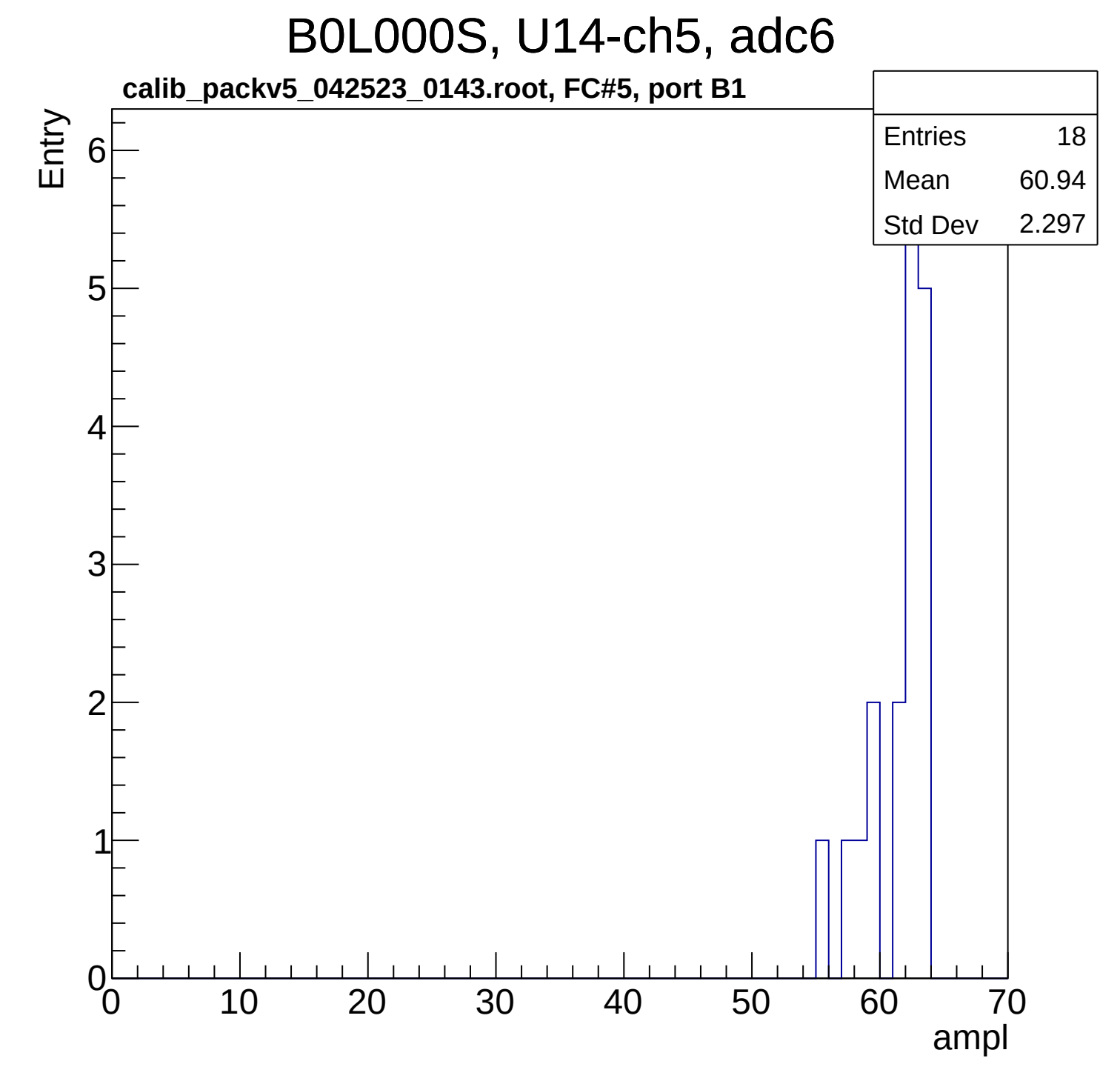
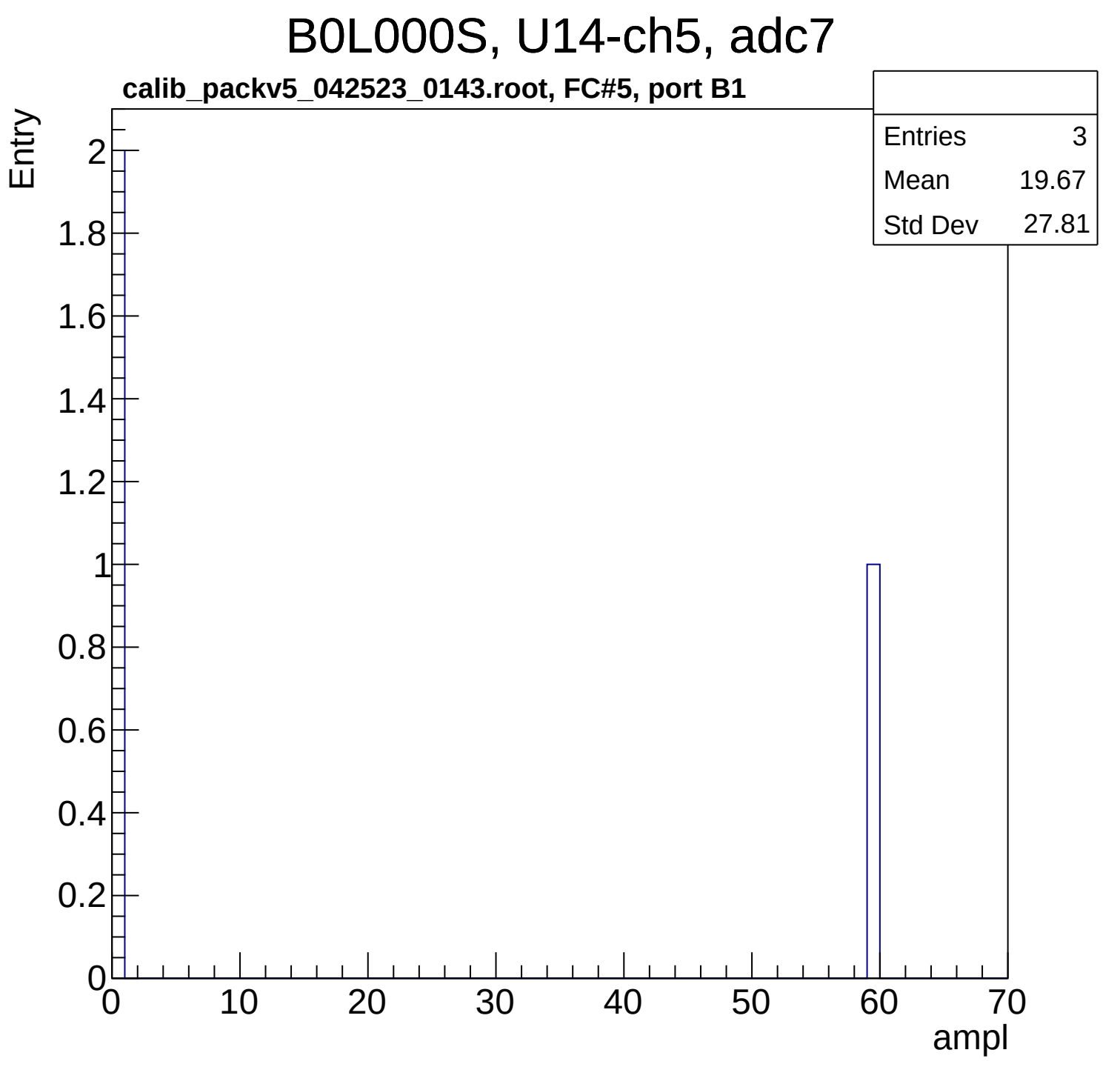
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 60.94
Std Dev 2.297
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 19.67
Std Dev 27.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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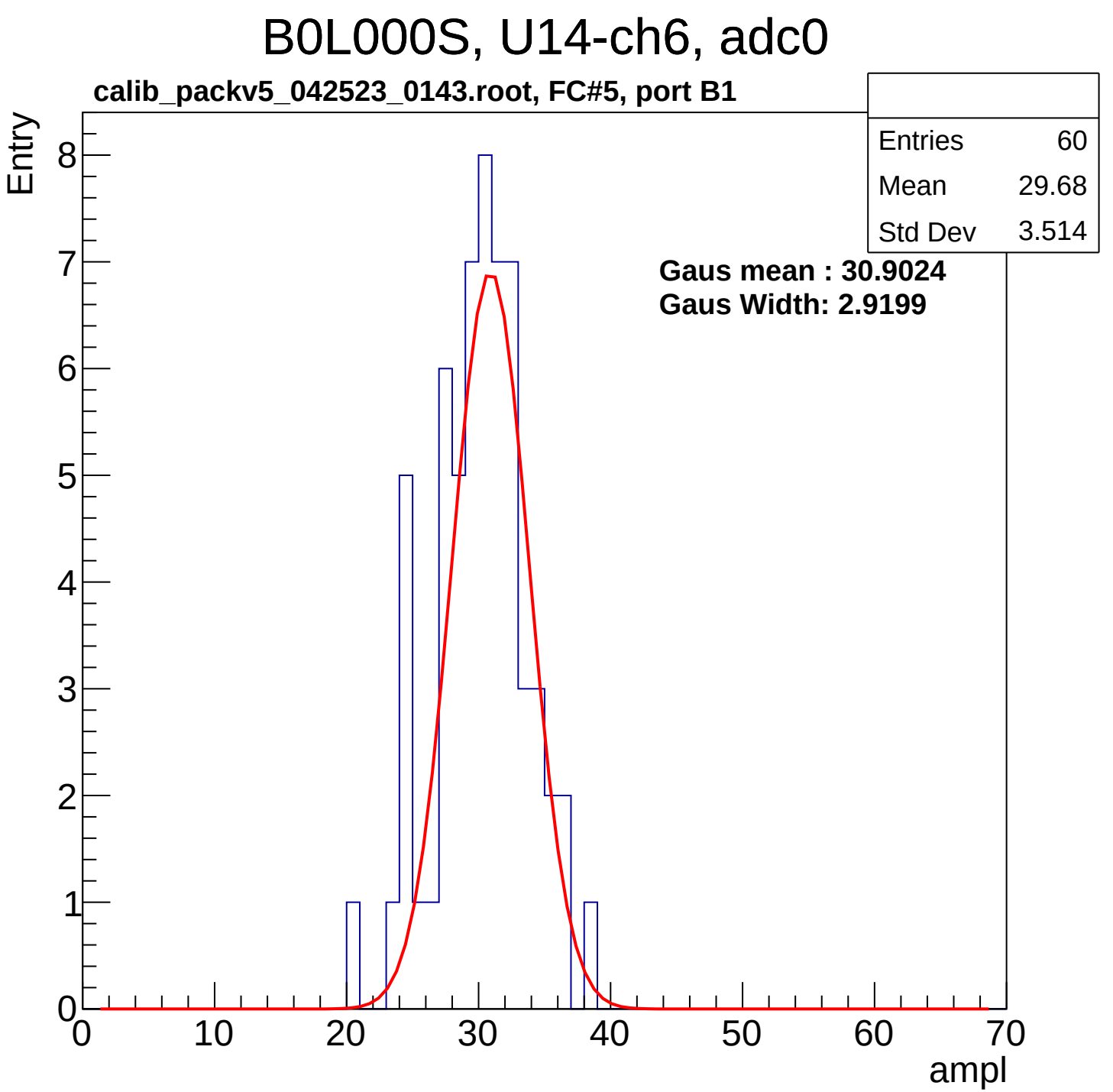
B0L000S, U14-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.68
Std Dev 3.514
Gaus mean : 30.9024
Gaus Width: 2.9199
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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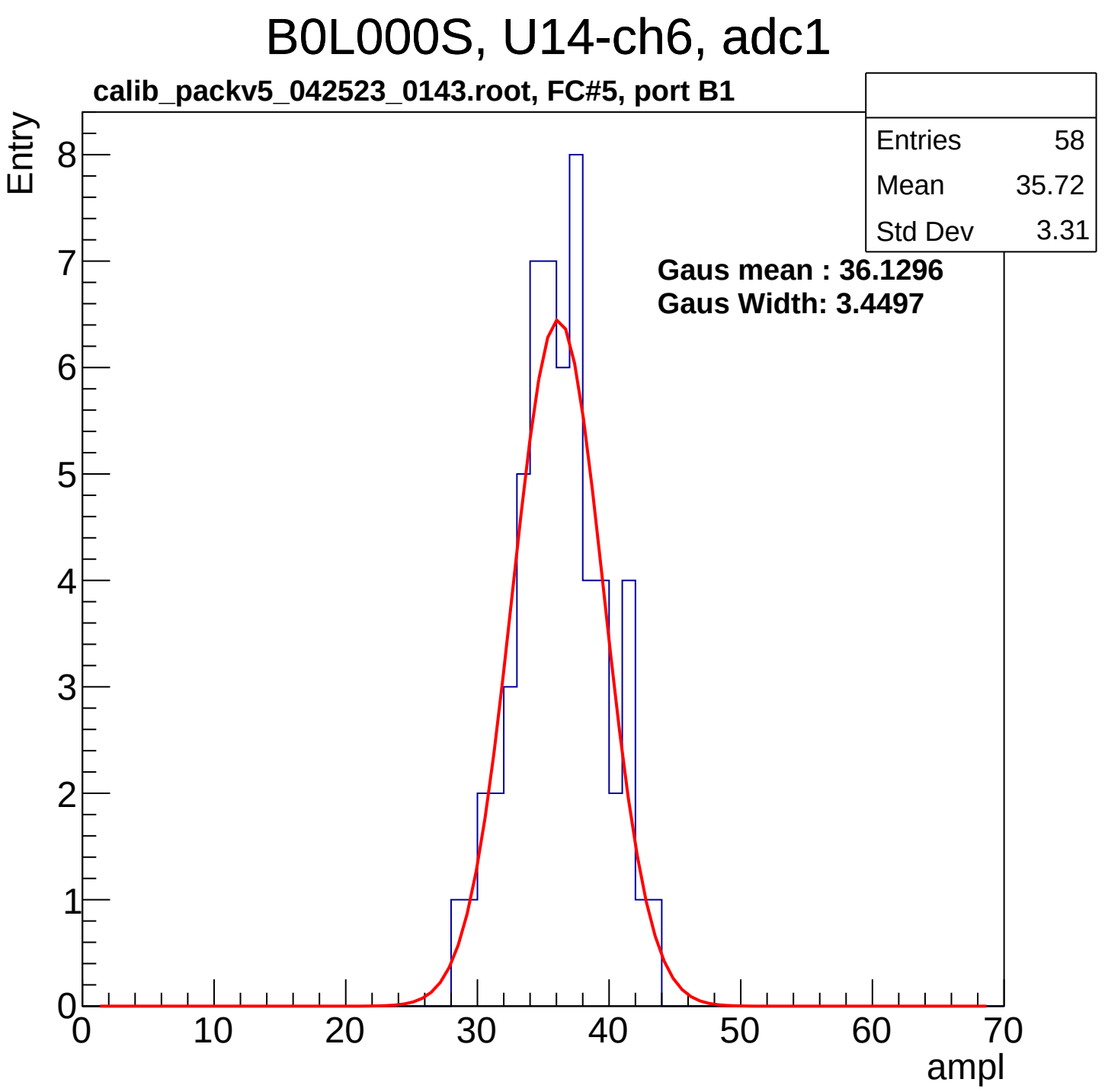
B0L000S, U14-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.72
Std Dev 3.31
Gaus mean : 36.1296
Gaus Width: 3.4497
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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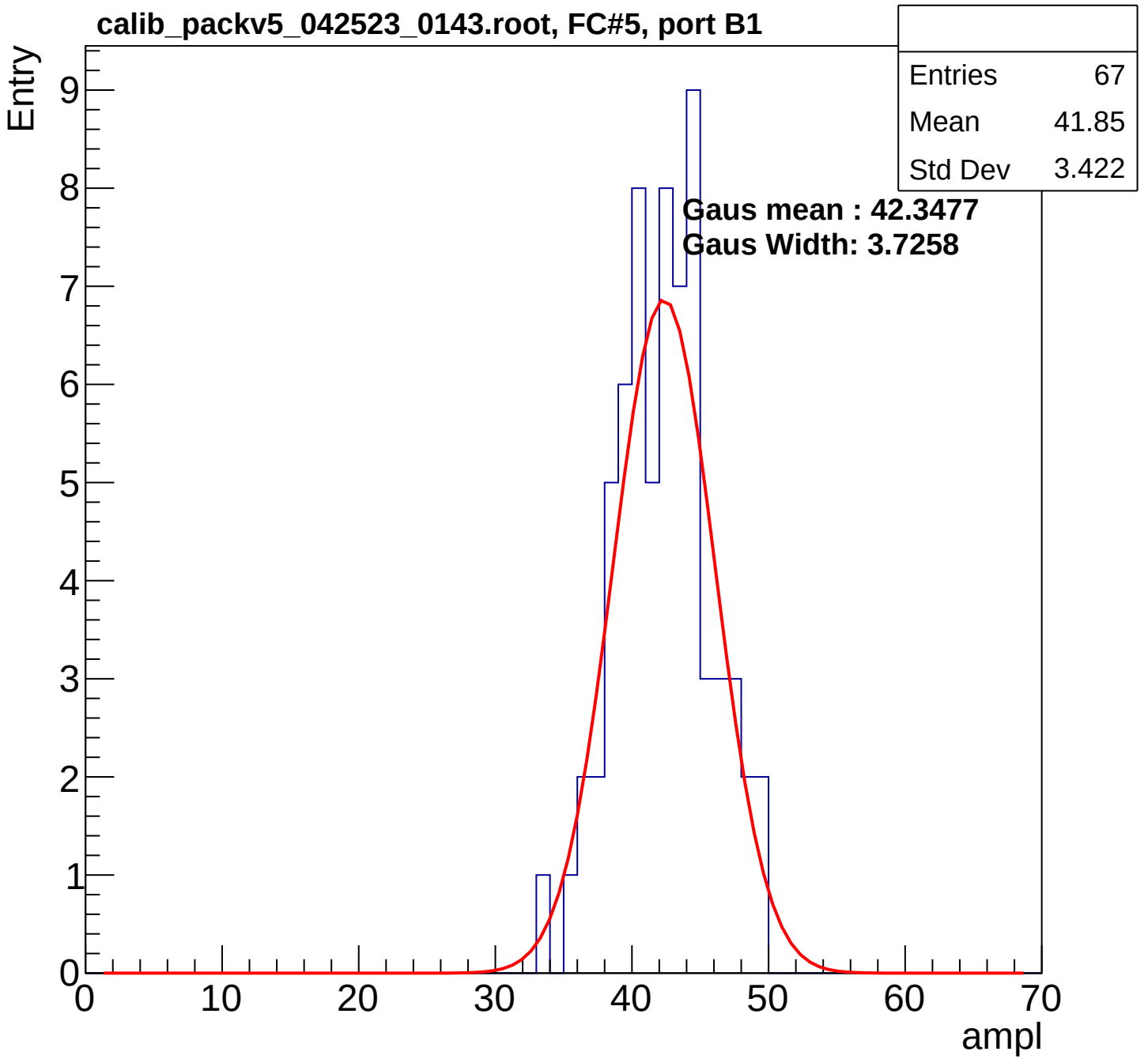
40
50
60
70


B0L000S, U14-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.85
Std Dev 3.422
Gaus mean : 42.3477
Gaus Width: 3.7258
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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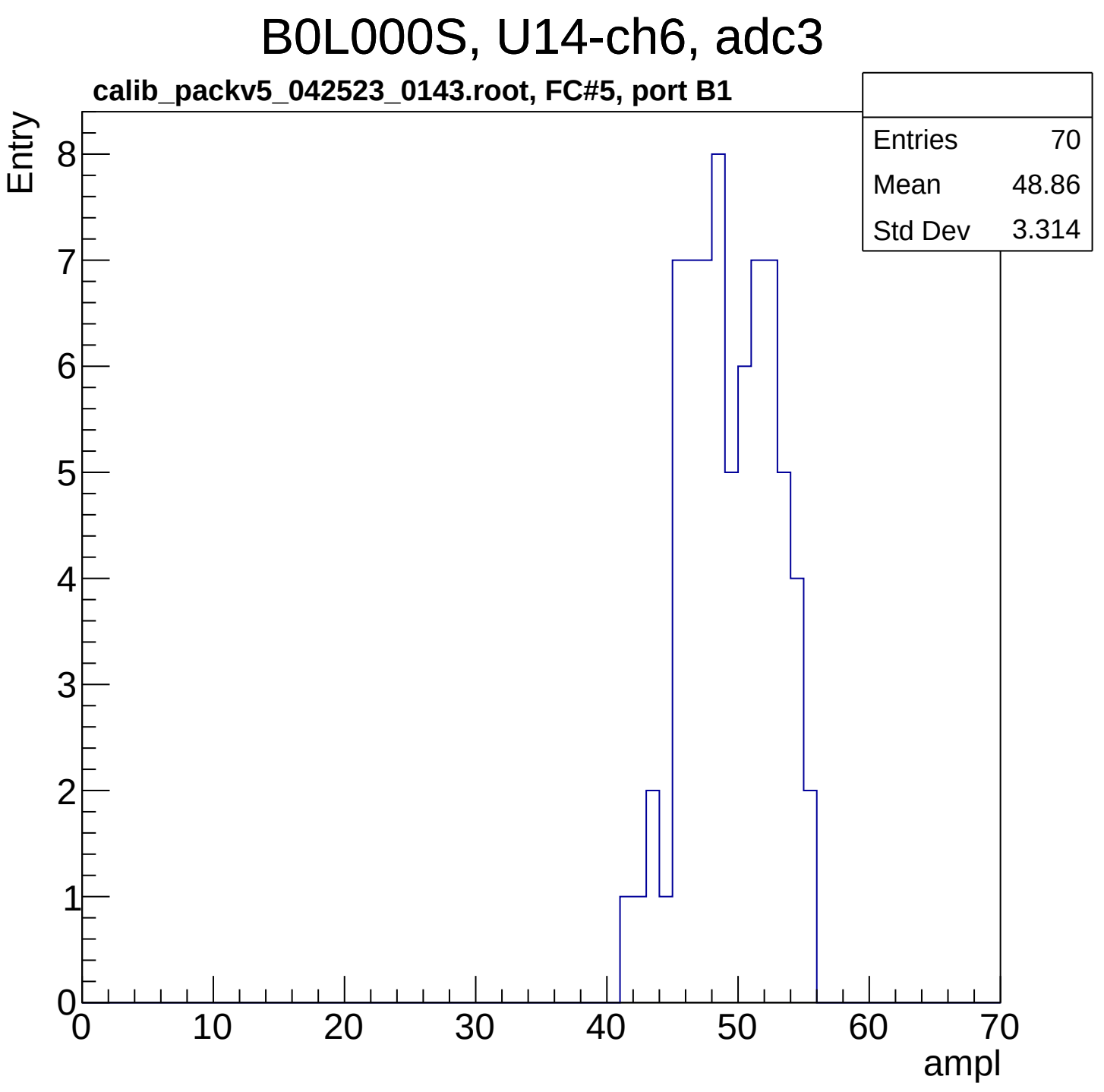
B0L000S, U14-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.86
Std Dev 3.314
Entry
ampl
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4
5
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0
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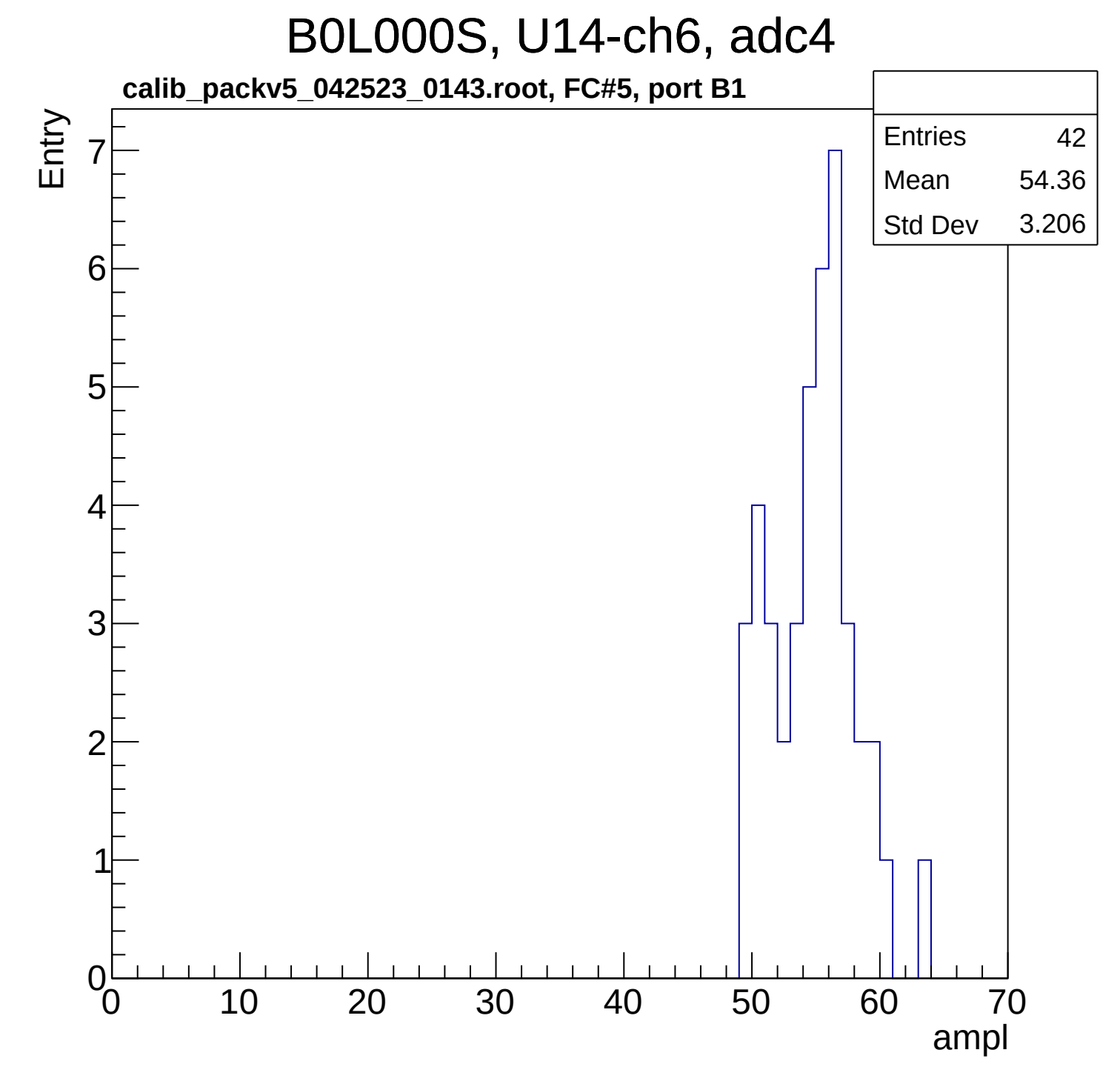
B0L000S, U14-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 54.36
Std Dev 3.206
Entry
ampl
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4
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6
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0
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60
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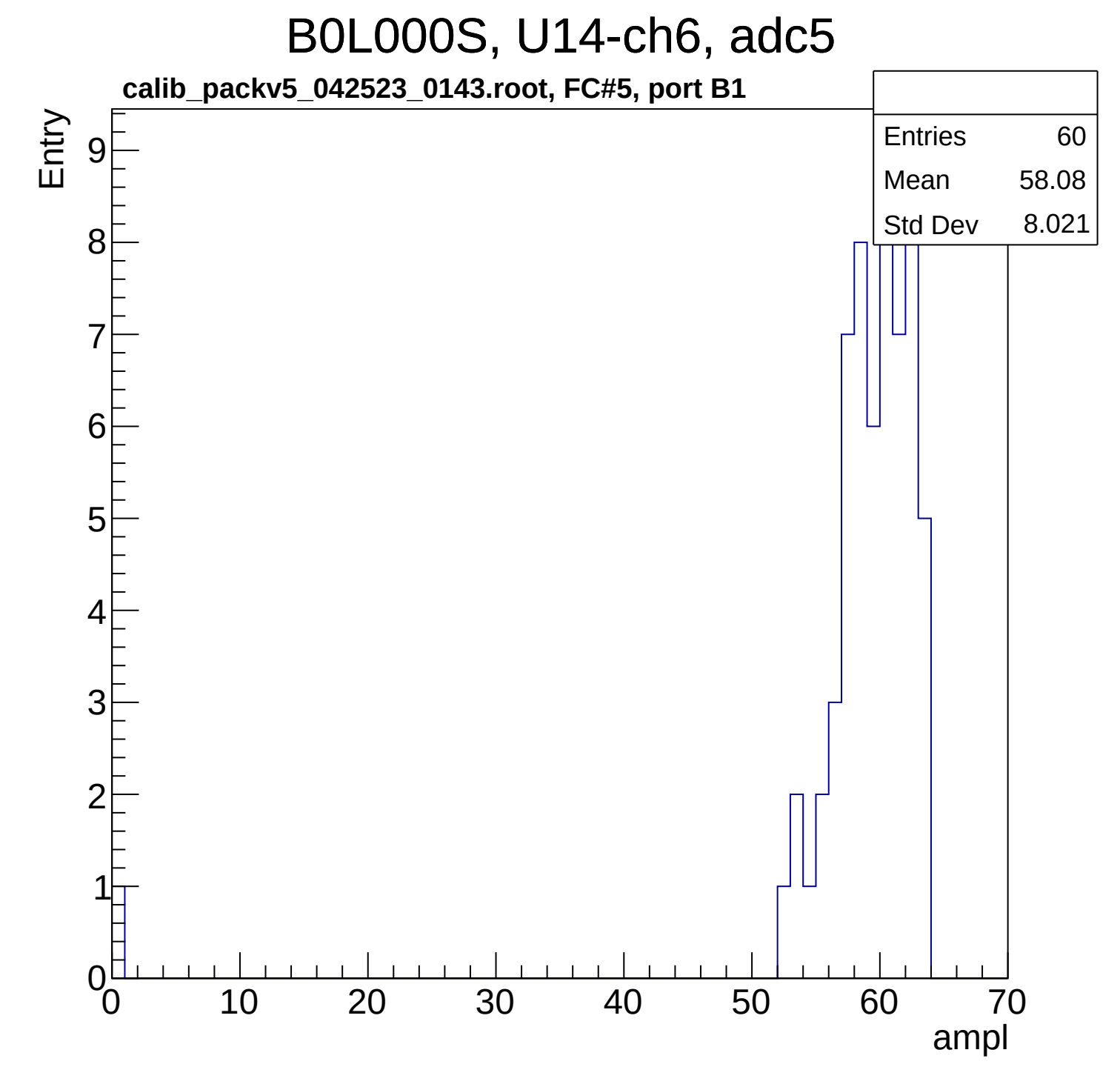
B0L000S, U14-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.08
Std Dev 8.021
Entry
ampl
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2
3
4
5
6
7
8
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0
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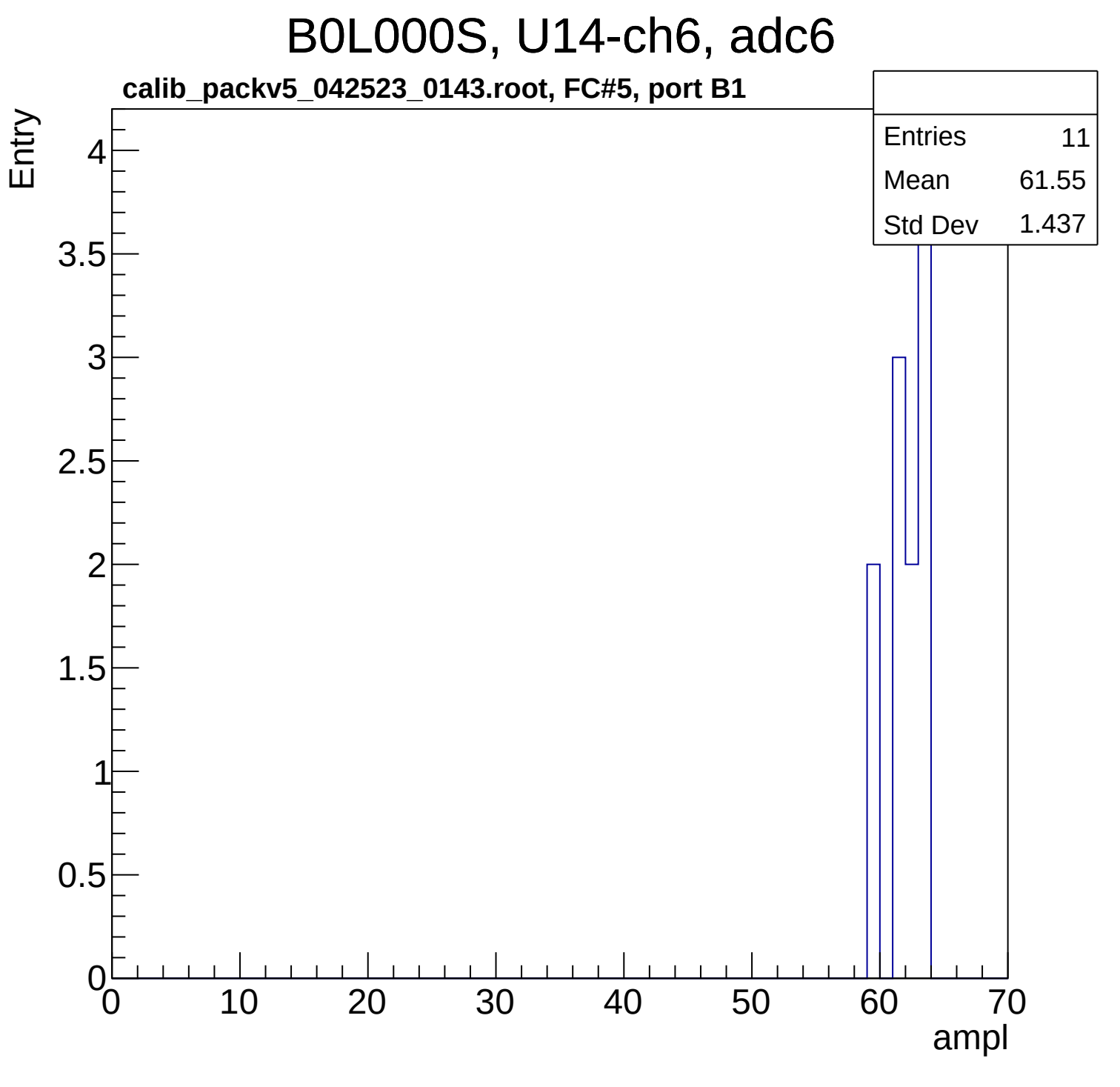
B0L000S, U14-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.55
Std Dev 1.437
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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50
60
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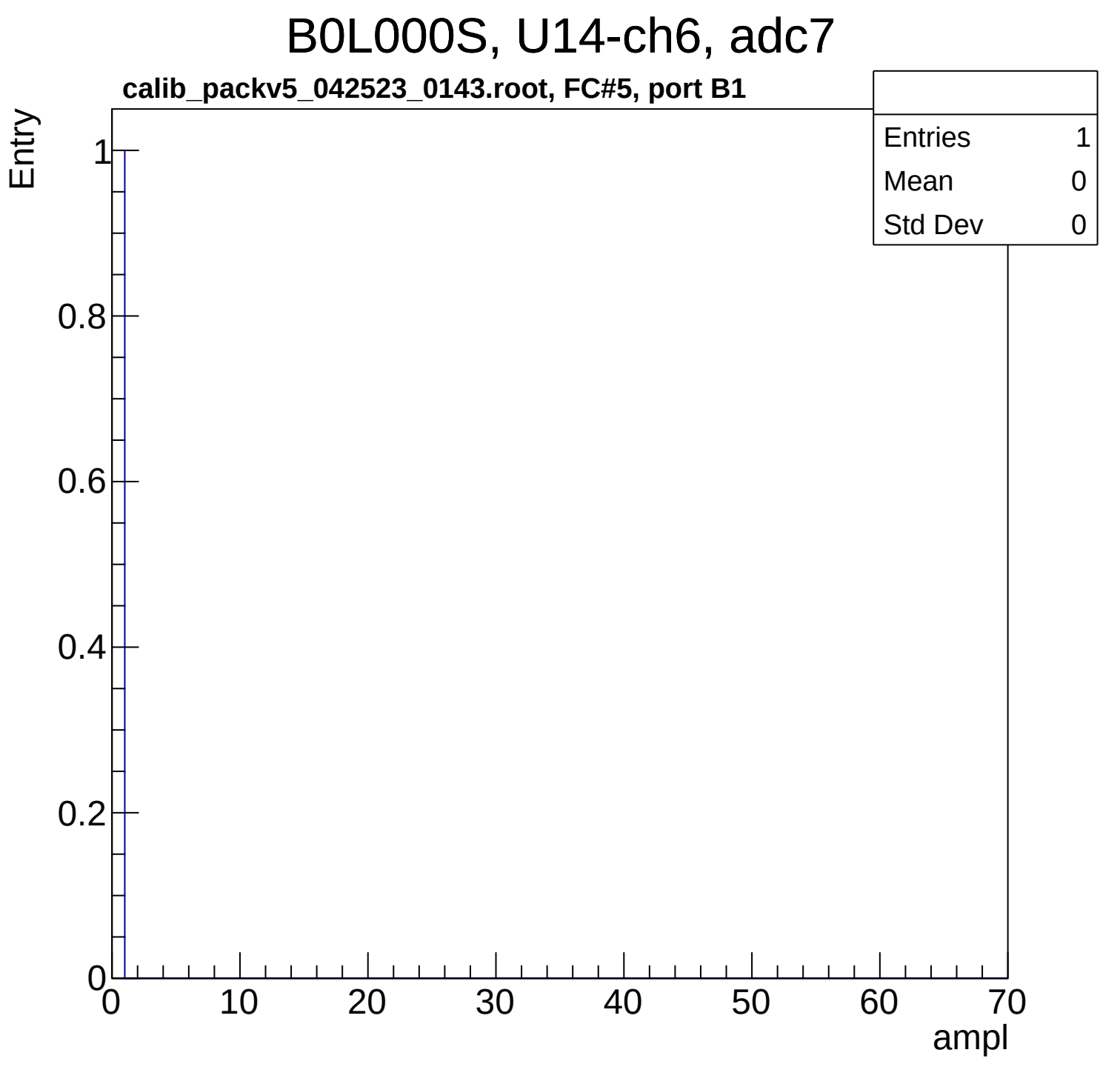
B0L000S, U14-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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30
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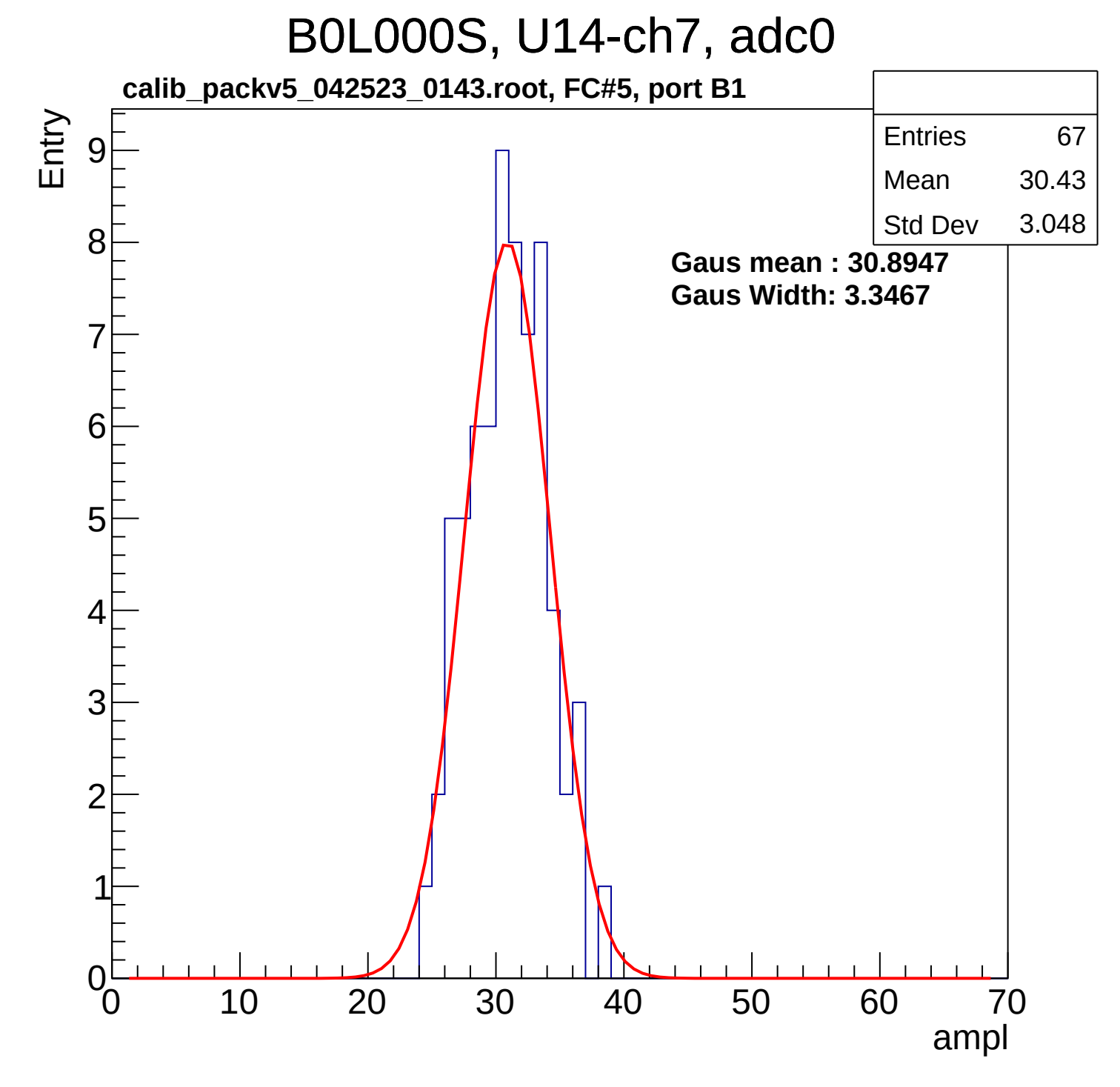
50
60
70
ampl


B0L000S, U14-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.43
Std Dev 3.048
Gaus mean : 30.8947
Gaus Width: 3.3467
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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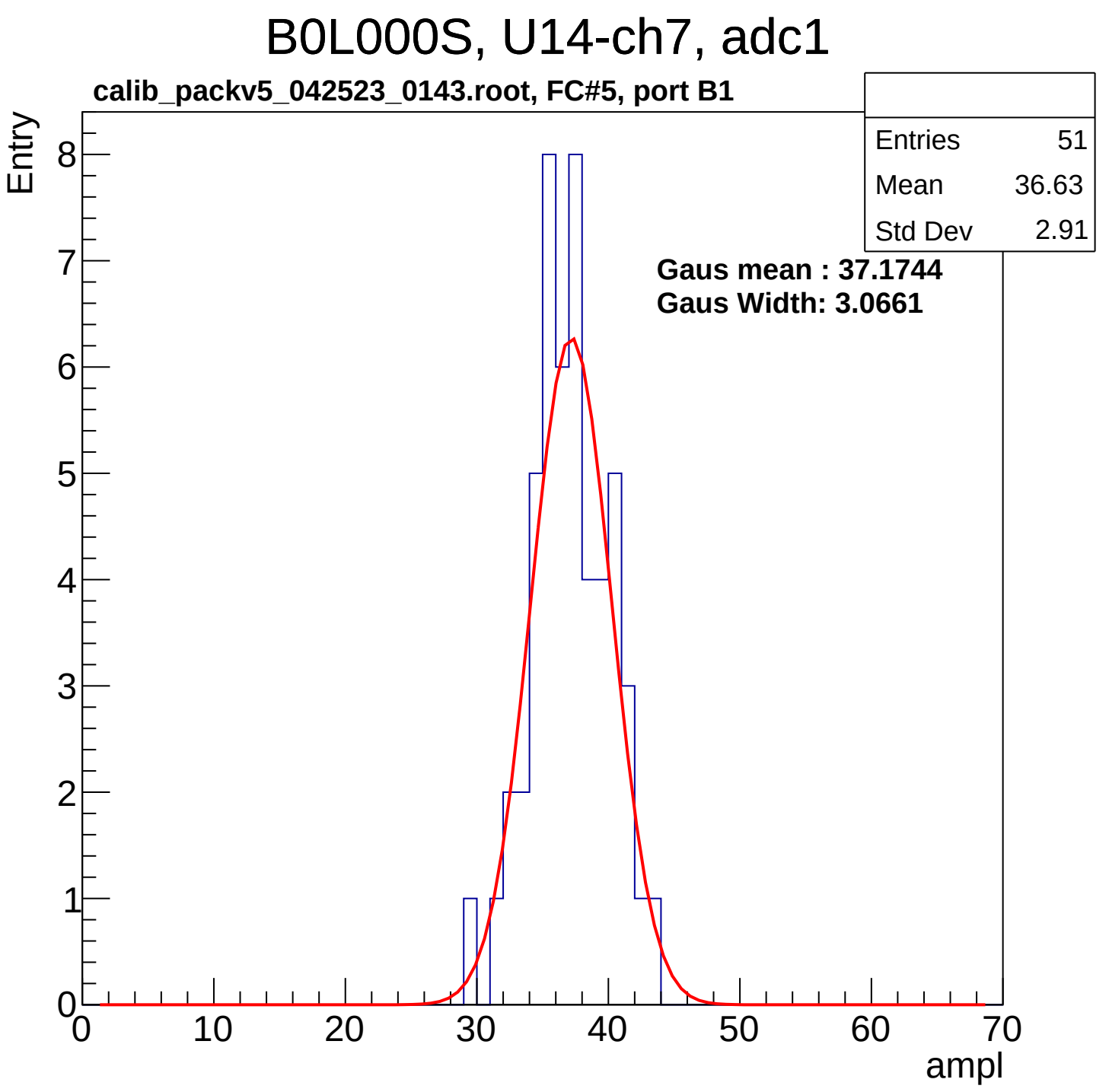
B0L000S, U14-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 36.63
Std Dev 2.91
Gaus mean : 37.1744
Gaus Width: 3.0661
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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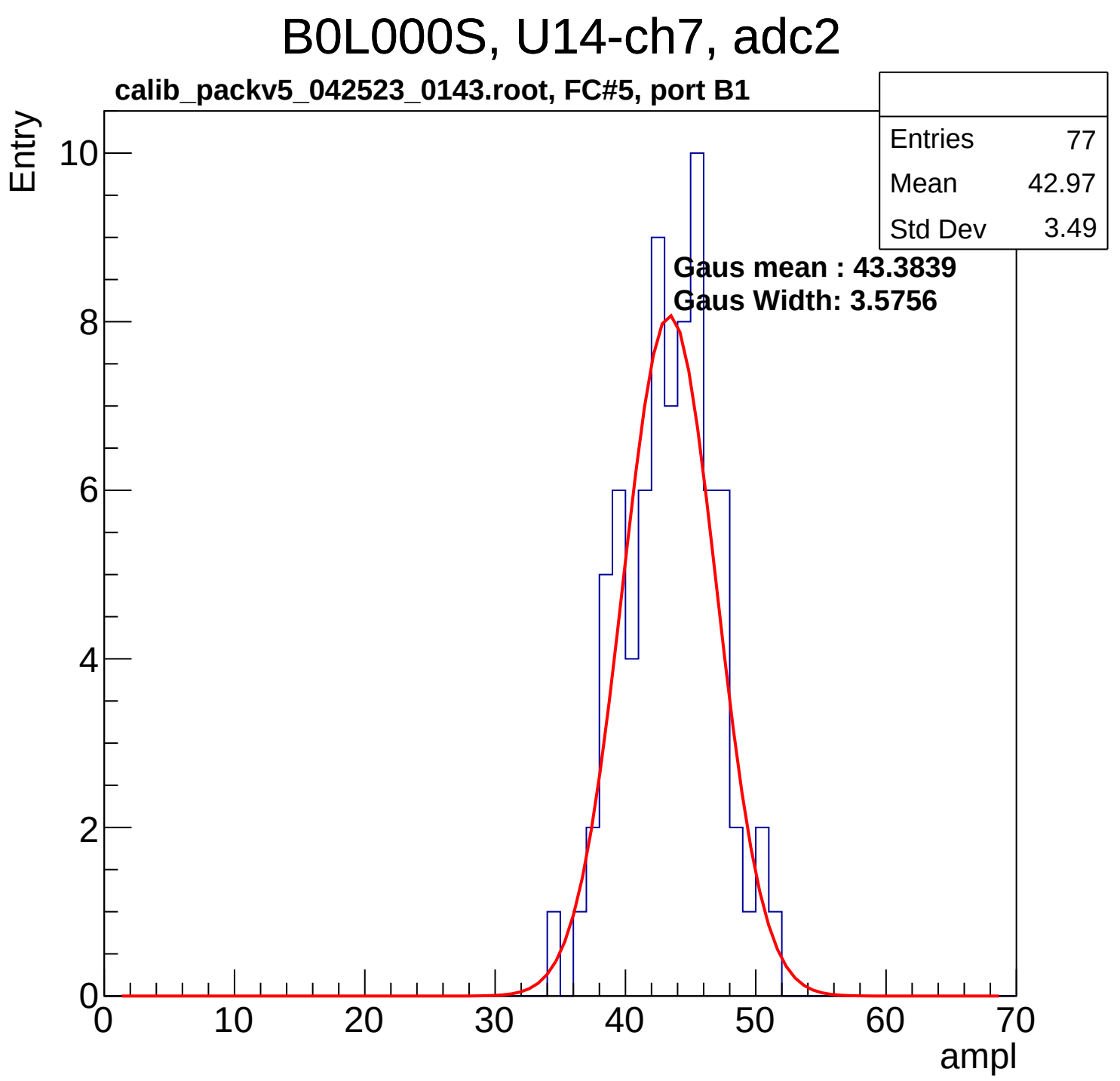
B0L000S, U14-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 42.97
Std Dev 3.49
Gaus mean : 43.3839
Gaus Width: 3.5756
Entry
ampl
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4
6
8
10
0
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40
50
60
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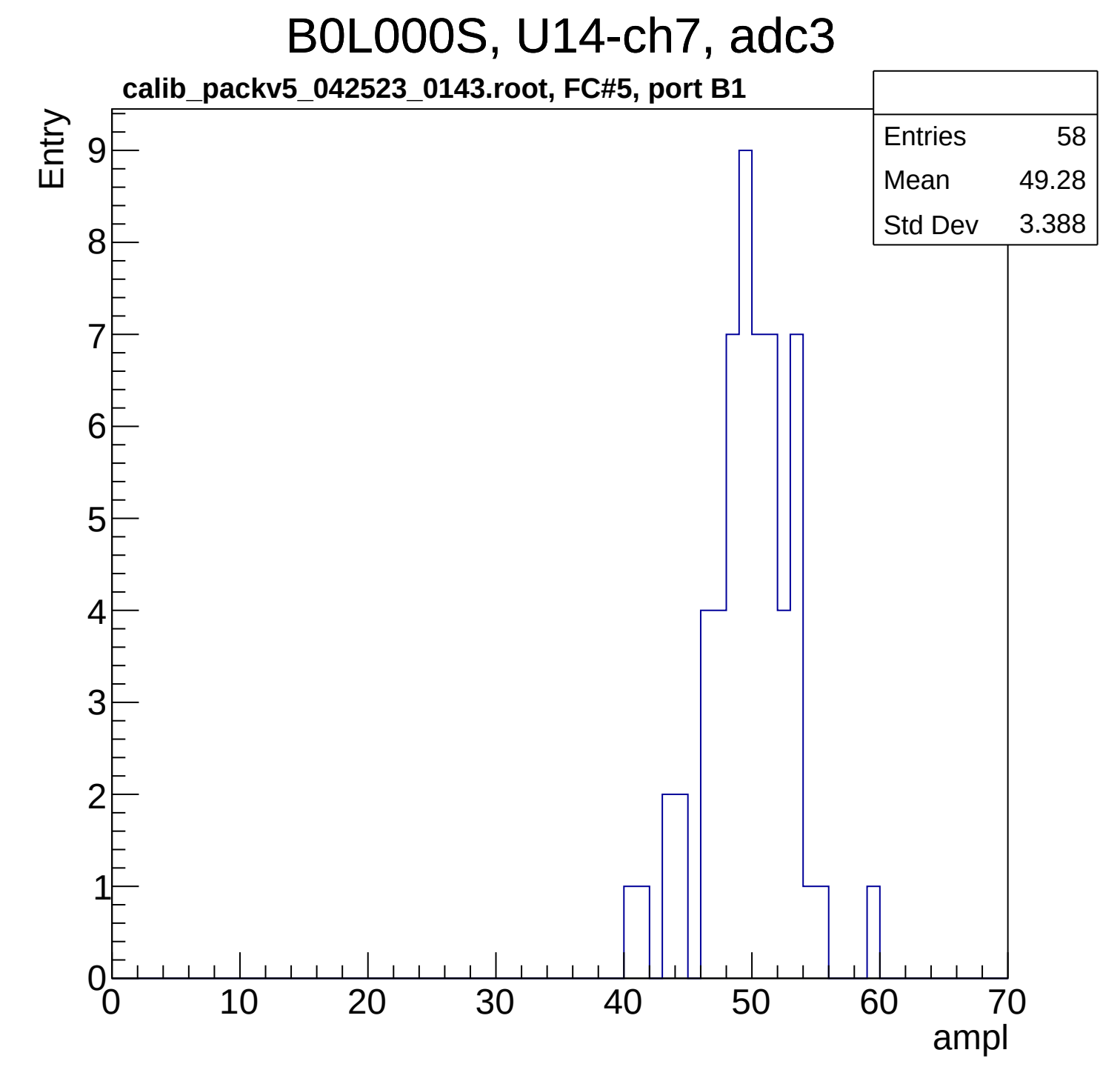
B0L000S, U14-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 49.28
Std Dev 3.388
Entry
ampl
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3
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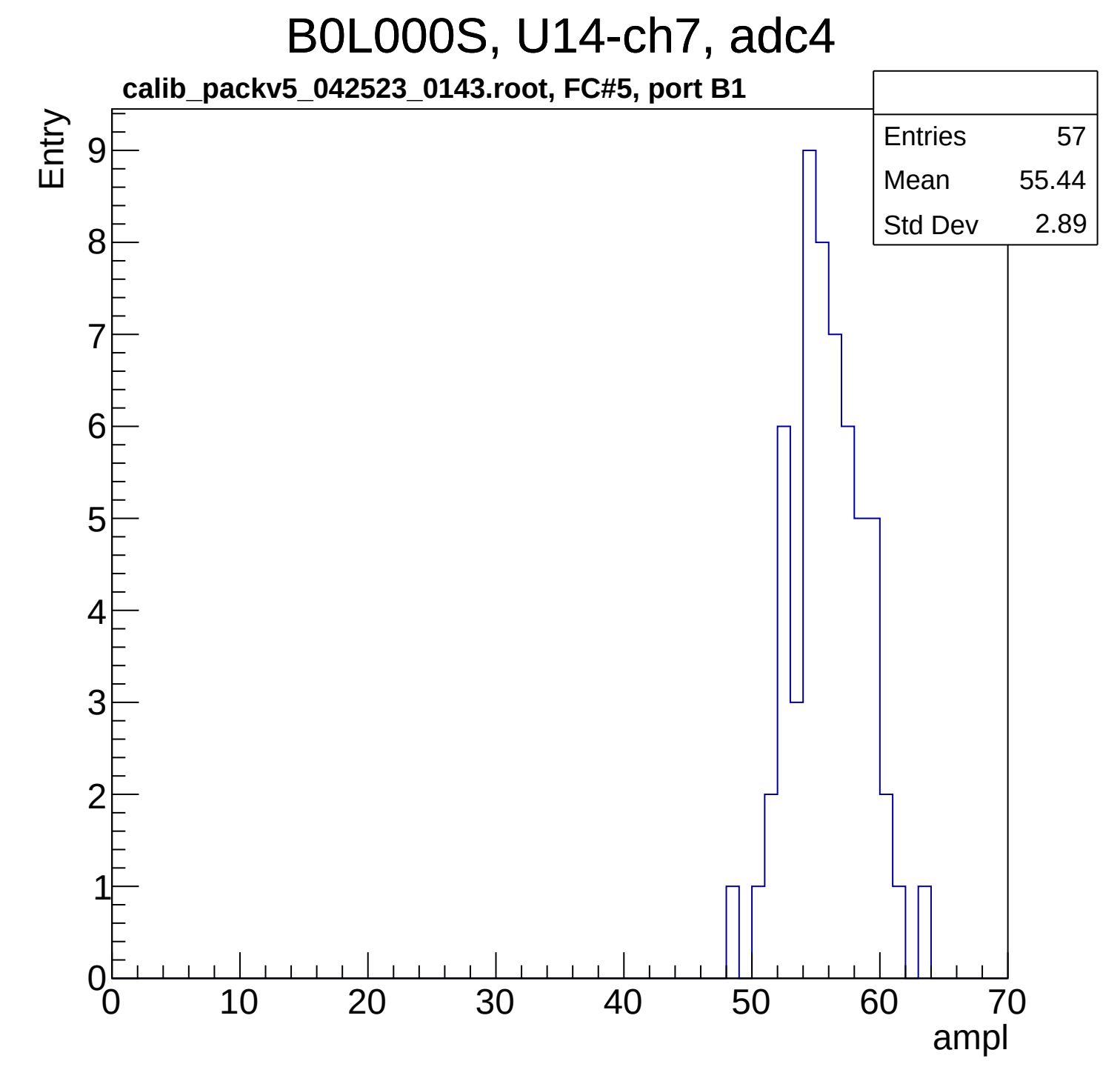
B0L000S, U14-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.44
Std Dev 2.89
Entry
ampl
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2
3
4
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6
7
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40
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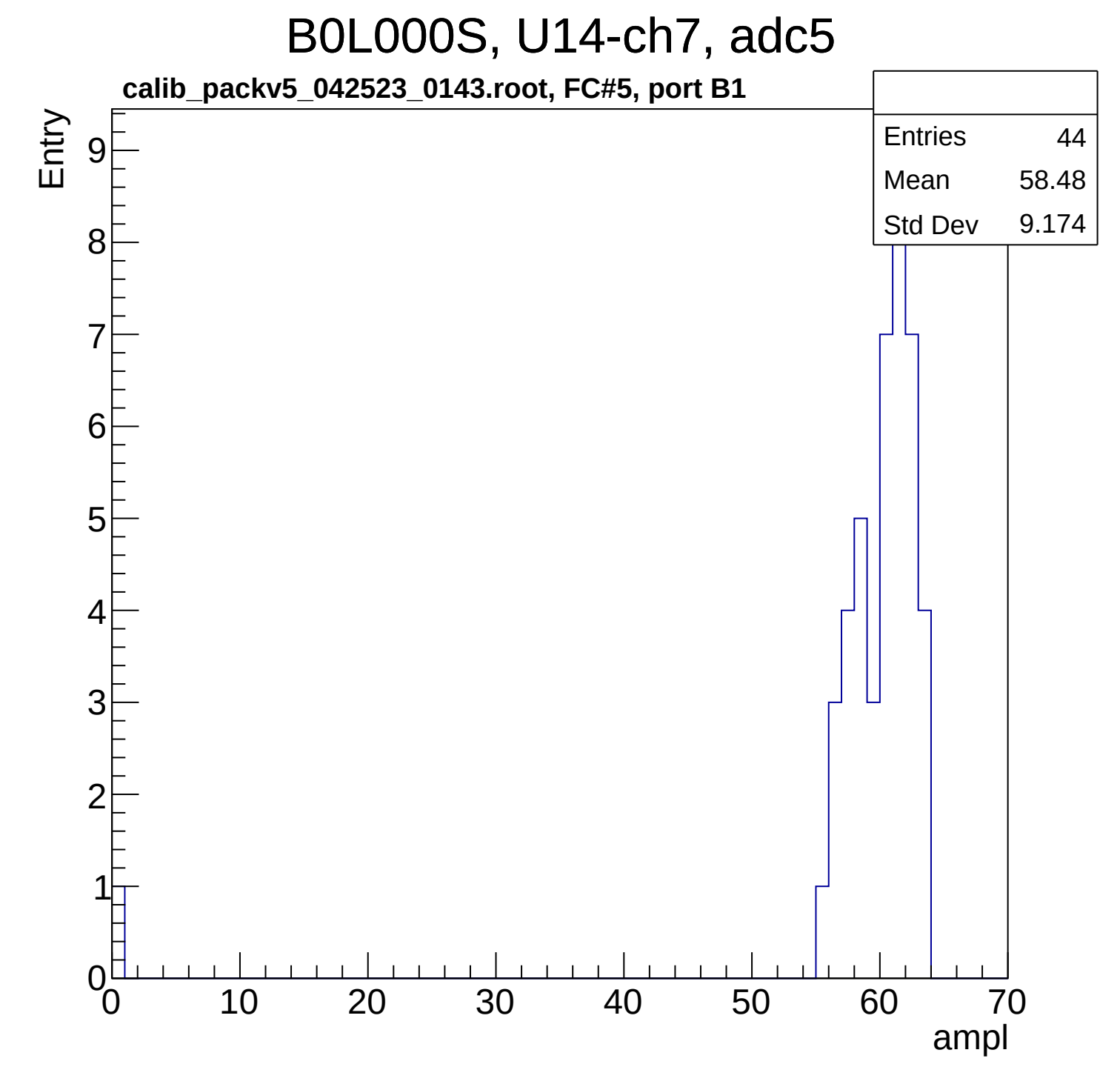
B0L000S, U14-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.48
Std Dev 9.174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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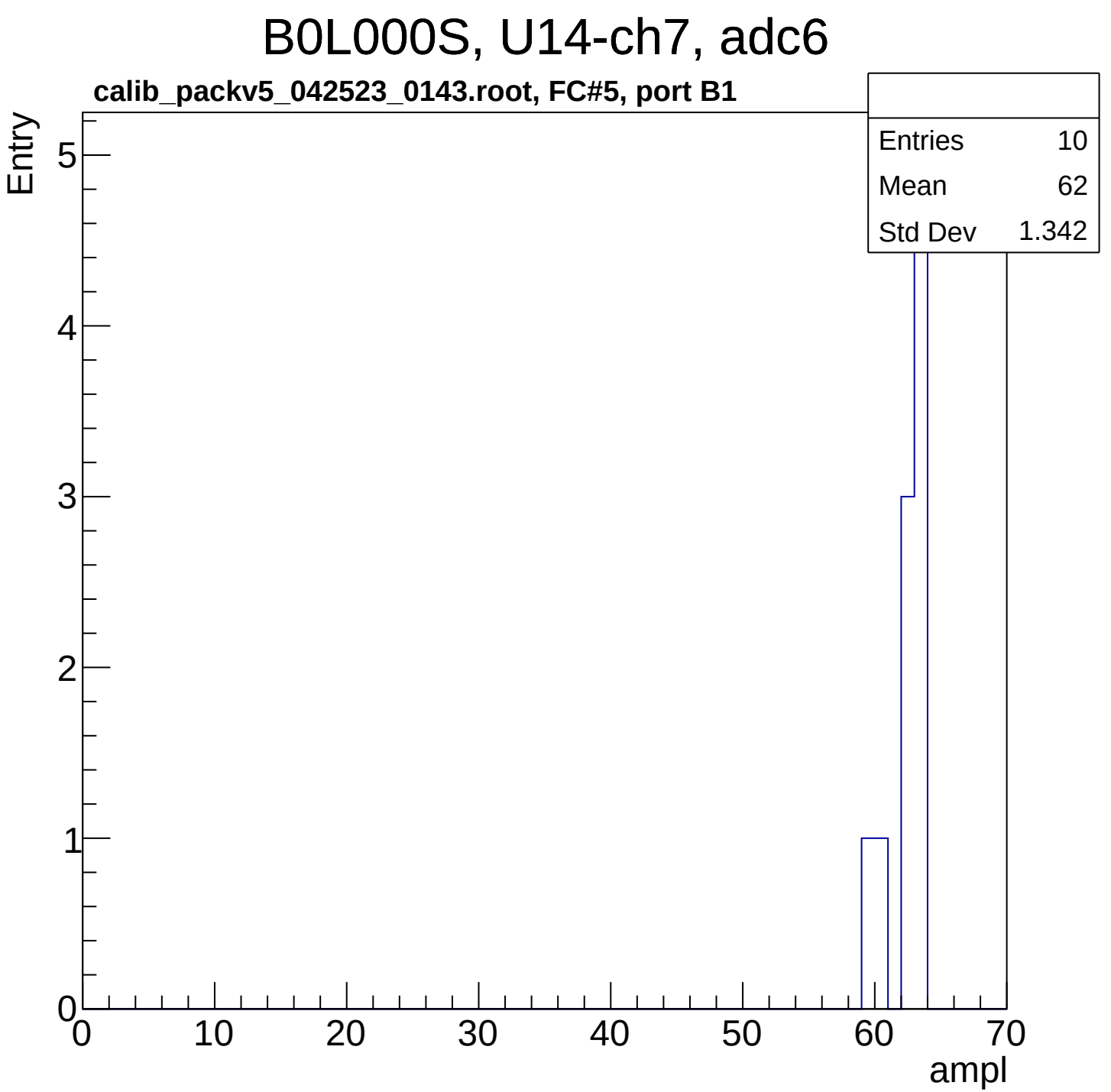
B0L000S, U14-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62
Std Dev 1.342
Entry
0
1
2
3
4
5
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10
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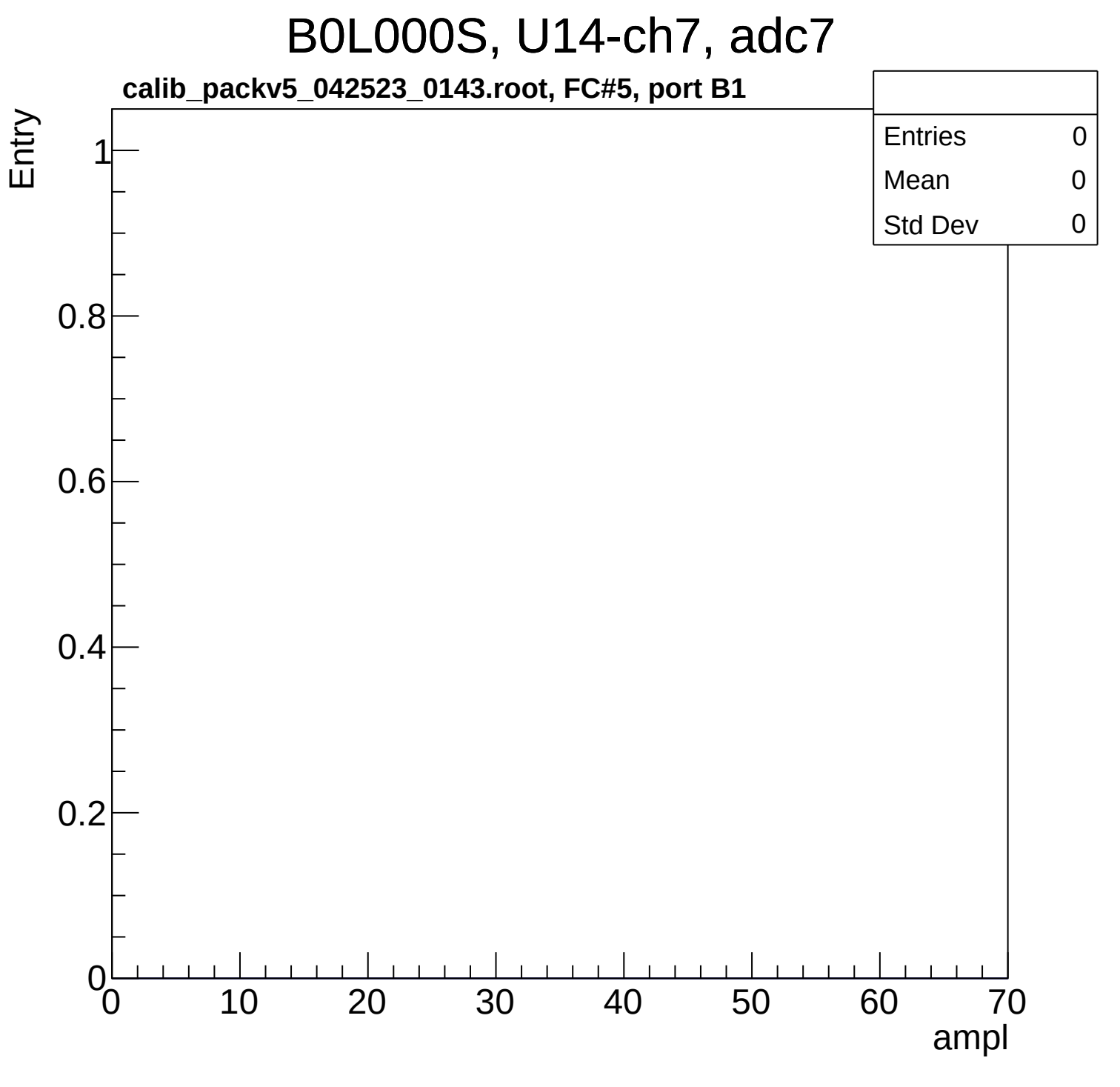
70
ampl


B0L000S, U14-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
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10
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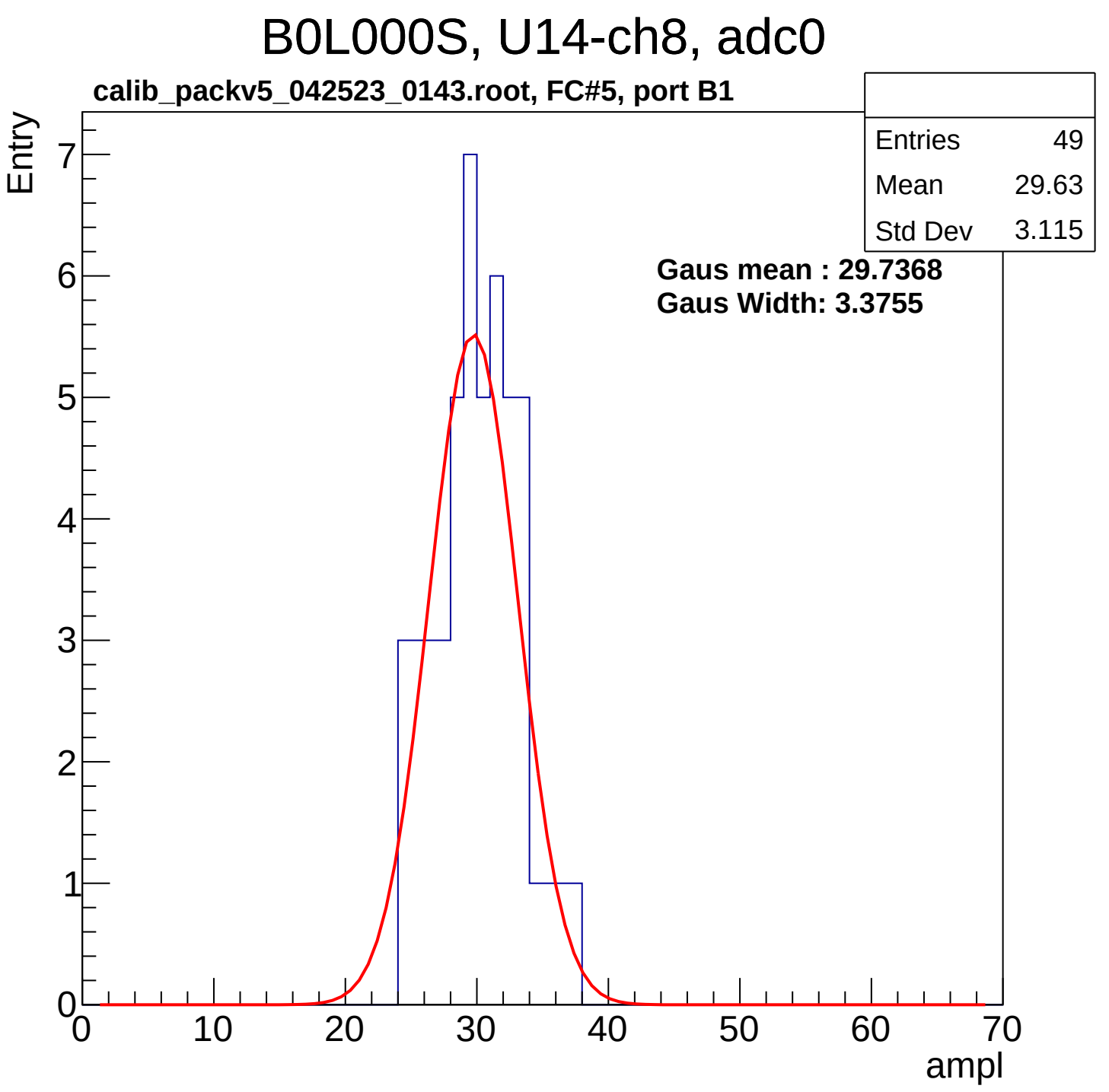
70
ampl


B0L000S, U14-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 29.63
Std Dev 3.115
Gaus mean : 29.7368
Gaus Width: 3.3755
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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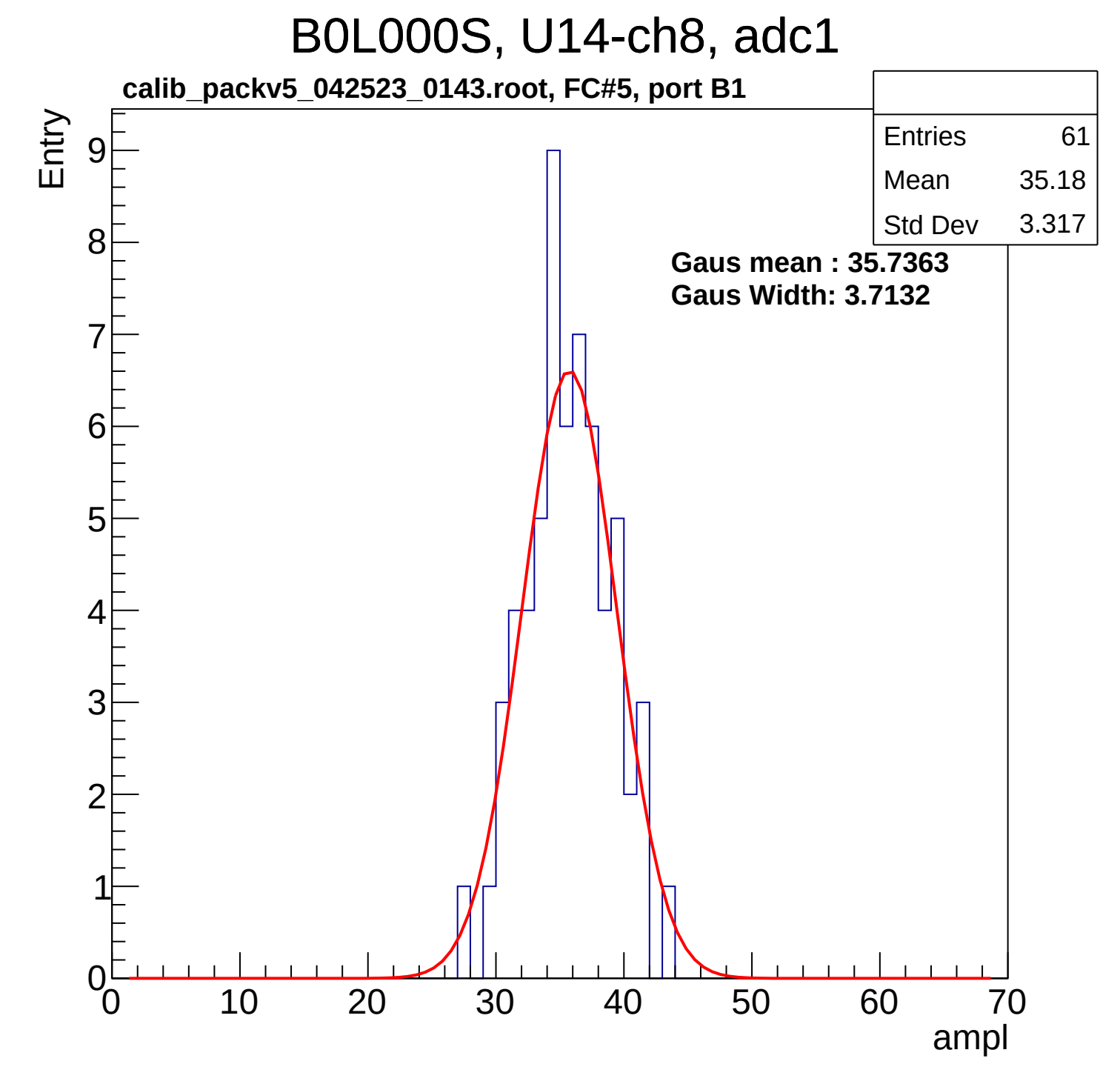
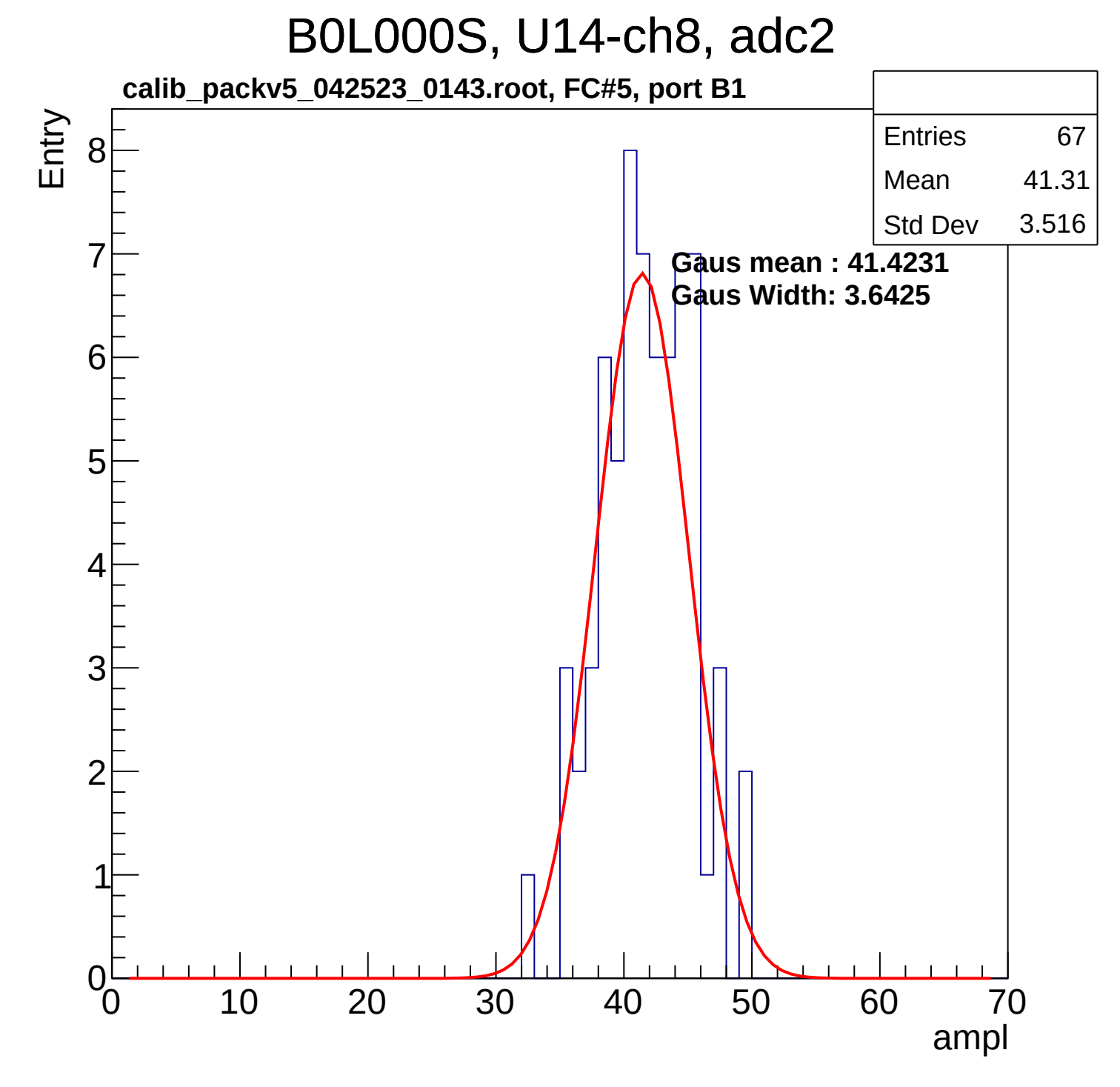

B0L000S, U14-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.18
Std Dev 3.317
Gaus mean : 35.7363
Gaus Width: 3.7132
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.31
Std Dev 3.516
Gaus mean : 41.4231
Gaus Width: 3.6425
Entry
ampl
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1
2
3
4
5
6
7
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0
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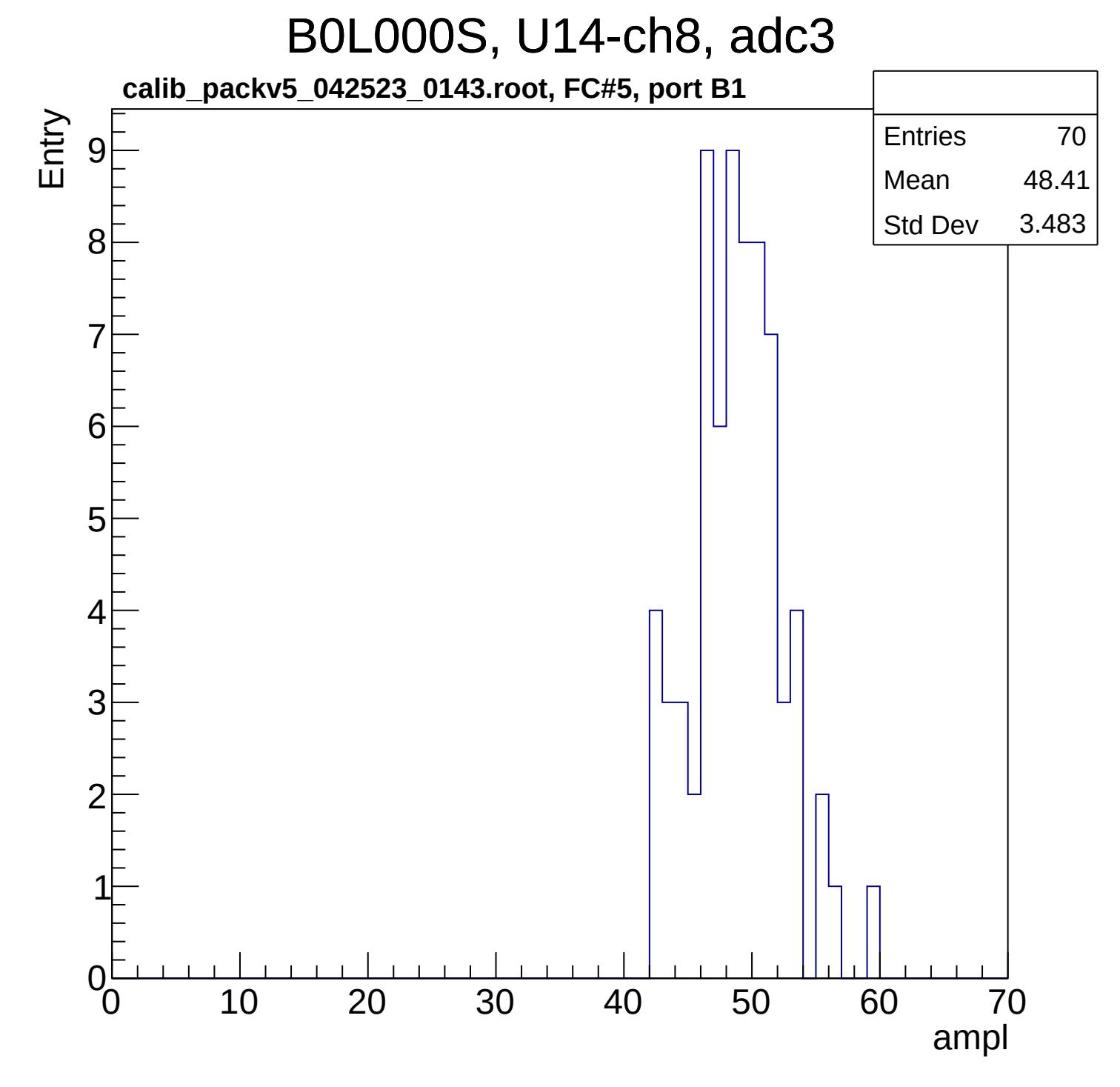
B0L000S, U14-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.41
Std Dev 3.483
Entry
ampl
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1
2
3
4
5
6
7
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9
0
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60
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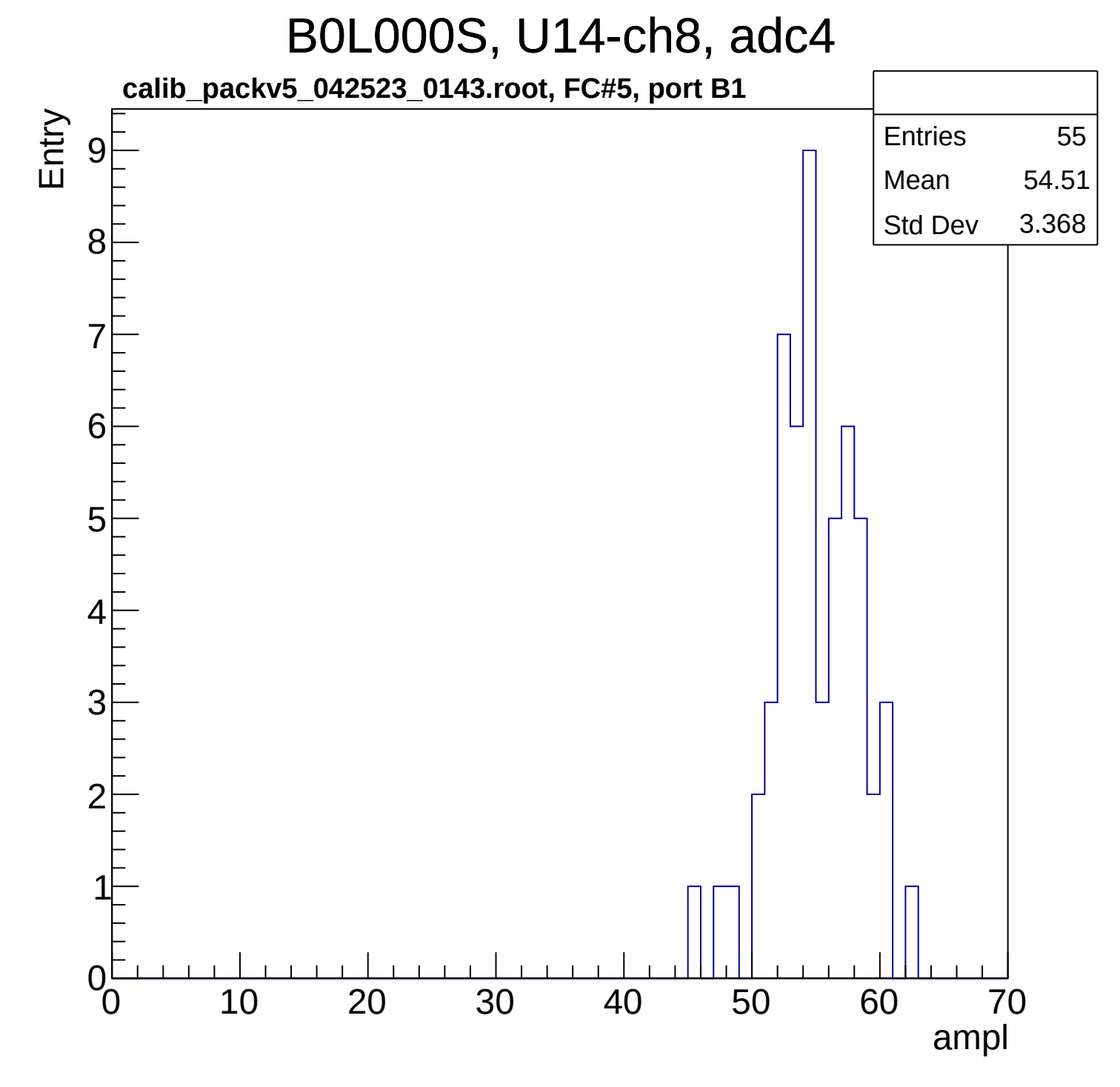
B0L000S, U14-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 54.51
Std Dev 3.368
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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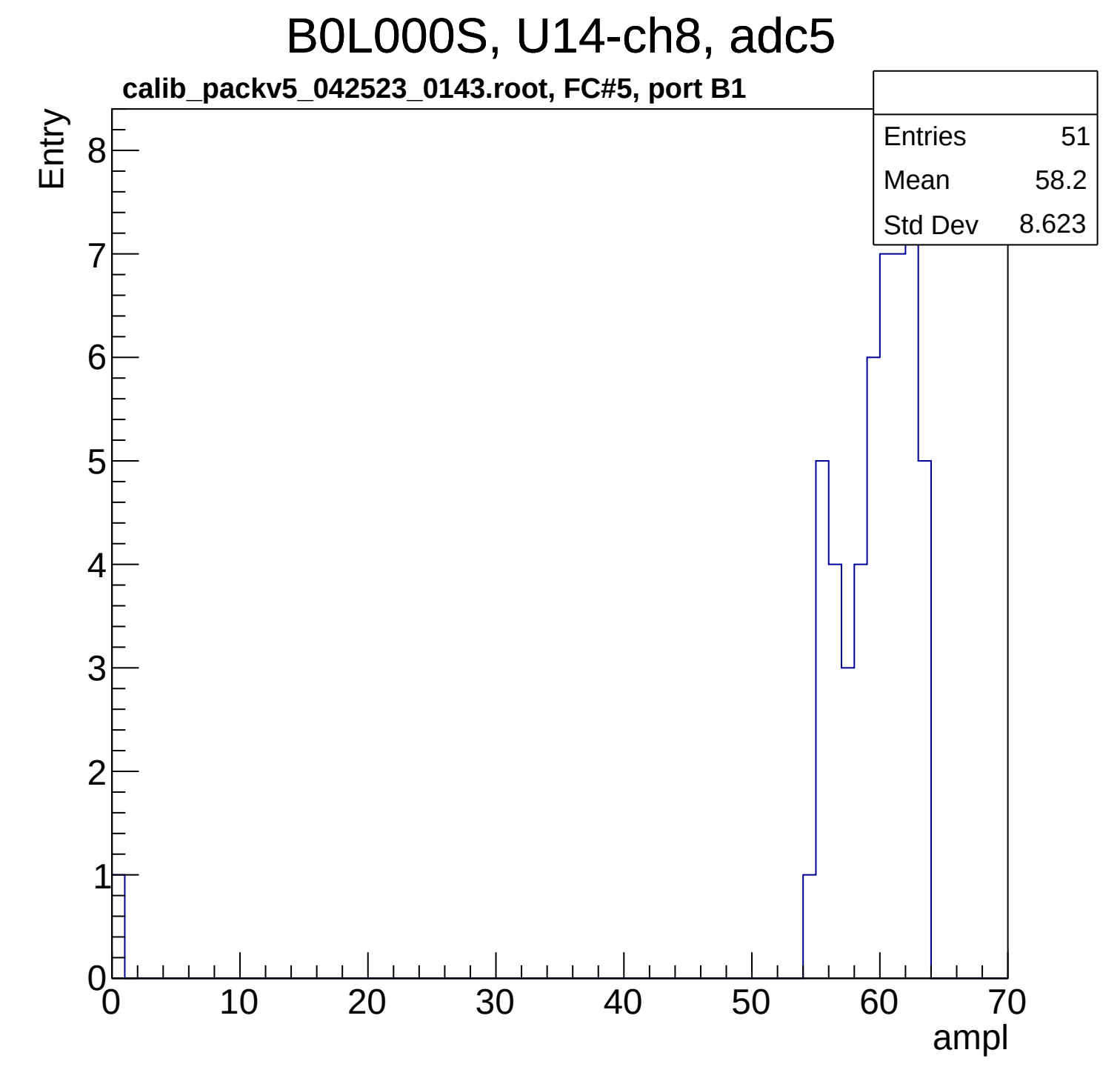
B0L000S, U14-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.2
Std Dev 8.623
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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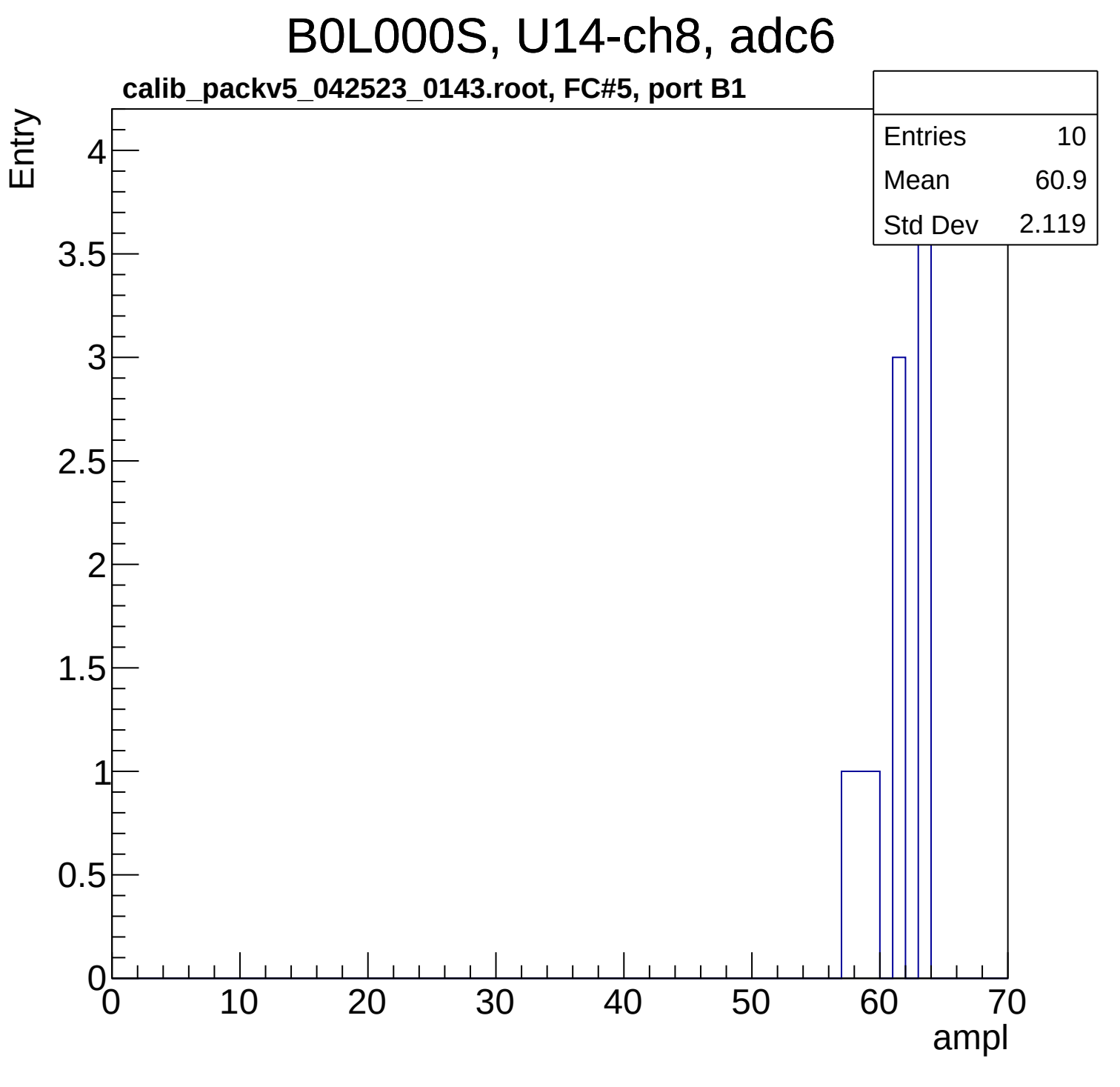
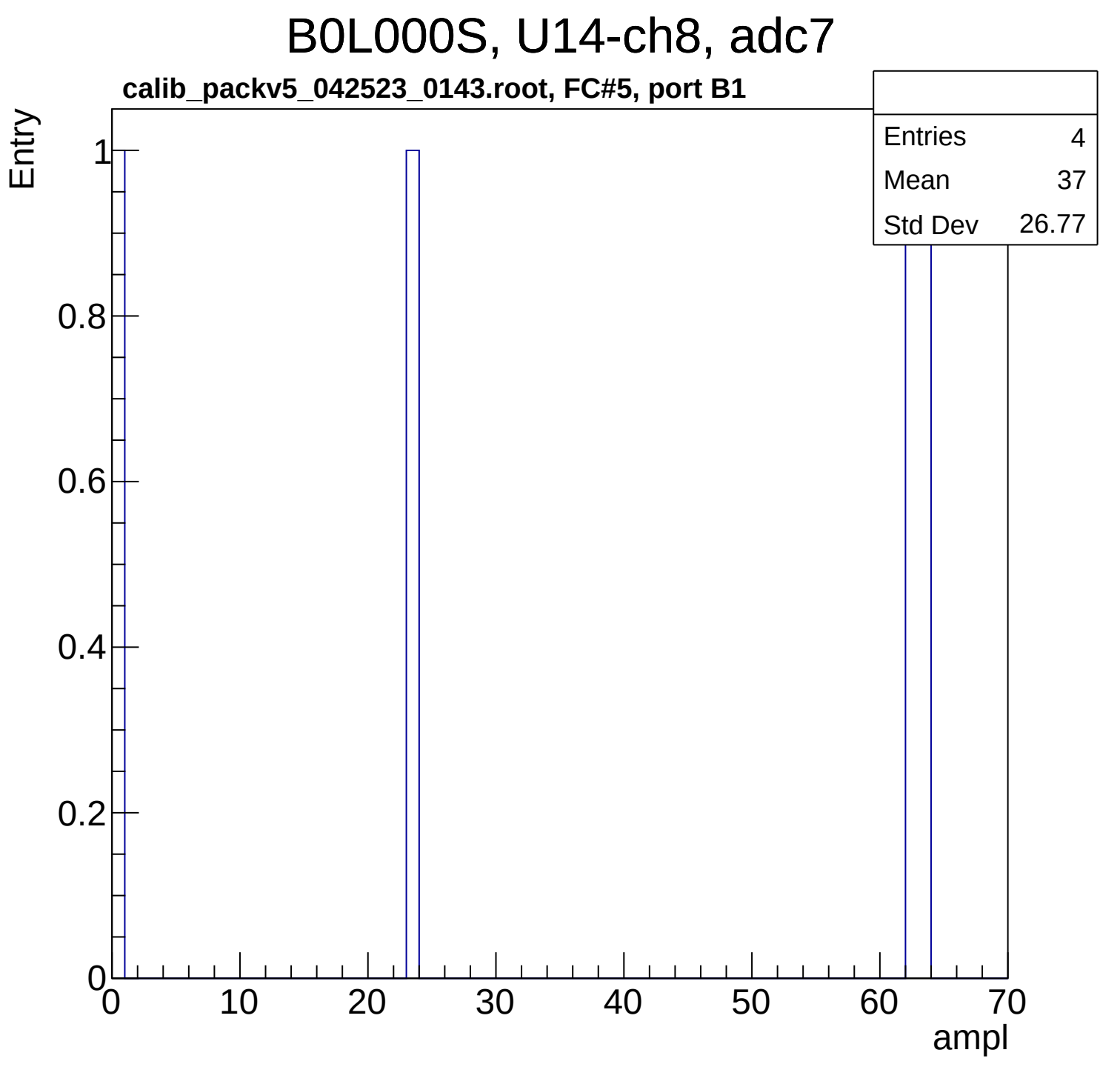
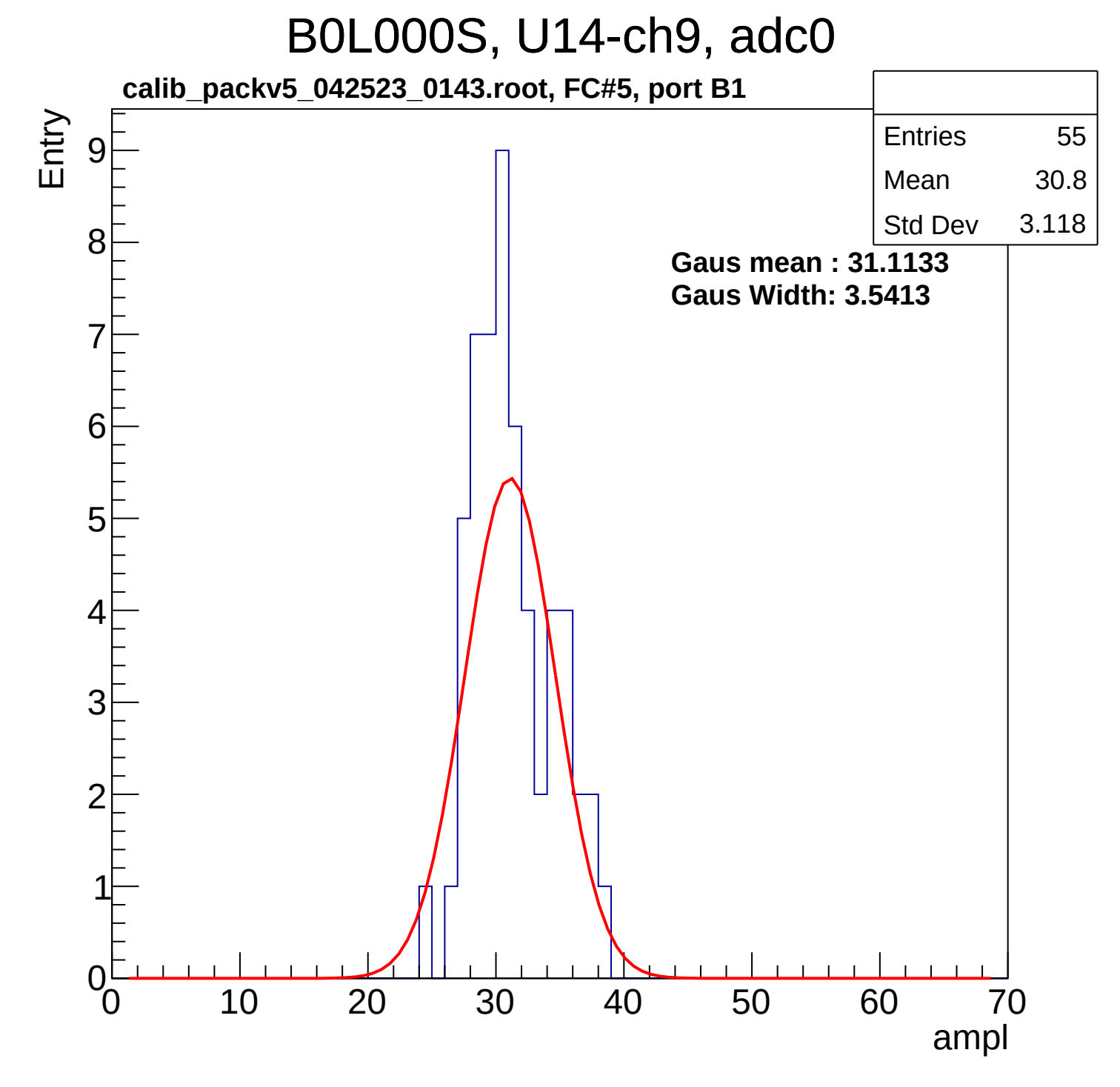

B0L000S, U14-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 60.9
Std Dev 2.119
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 37
Std Dev 26.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U14-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.8
Std Dev 3.118
Gaus mean : 31.1133
Gaus Width: 3.5413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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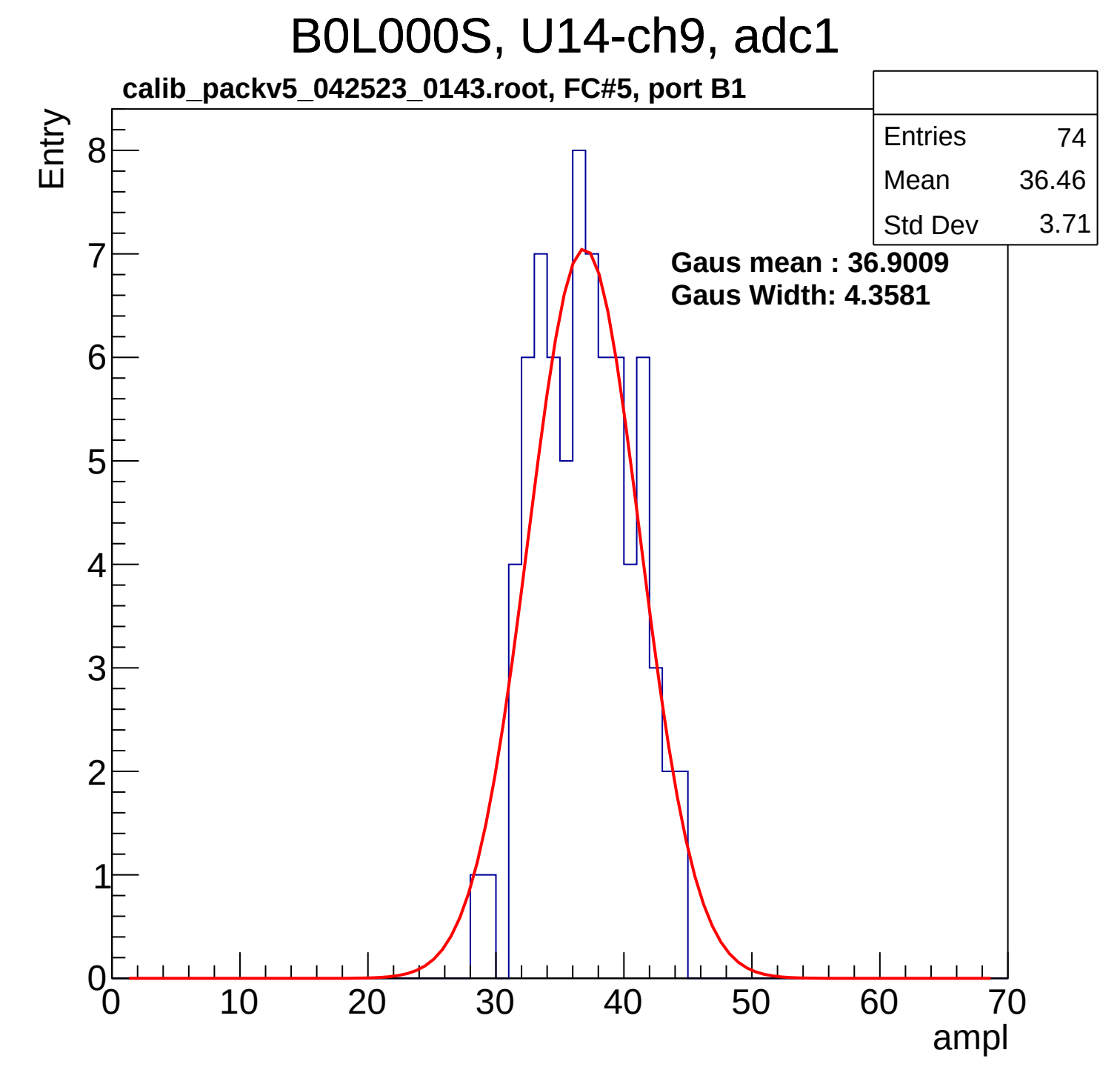
B0L000S, U14-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.46
Std Dev 3.71
Gaus mean : 36.9009
Gaus Width: 4.3581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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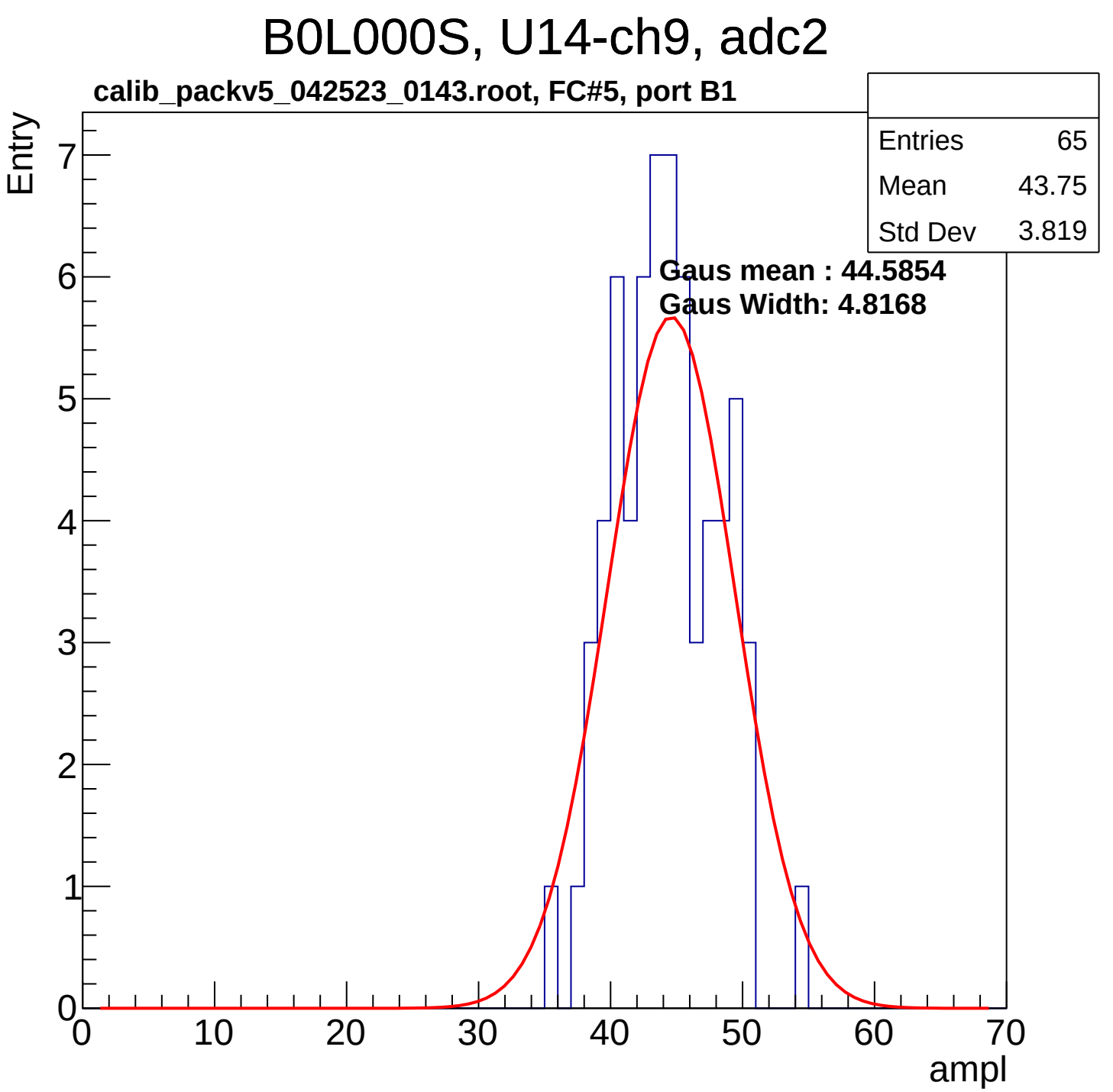
B0L000S, U14-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.75
Std Dev 3.819
Gaus mean : 44.5854
Gaus Width: 4.8168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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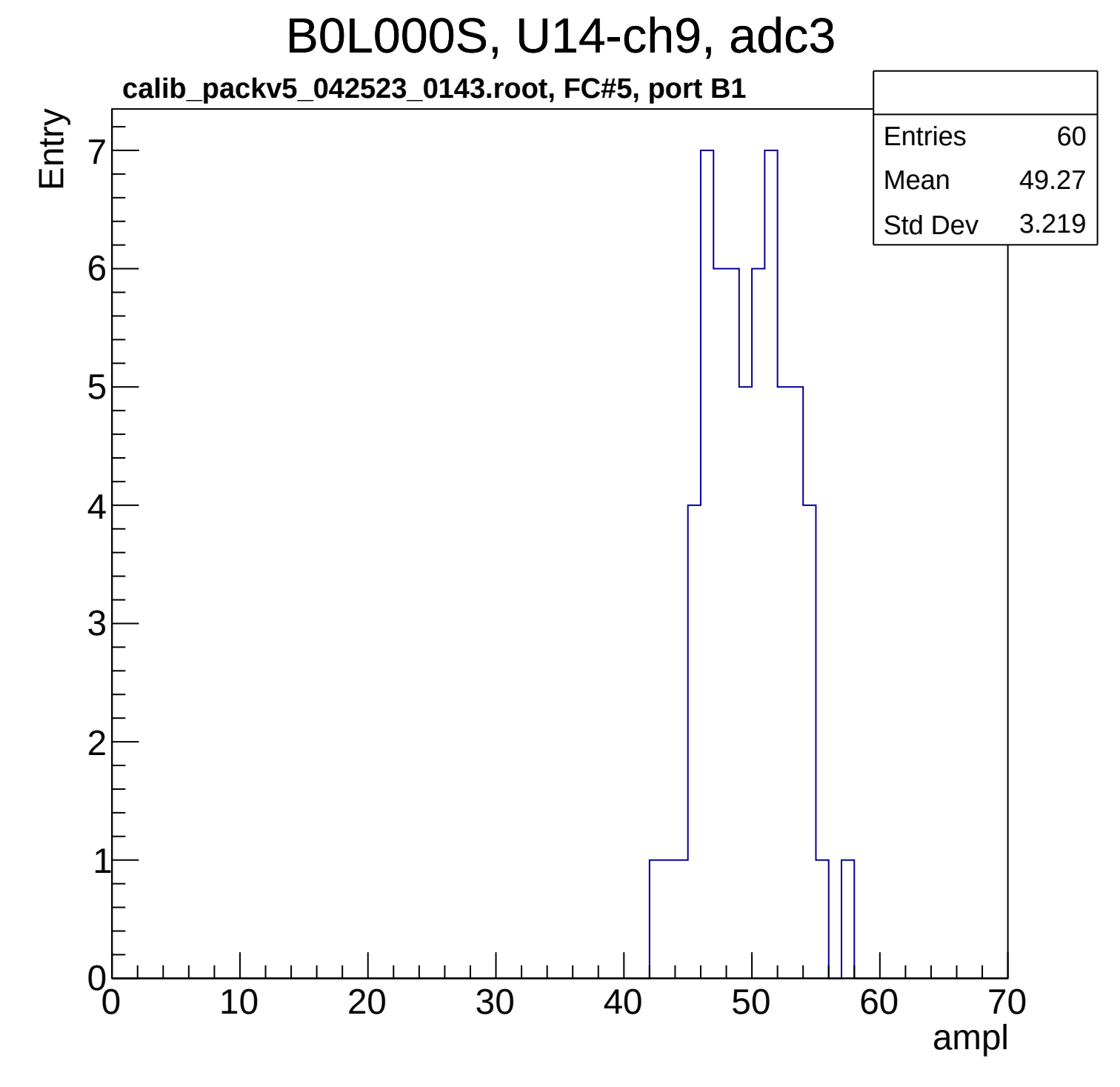
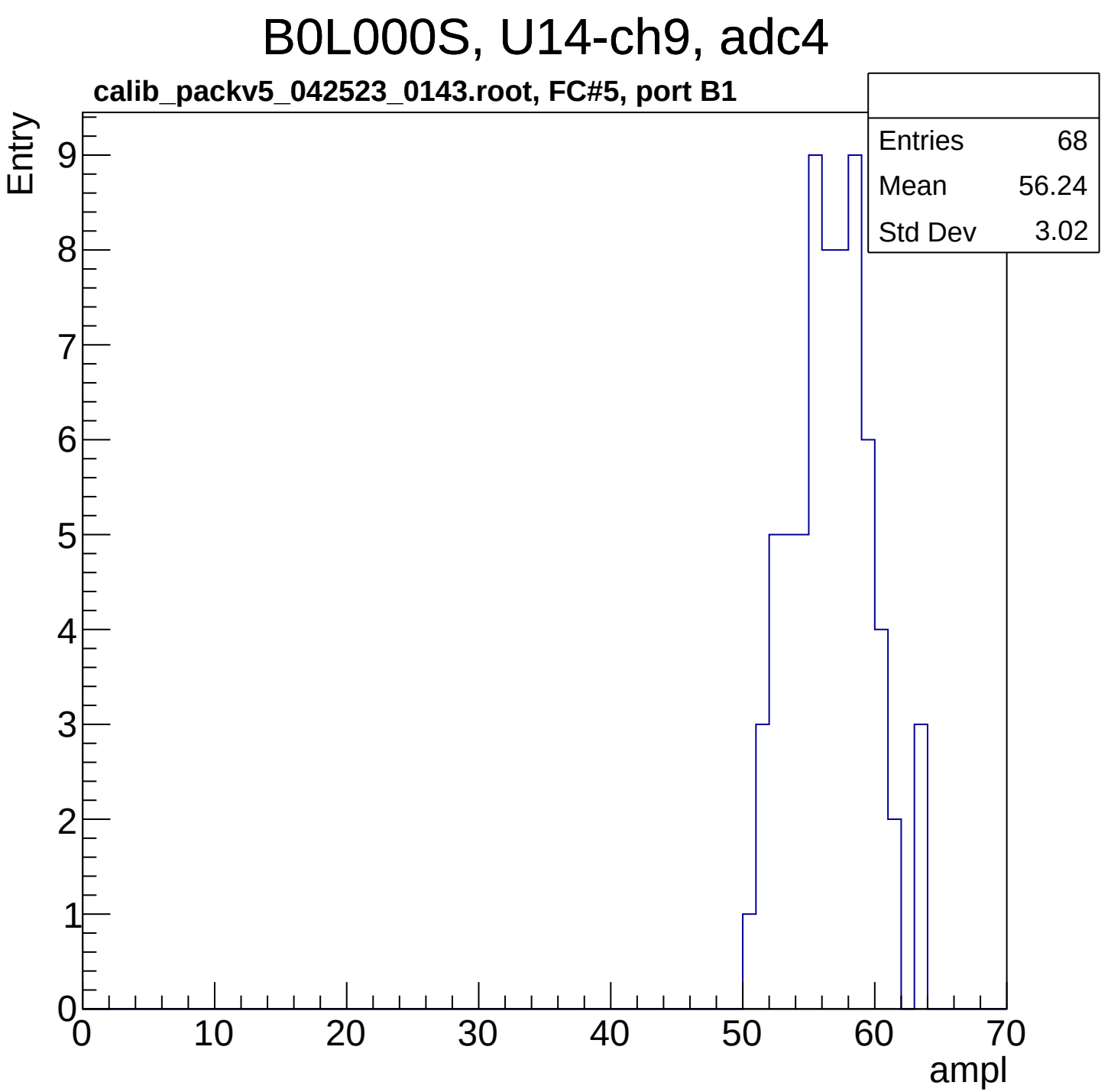
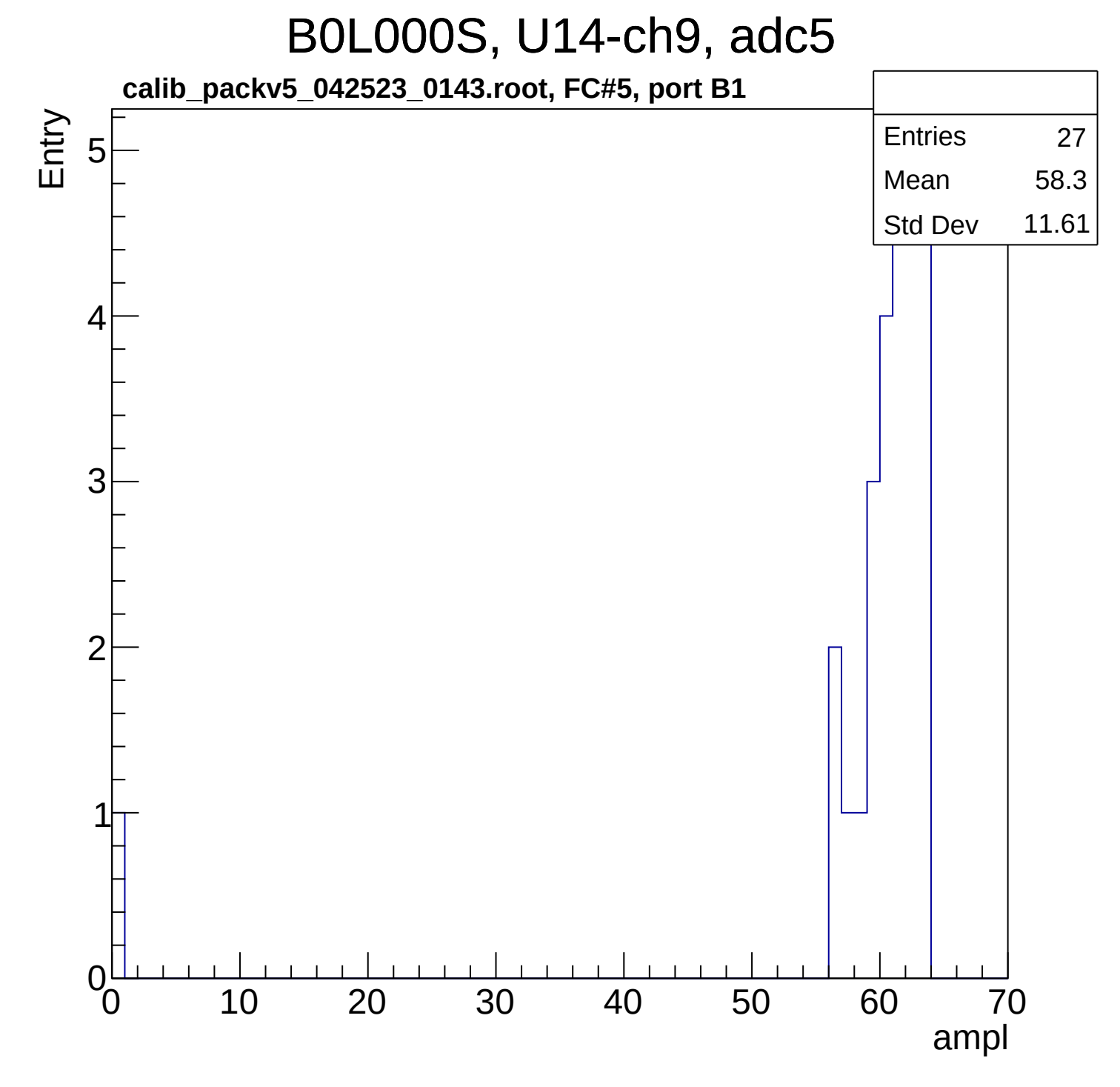
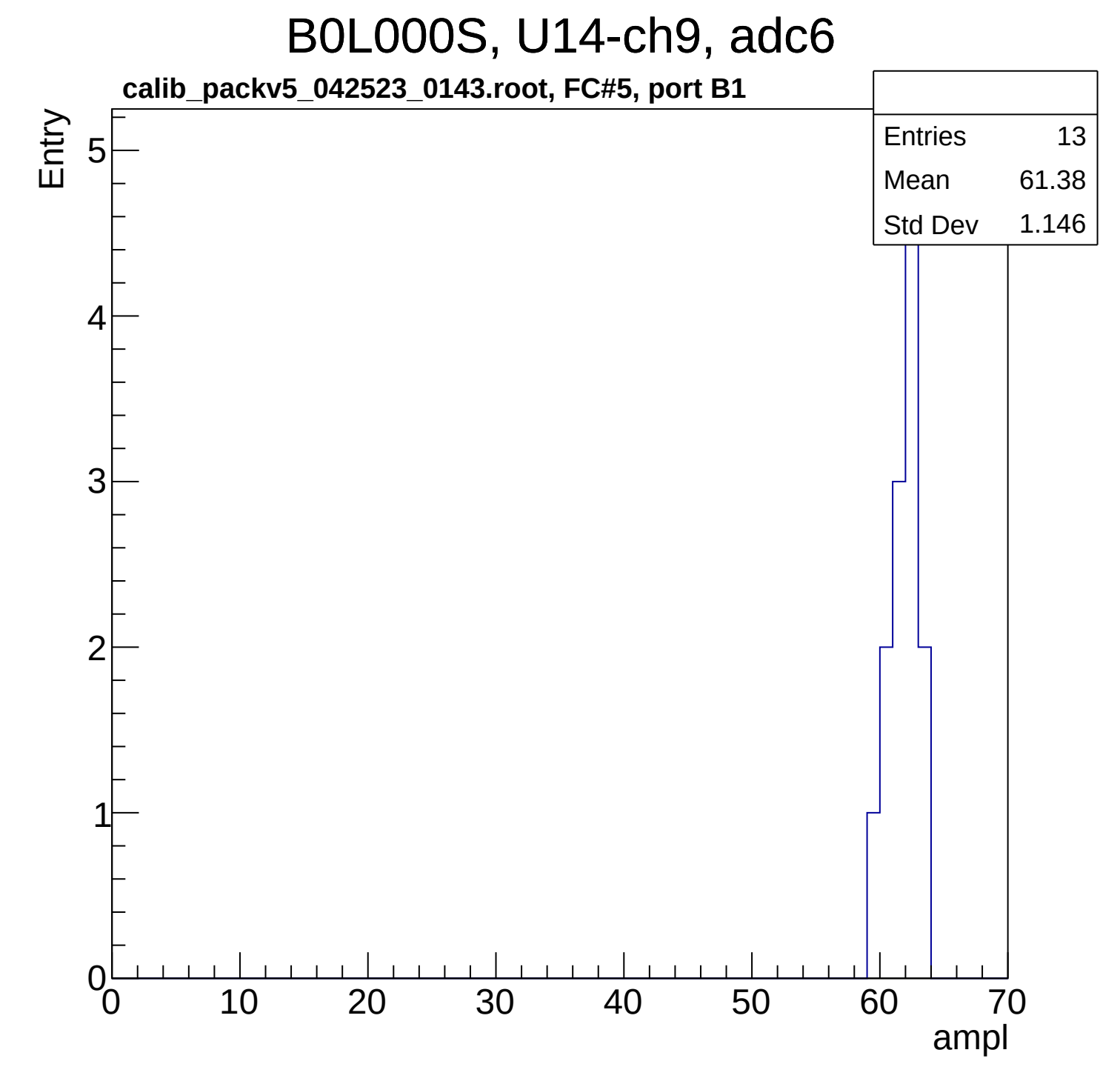
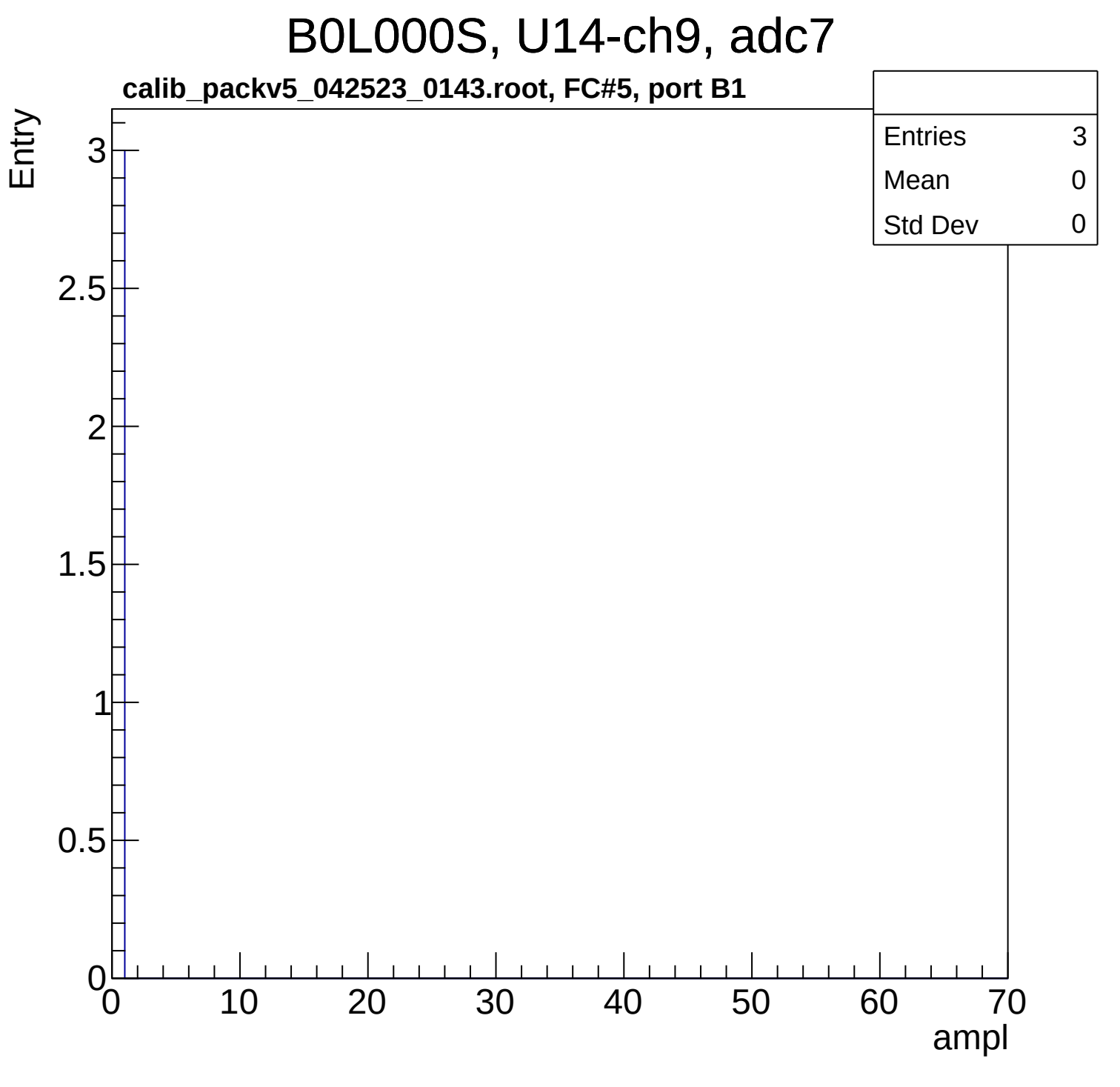

B0L000S, U14-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 49.27
Std Dev 3.219
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 56.24
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.3
Std Dev 11.61
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.38
Std Dev 1.146
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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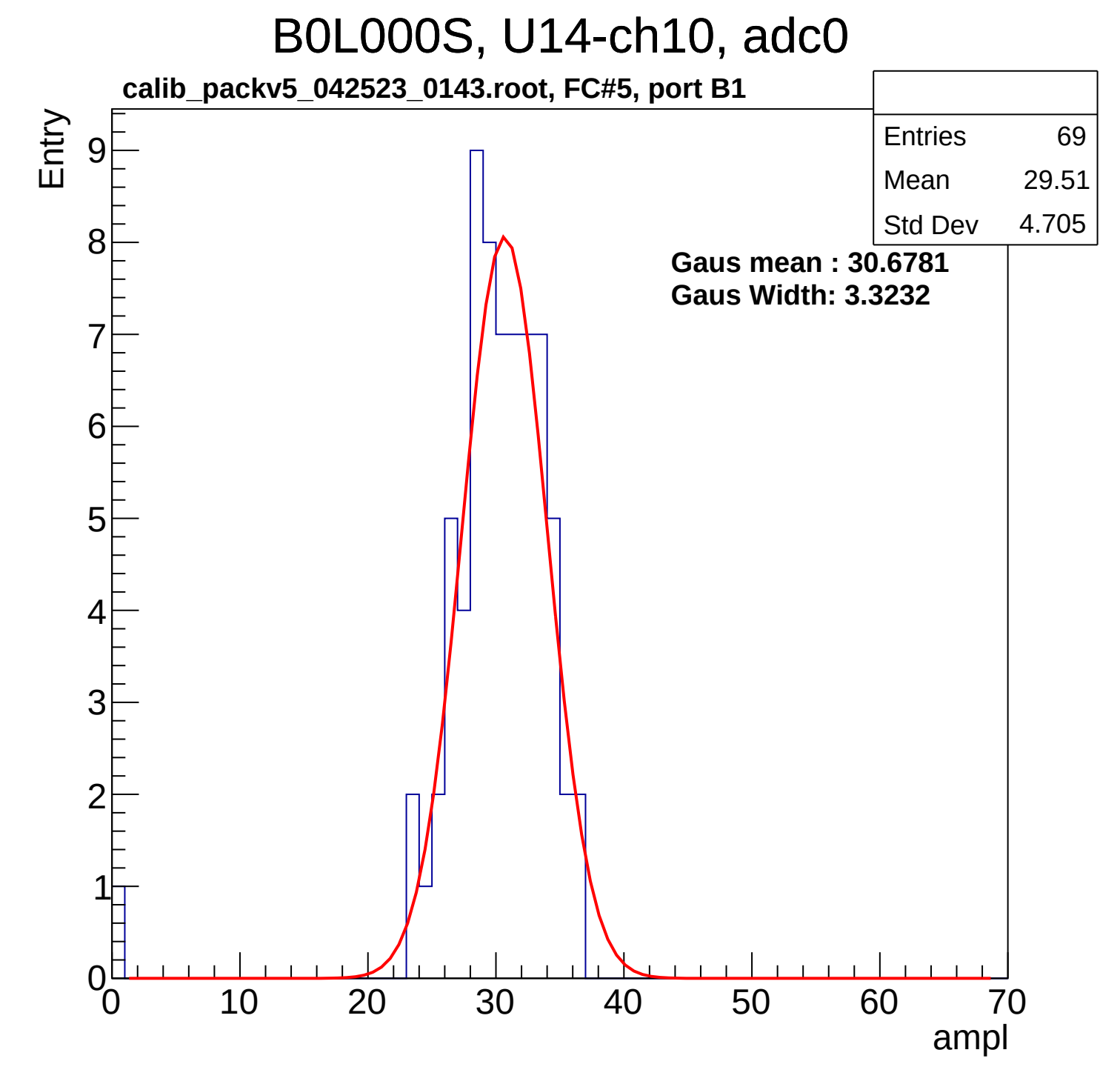
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.51
Std Dev 4.705
Gaus mean : 30.6781
Gaus Width: 3.3232
Entry
ampl
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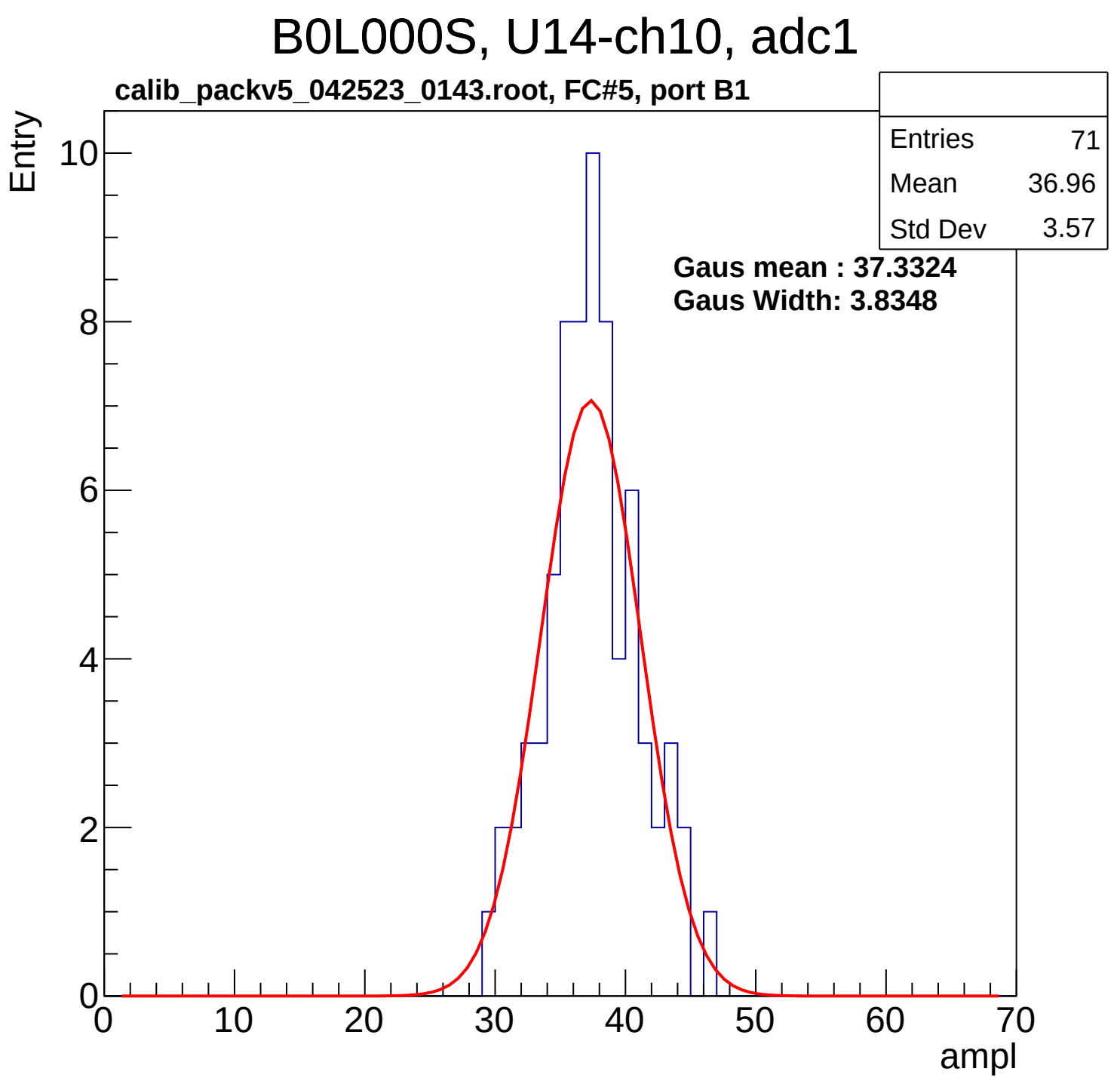
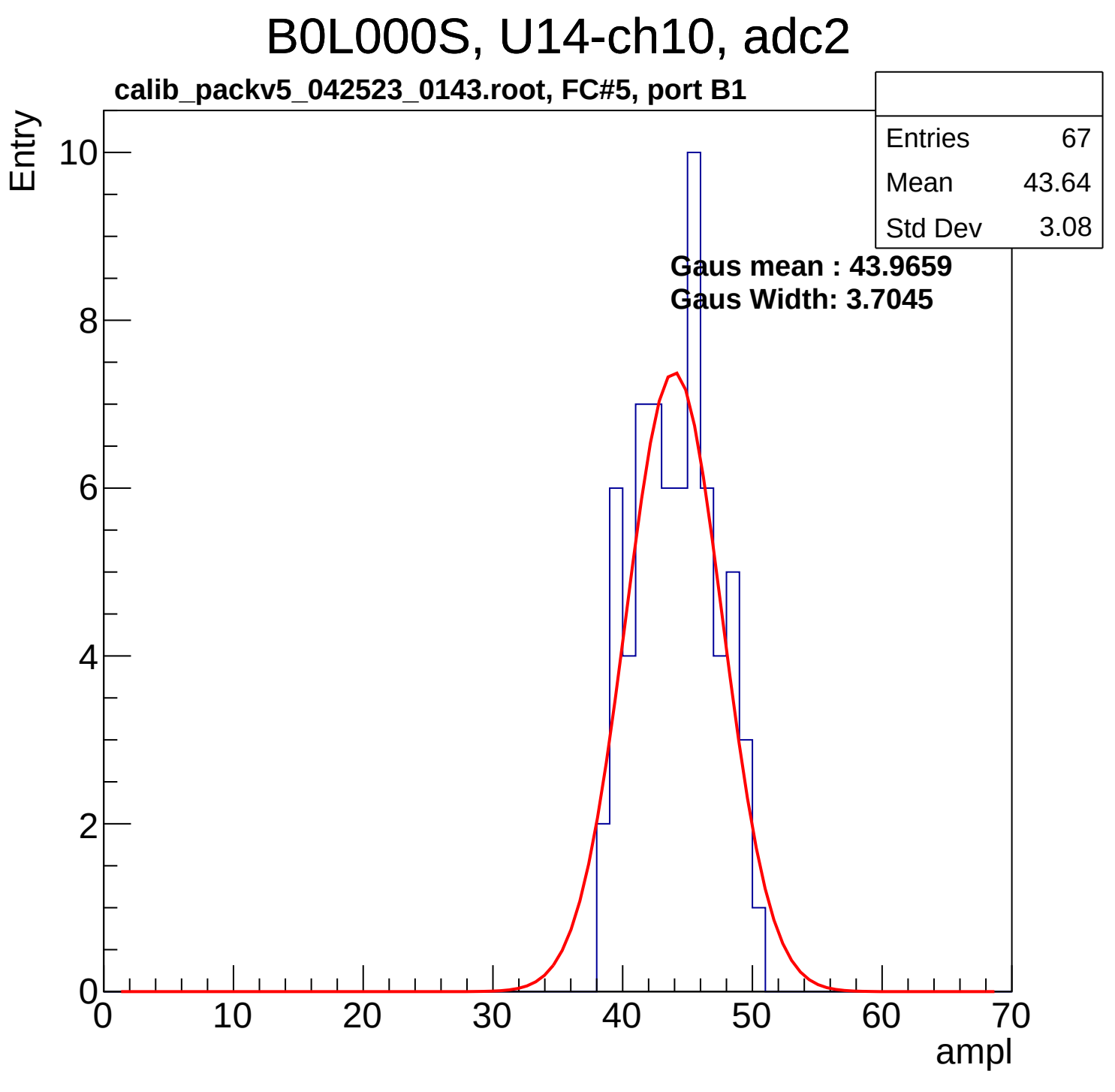
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.96
Std Dev 3.57
Gaus mean : 37.3324
Gaus Width: 3.8348
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.64
Std Dev 3.08
Gaus mean : 43.9659
Gaus Width: 3.7045
Entry
ampl
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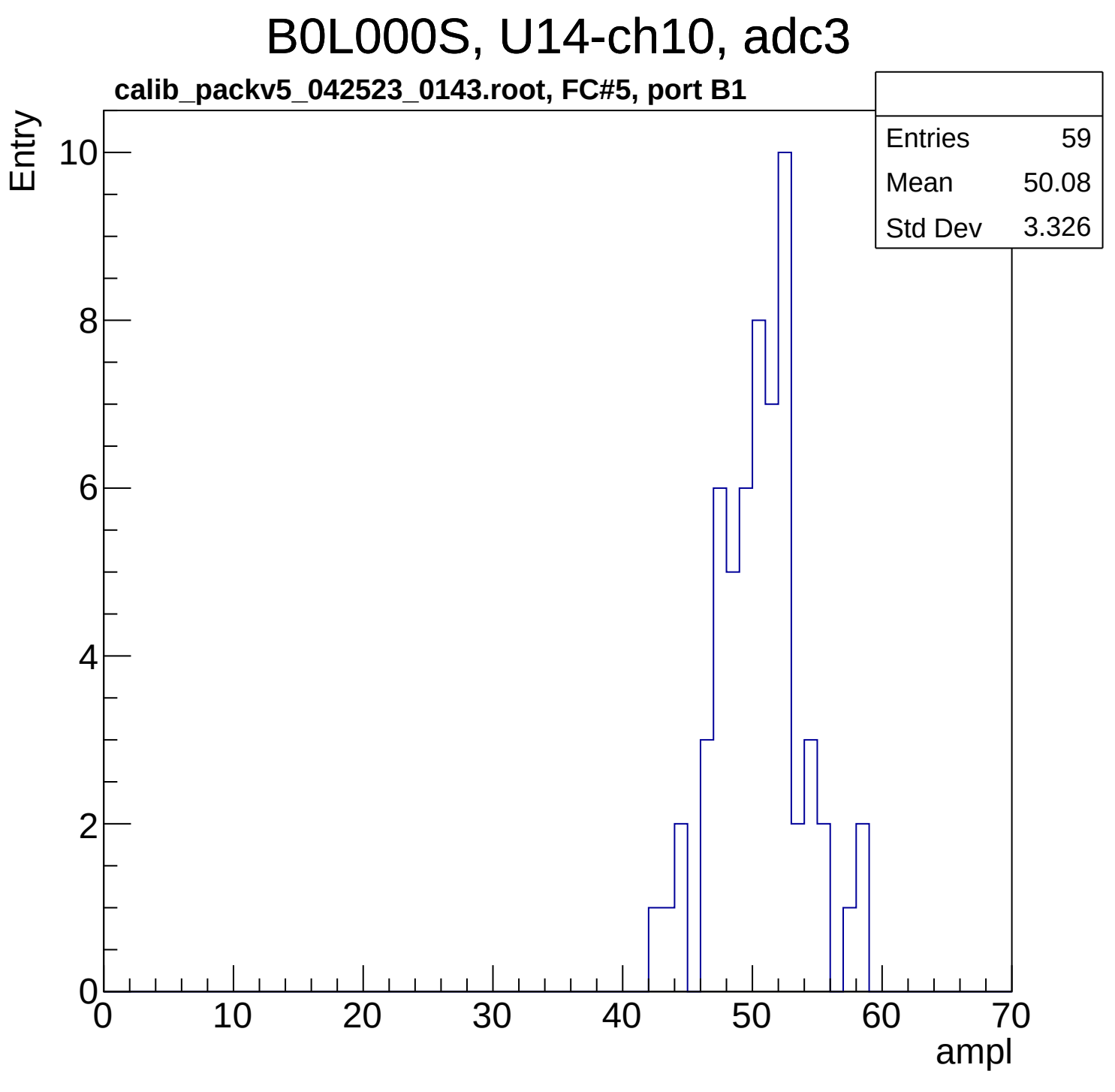
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 50.08
Std Dev 3.326
Entry
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2
4
6
8
10
0
10
20
30
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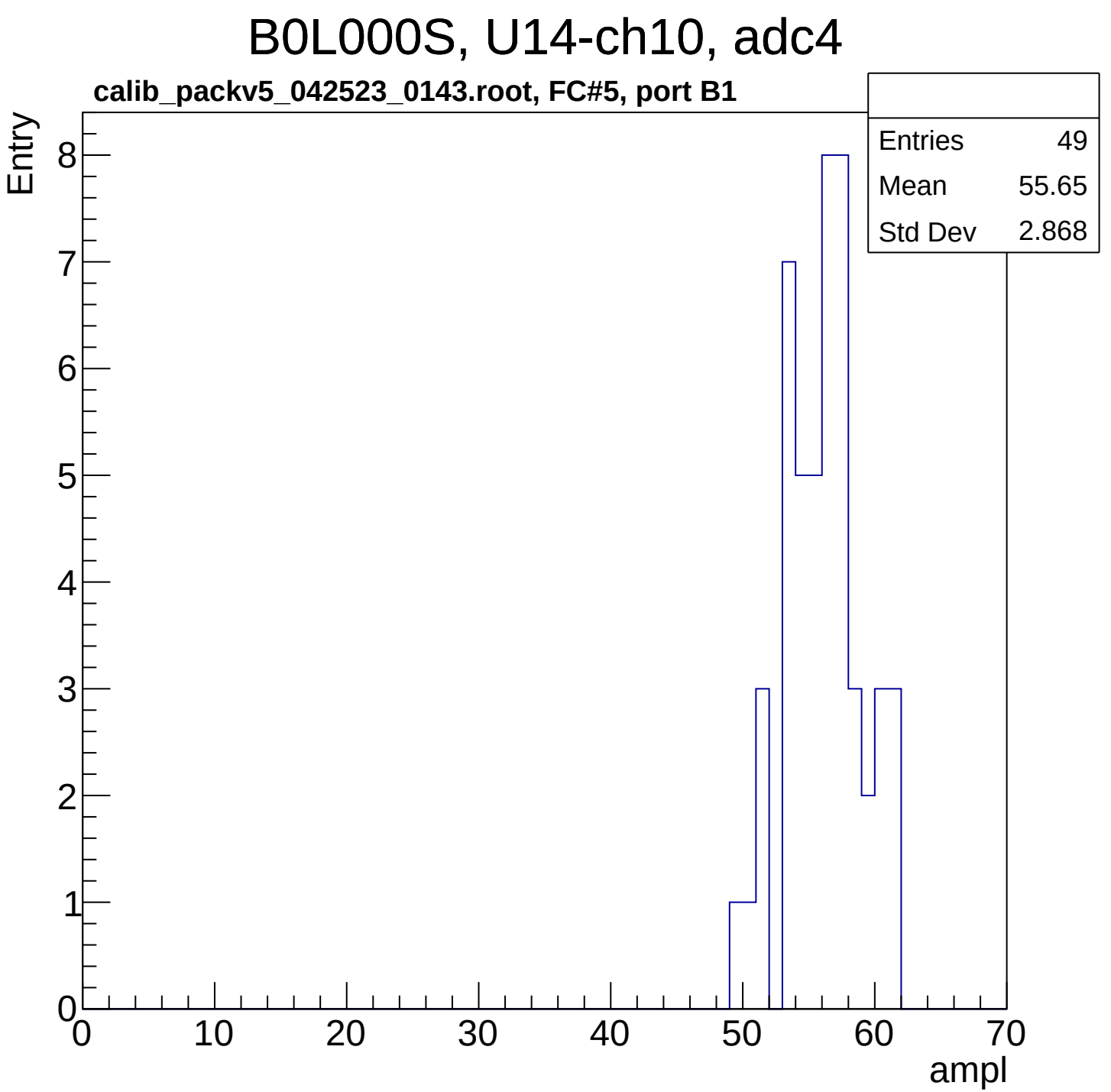
50
60
70
ampl


B0L000S, U14-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 55.65
Std Dev 2.868
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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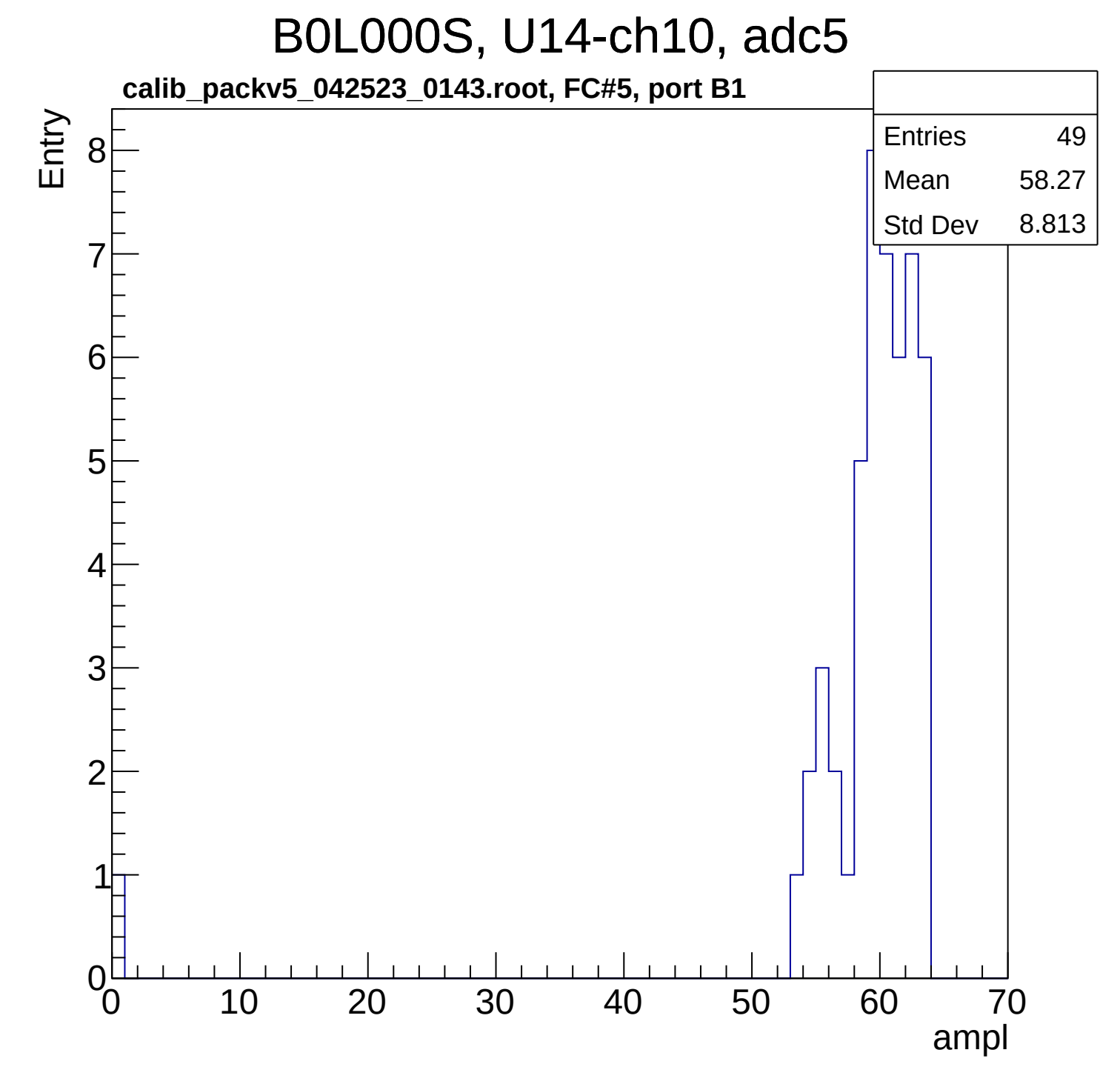
50
60
70


B0L000S, U14-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.27
Std Dev 8.813
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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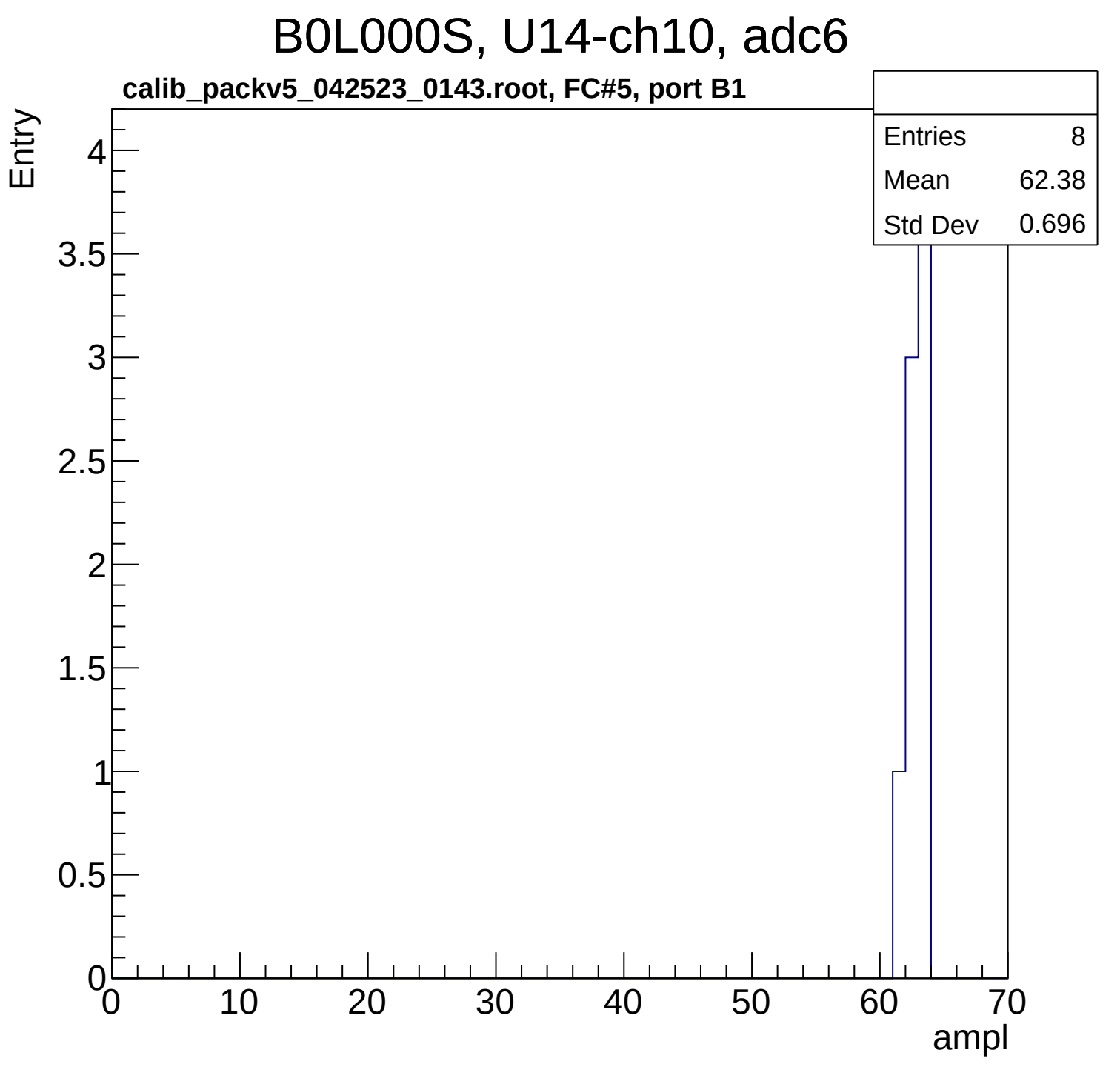
B0L000S, U14-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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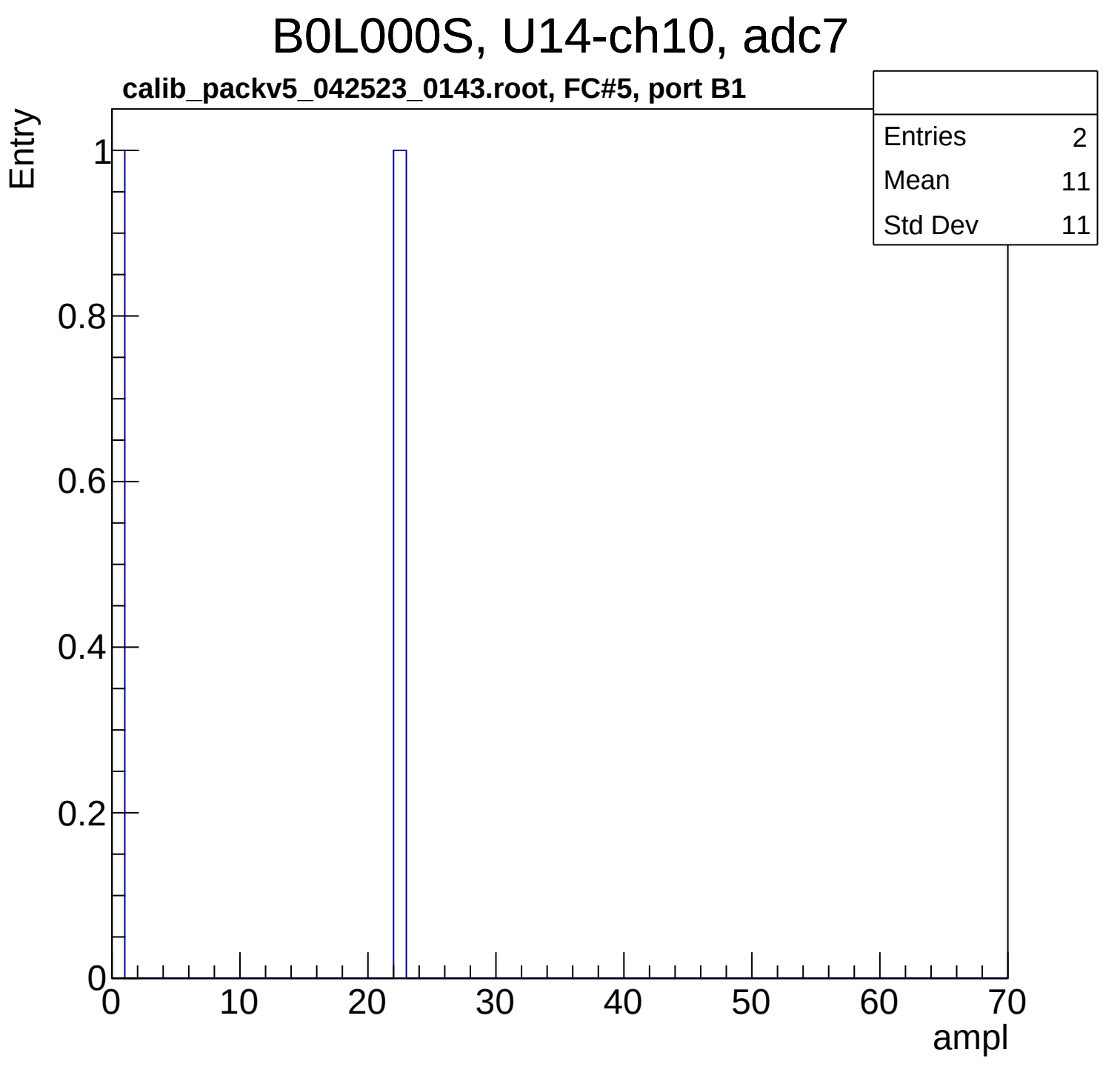
B0L000S, U14-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.4
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10
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30
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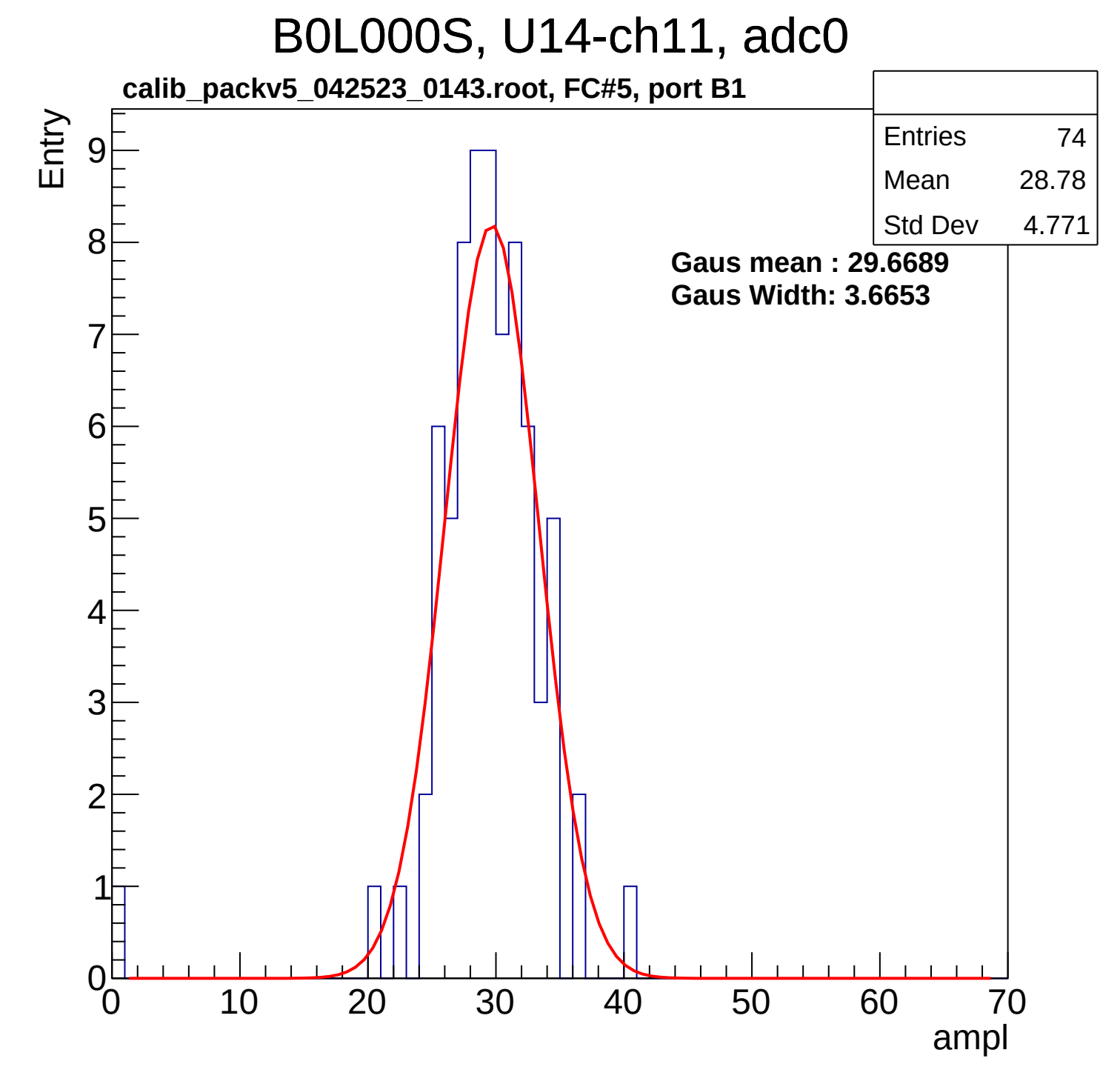
50
60
70
ampl


B0L000S, U14-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.78
Std Dev 4.771
Gaus mean : 29.6689
Gaus Width: 3.6653
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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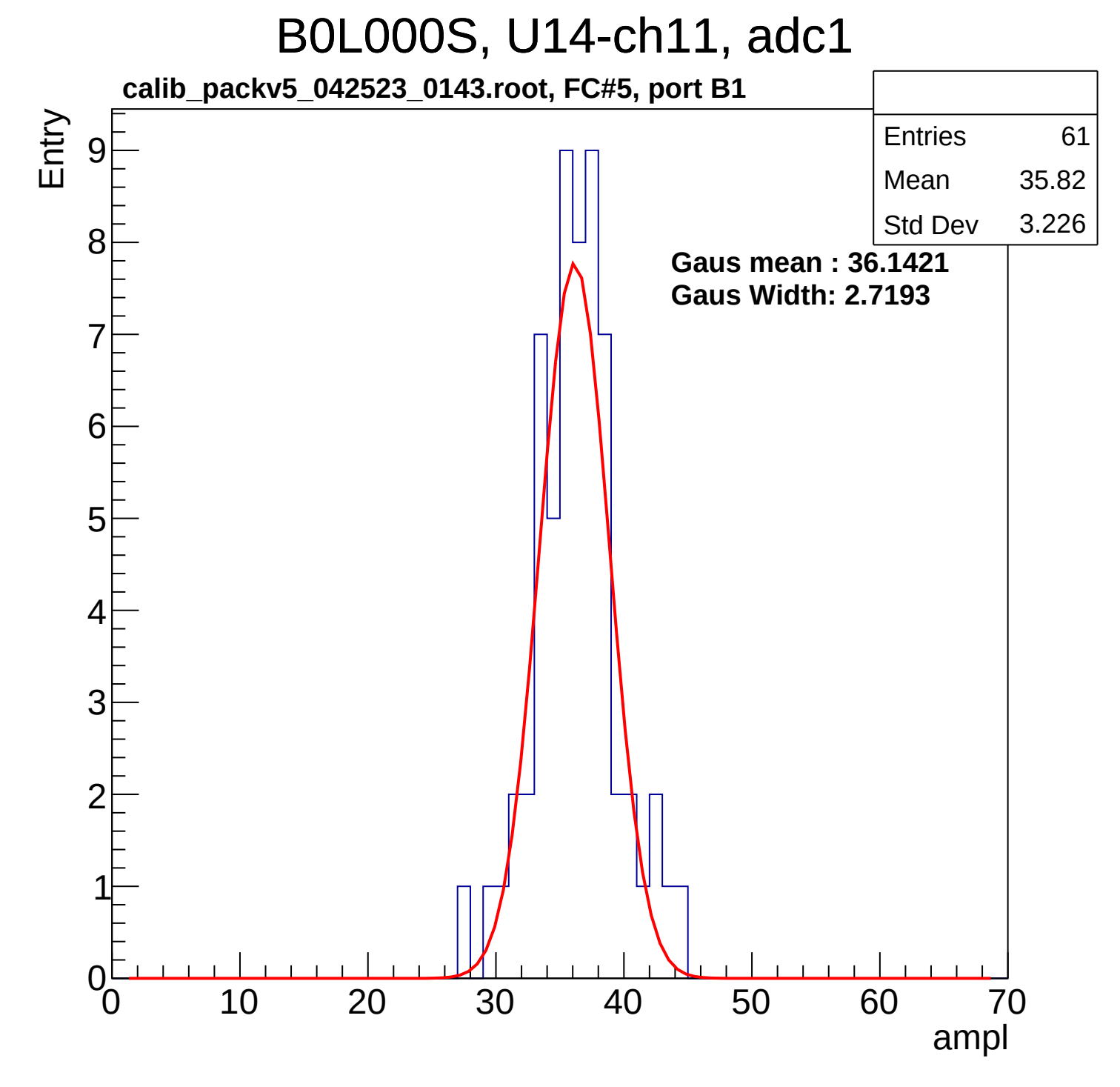
B0L000S, U14-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.82
Std Dev 3.226
Gaus mean : 36.1421
Gaus Width: 2.7193
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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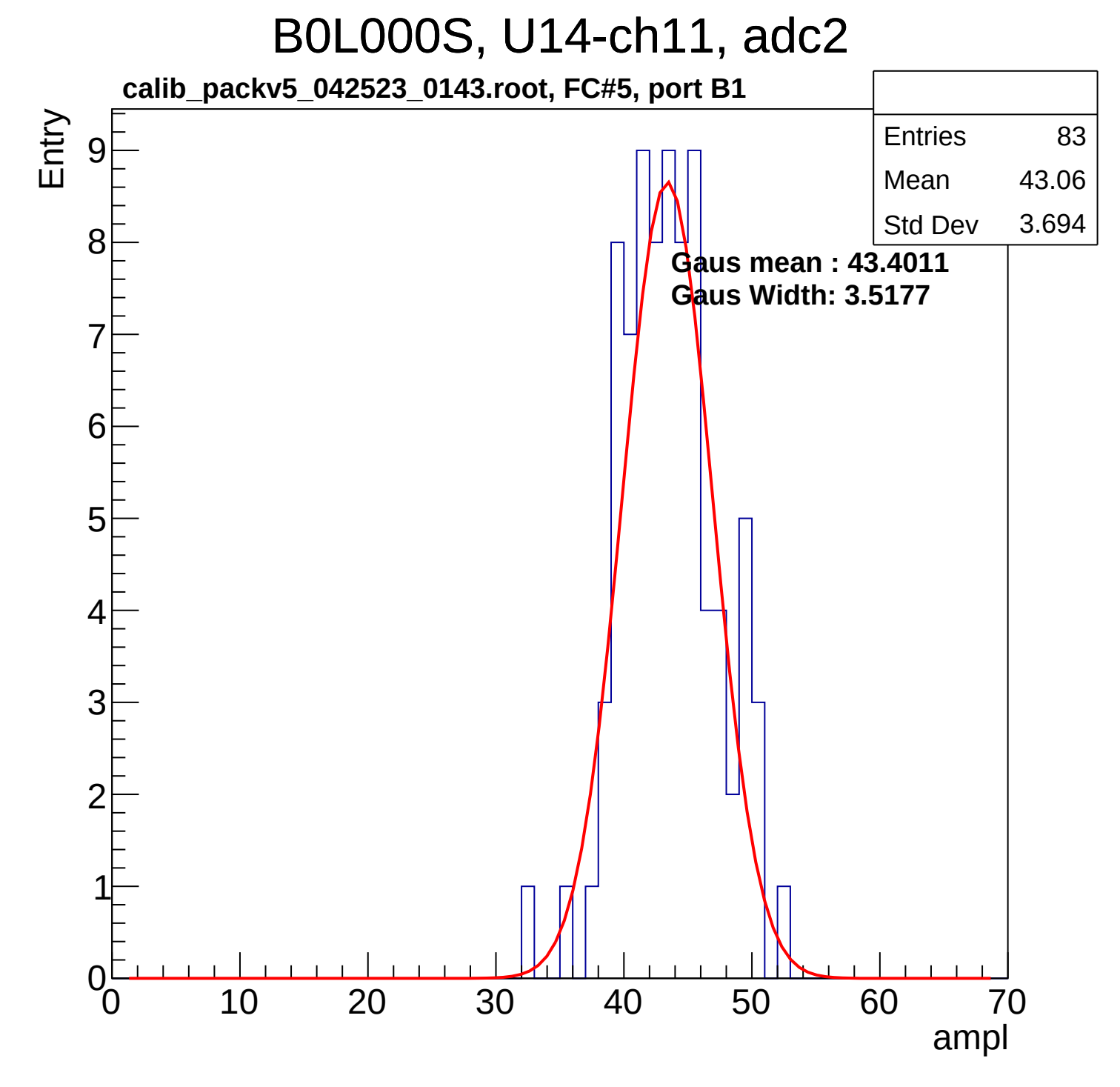
B0L000S, U14-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 43.06
Std Dev 3.694
Gaus mean : 43.4011
Gaus Width: 3.5177
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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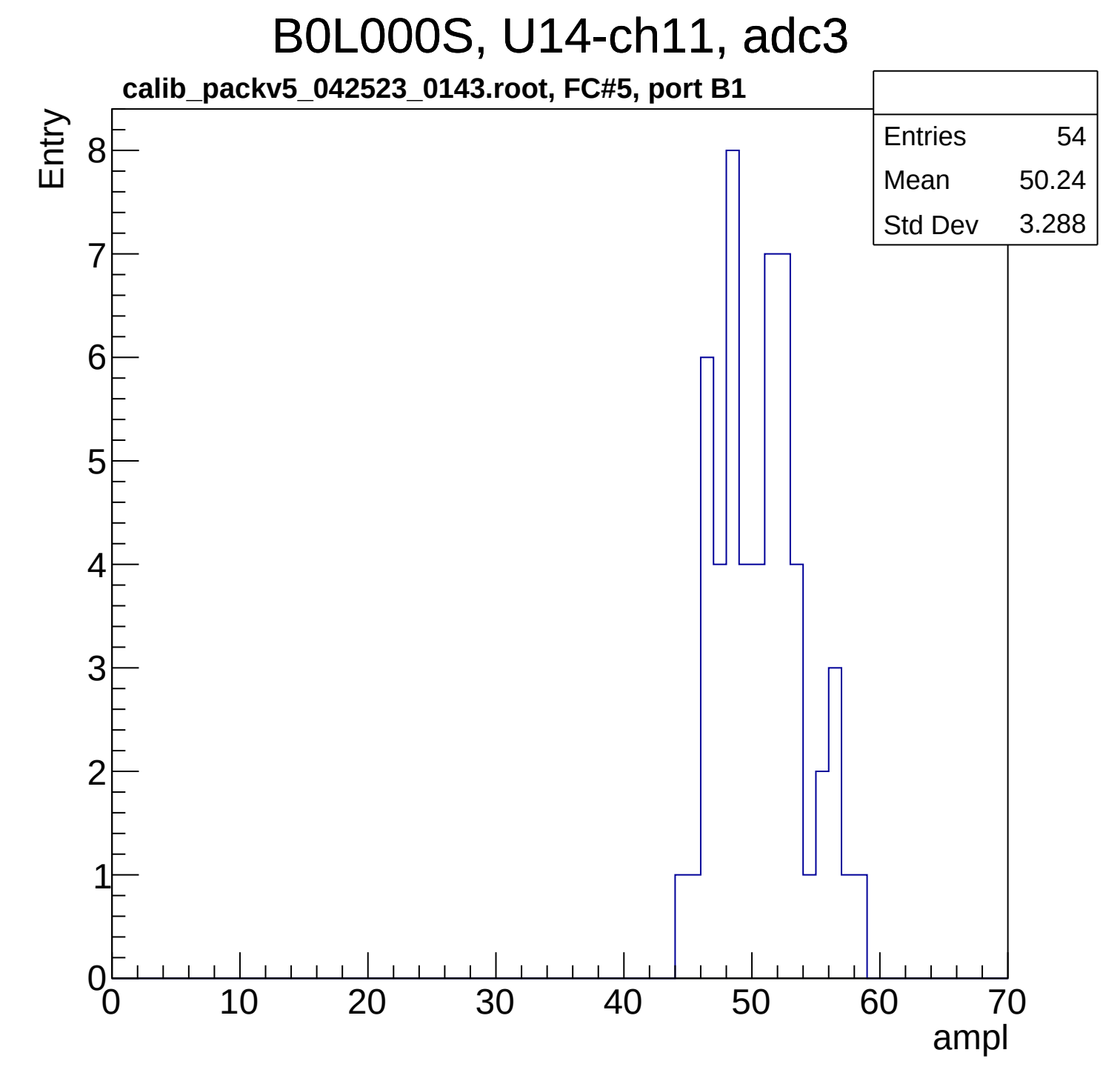
B0L000S, U14-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.24
Std Dev 3.288
Entry
ampl
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3
4
5
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7
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0
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40
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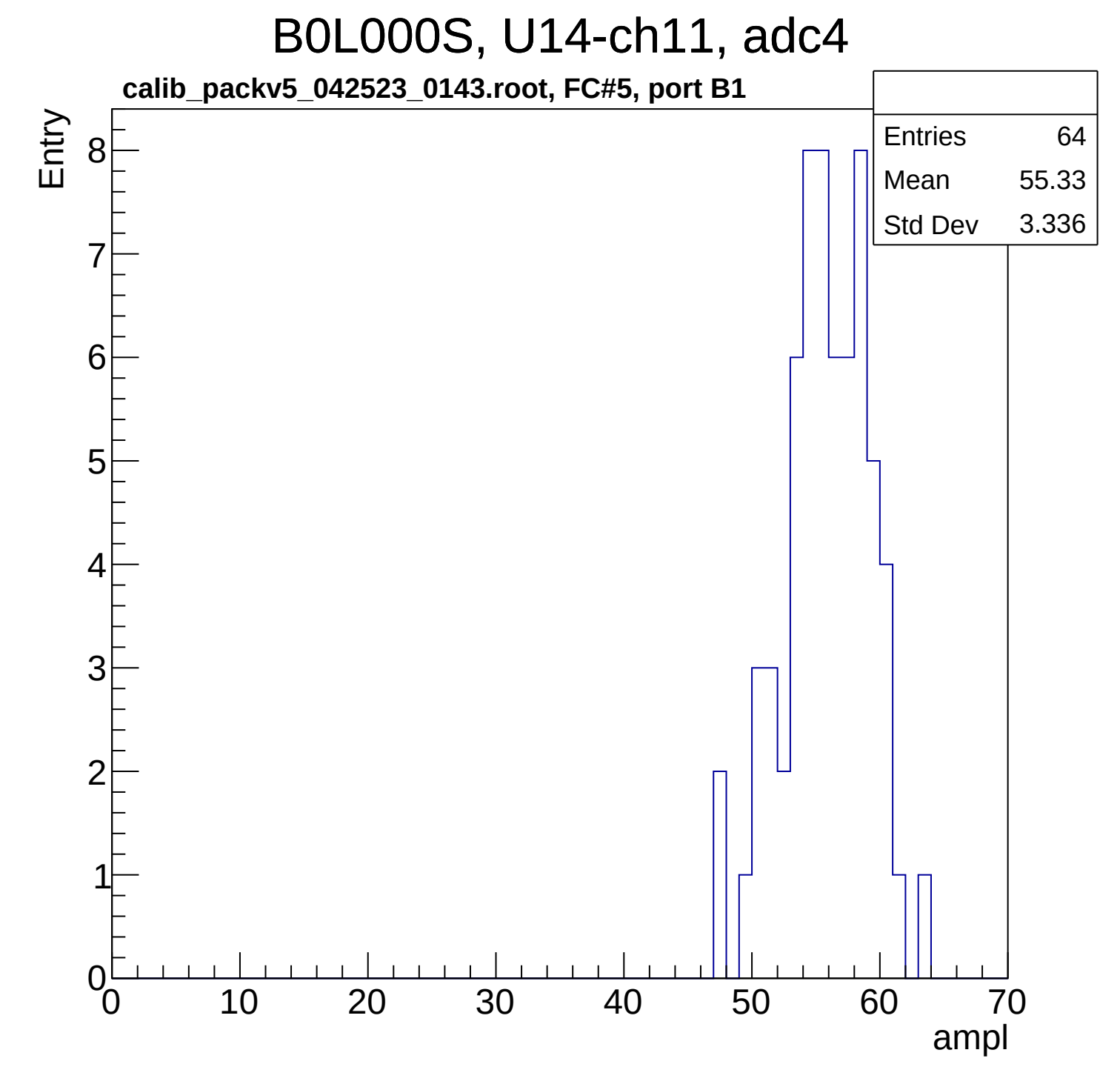
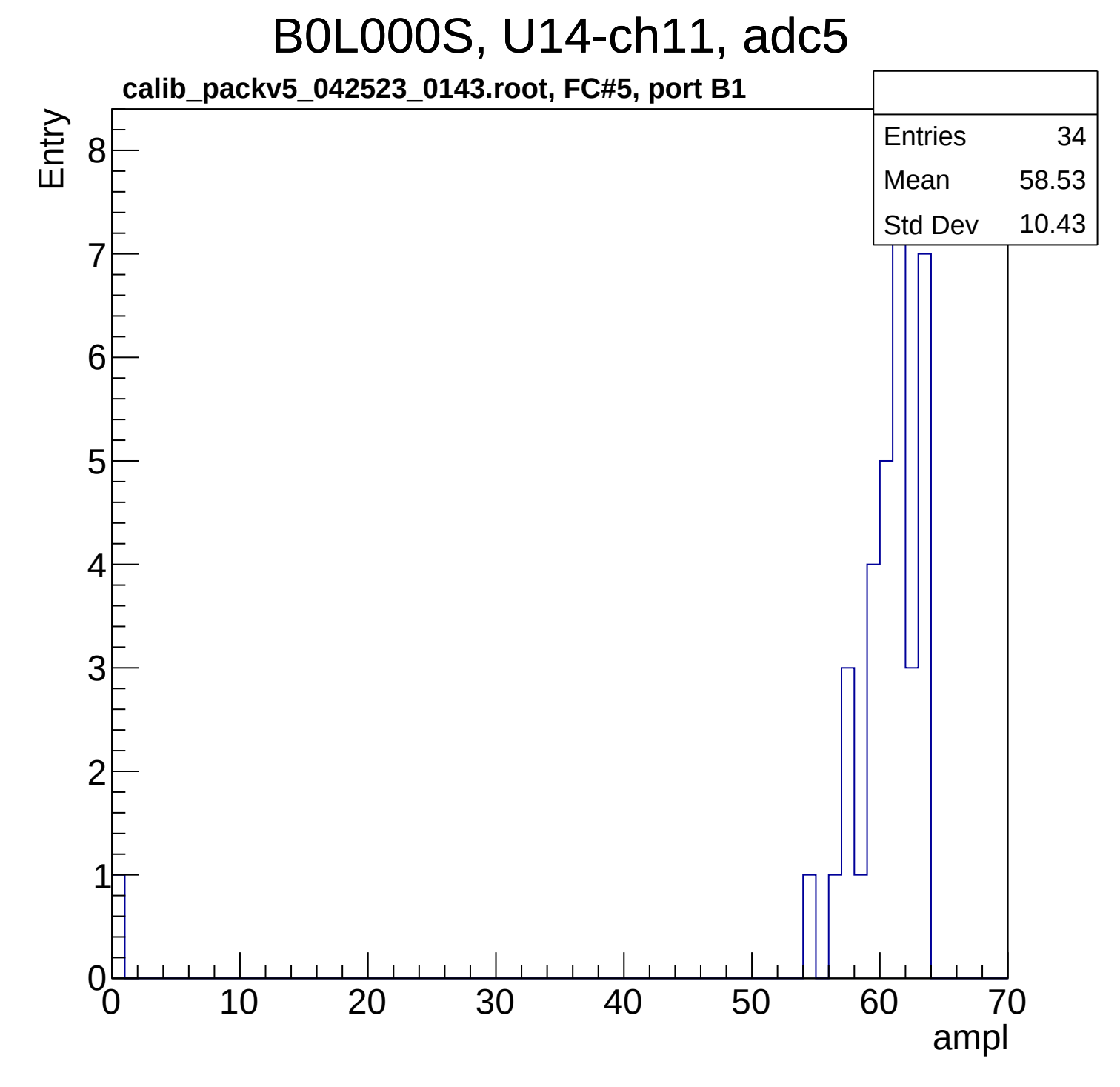

B0L000S, U14-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 55.33
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 58.53
Std Dev 10.43
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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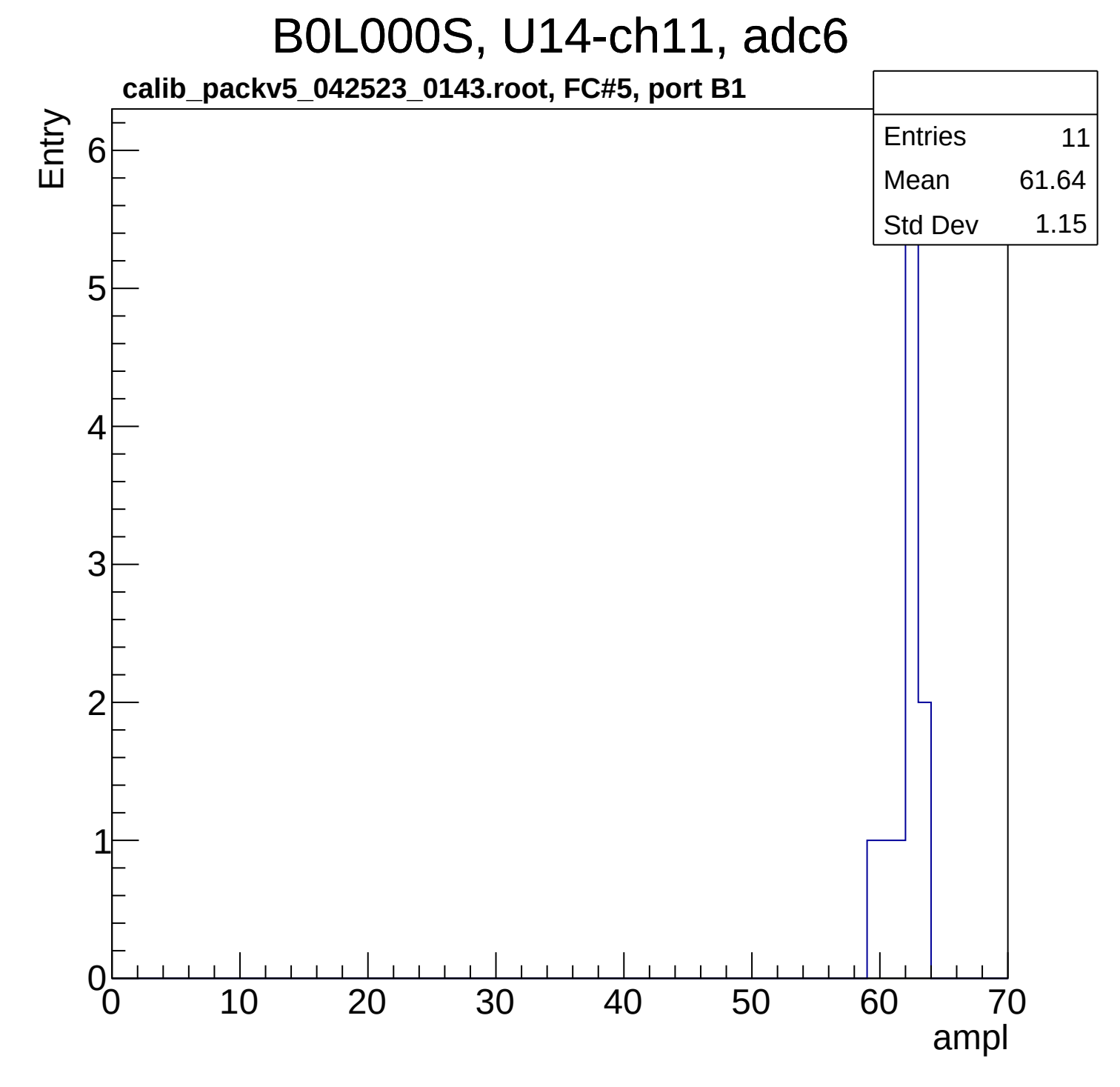
B0L000S, U14-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.64
Std Dev 1.15
Entry
0
1
2
3
4
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10
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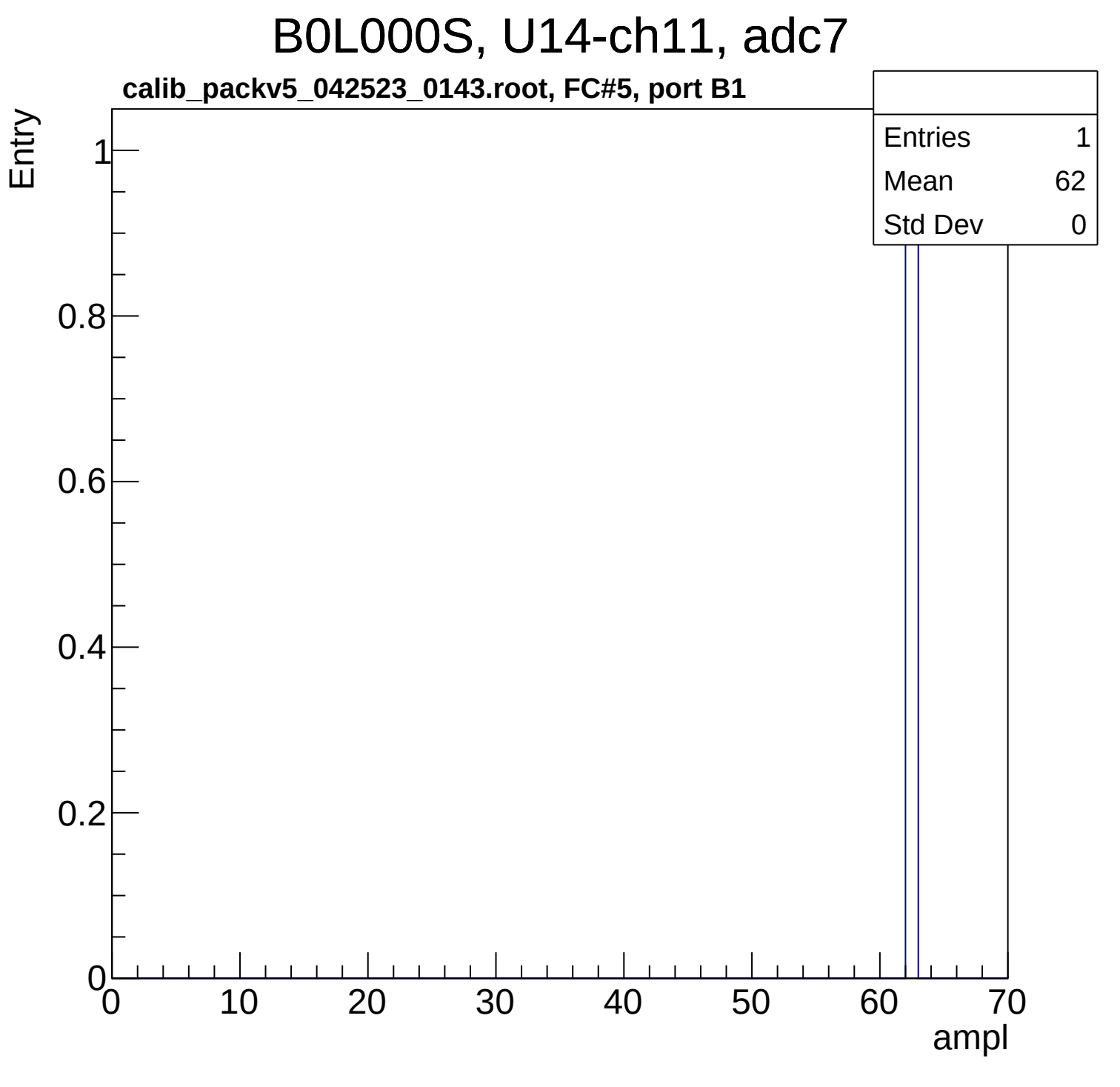
70
ampl


B0L000S, U14-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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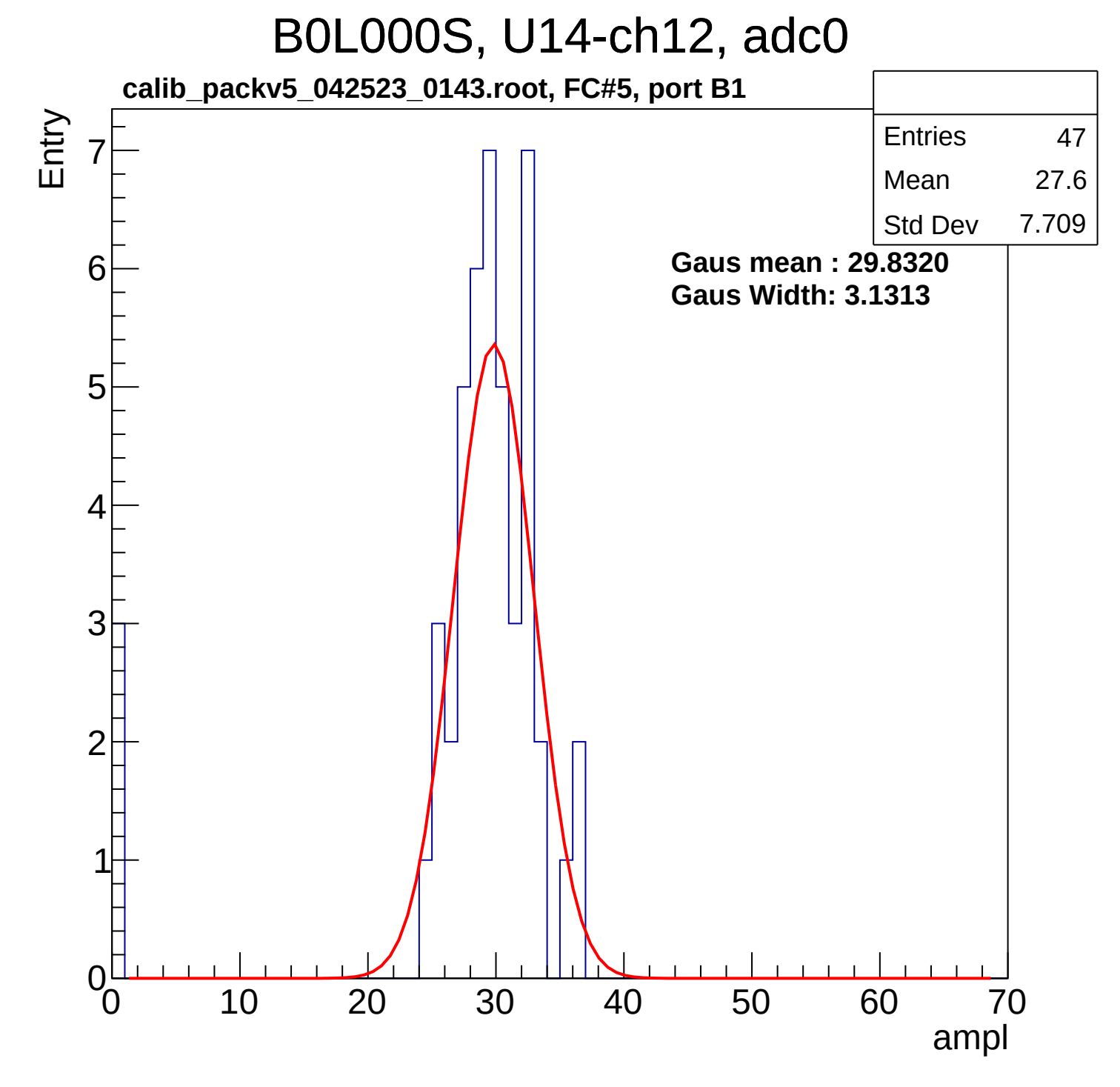
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 27.6
Std Dev 7.709
Gaus mean : 29.8320
Gaus Width: 3.1313
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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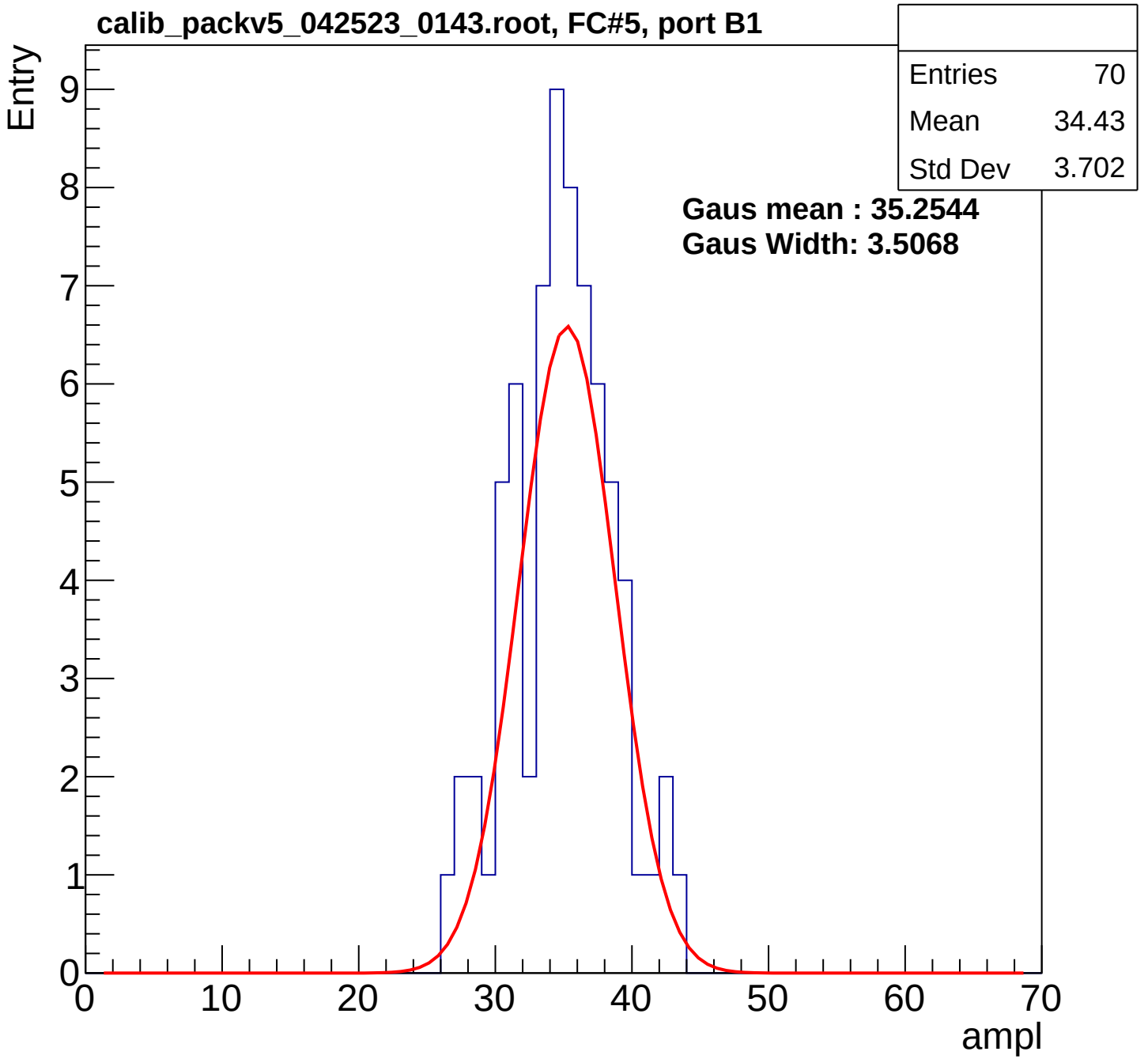
B0L000S, U14-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 34.43
Std Dev 3.702
Gaus mean : 35.2544
Gaus Width: 3.5068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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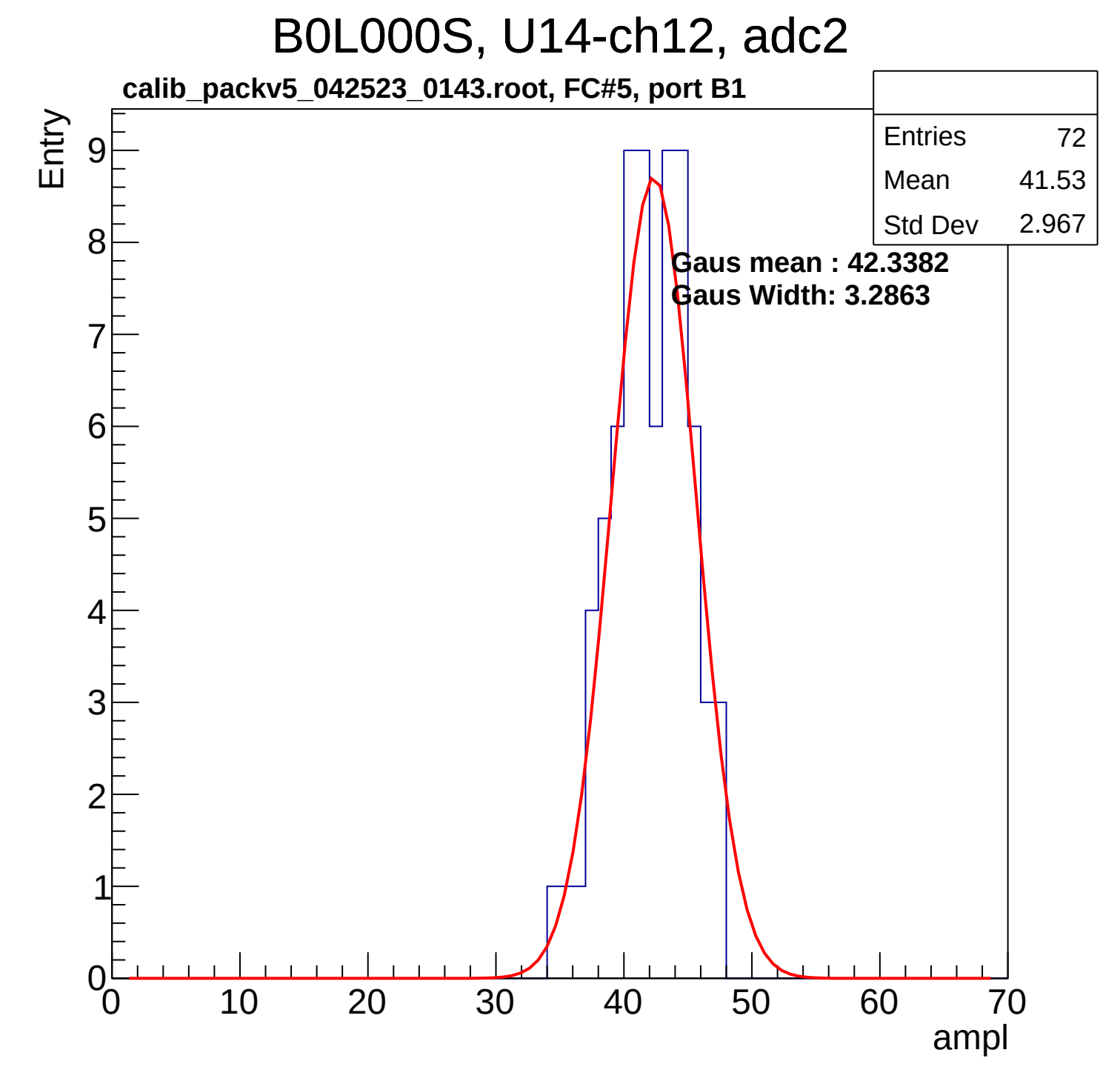
B0L000S, U14-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 41.53
Std Dev 2.967
Gaus mean : 42.3382
Gaus Width: 3.2863
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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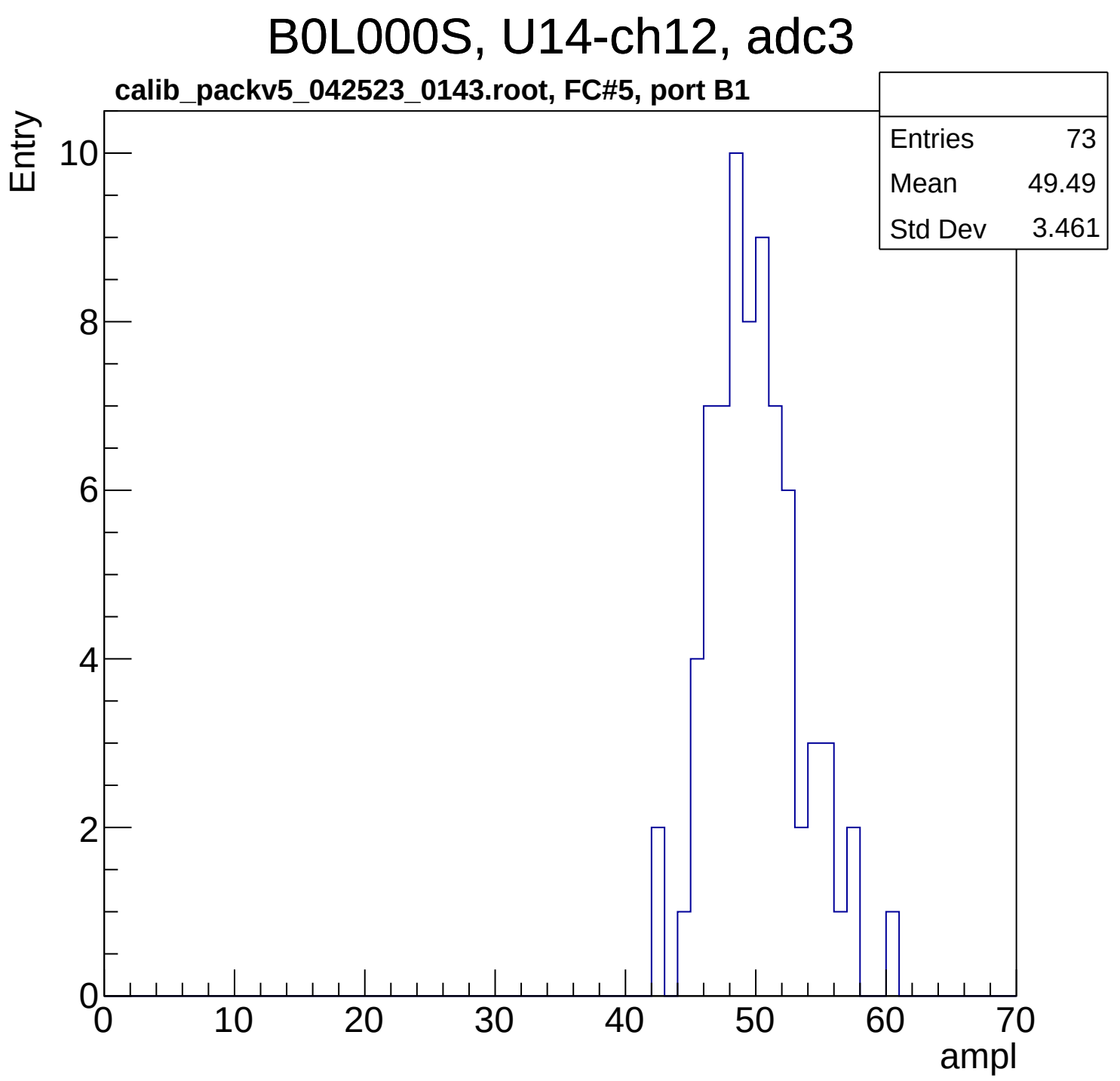
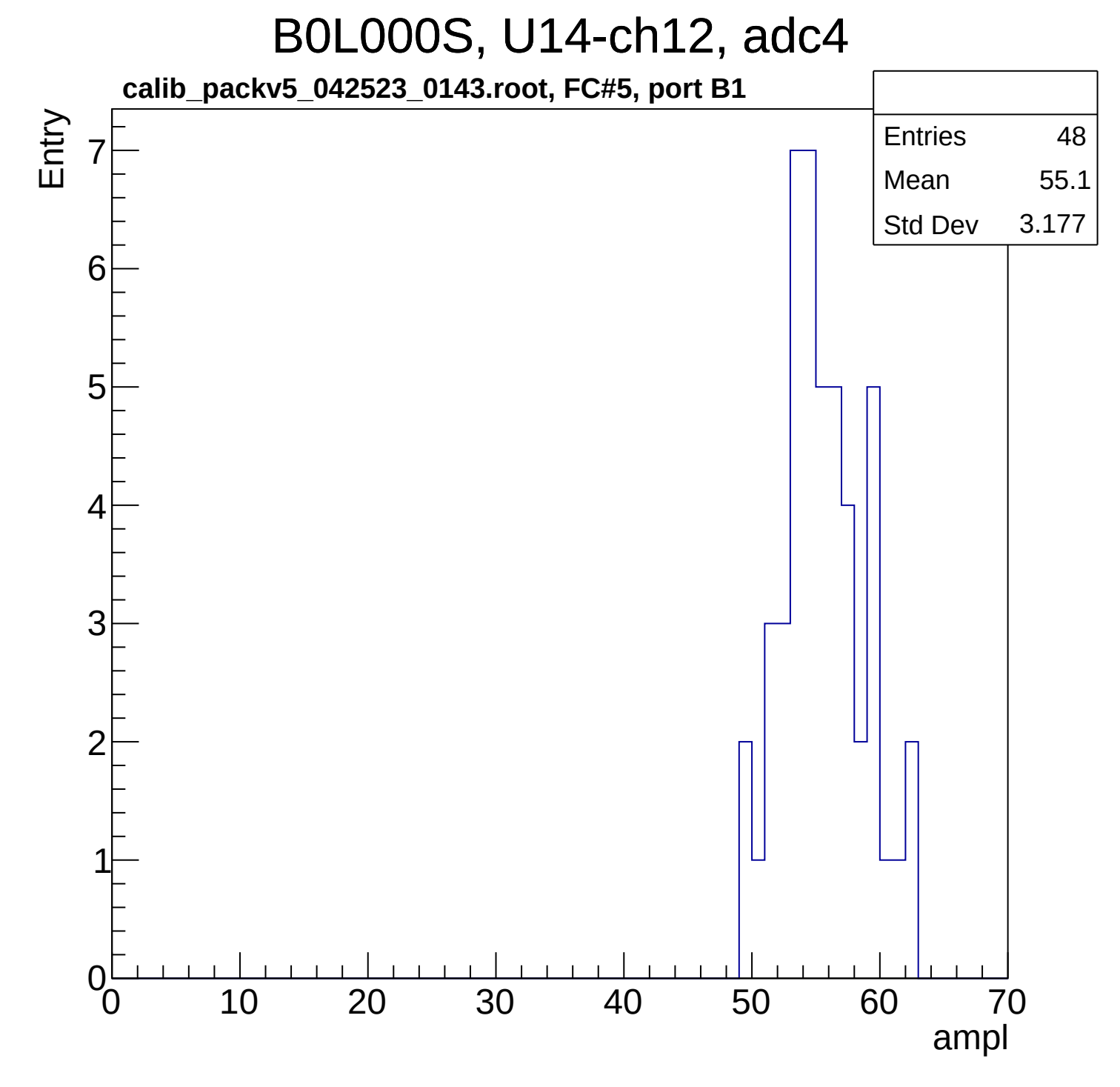

B0L000S, U14-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.49
Std Dev 3.461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.1
Std Dev 3.177
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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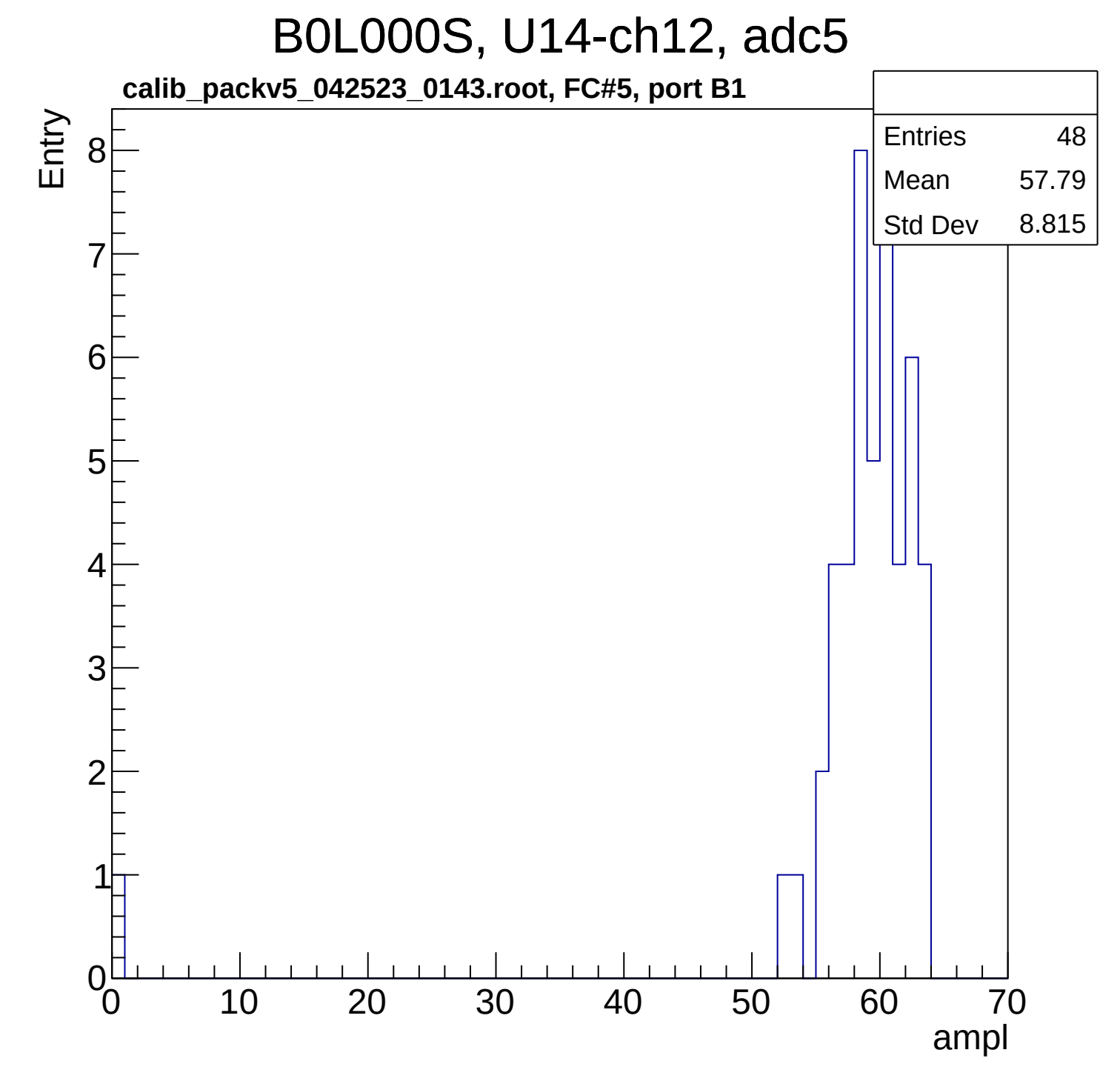
B0L000S, U14-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 57.79
Std Dev 8.815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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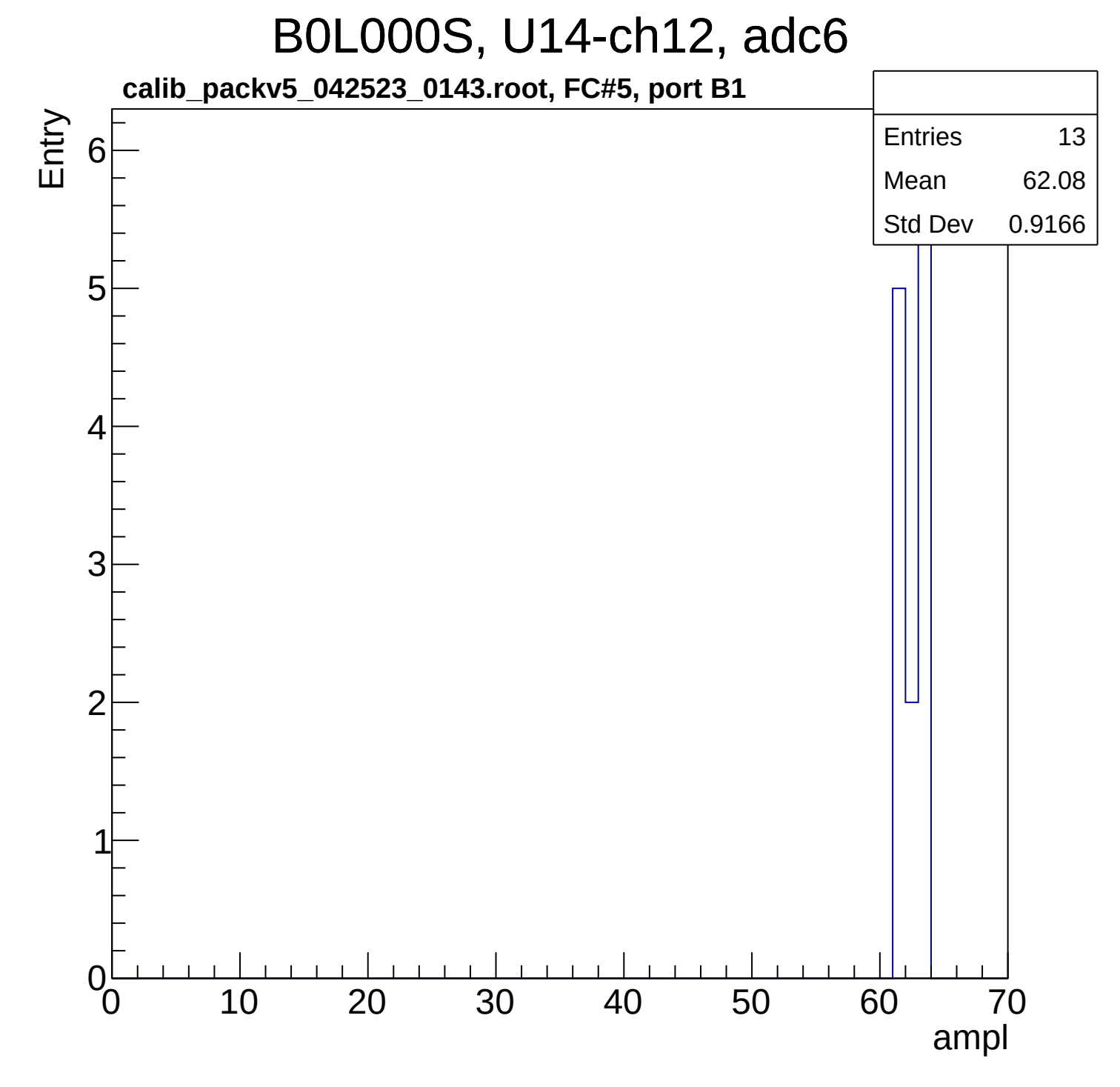
B0L000S, U14-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 62.08
Std Dev 0.9166
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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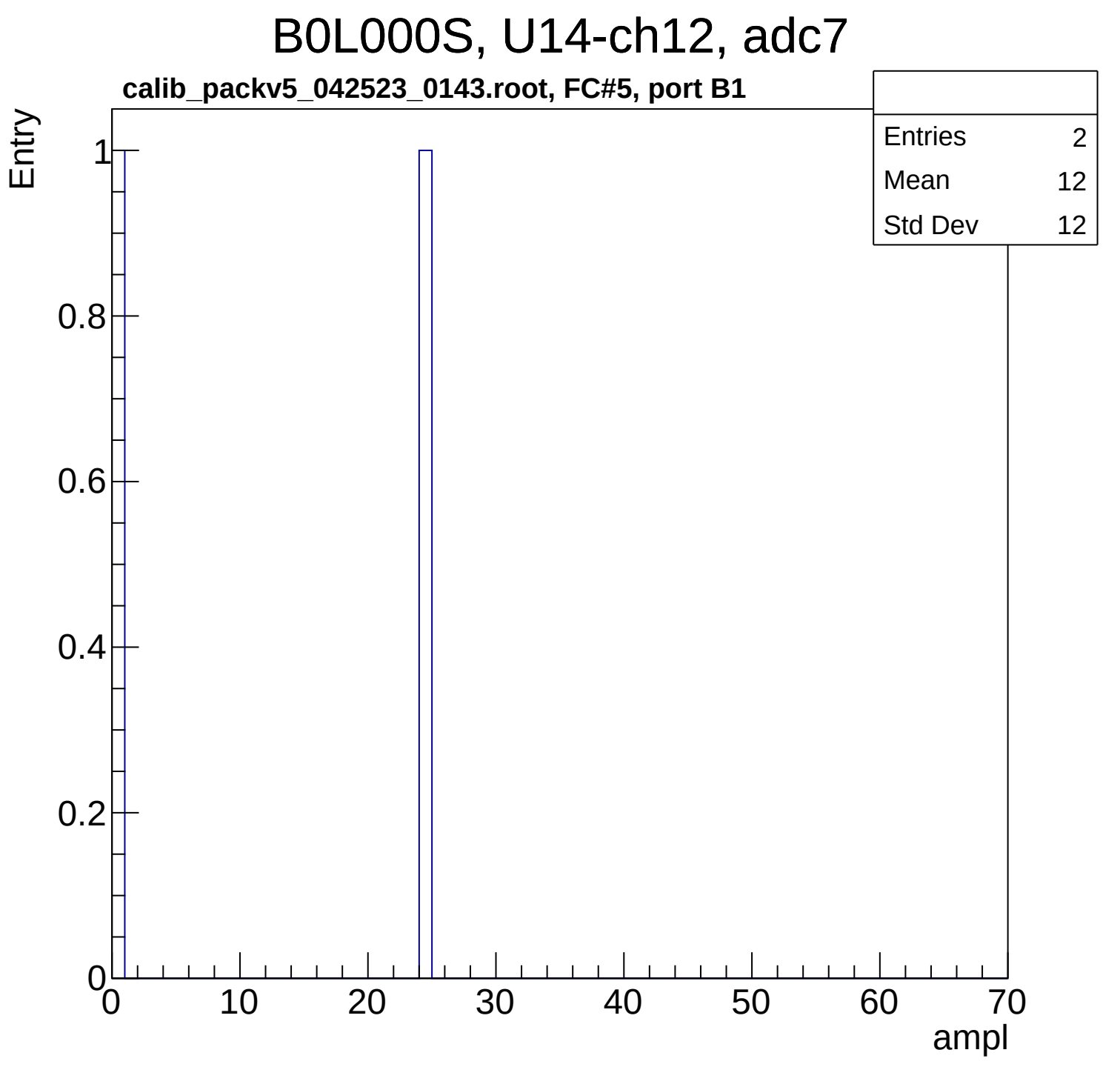
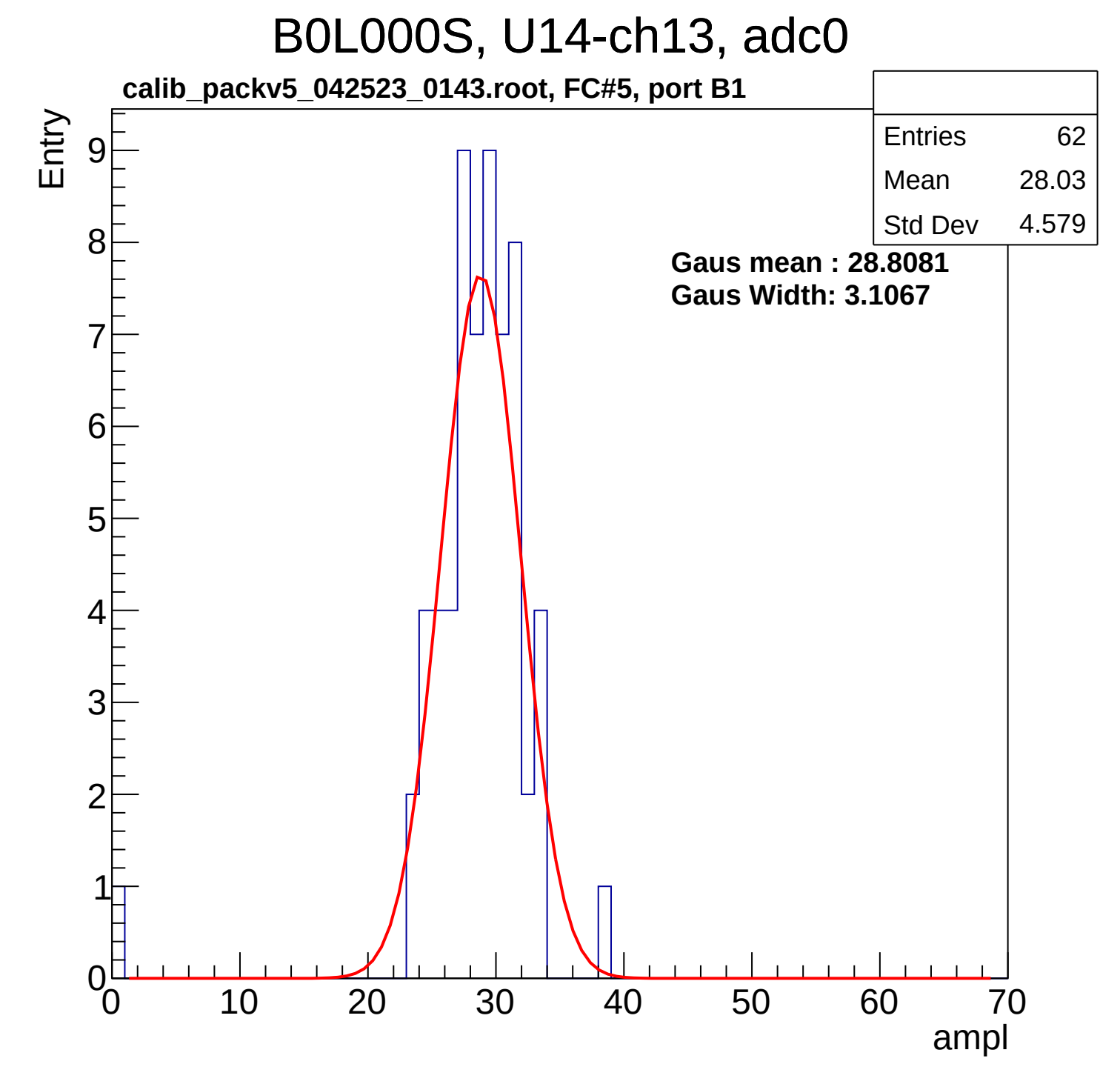

B0L000S, U14-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 28.03
Std Dev 4.579
Gaus mean : 28.8081
Gaus Width: 3.1067
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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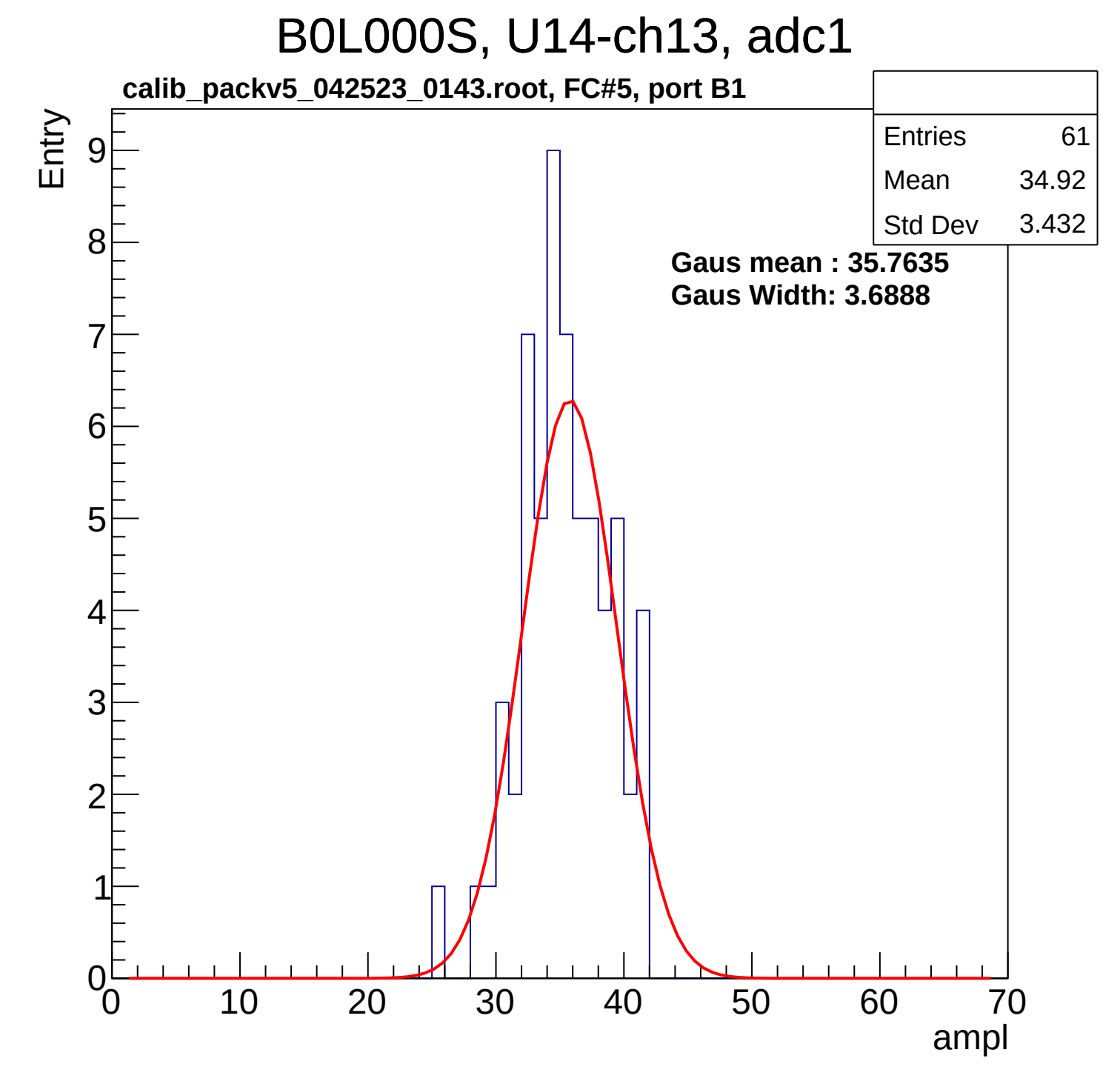
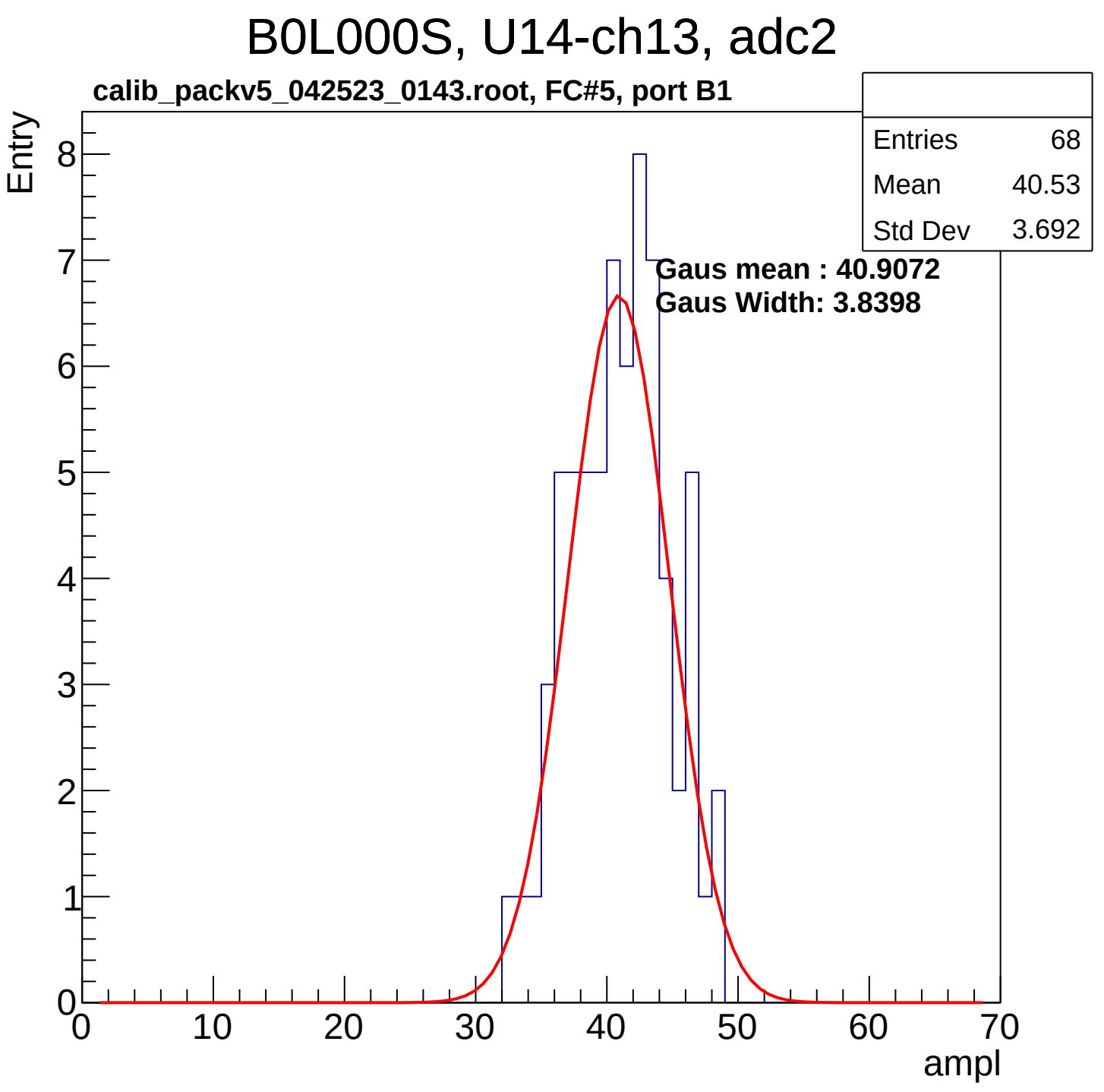
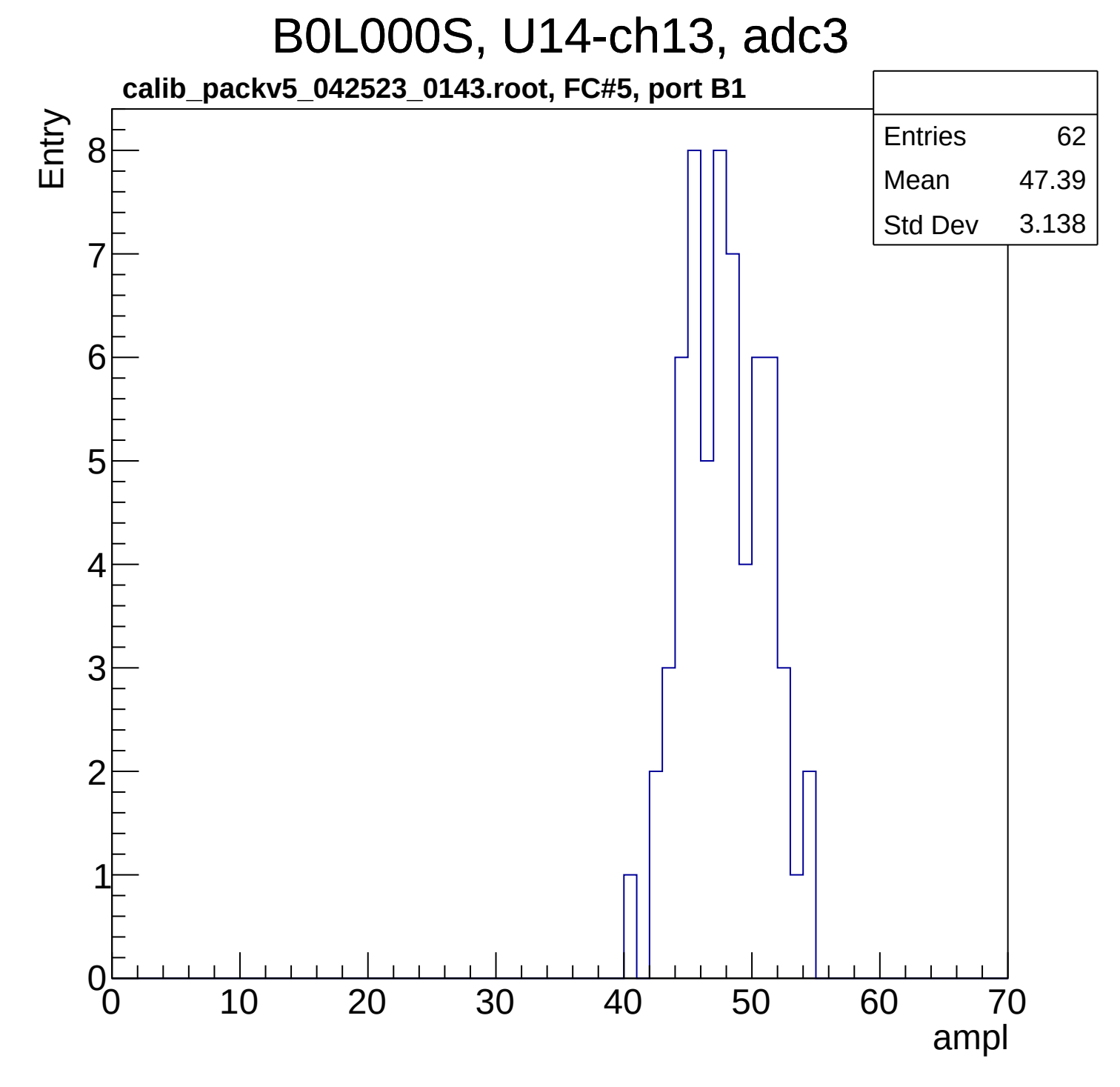
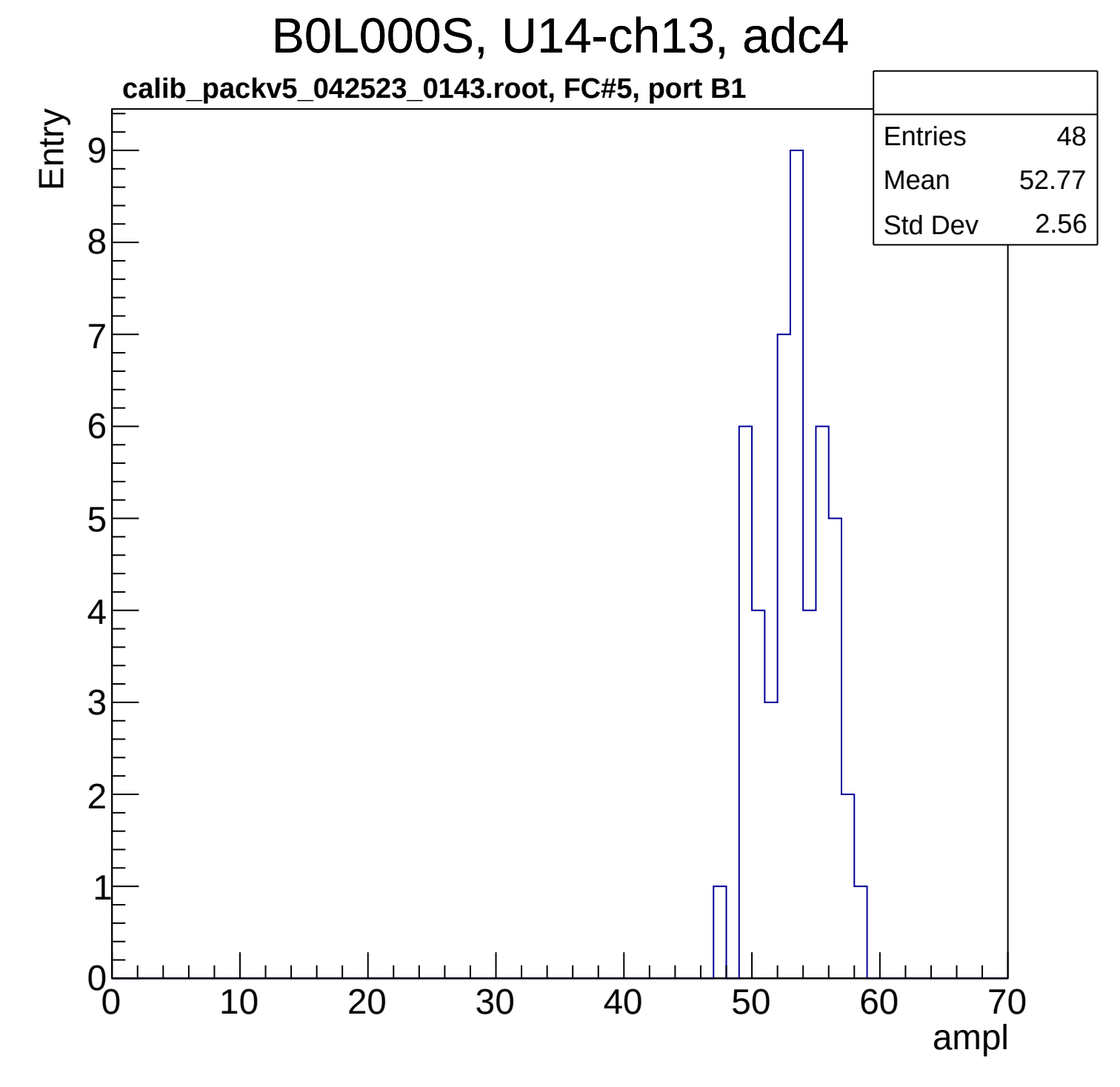
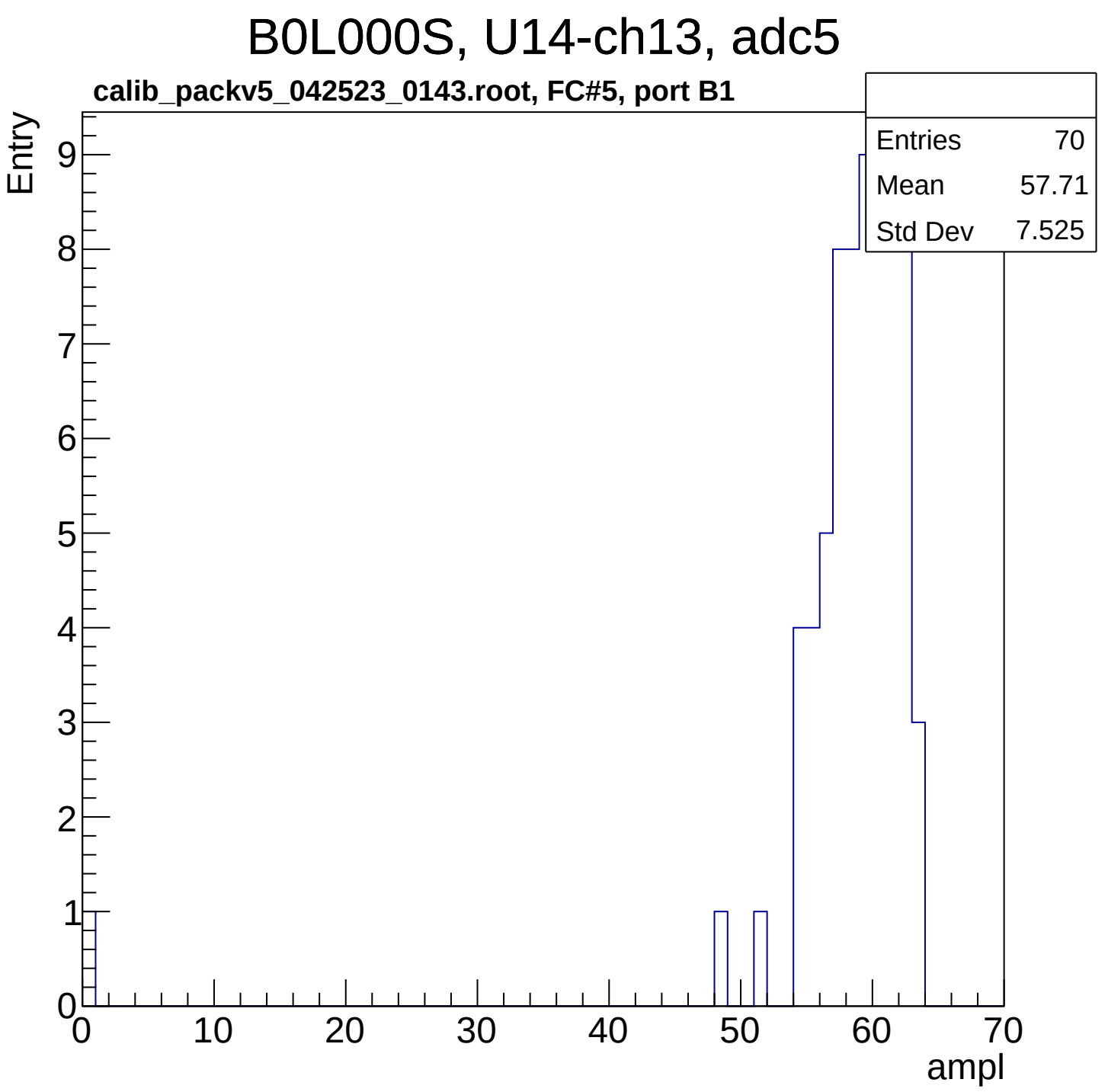

B0L000S, U14-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.92
Std Dev 3.432
Gaus mean : 35.7635
Gaus Width: 3.6888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 40.53
Std Dev 3.692
Gaus mean : 40.9072
Gaus Width: 3.8398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 47.39
Std Dev 3.138
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 52.77
Std Dev 2.56
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 57.71
Std Dev 7.525
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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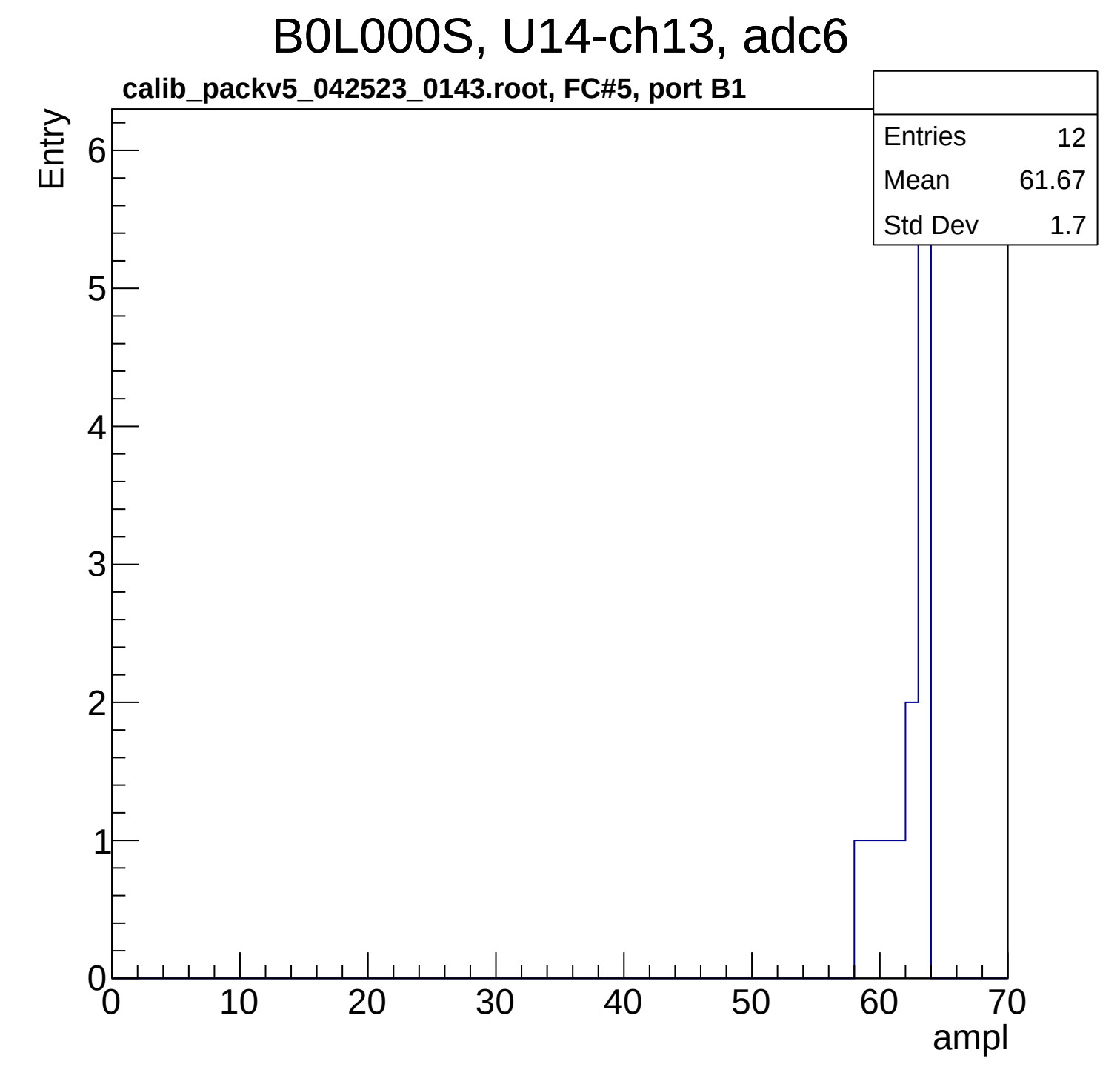
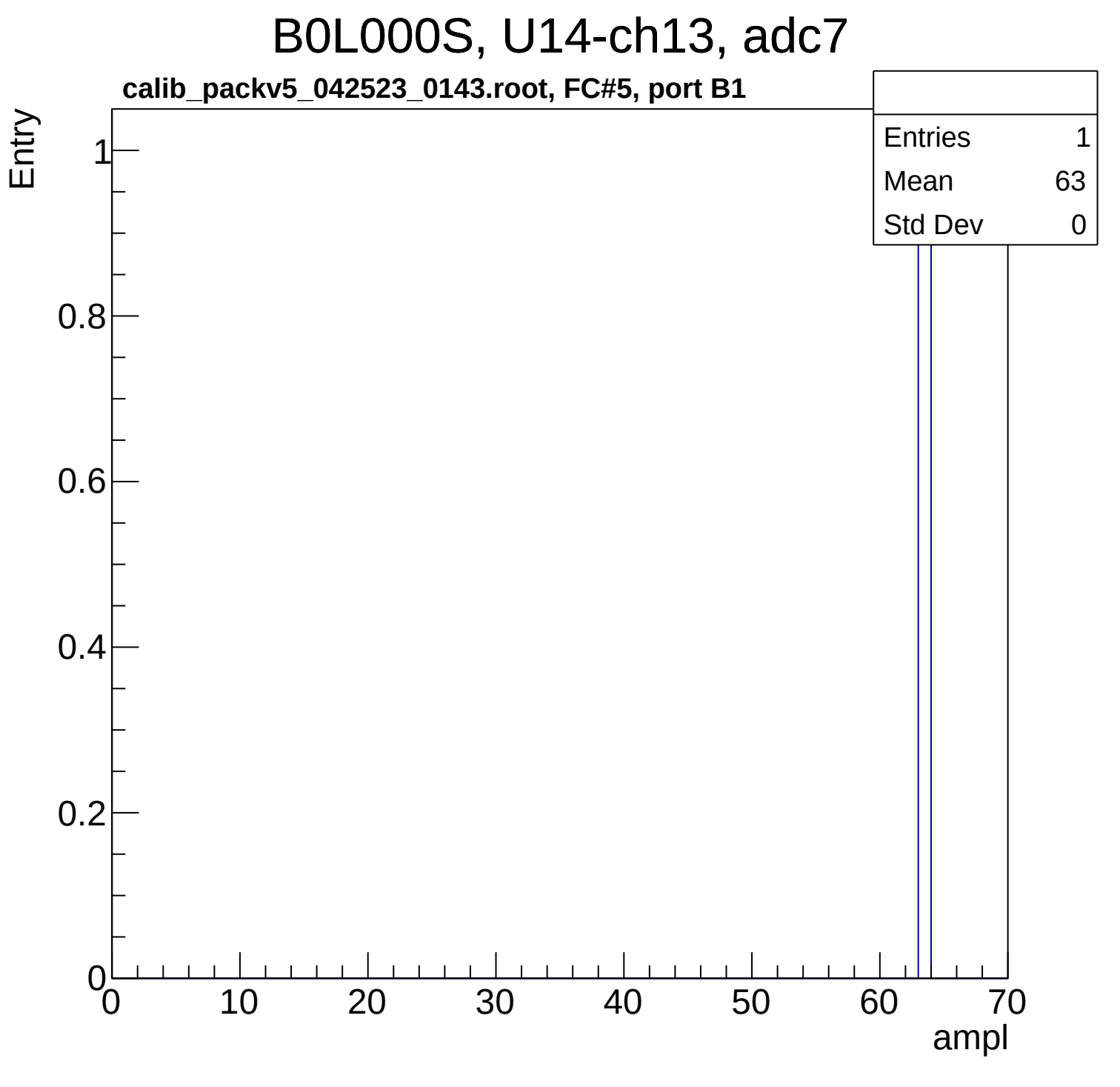
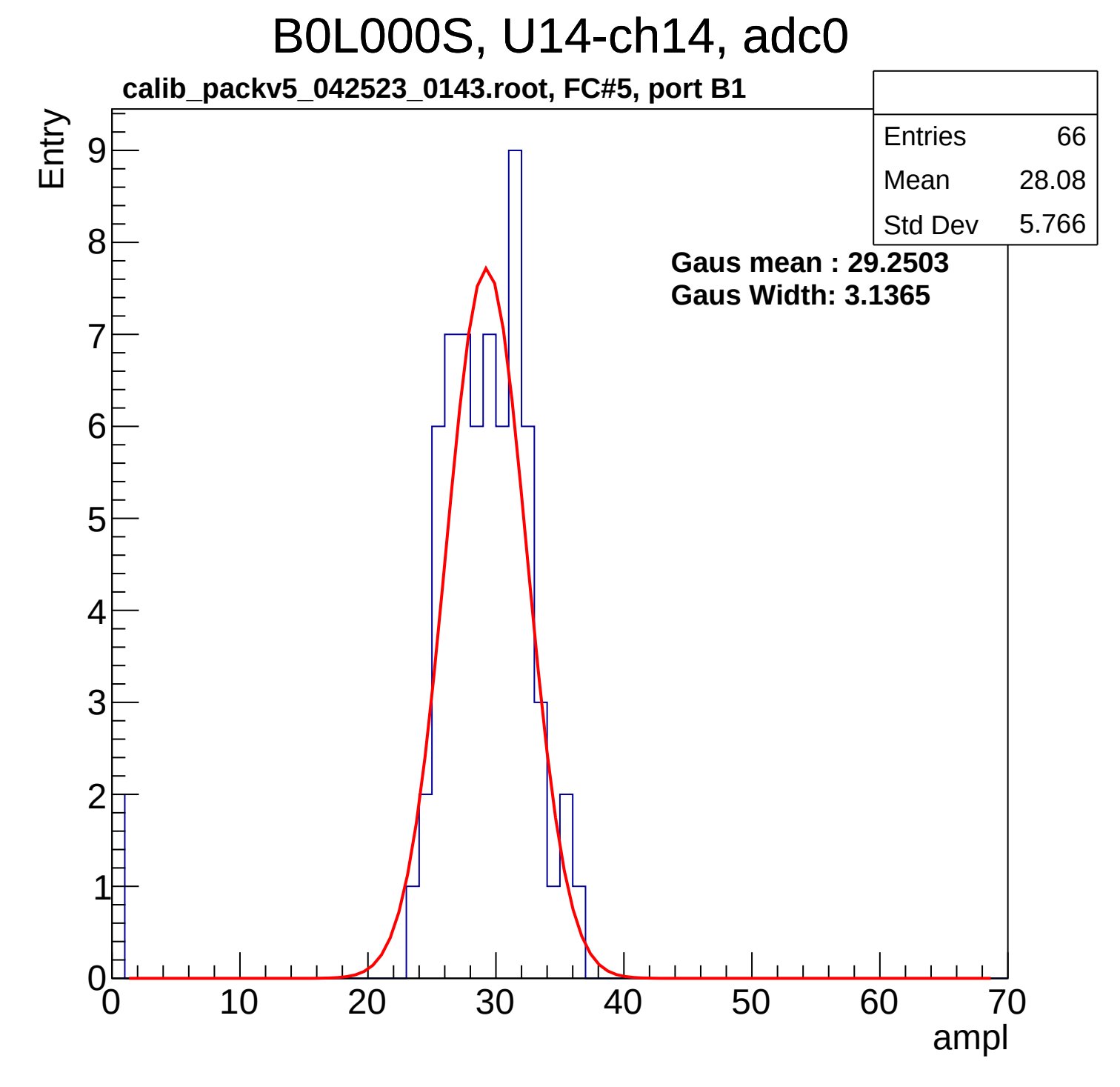
40
50
60
70


B0L000S, U14-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.67
Std Dev 1.7
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.08
Std Dev 5.766
Gaus mean : 29.2503
Gaus Width: 3.1365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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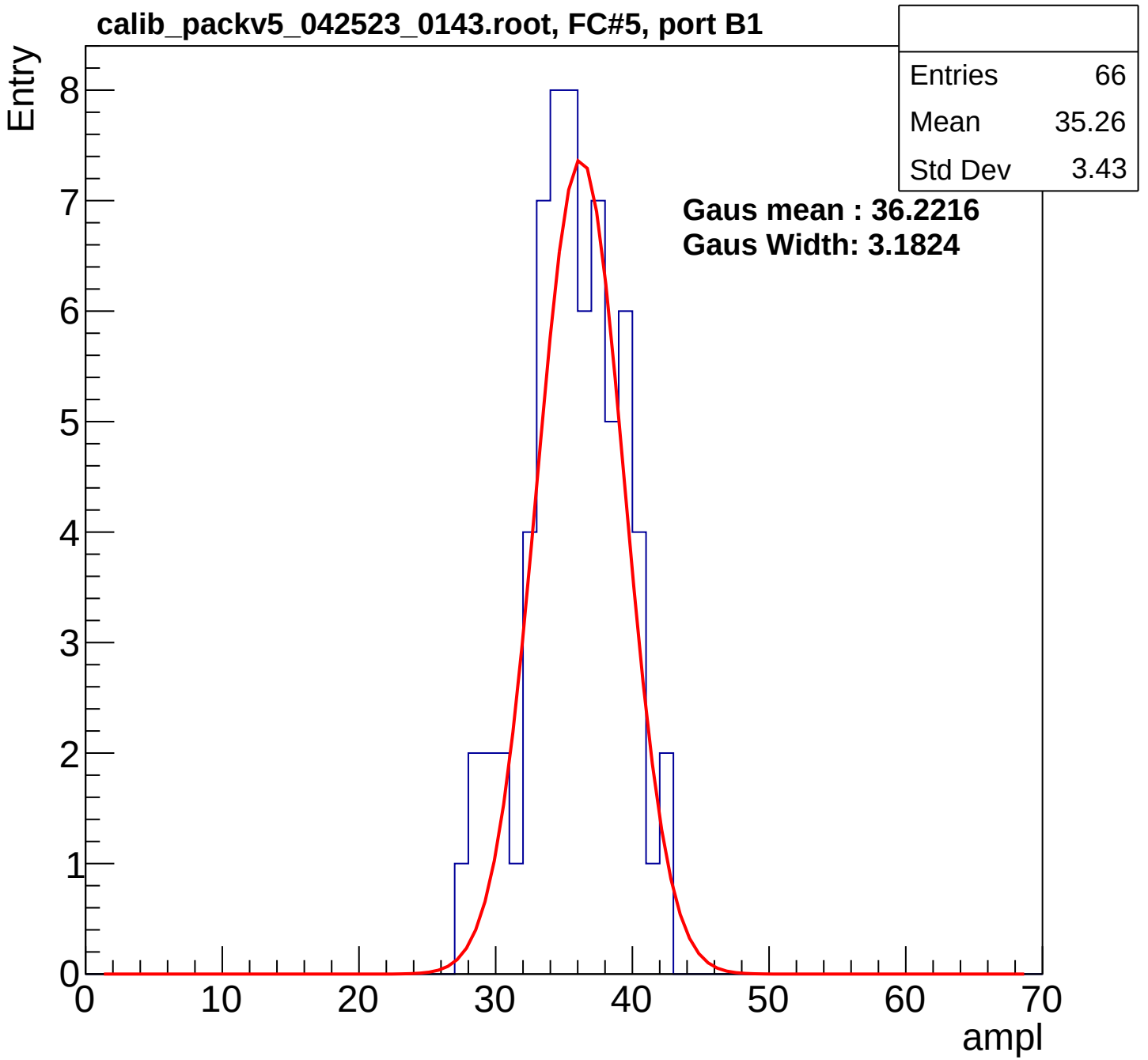
B0L000S, U14-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.26
Std Dev 3.43
Gaus mean : 36.2216
Gaus Width: 3.1824
Entry
ampl
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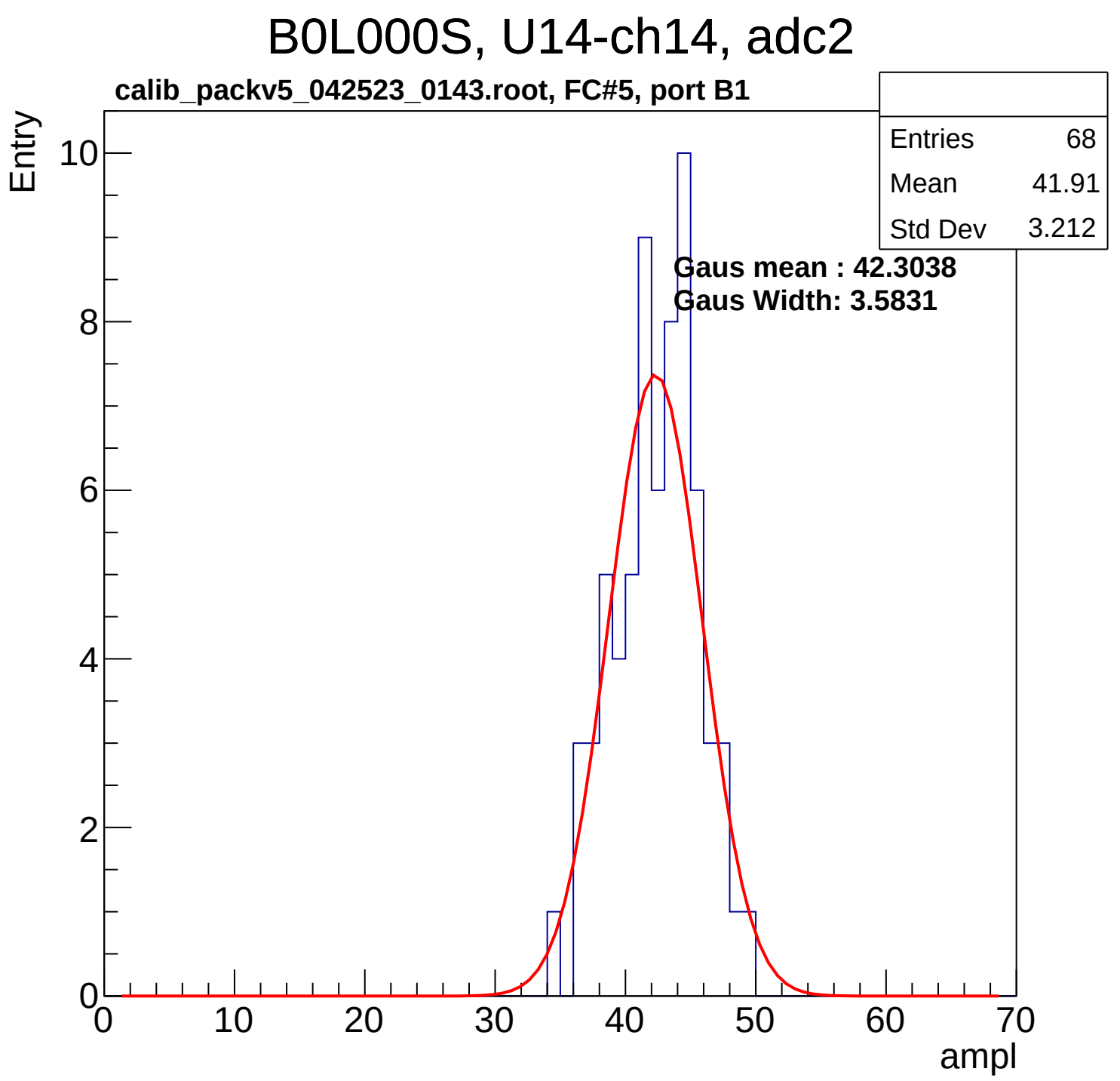
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 41.91
Std Dev 3.212
Gaus mean : 42.3038
Gaus Width: 3.5831
Entry
ampl
0
2
4
6
8
10
0
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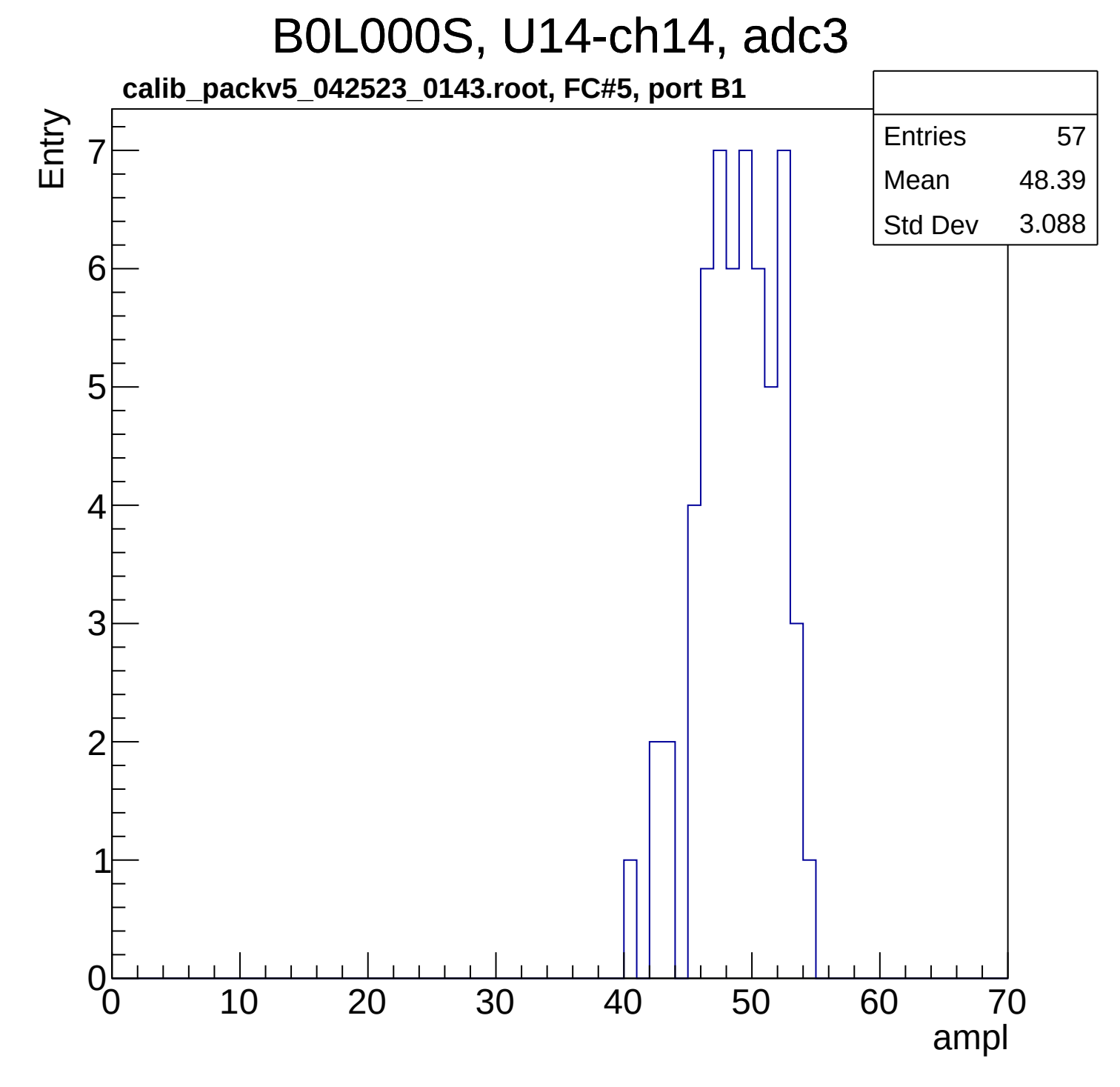
20
30
40
50
60
70


B0L000S, U14-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 48.39
Std Dev 3.088
Entry
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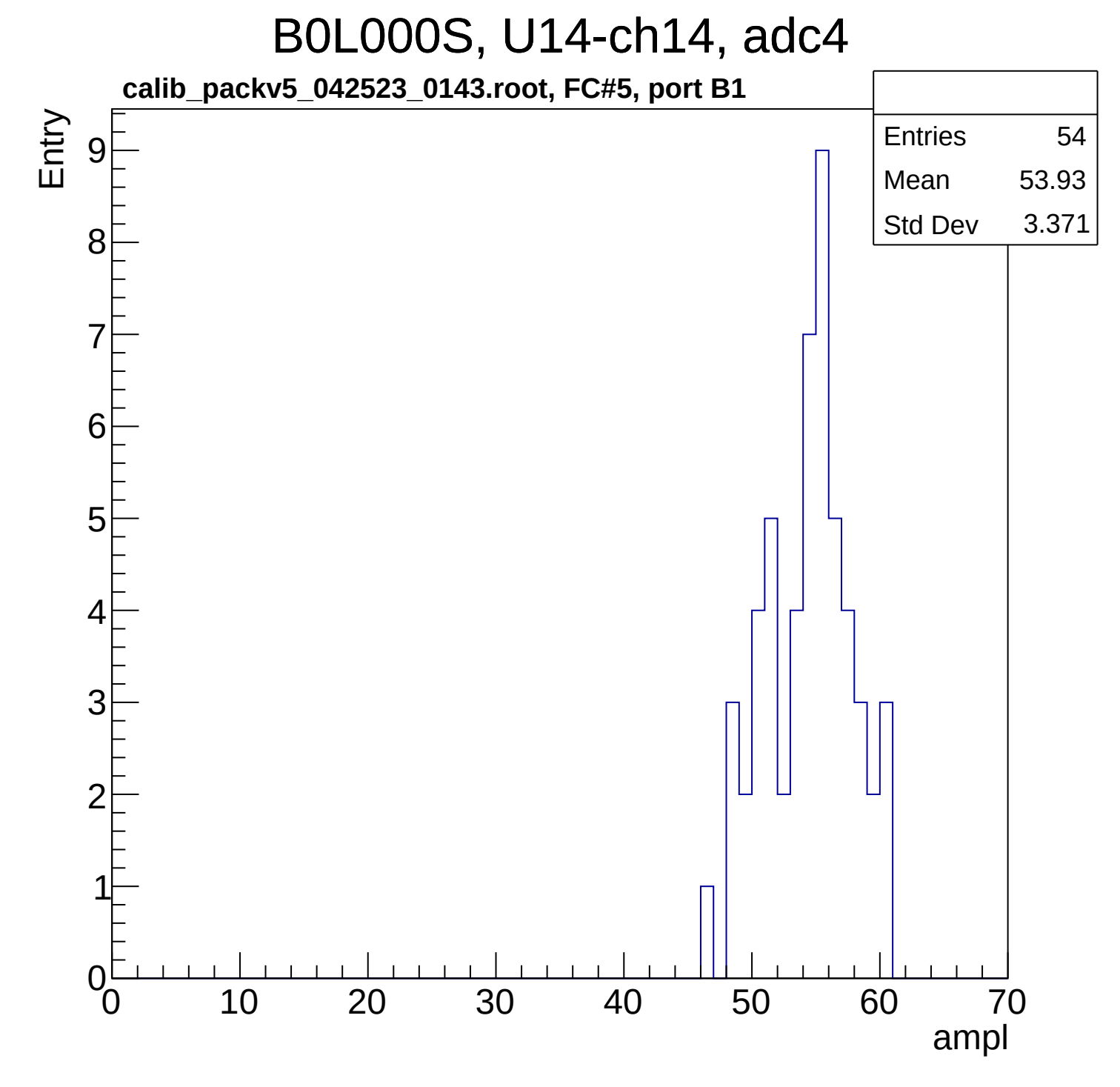
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.93
Std Dev 3.371
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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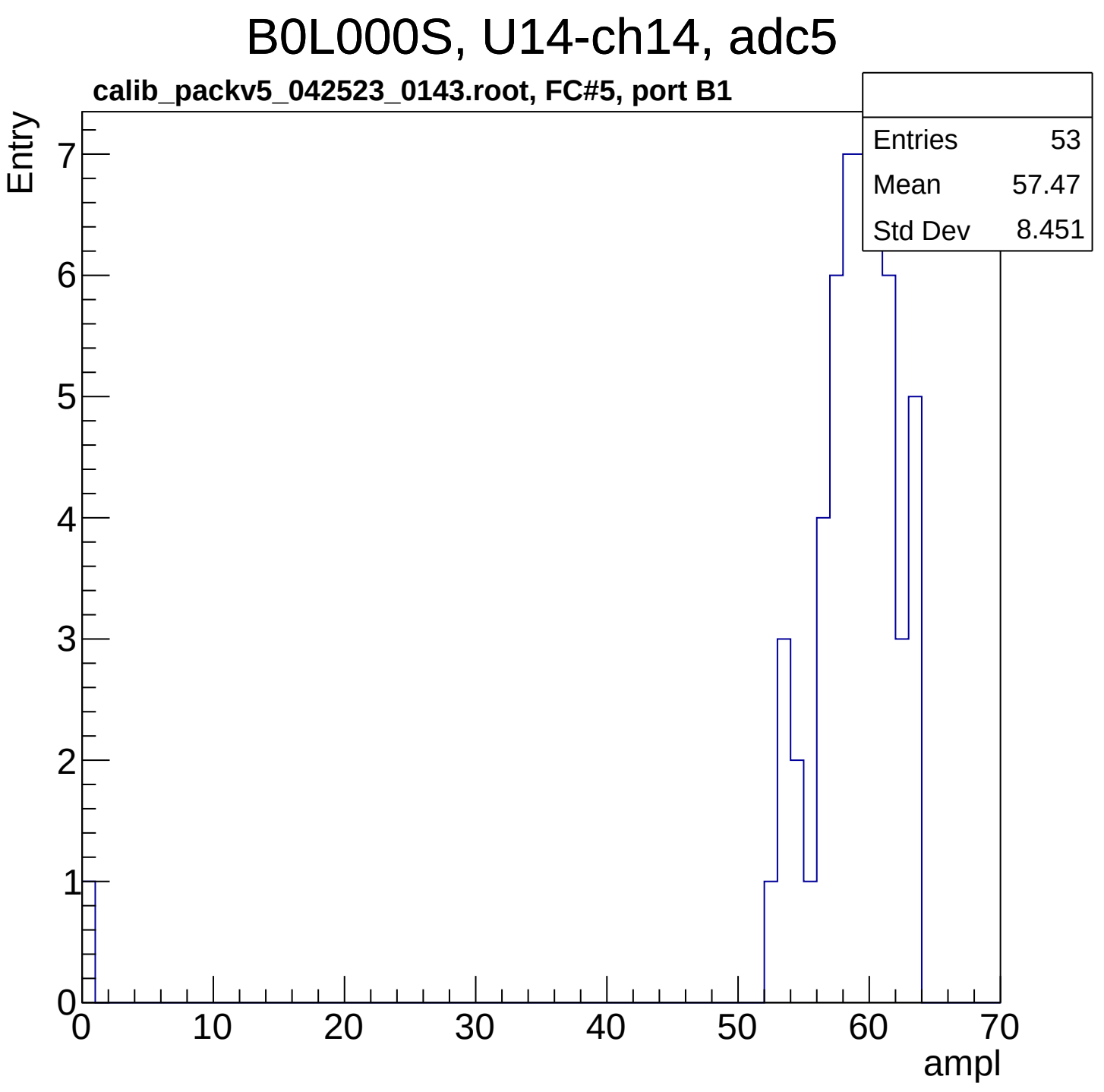
B0L000S, U14-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.47
Std Dev 8.451
Entry
ampl
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2
3
4
5
6
7
0
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20
30
40
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60
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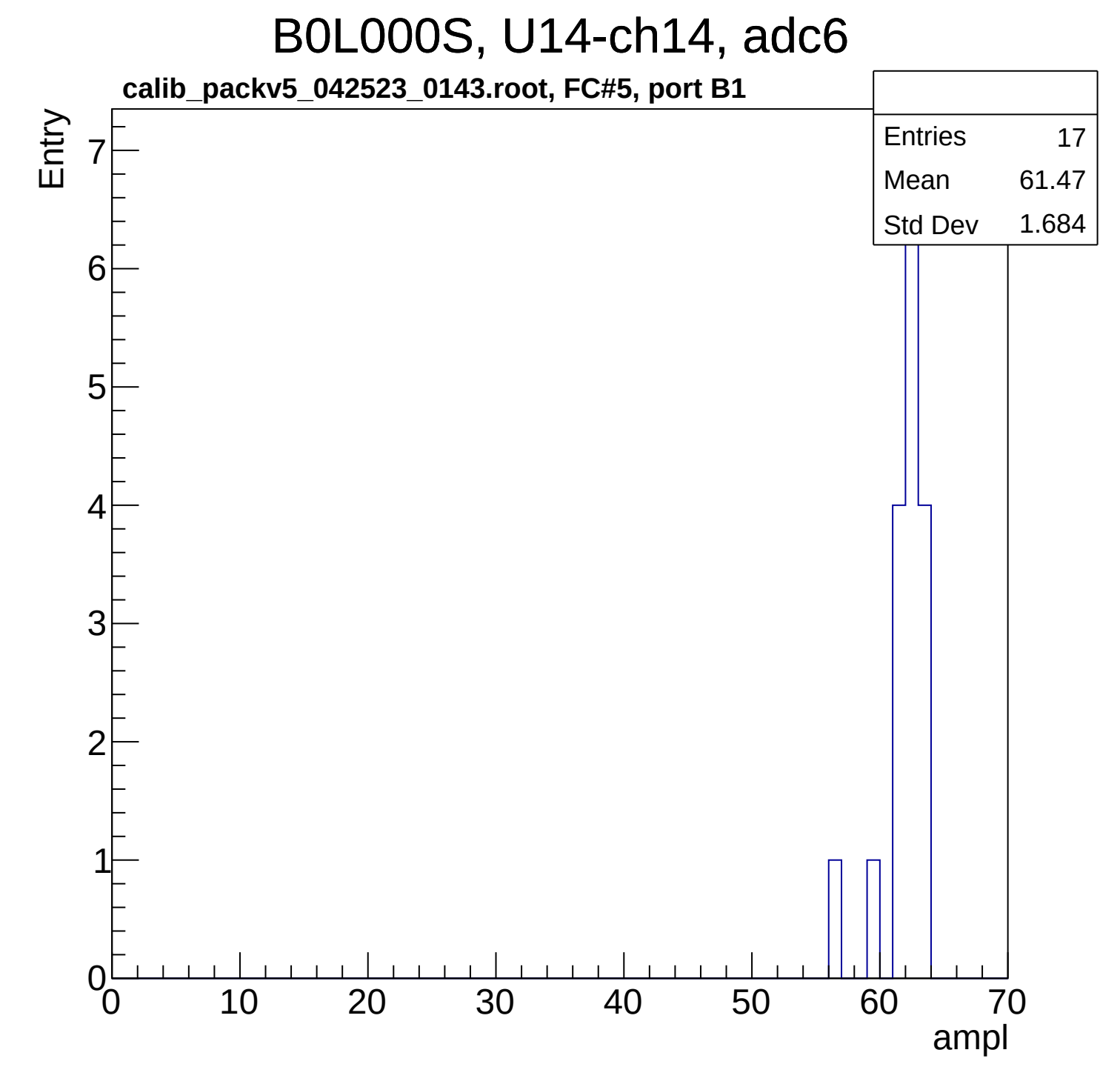
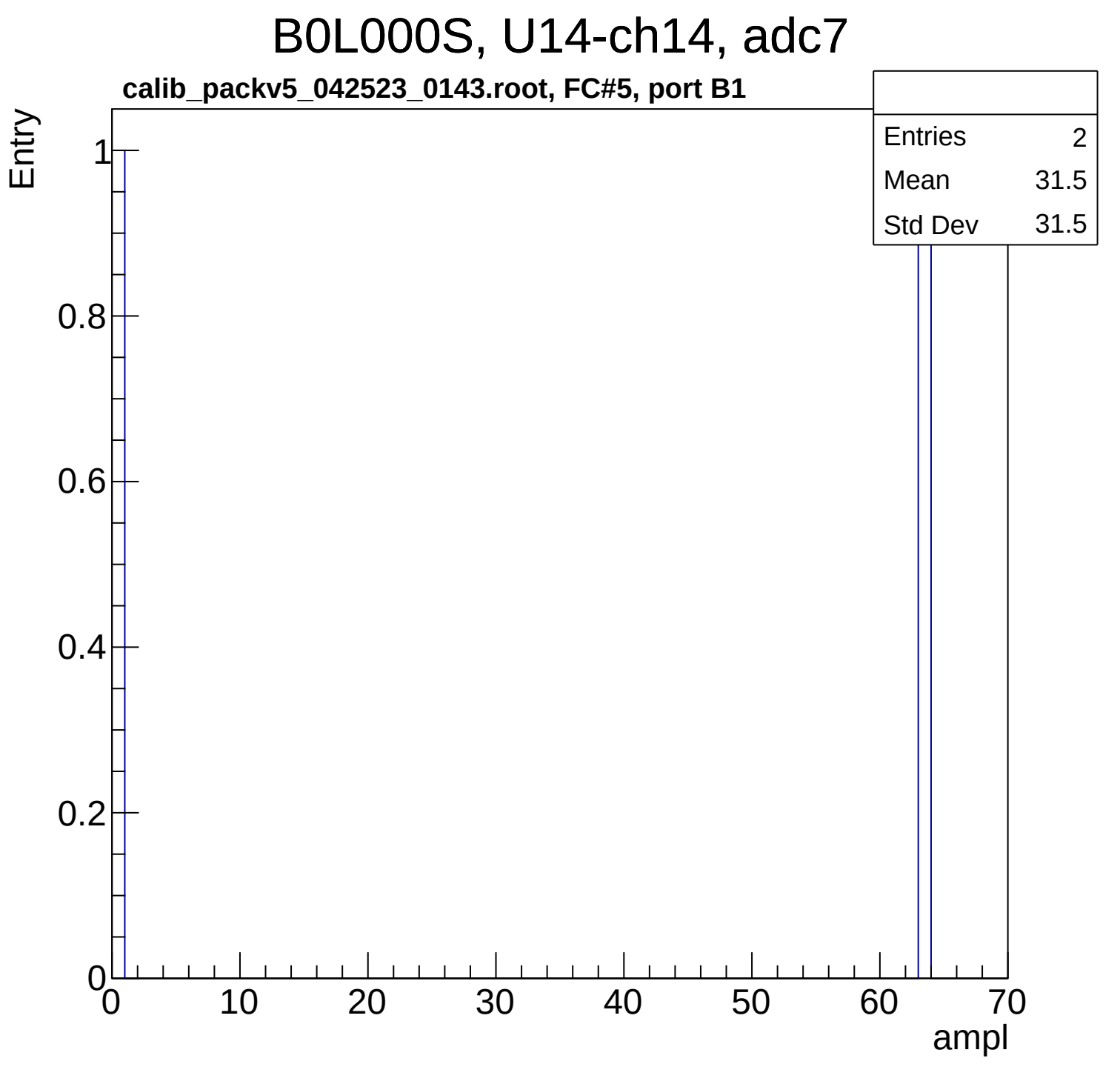

B0L000S, U14-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.47
Std Dev 1.684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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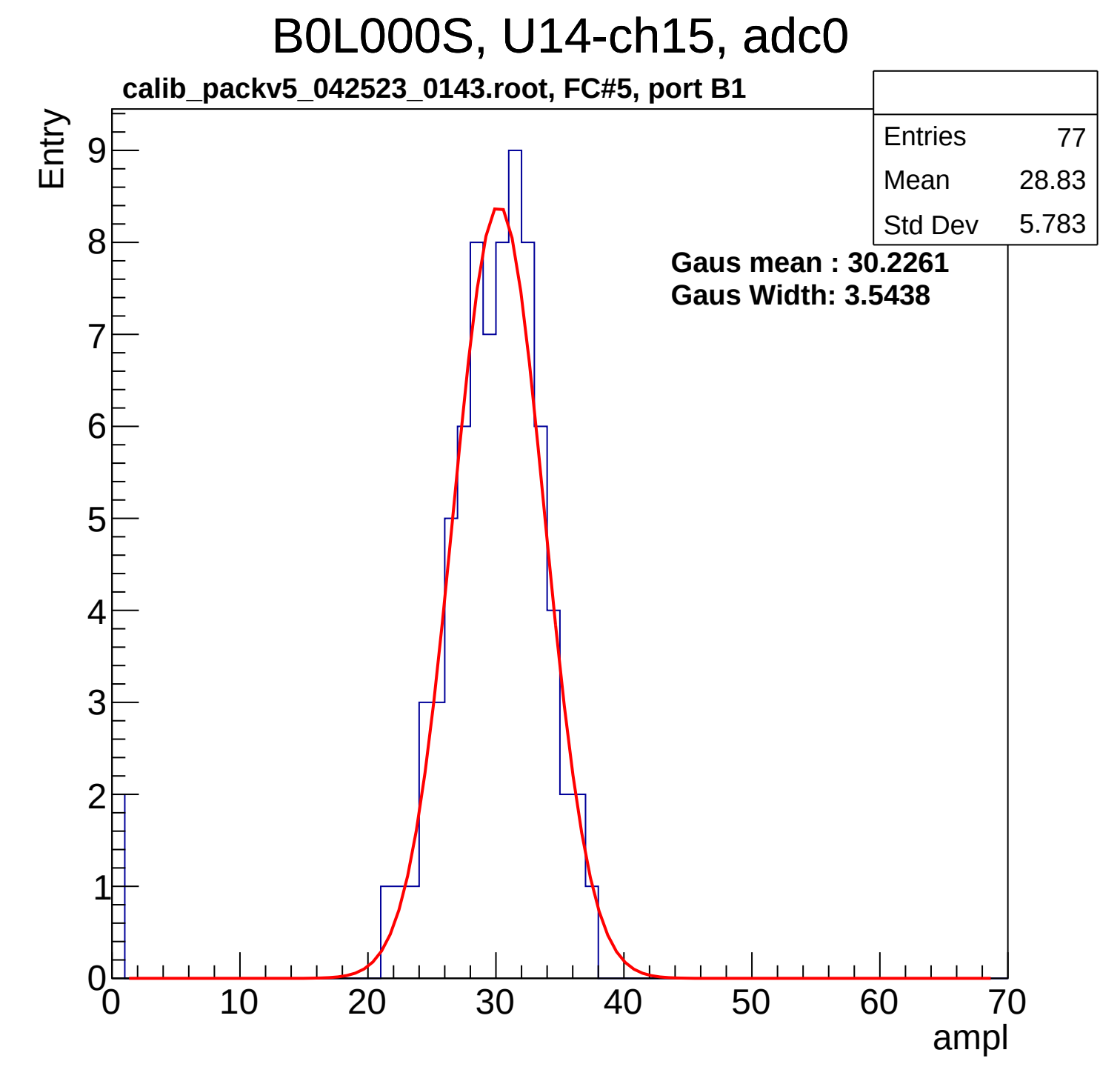
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.83
Std Dev 5.783
Gaus mean : 30.2261
Gaus Width: 3.5438
Entry
ampl
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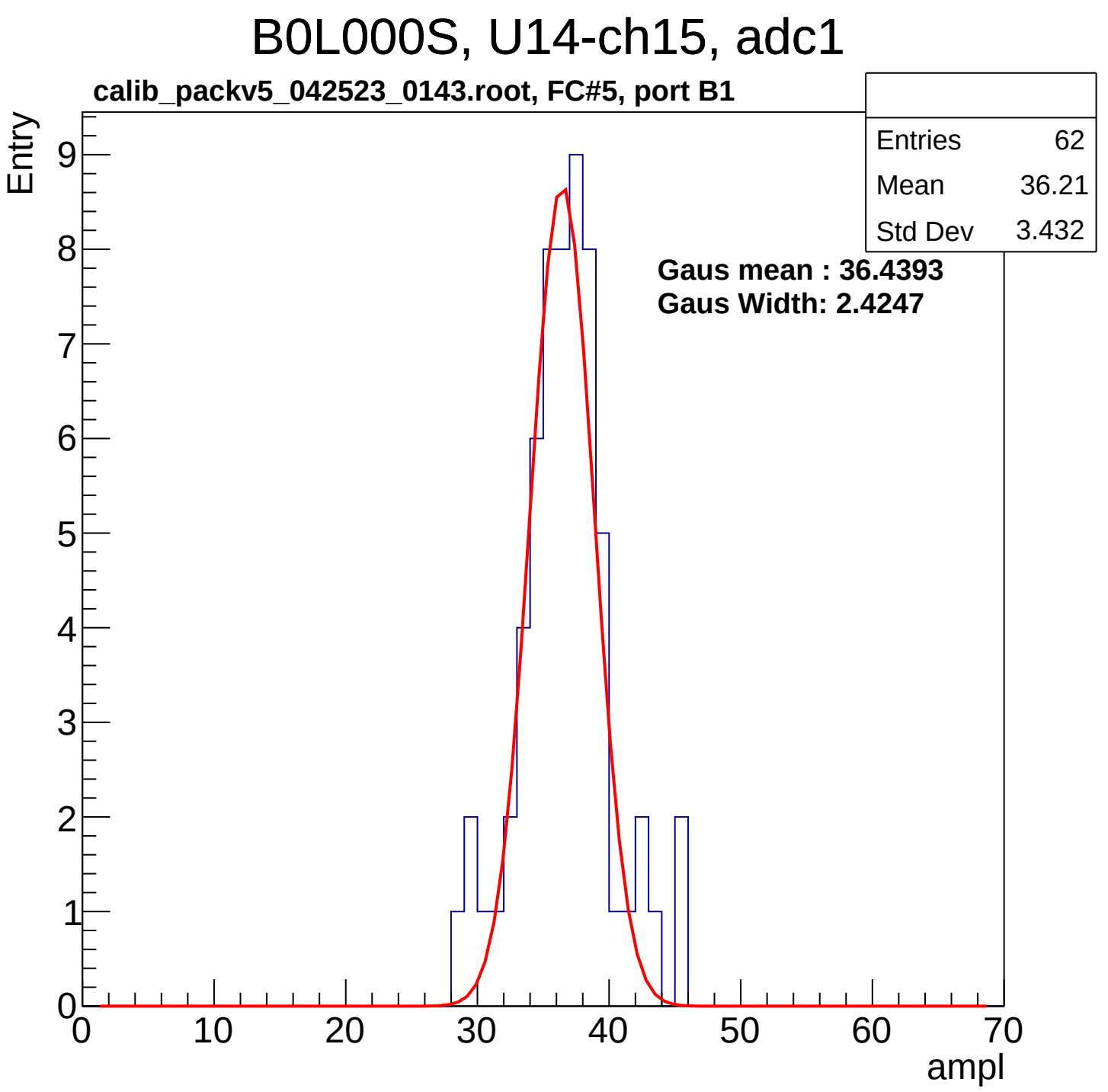
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.21
Std Dev 3.432
Gaus mean : 36.4393
Gaus Width: 2.4247
Entry
ampl
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2
3
4
5
6
7
8
9
0
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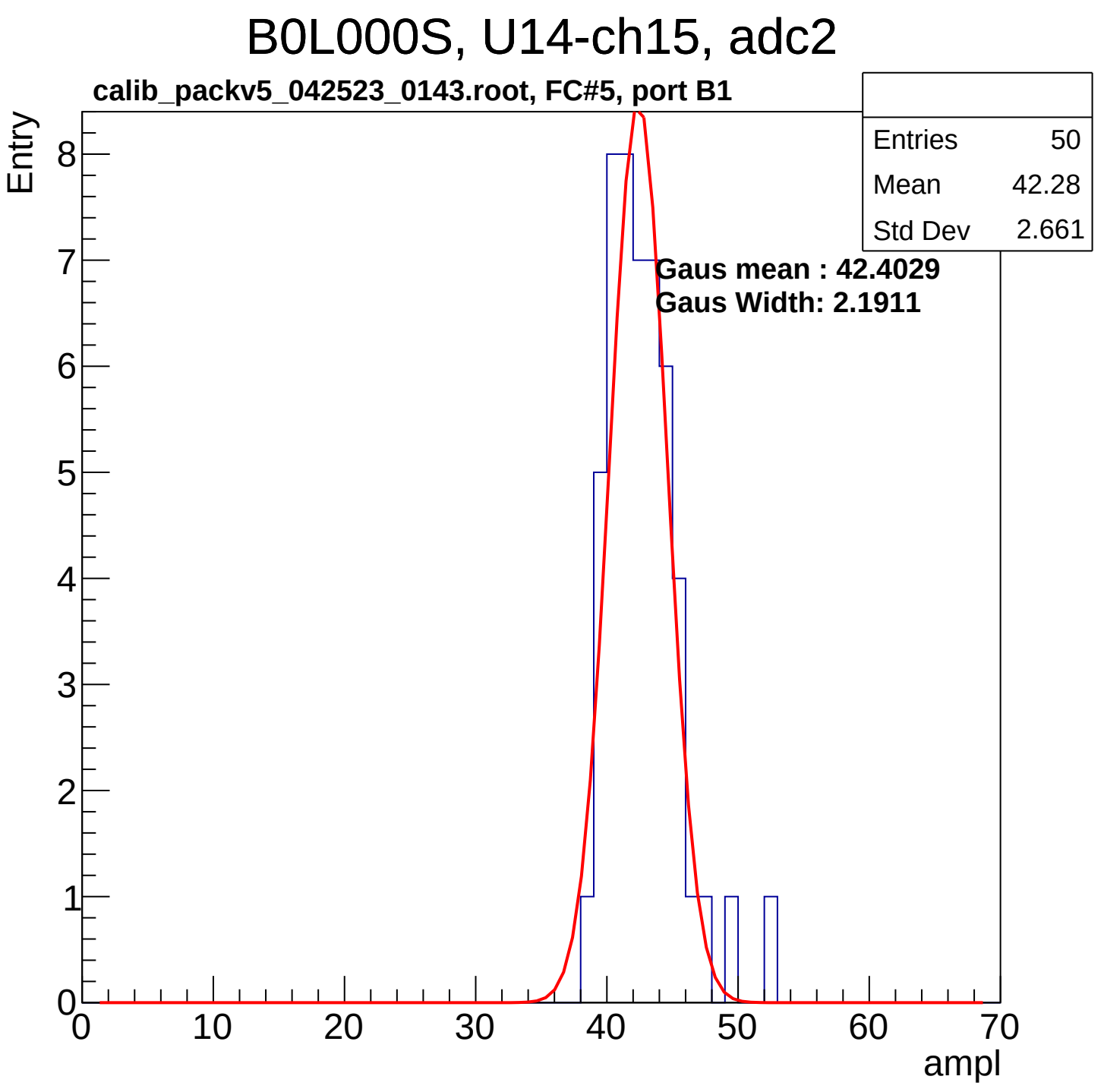
B0L000S, U14-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 42.28
Std Dev 2.661
Gaus mean : 42.4029
Gaus Width: 2.1911
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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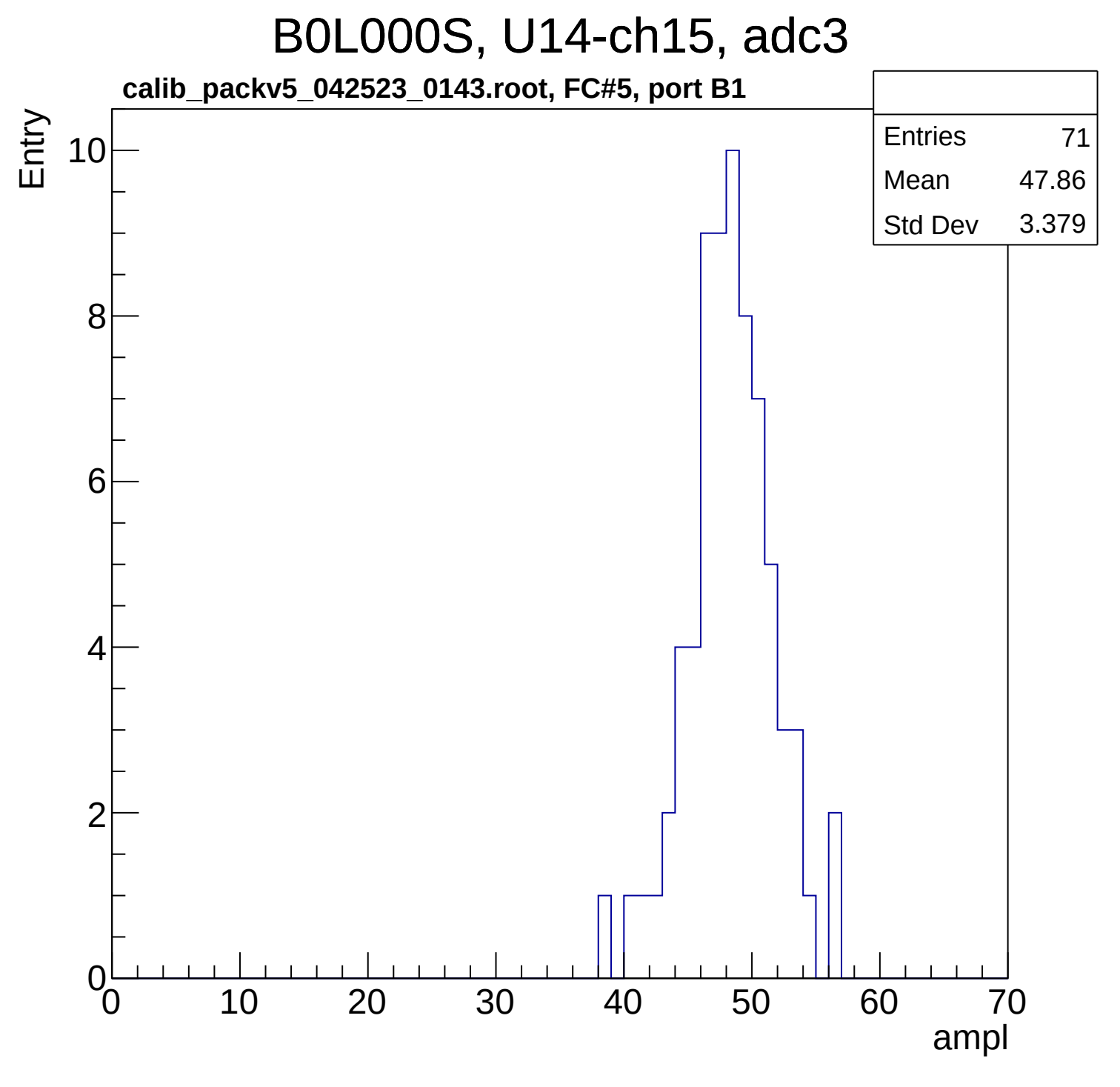
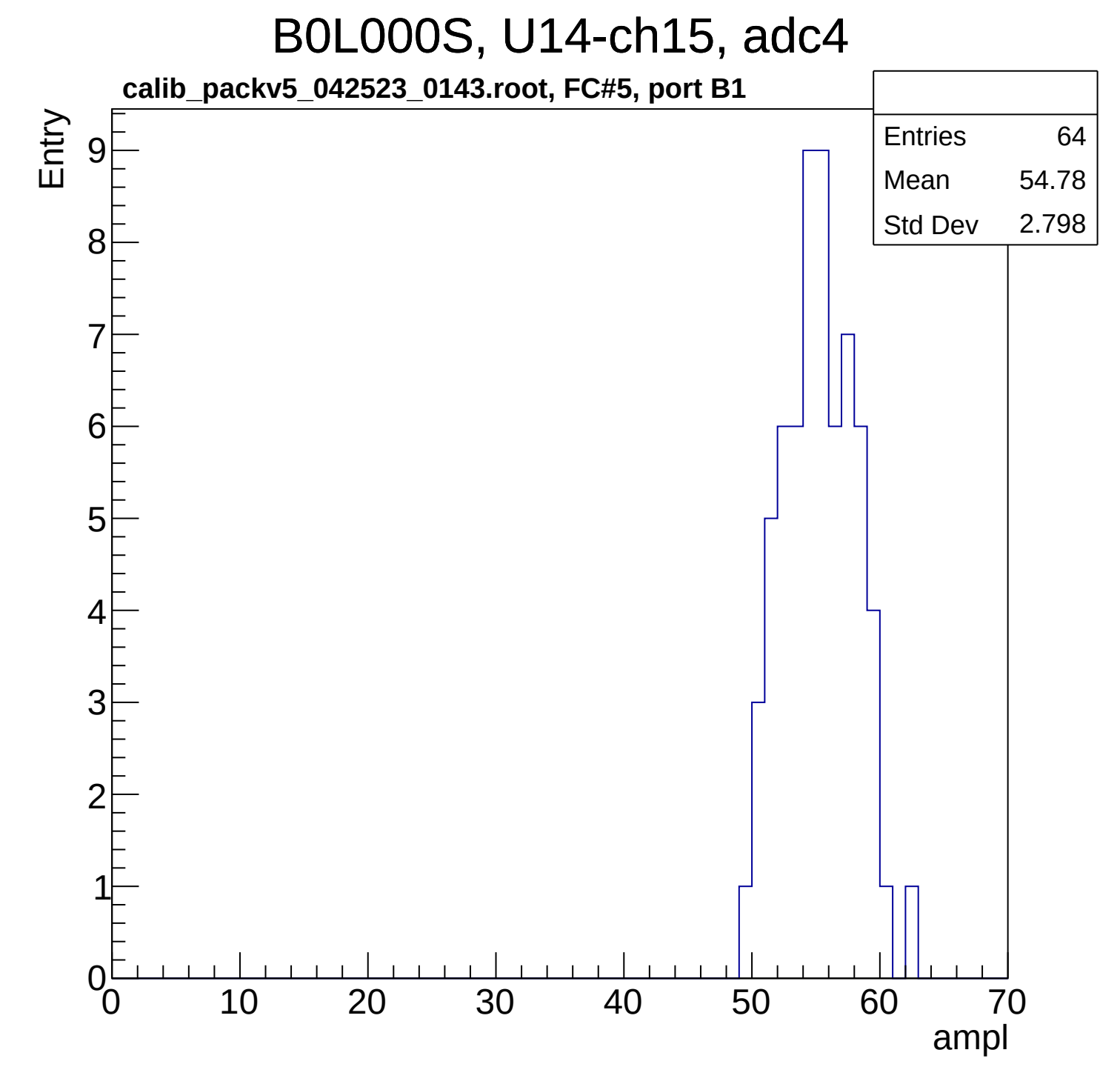

B0L000S, U14-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 47.86
Std Dev 3.379
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 54.78
Std Dev 2.798
Entry
ampl
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3
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5
6
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8
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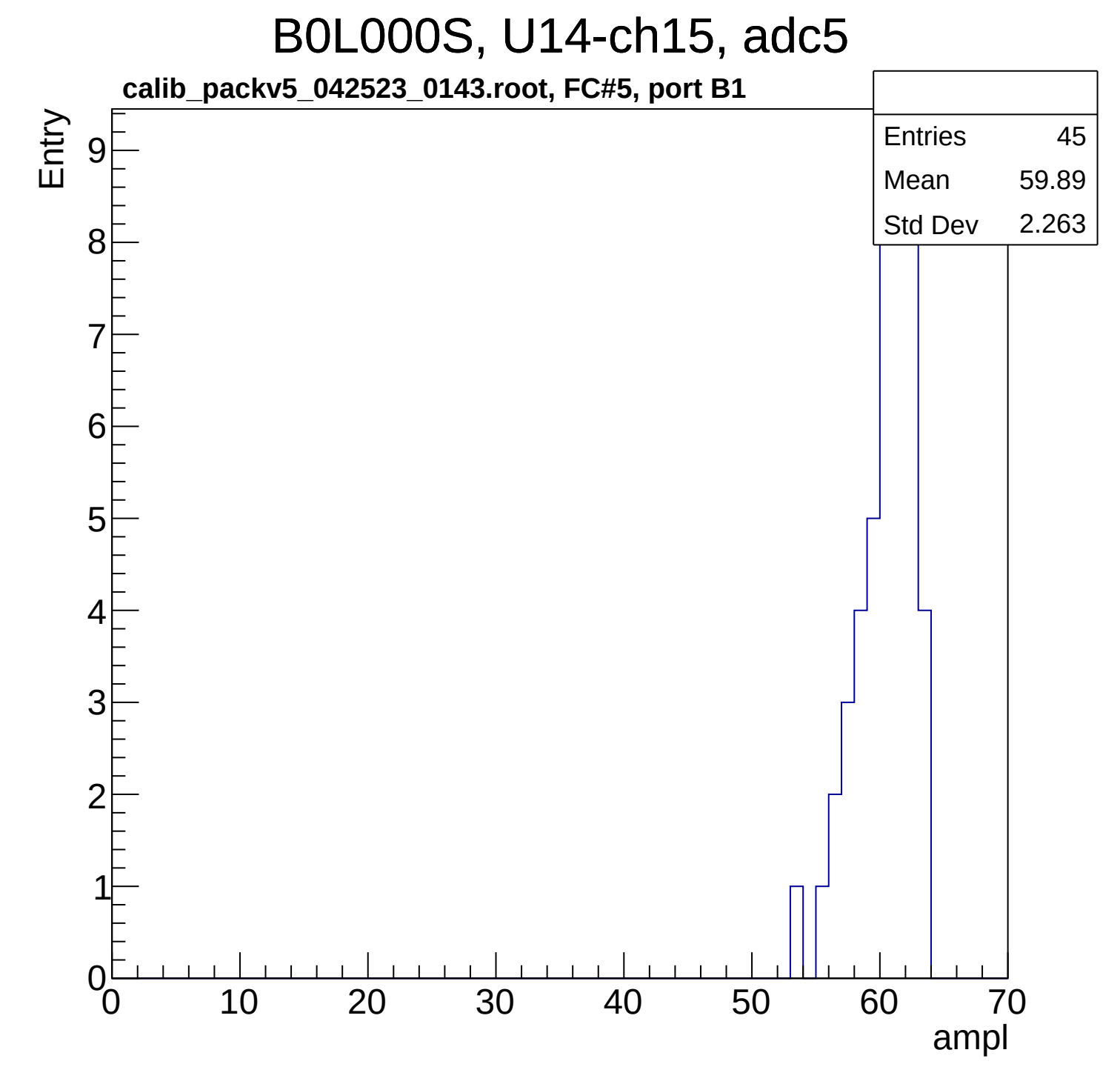
B0L000S, U14-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.89
Std Dev 2.263
Entry
ampl
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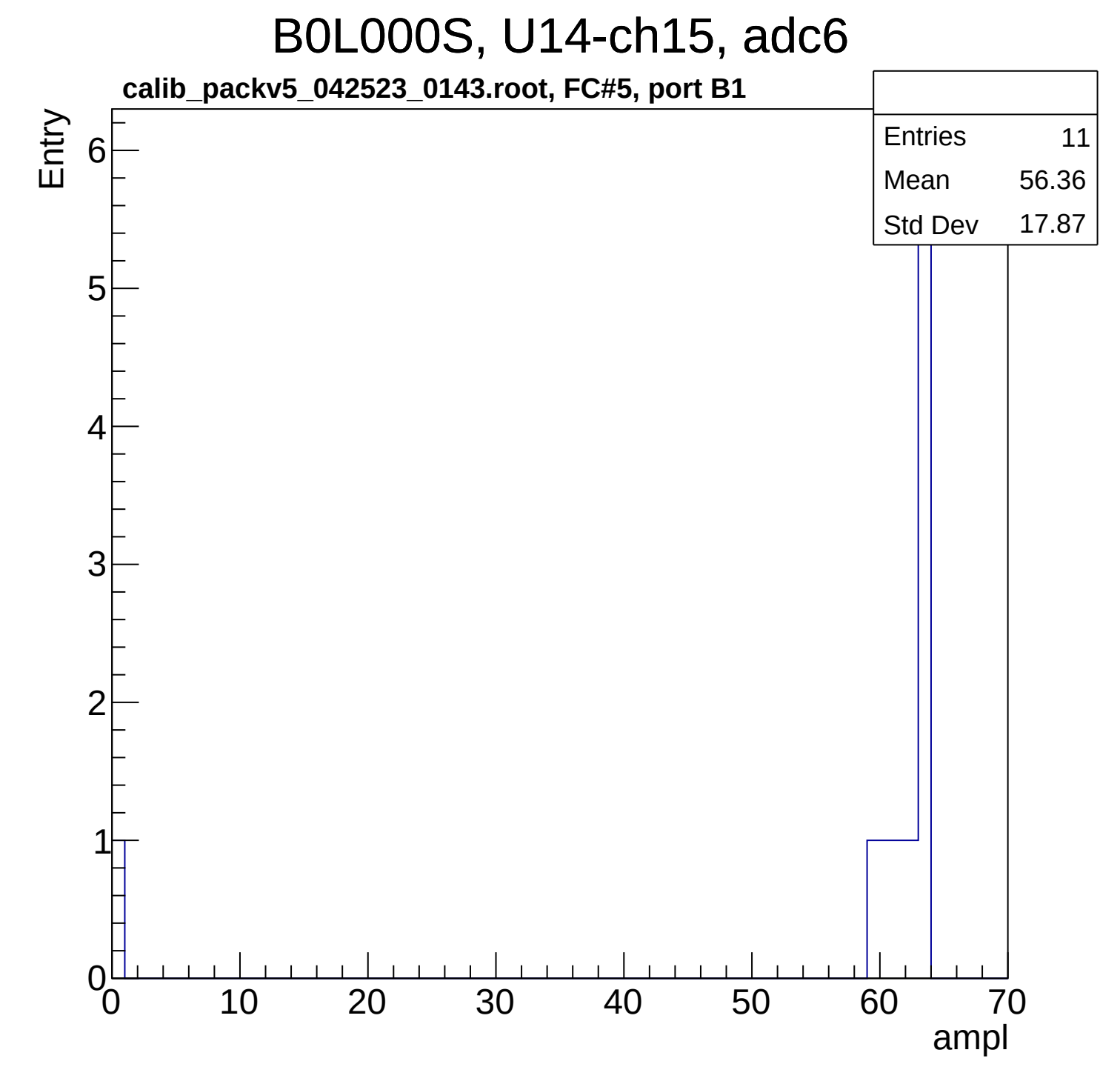
B0L000S, U14-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.36
Std Dev 17.87
Entry
ampl
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2
3
4
5
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0
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40
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60
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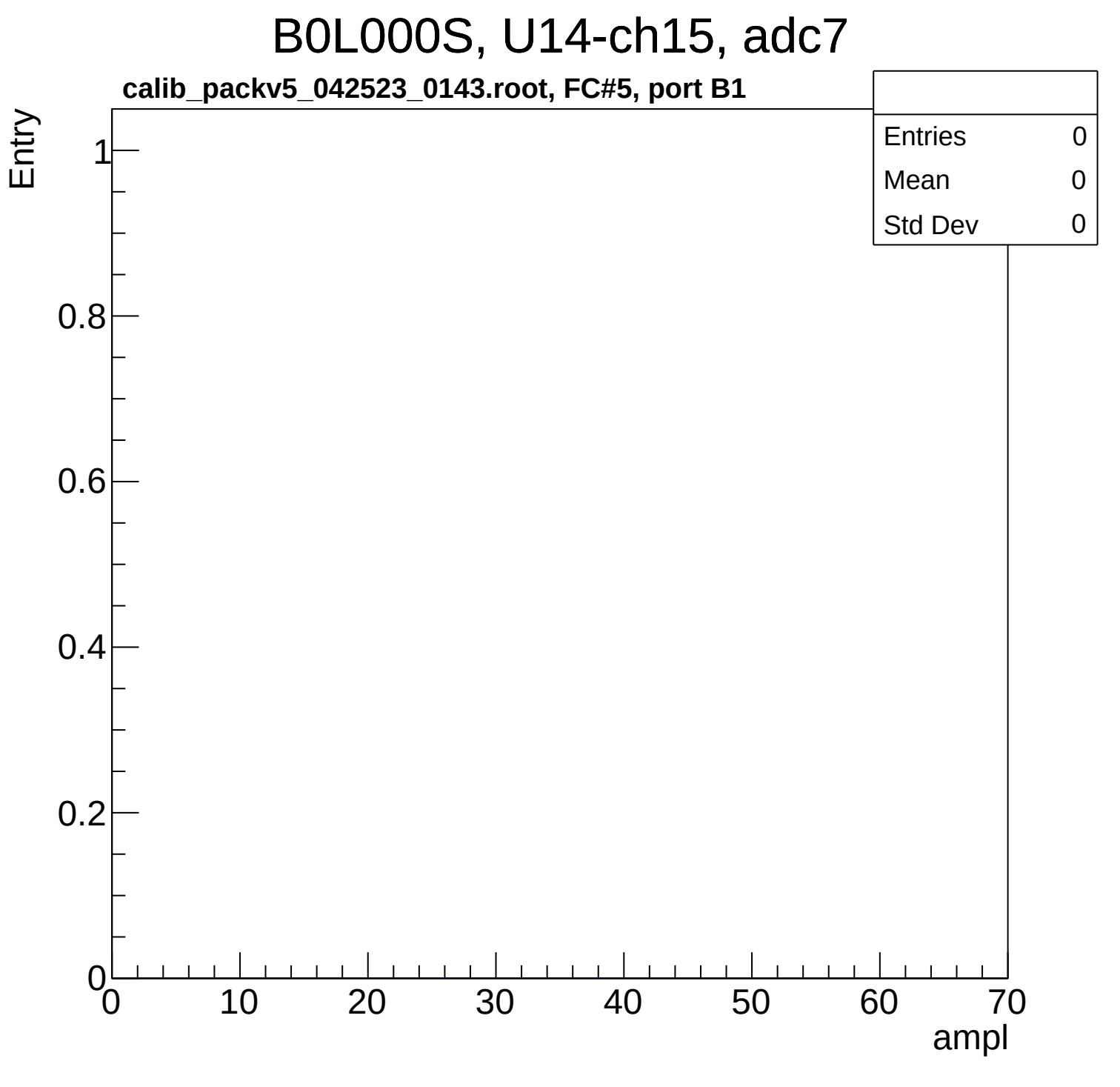
B0L000S, U14-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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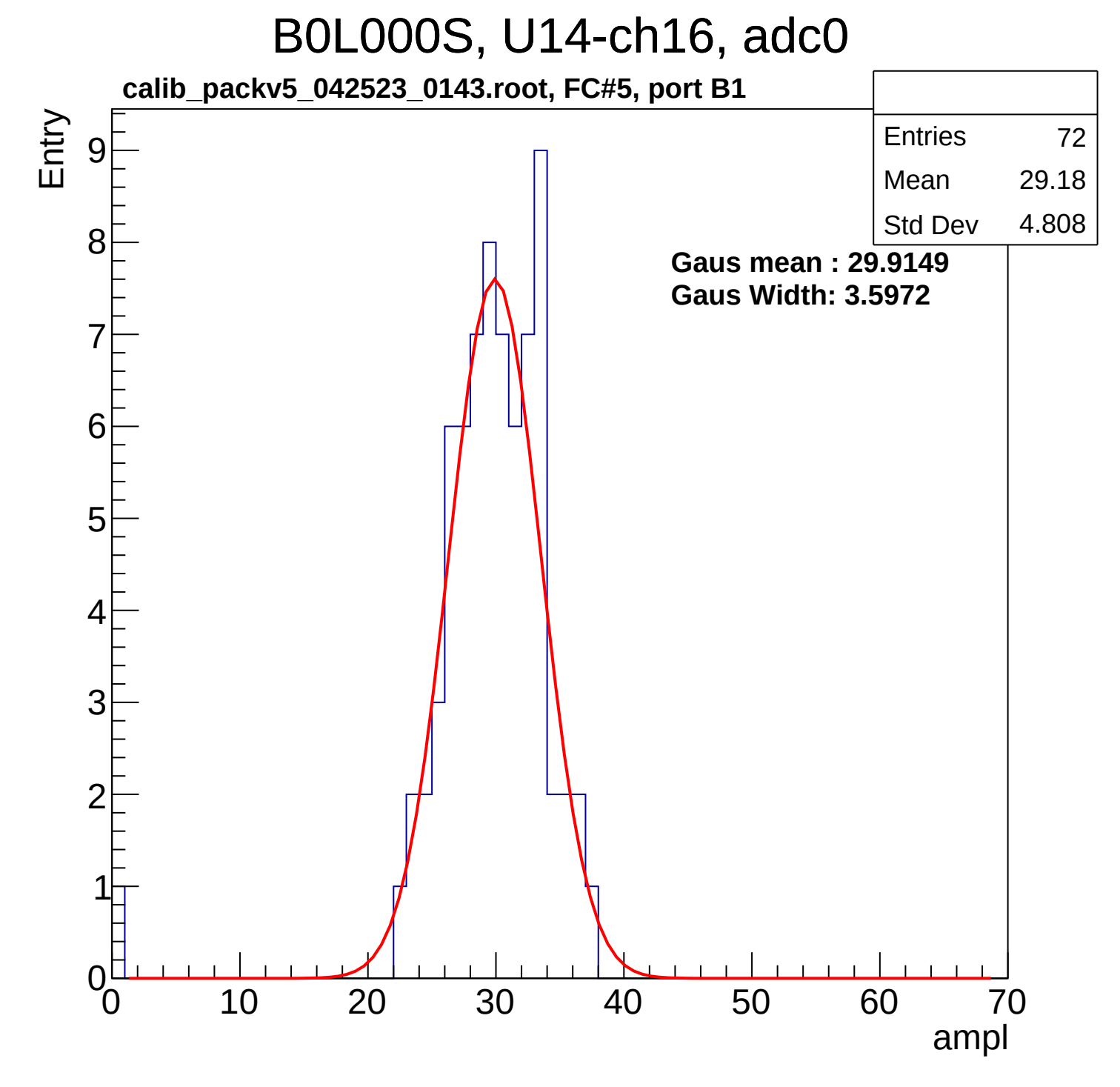
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.18
Std Dev 4.808
Gaus mean : 29.9149
Gaus Width: 3.5972
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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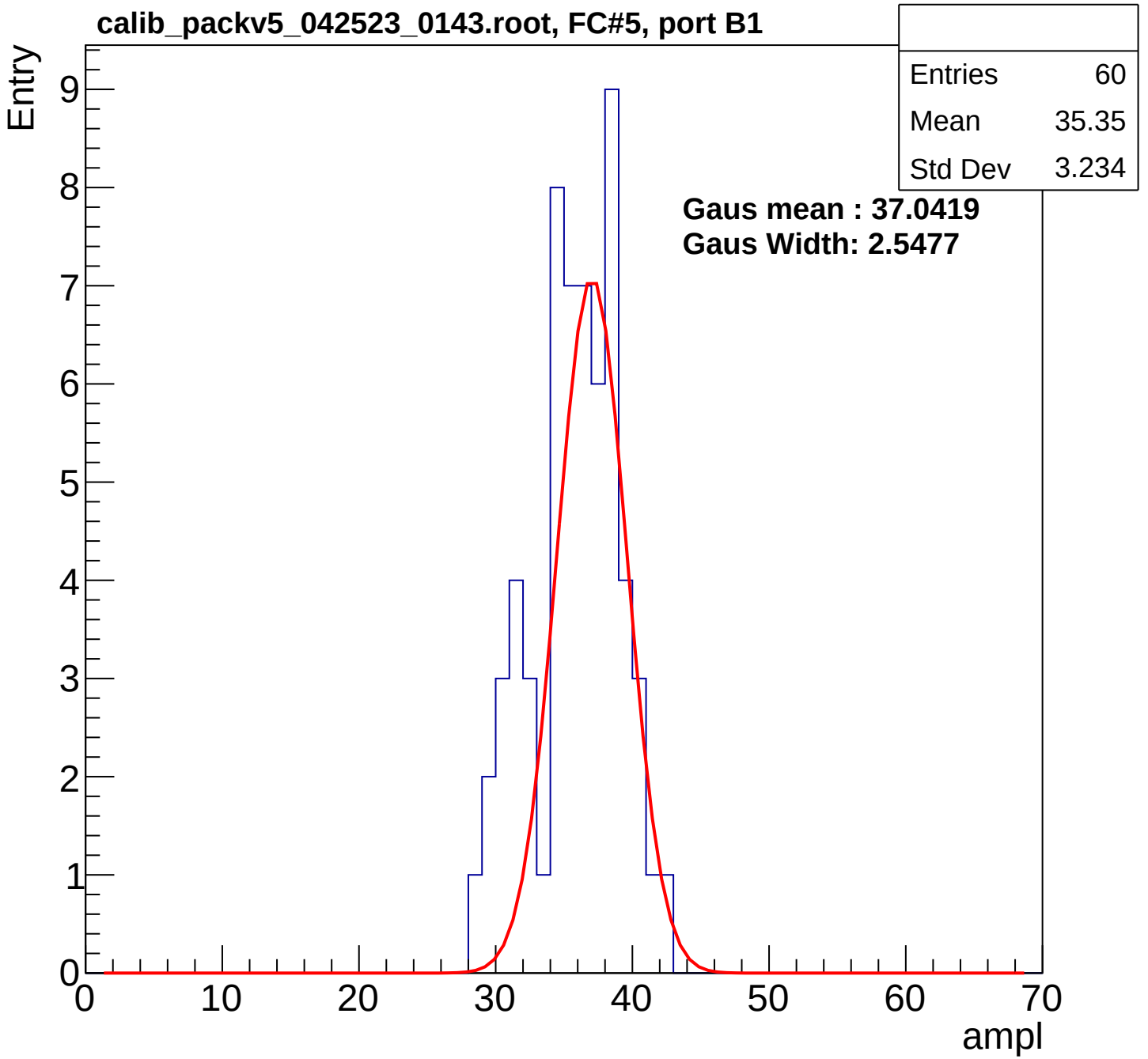
B0L000S, U14-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.35
Std Dev 3.234
Gaus mean : 37.0419
Gaus Width: 2.5477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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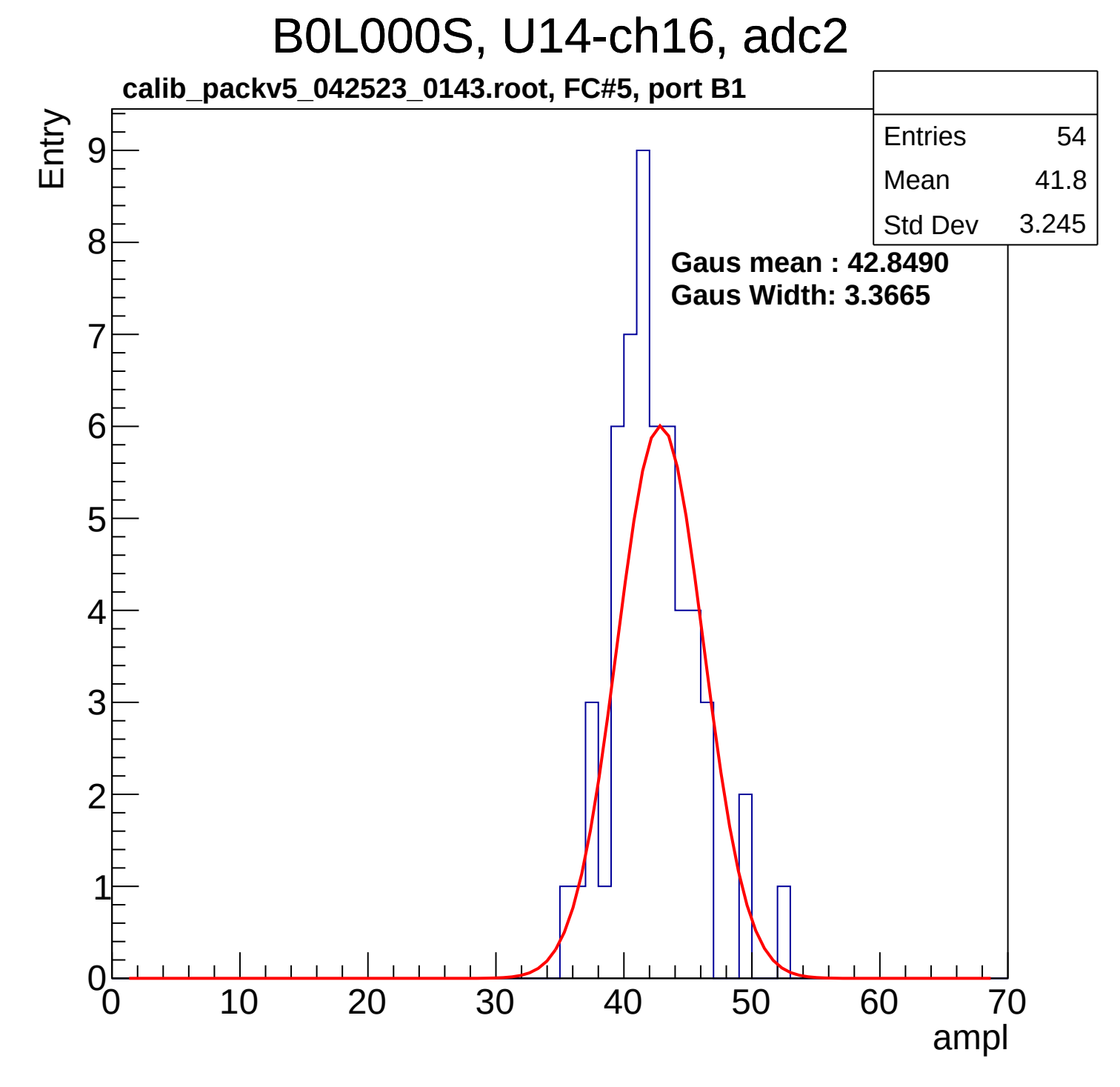
B0L000S, U14-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 41.8
Std Dev 3.245
Gaus mean : 42.8490
Gaus Width: 3.3665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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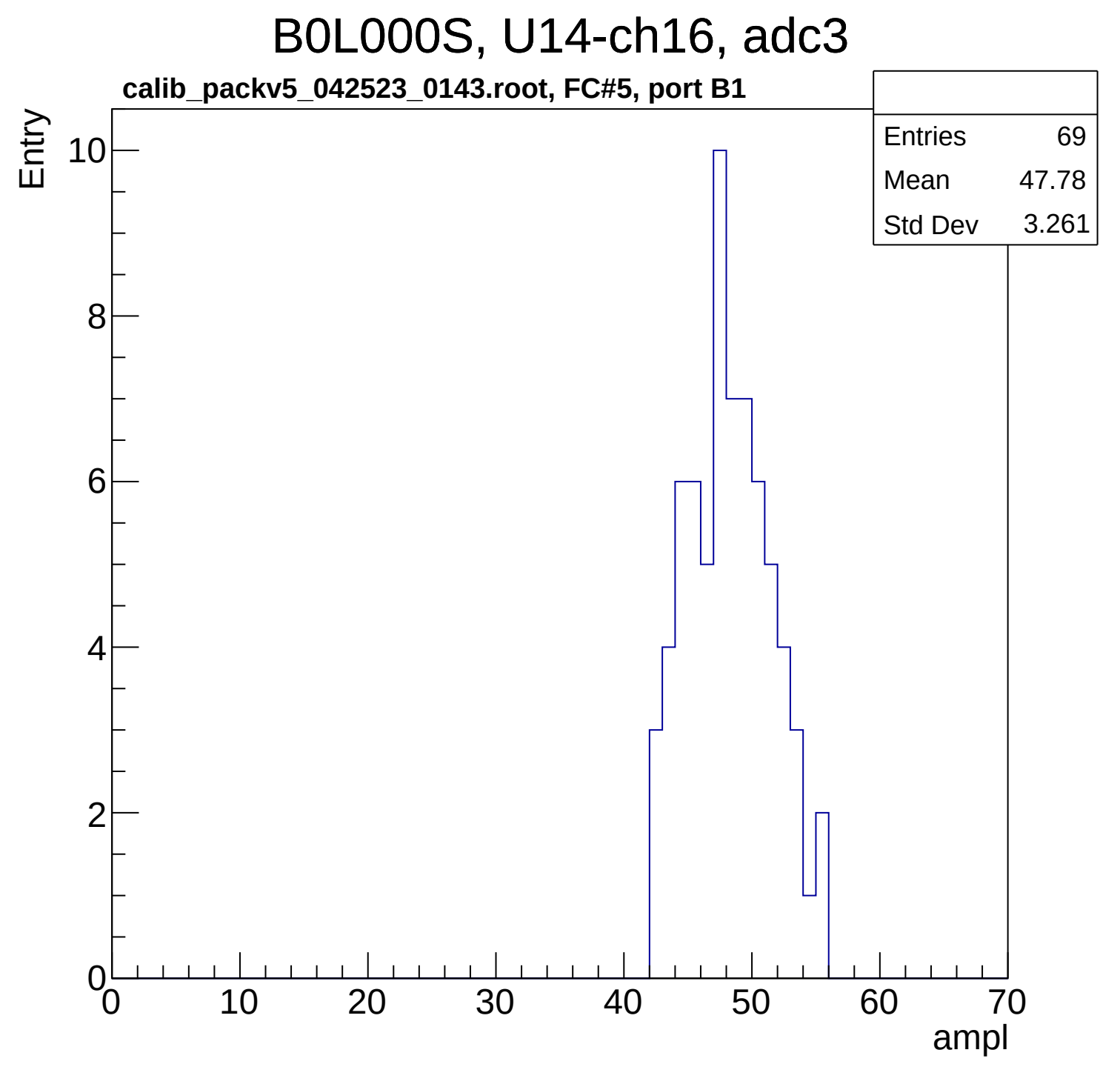
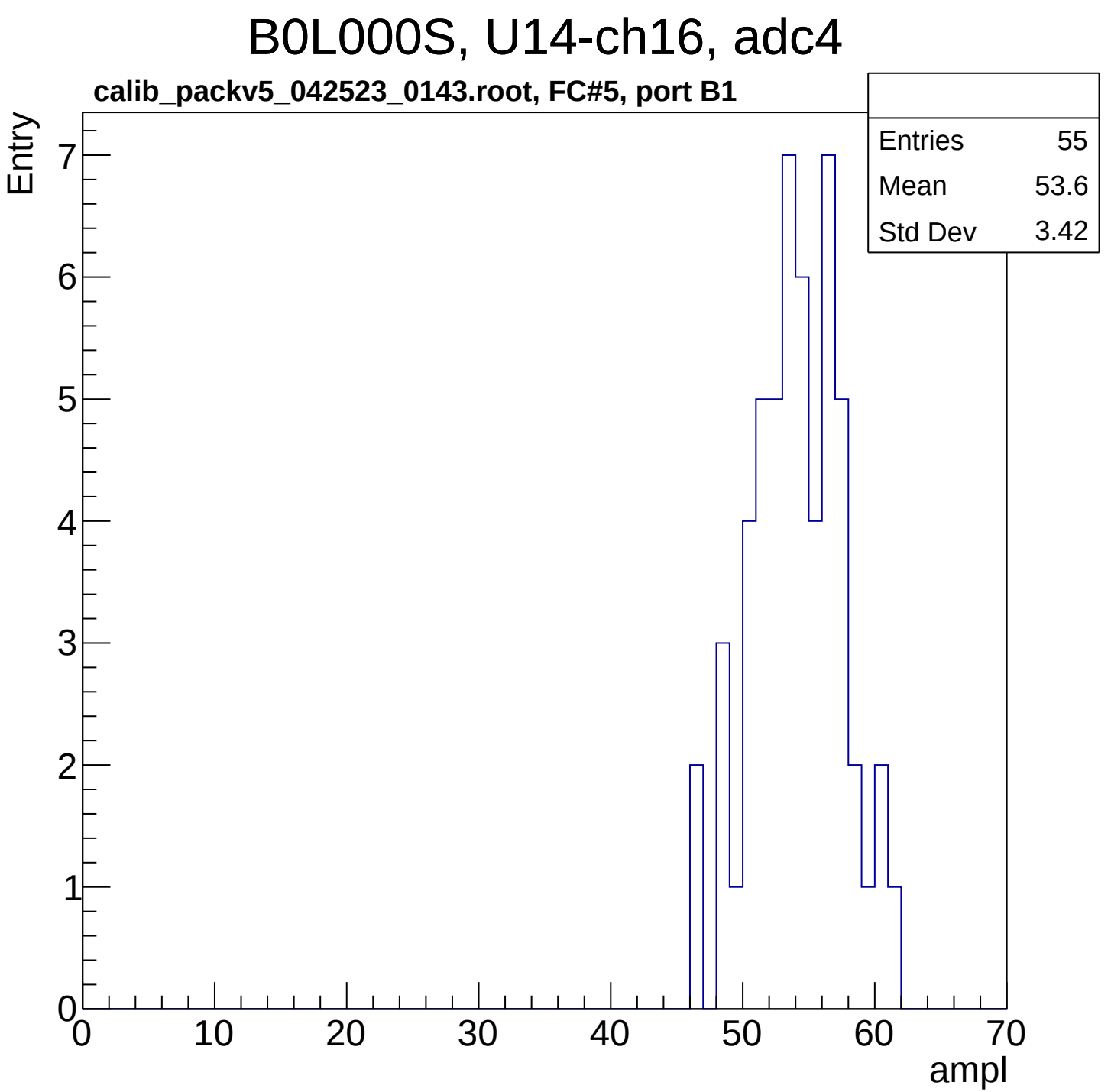
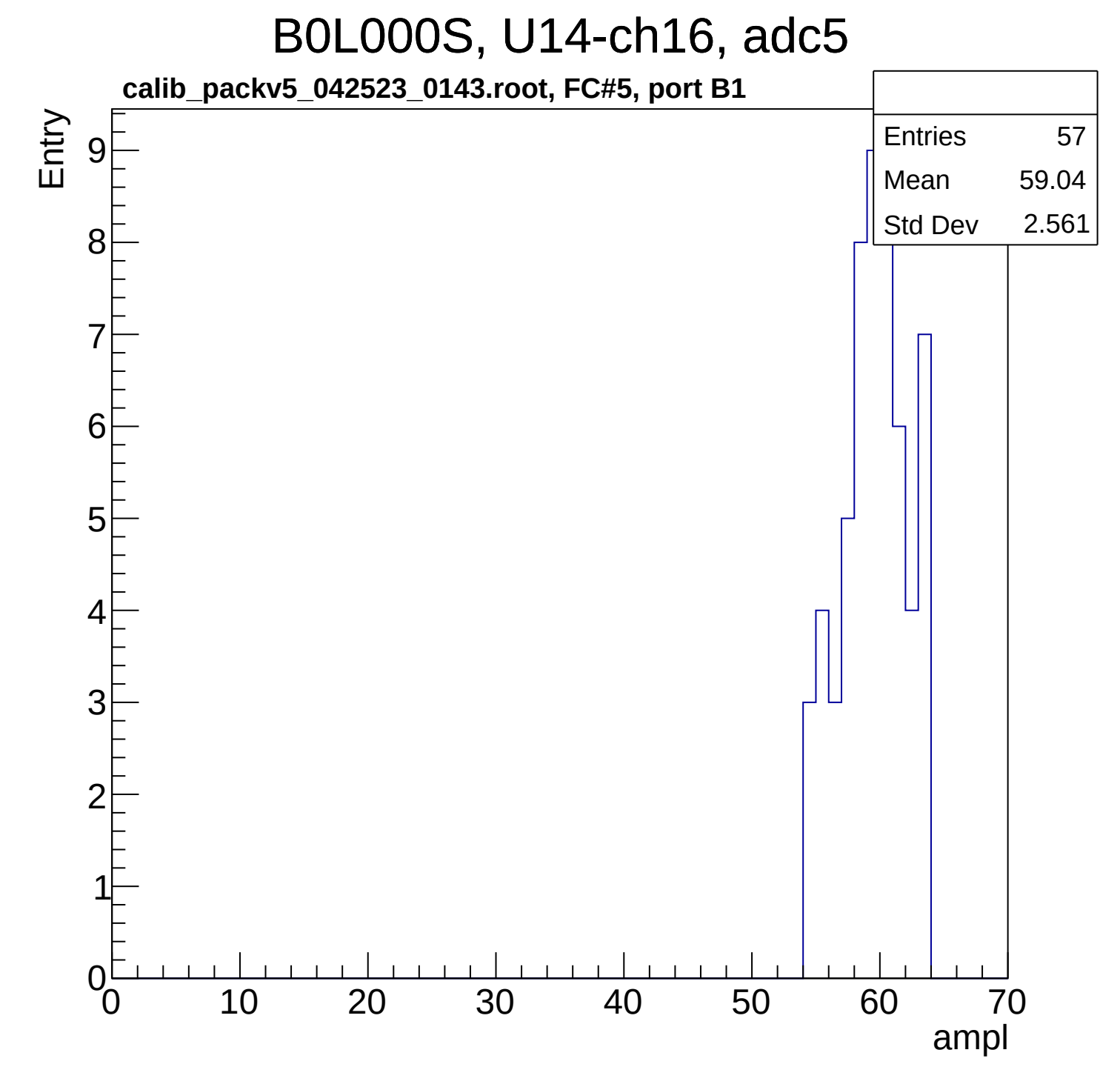

B0L000S, U14-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.78
Std Dev 3.261
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.6
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 59.04
Std Dev 2.561
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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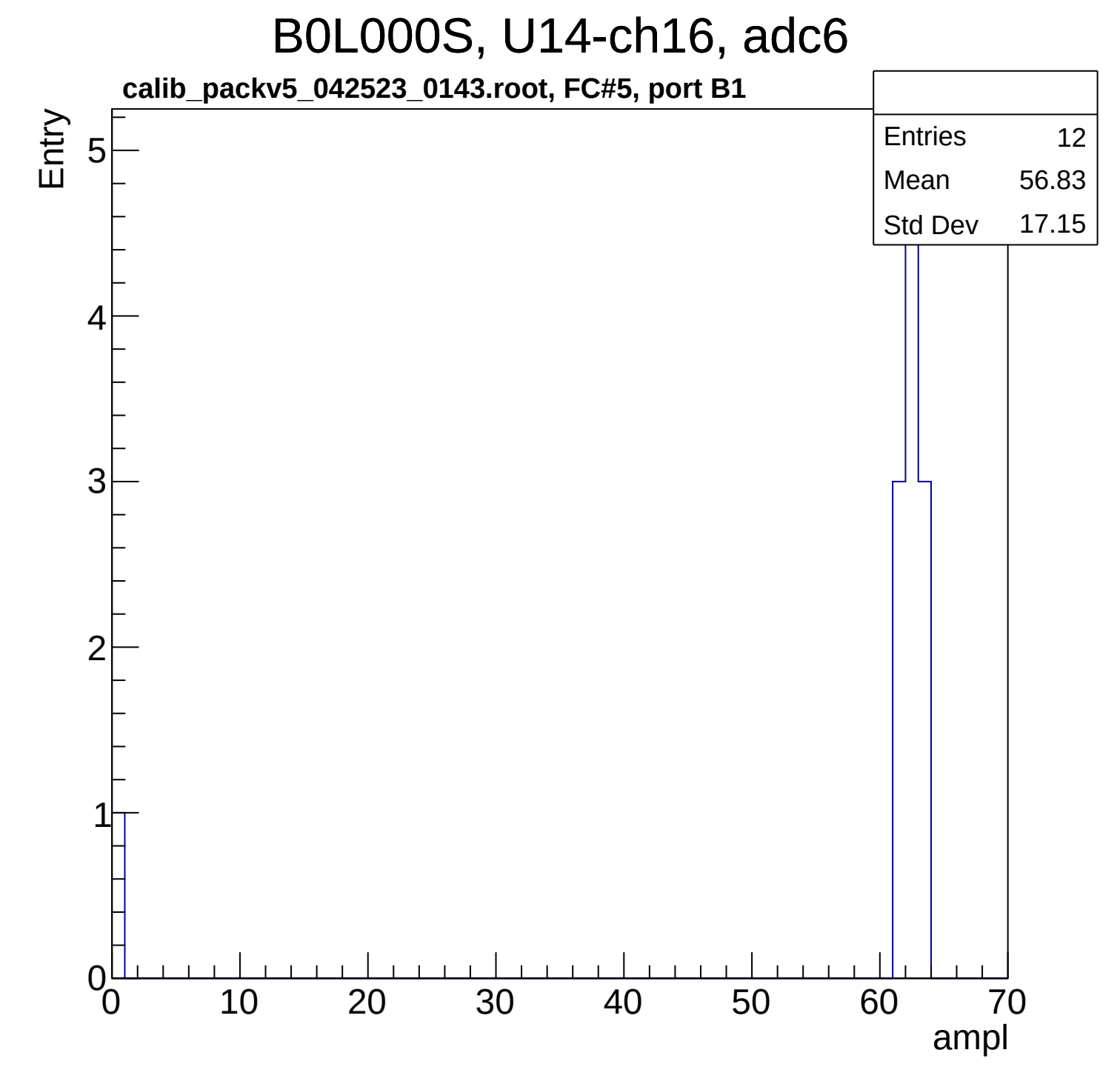
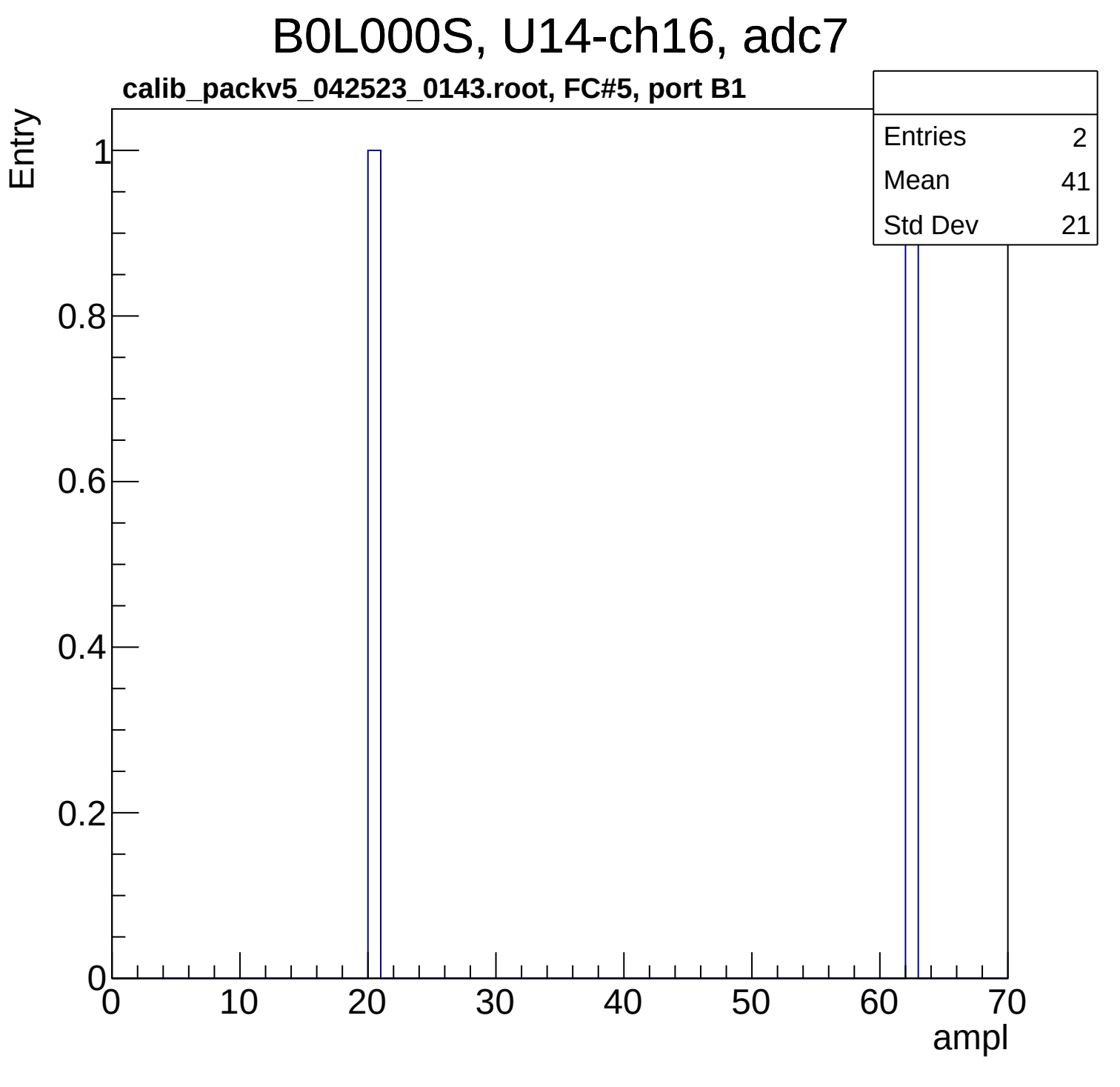

B0L000S, U14-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.83
Std Dev 17.15
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 41
Std Dev 21
Entry
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0.6
0.4
0.2
0
0
10
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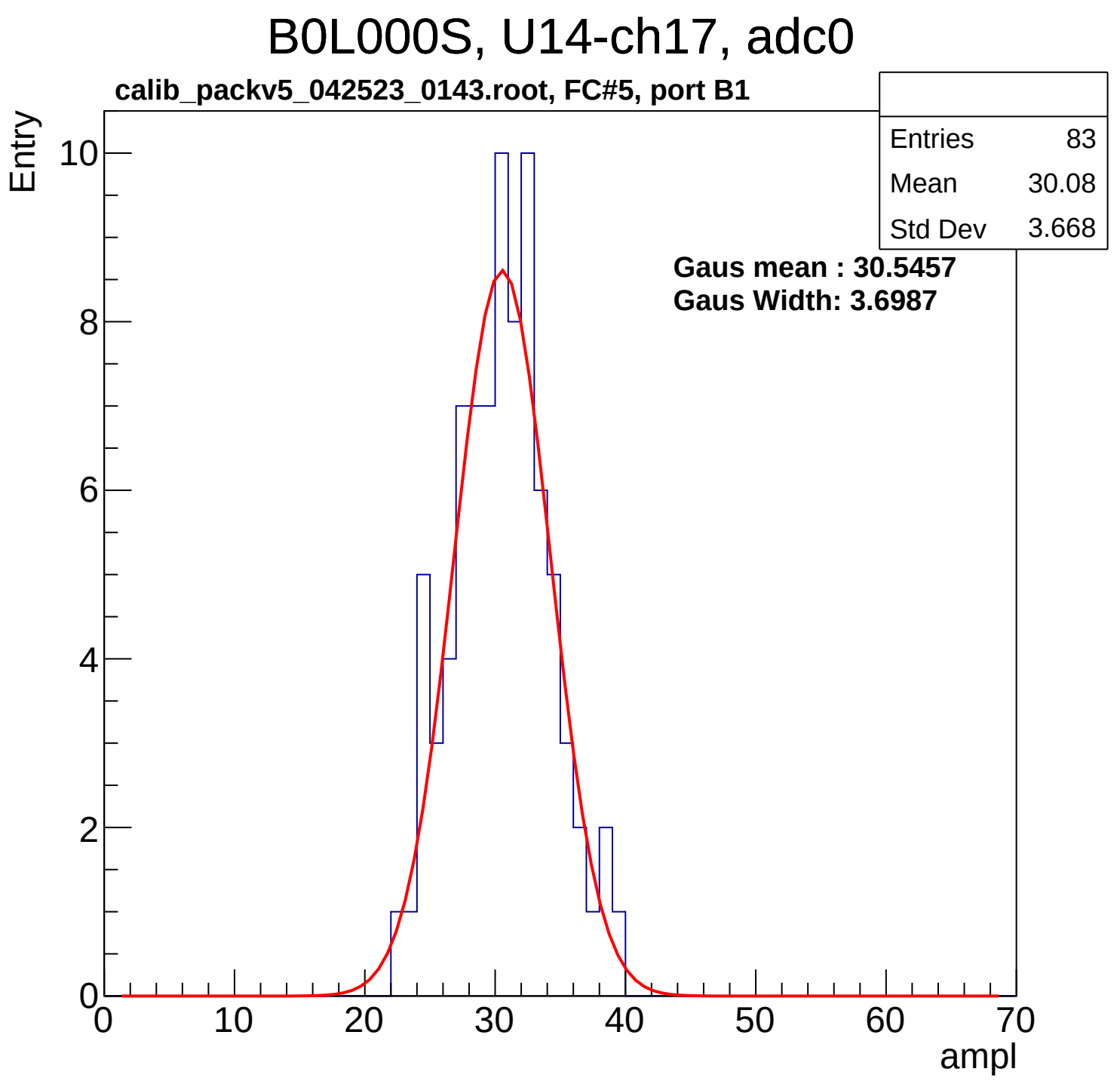
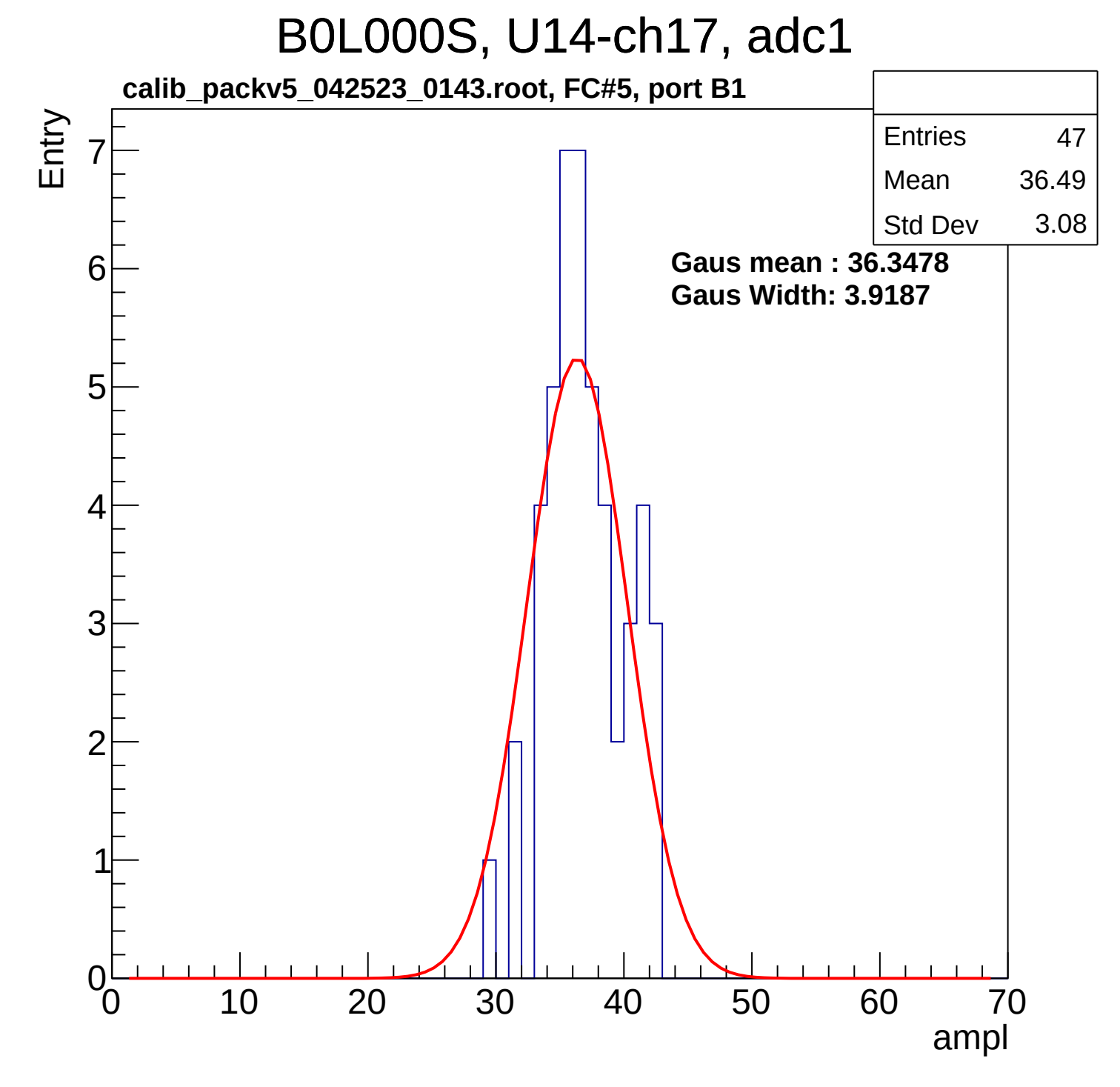
50
60
70
ampl


B0L000S, U14-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 30.08
Std Dev 3.668
Gaus mean : 30.5457
Gaus Width: 3.6987
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 36.49
Std Dev 3.08
Gaus mean : 36.3478
Gaus Width: 3.9187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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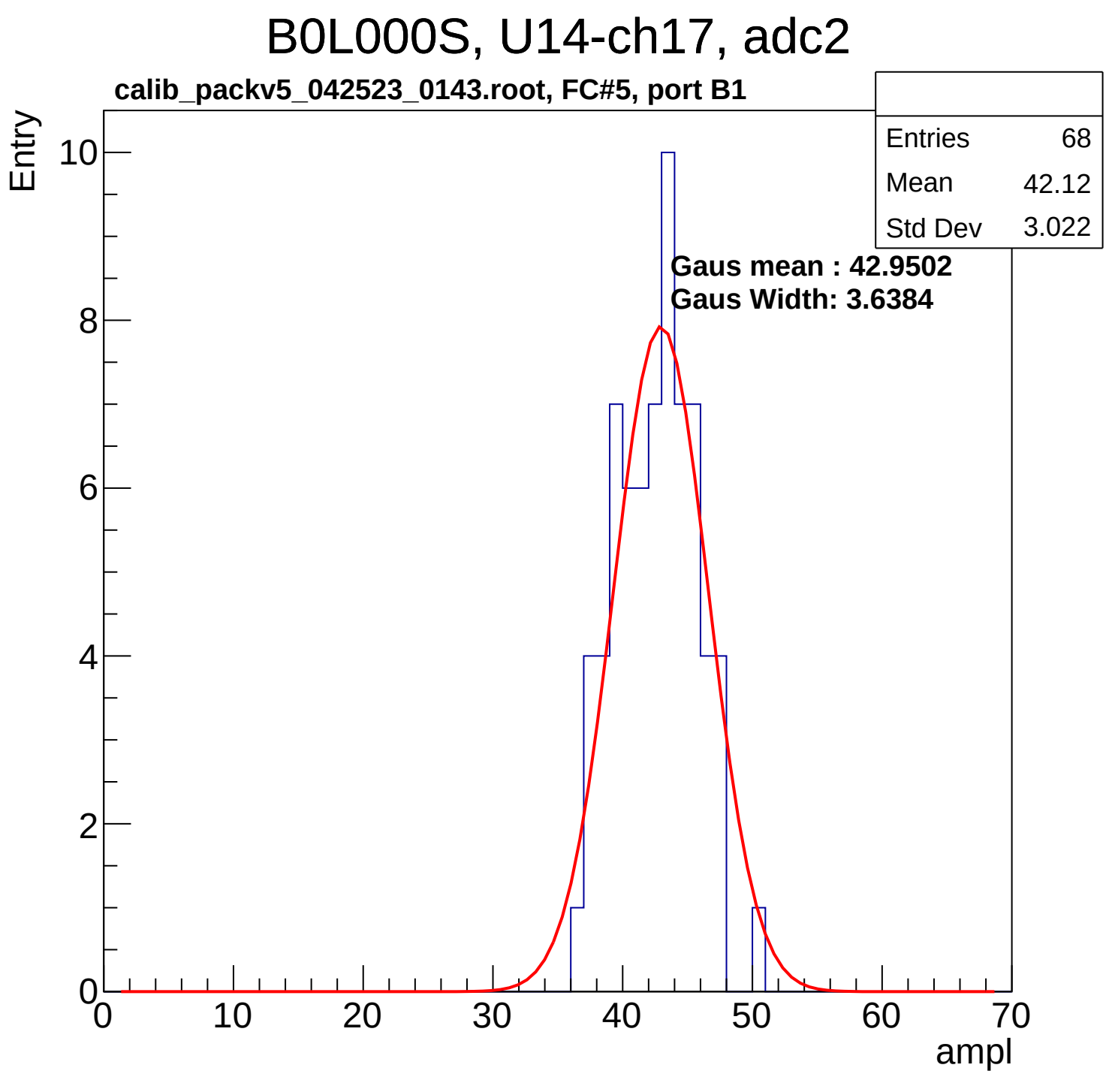
B0L000S, U14-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.12
Std Dev 3.022
Gaus mean : 42.9502
Gaus Width: 3.6384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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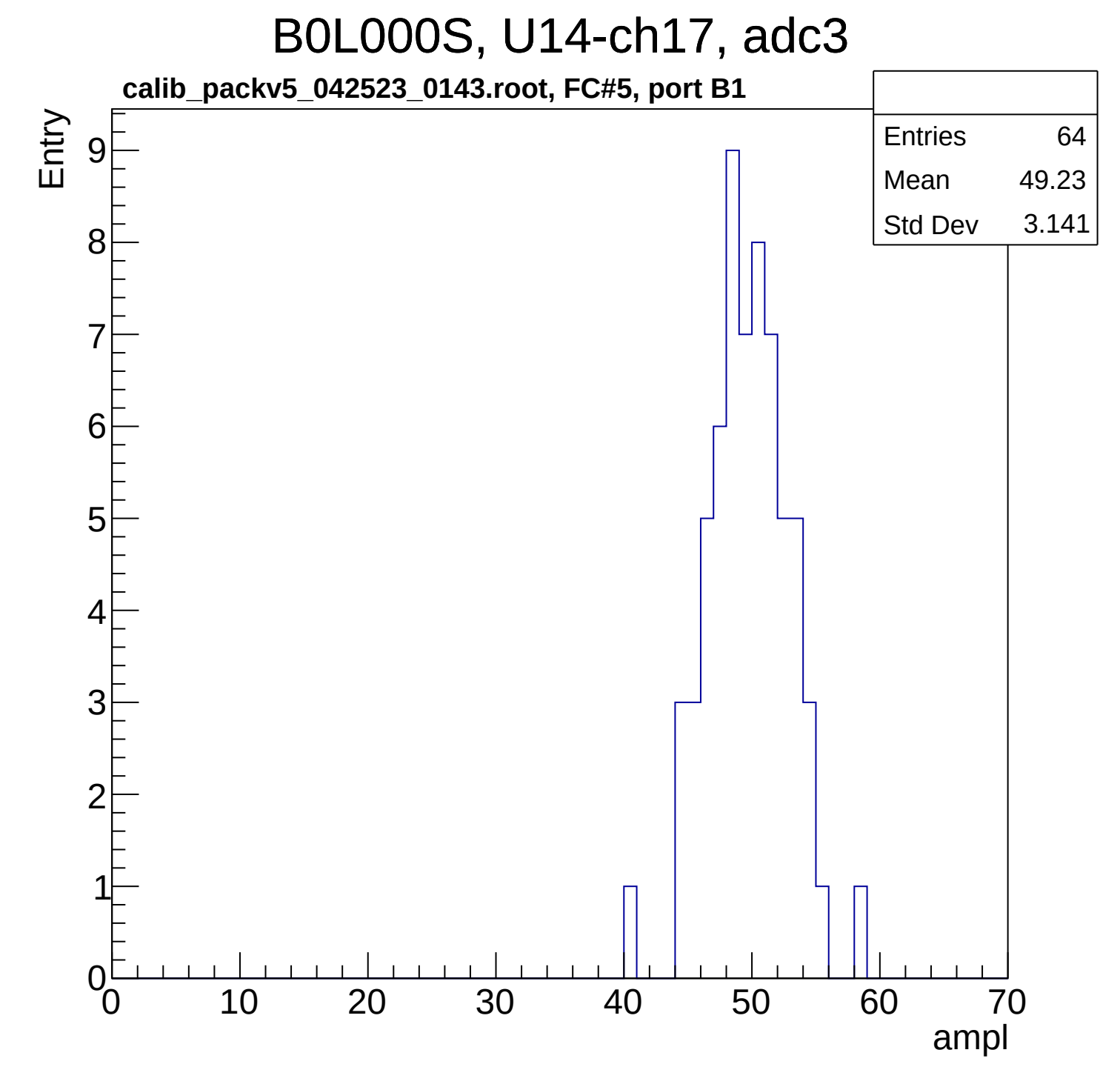
B0L000S, U14-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.23
Std Dev 3.141
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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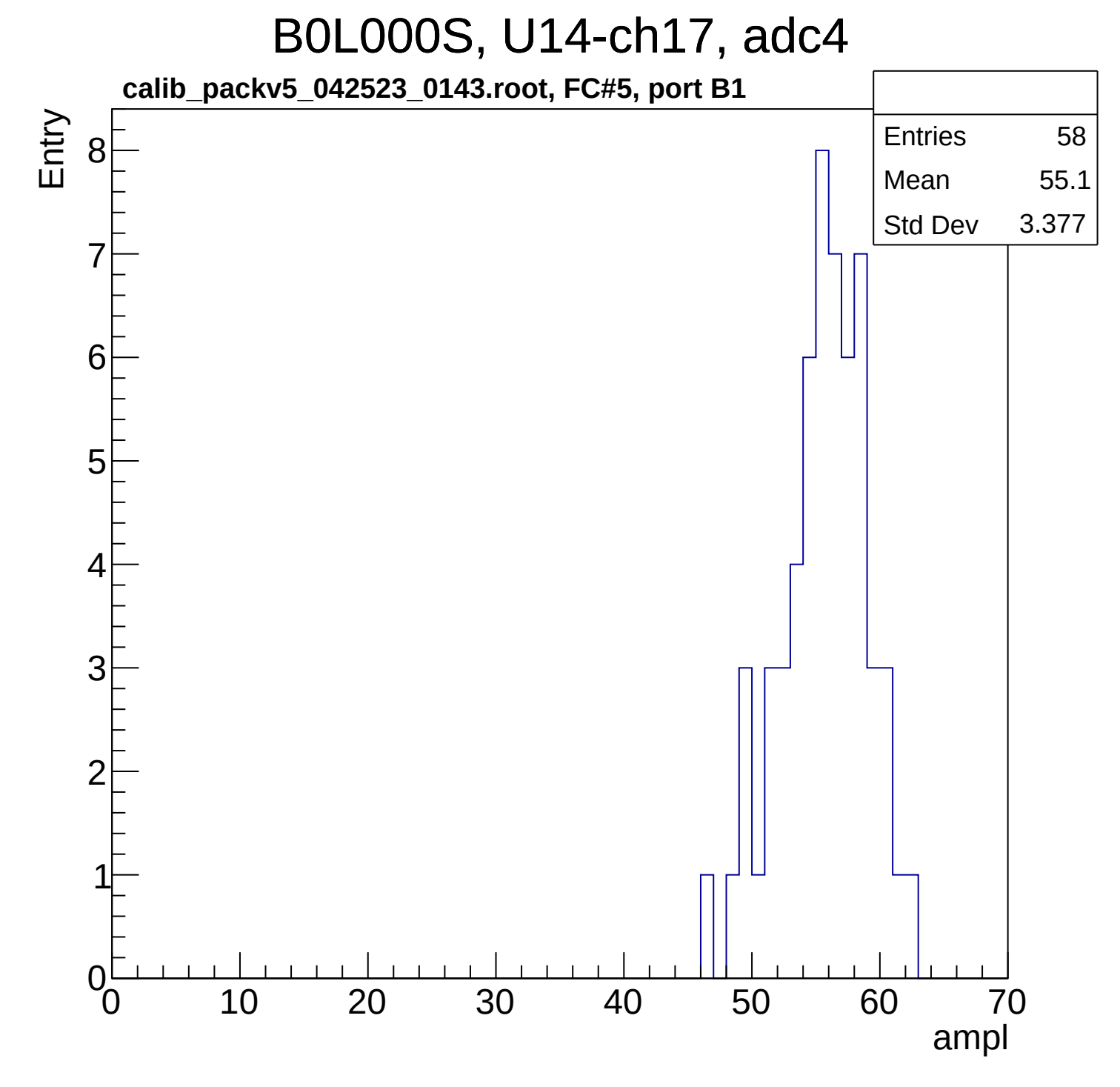
B0L000S, U14-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.1
Std Dev 3.377
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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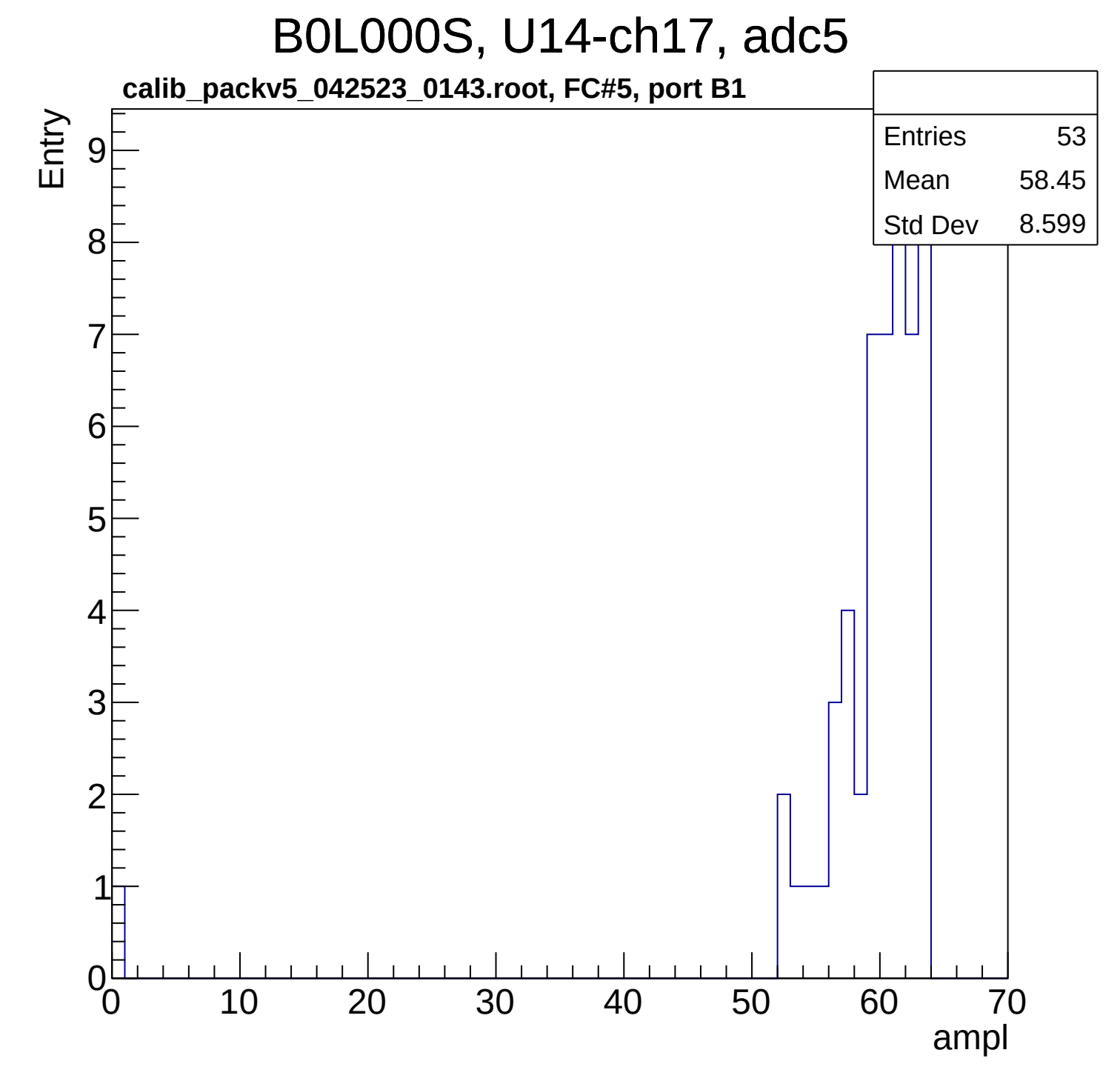
B0L000S, U14-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.45
Std Dev 8.599
Entry
ampl
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3
4
5
6
7
8
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0
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60
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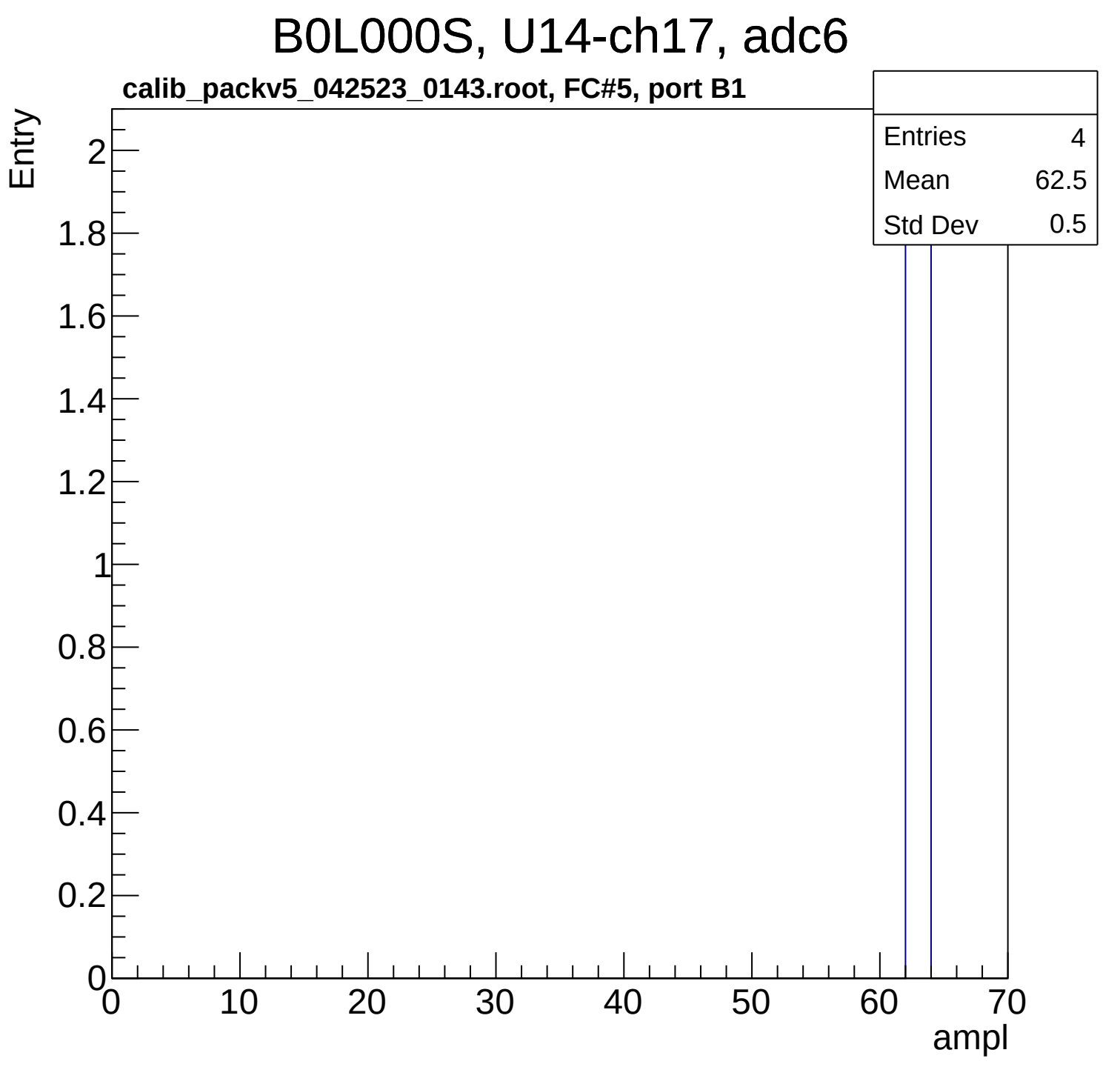
B0L000S, U14-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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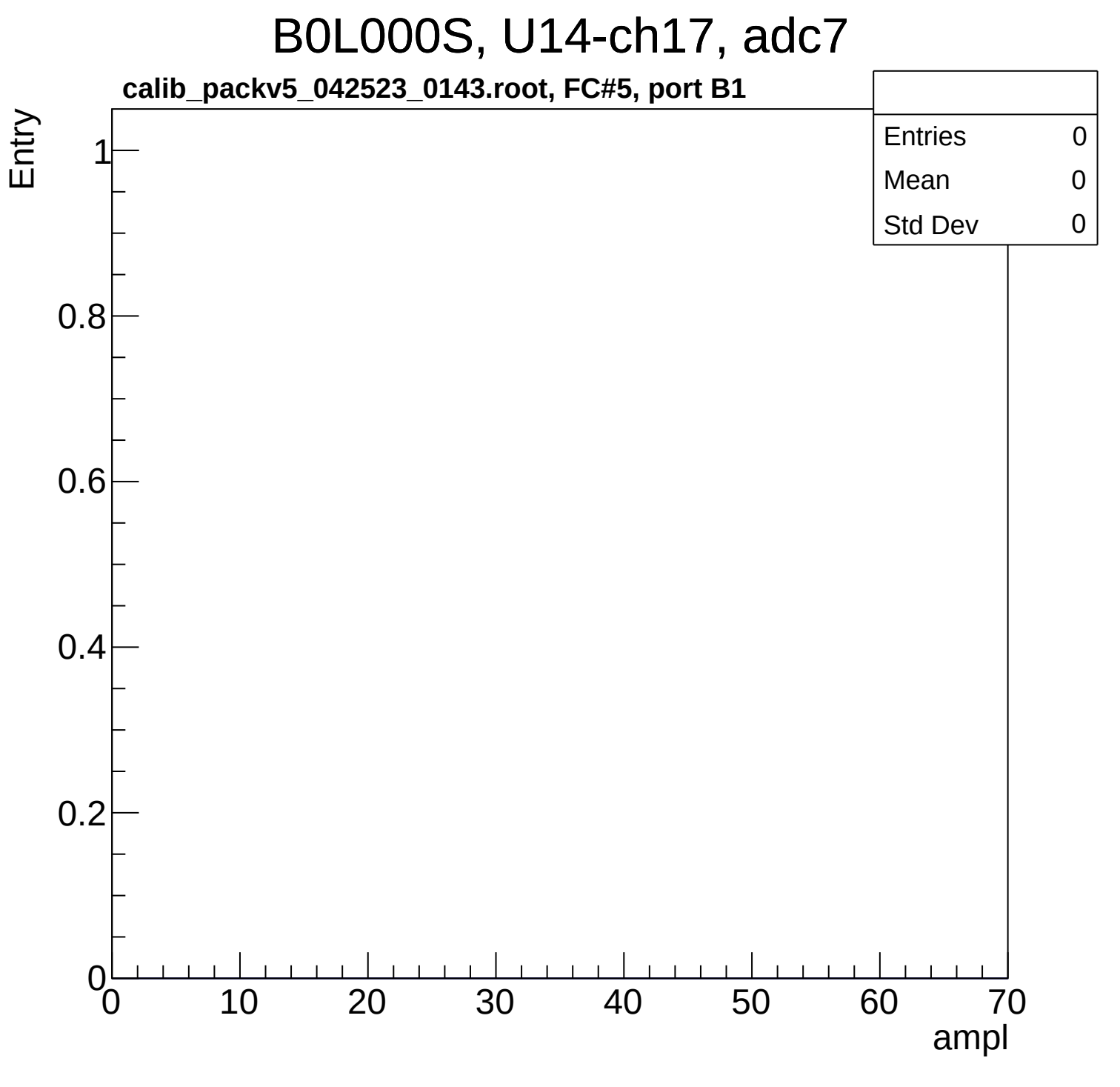
B0L000S, U14-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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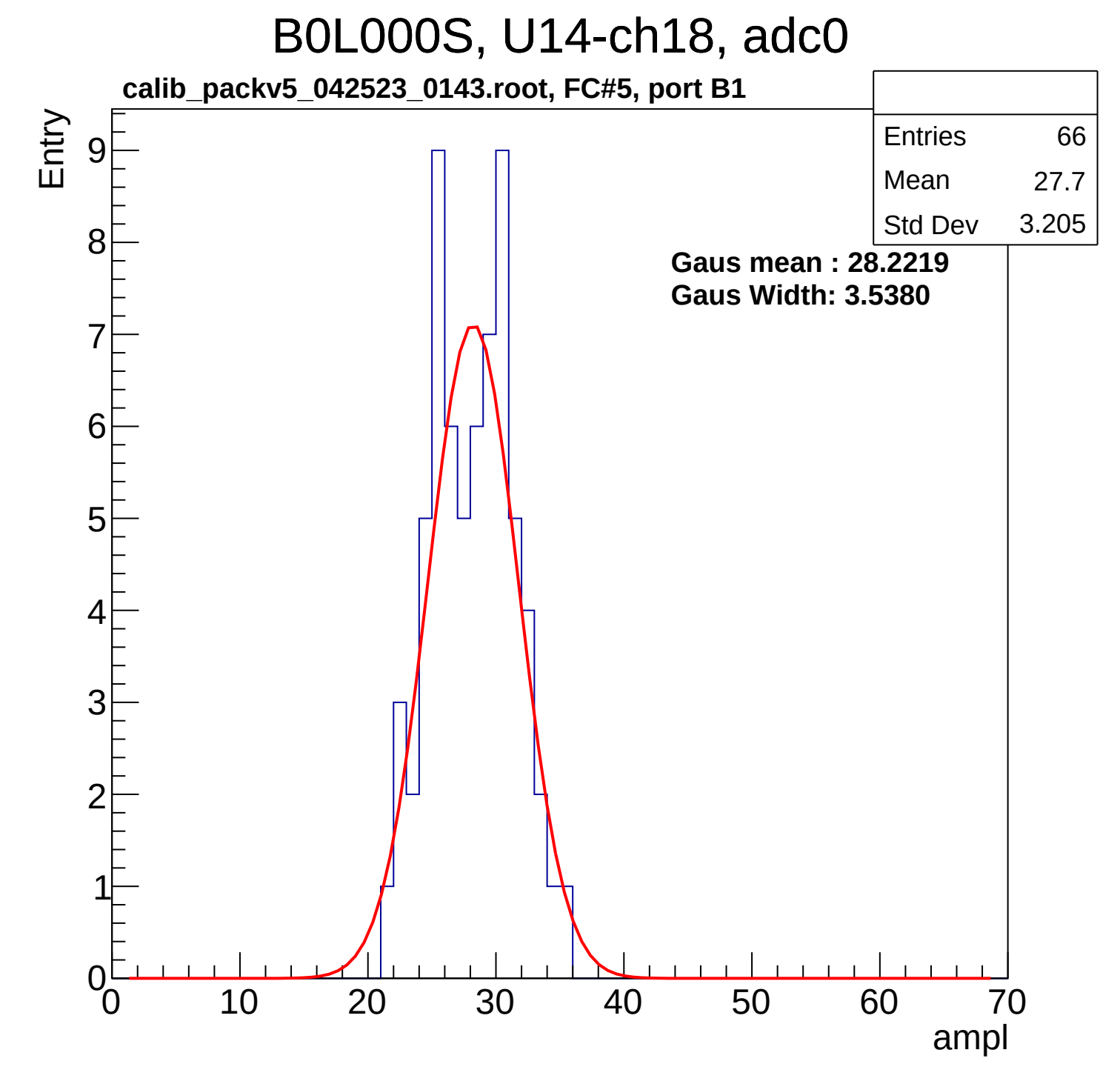
50
60
70
ampl


B0L000S, U14-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 27.7
Std Dev 3.205
Gaus mean : 28.2219
Gaus Width: 3.5380
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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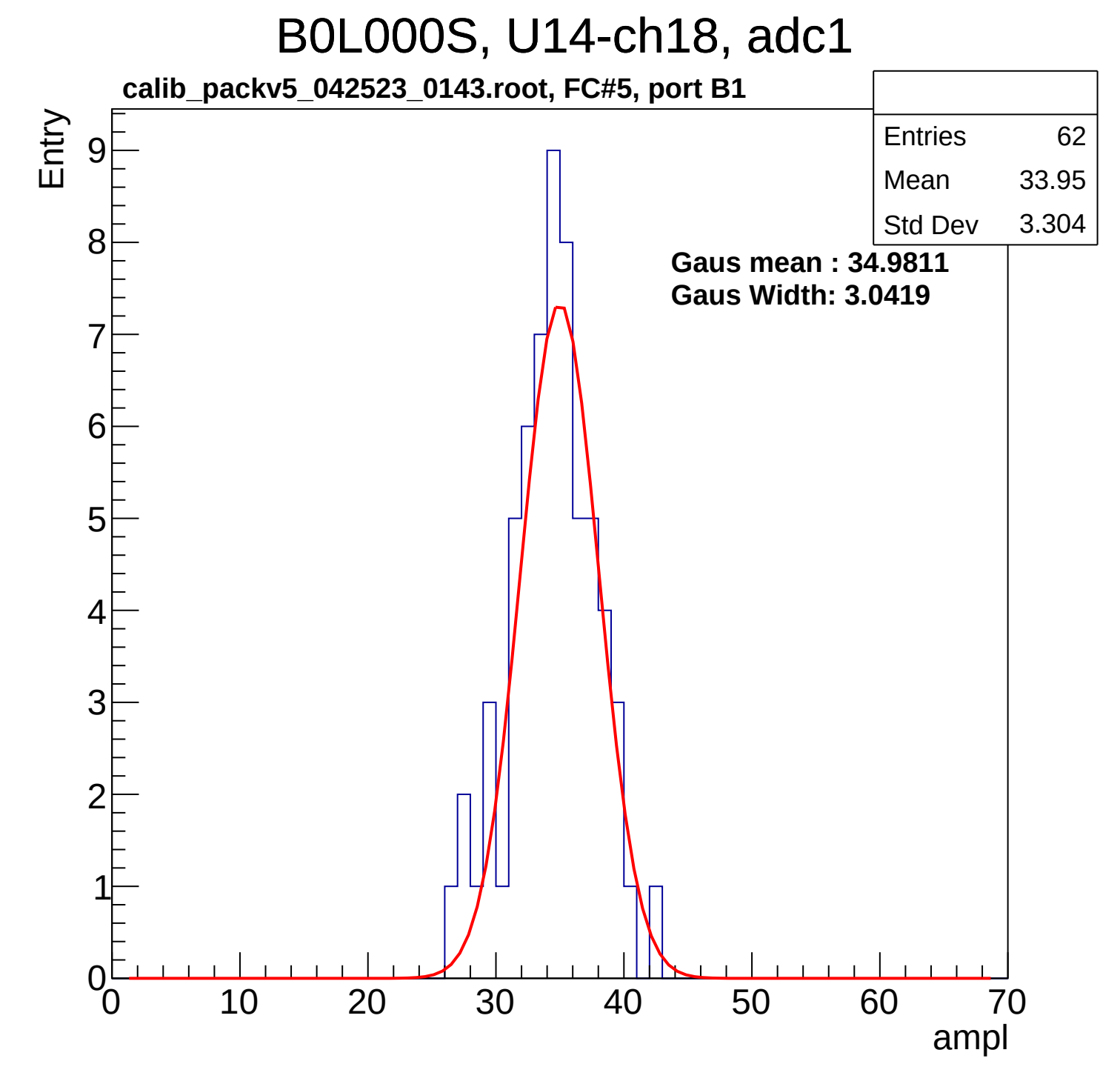
B0L000S, U14-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 33.95
Std Dev 3.304
Gaus mean : 34.9811
Gaus Width: 3.0419
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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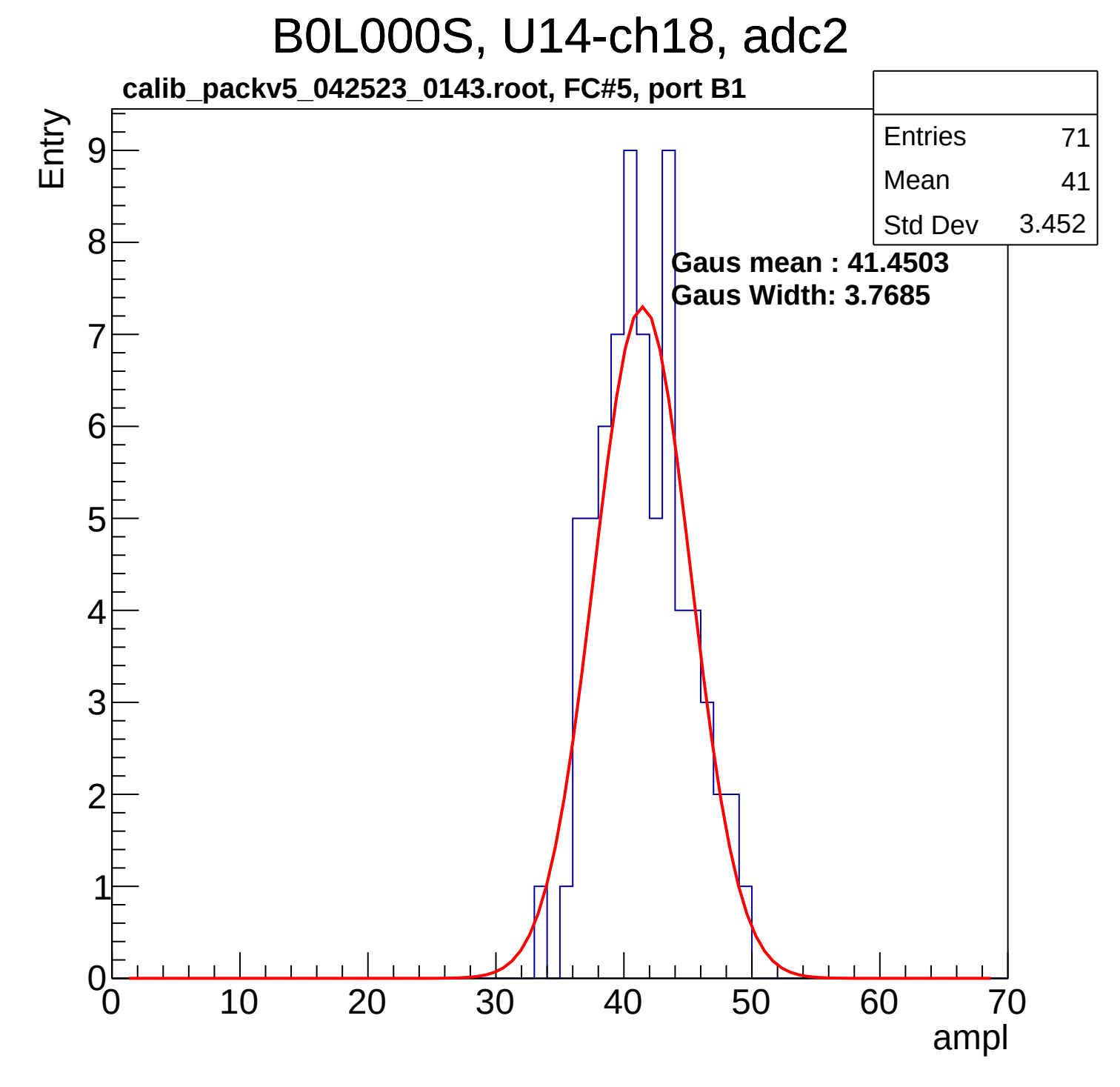
B0L000S, U14-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 41
Std Dev 3.452
Gaus mean : 41.4503
Gaus Width: 3.7685
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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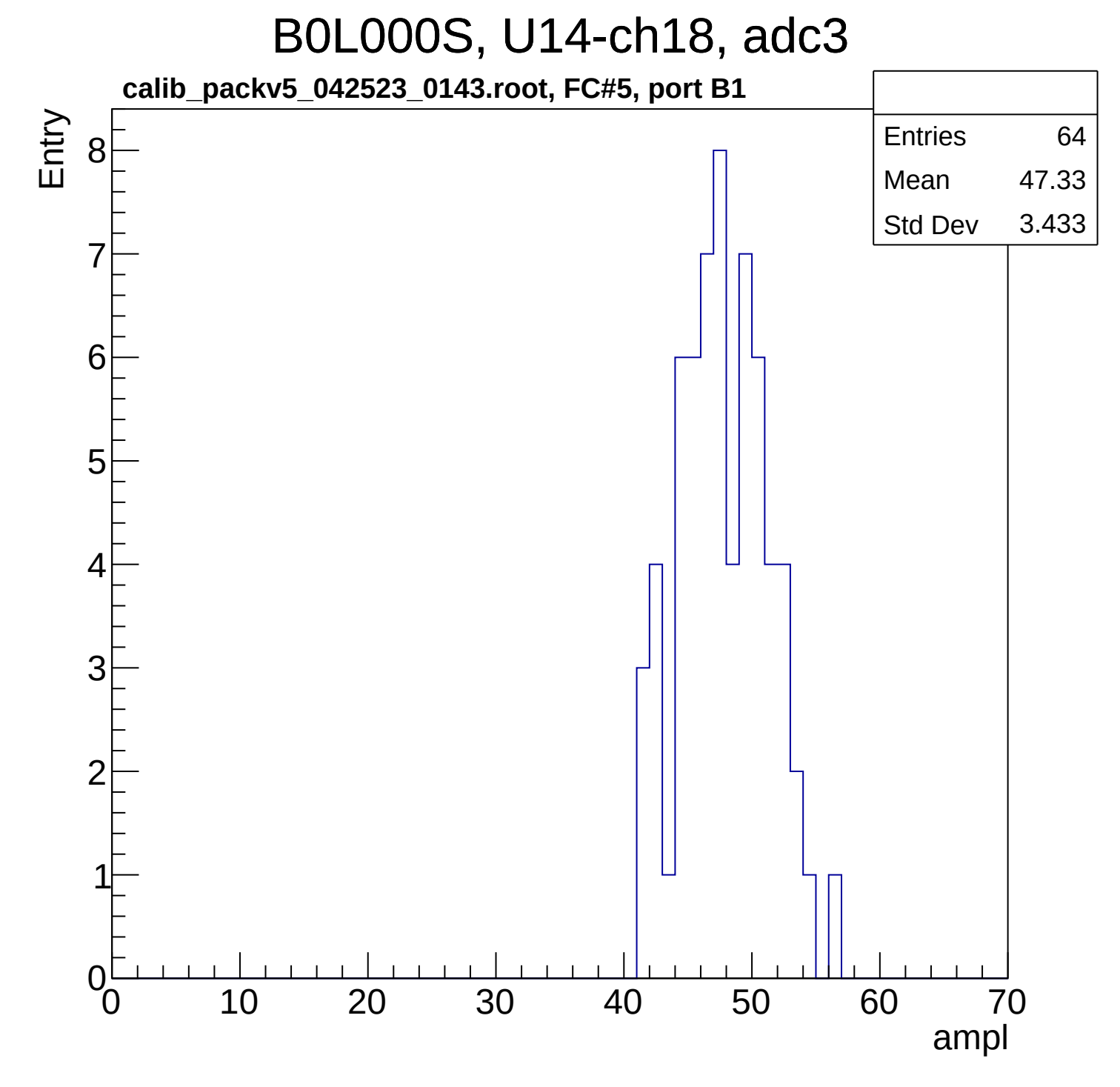
B0L000S, U14-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.33
Std Dev 3.433
Entry
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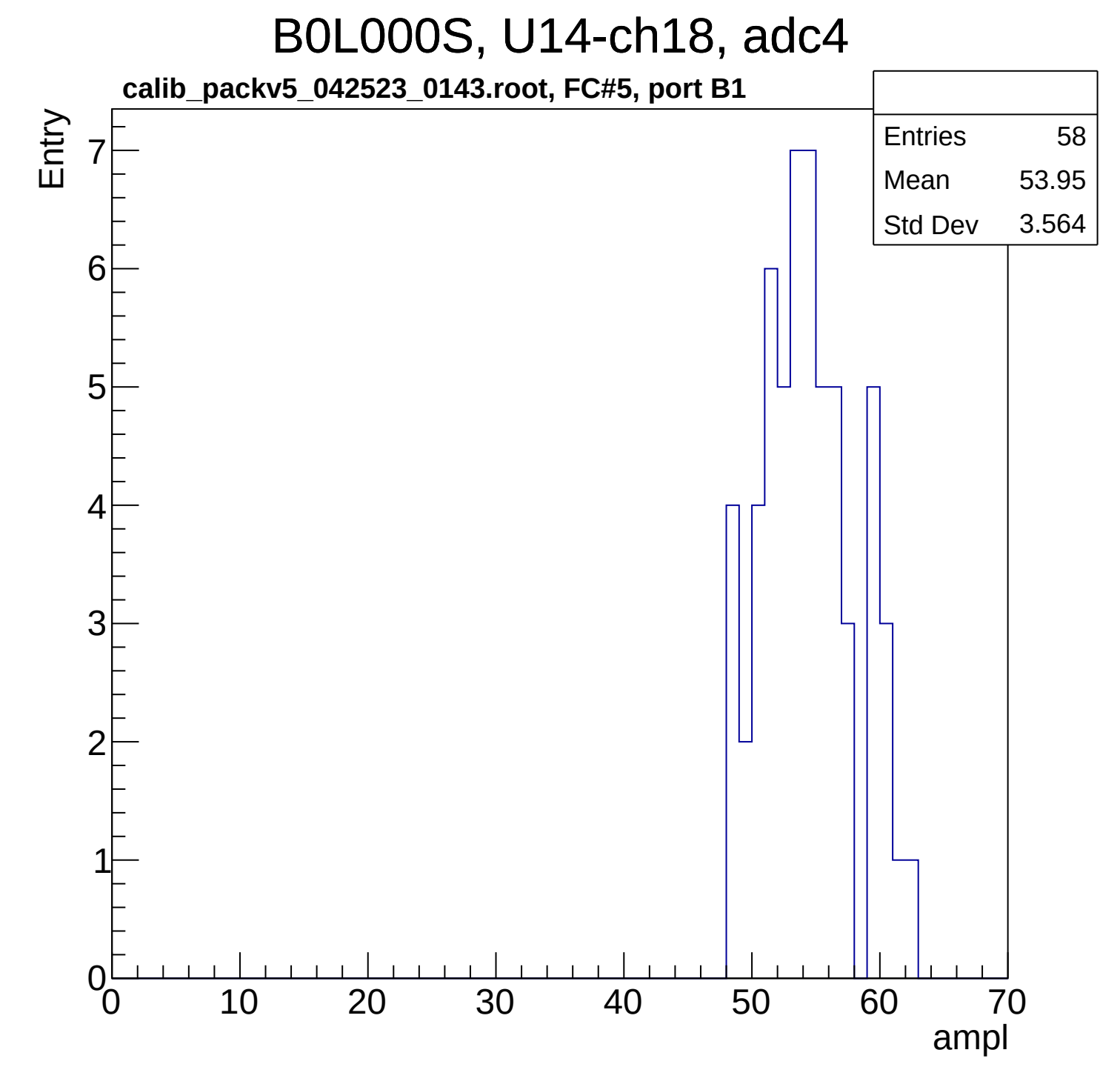
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.95
Std Dev 3.564
Entry
ampl
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3
4
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7
0
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60
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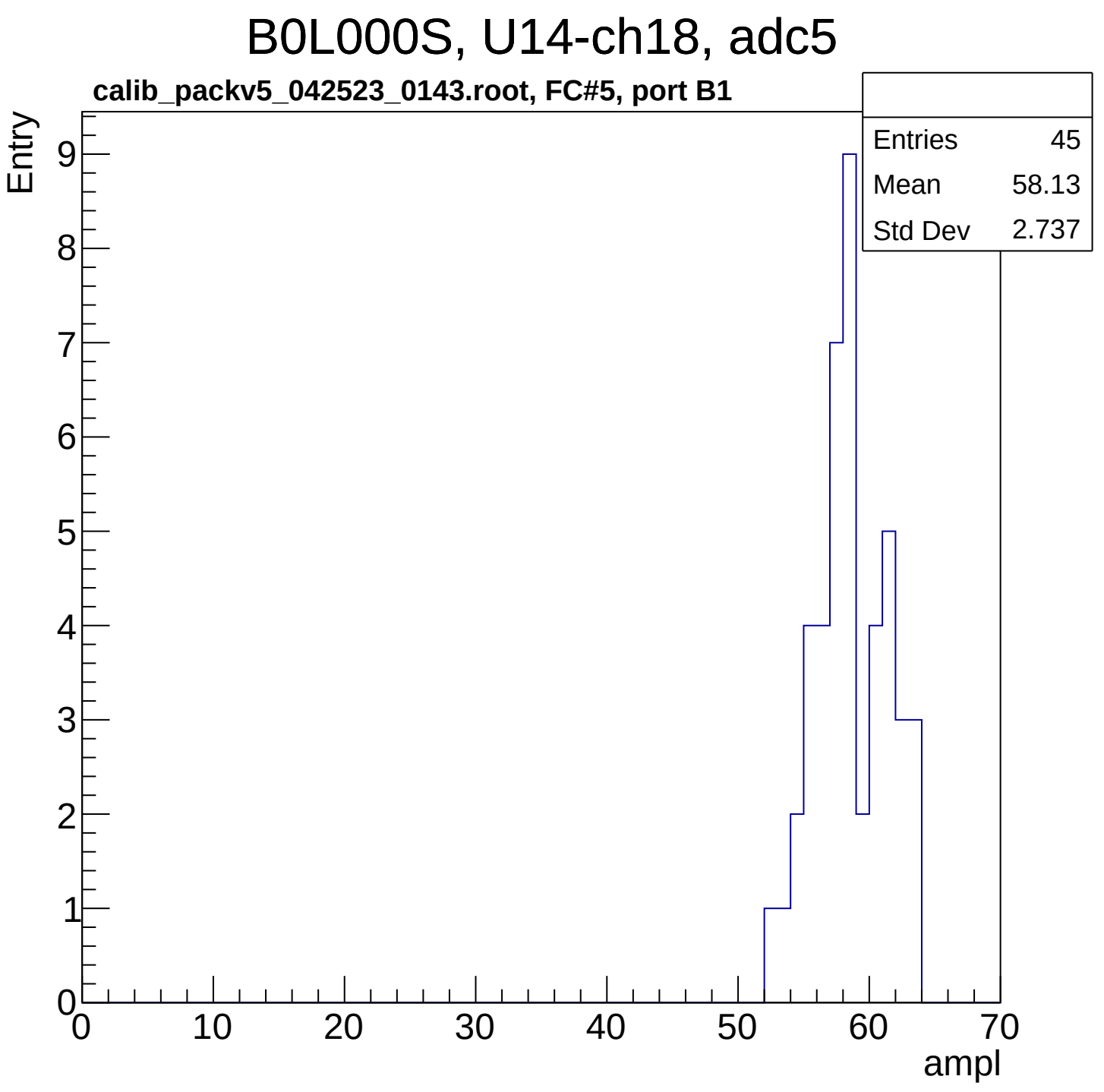
B0L000S, U14-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.13
Std Dev 2.737
Entry
ampl
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2
3
4
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6
7
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9
0
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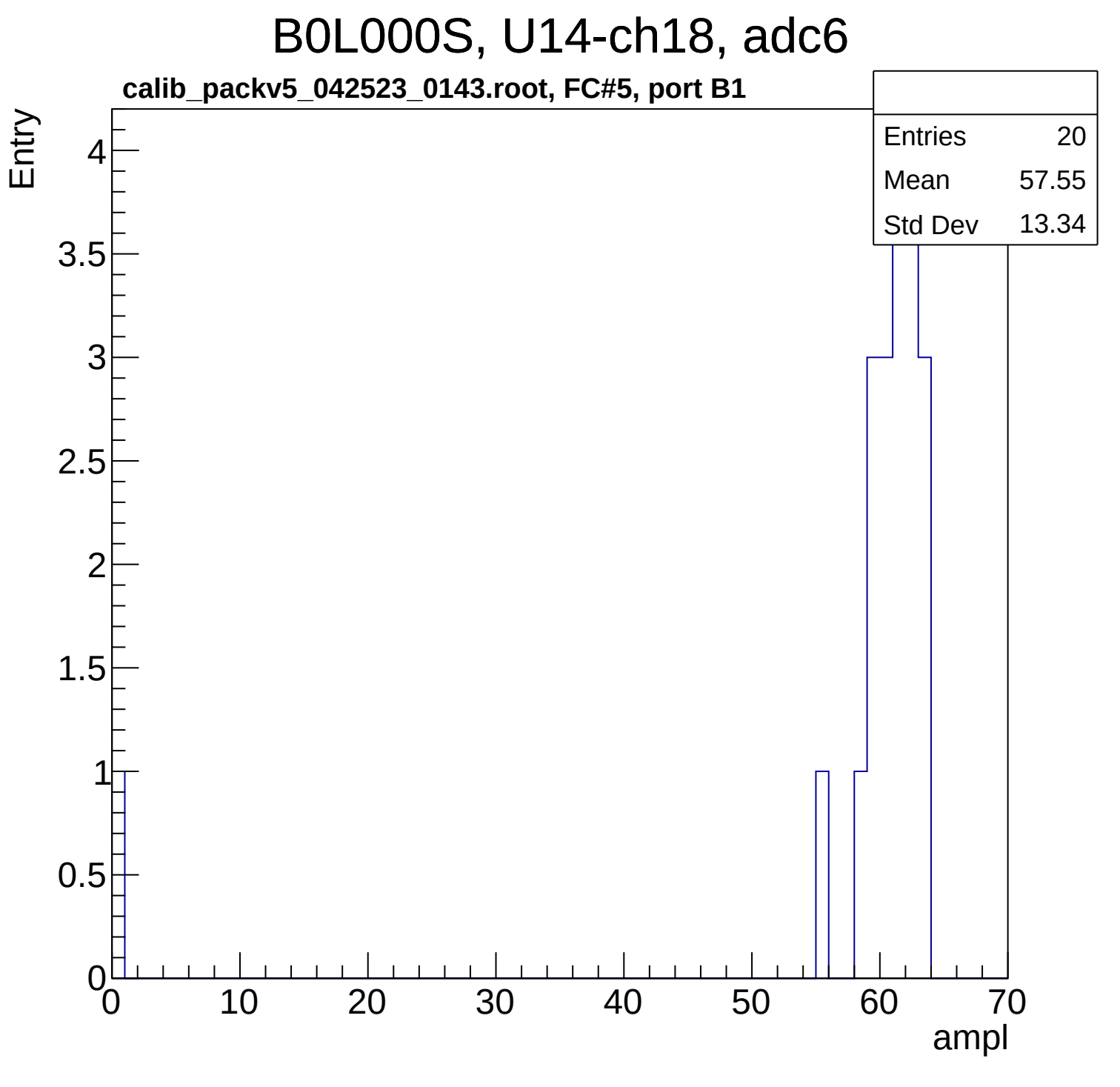
B0L000S, U14-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 57.55
Std Dev 13.34
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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30
40
50
60
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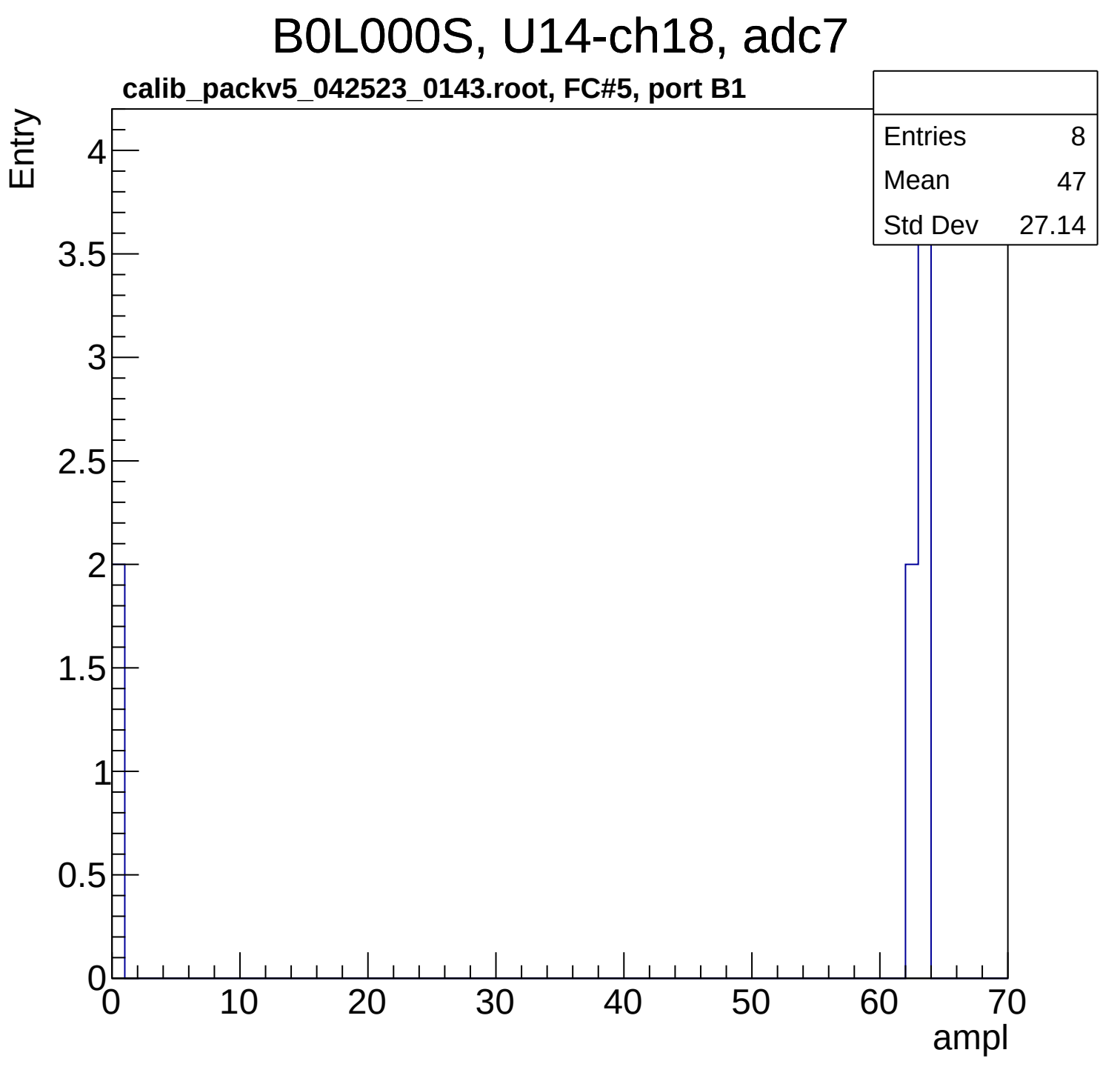
B0L000S, U14-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 47
Std Dev 27.14
Entry
ampl
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0.5
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1.5
2
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3
3.5
4
0
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60
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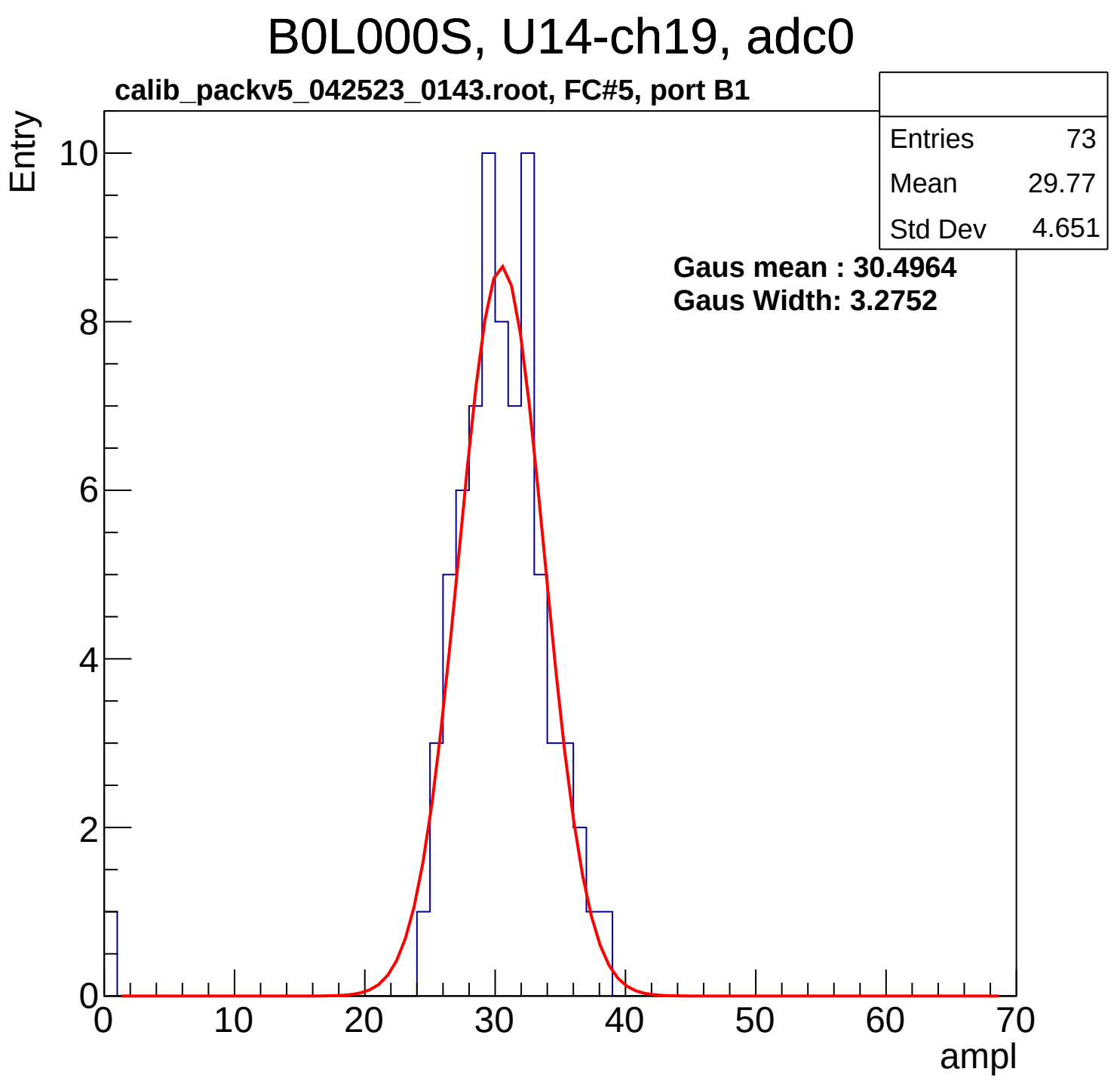
B0L000S, U14-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.77
Std Dev 4.651
Gaus mean : 30.4964
Gaus Width: 3.2752
Entry
ampl
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6
8
10
0
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60
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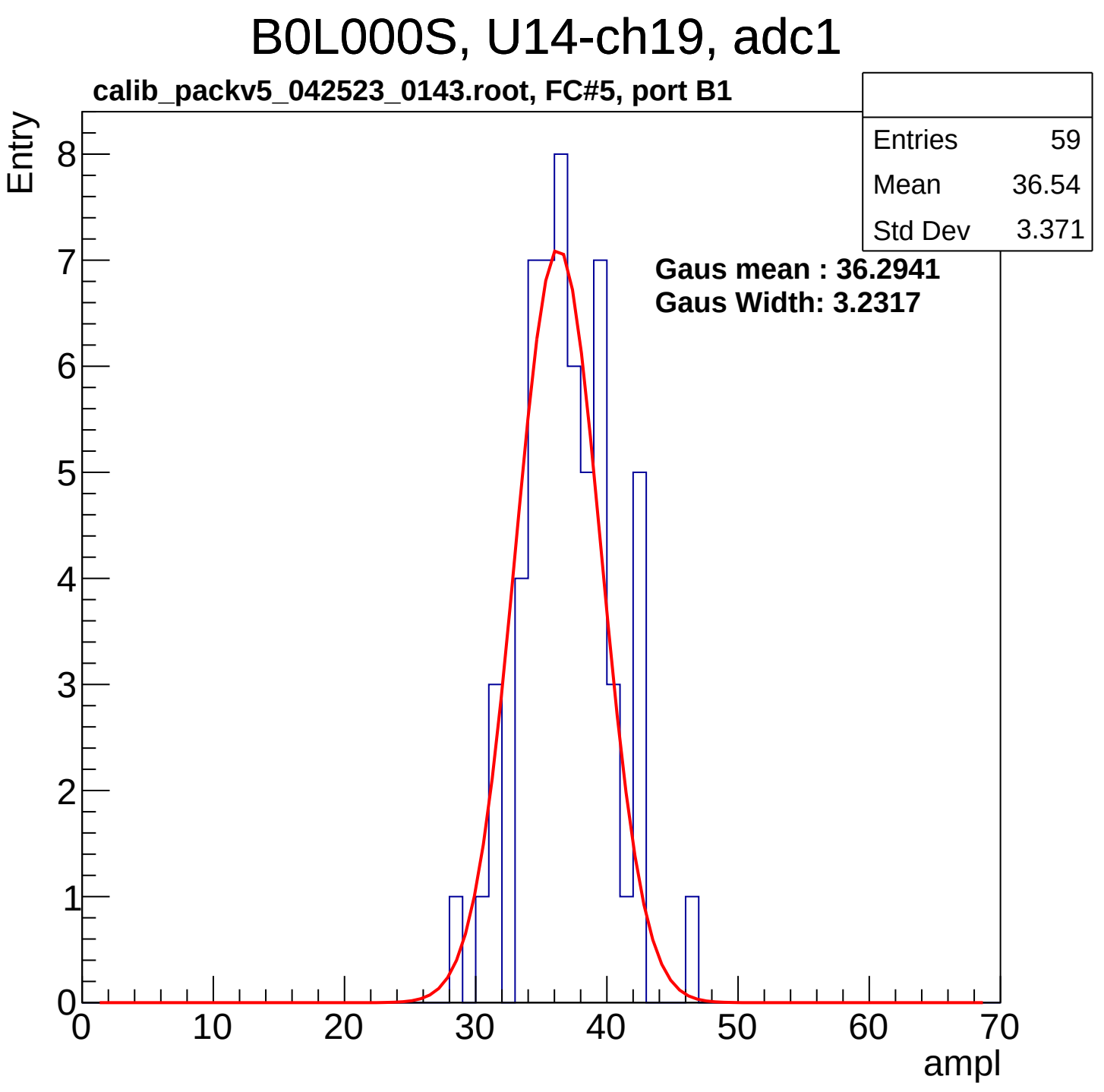
B0L000S, U14-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 36.54
Std Dev 3.371
Gaus mean : 36.2941
Gaus Width: 3.2317
Entry
ampl
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1
2
3
4
5
6
7
8
0
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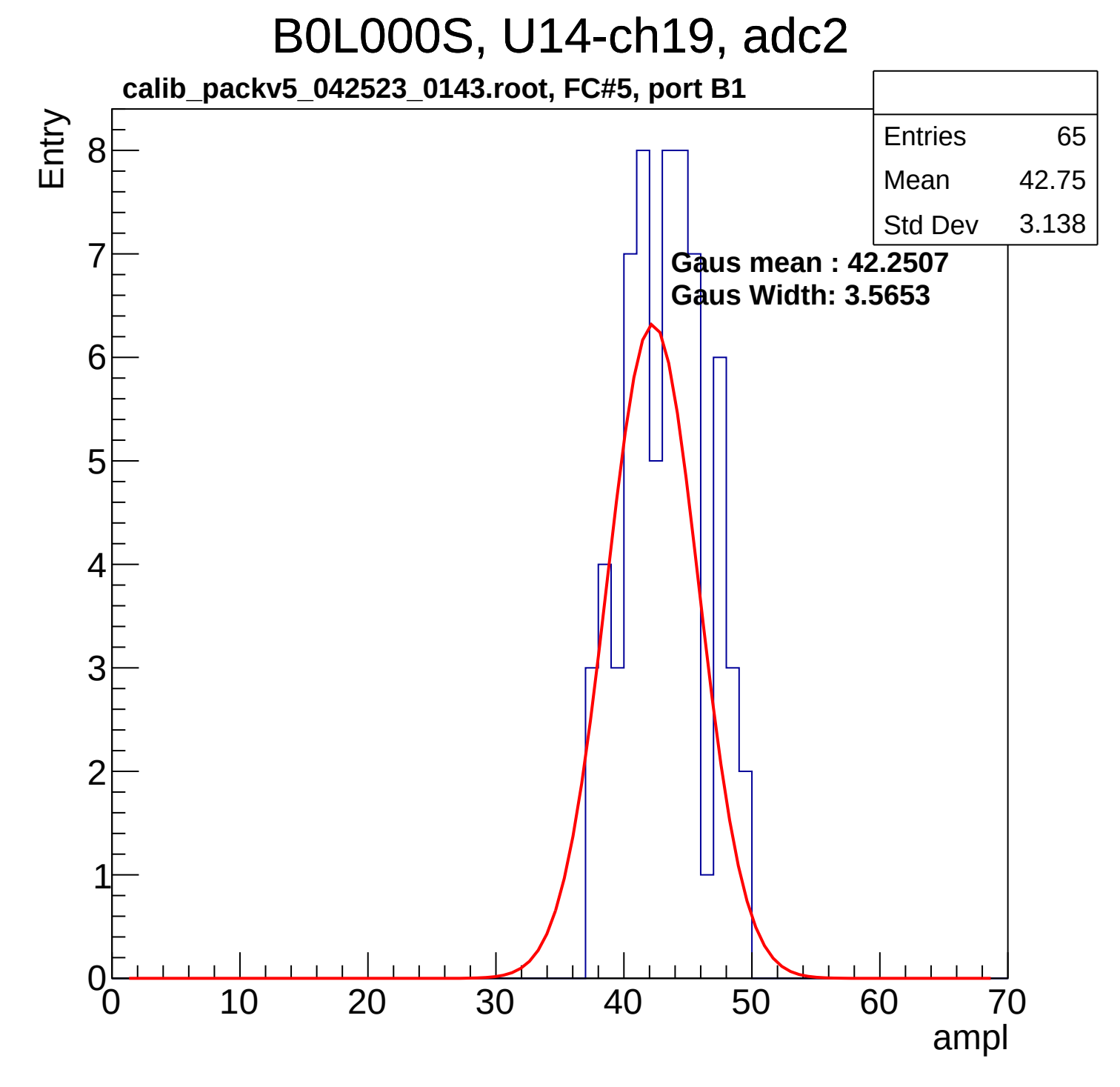
B0L000S, U14-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.75
Std Dev 3.138
Gaus mean : 42.2507
Gaus Width: 3.5653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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60
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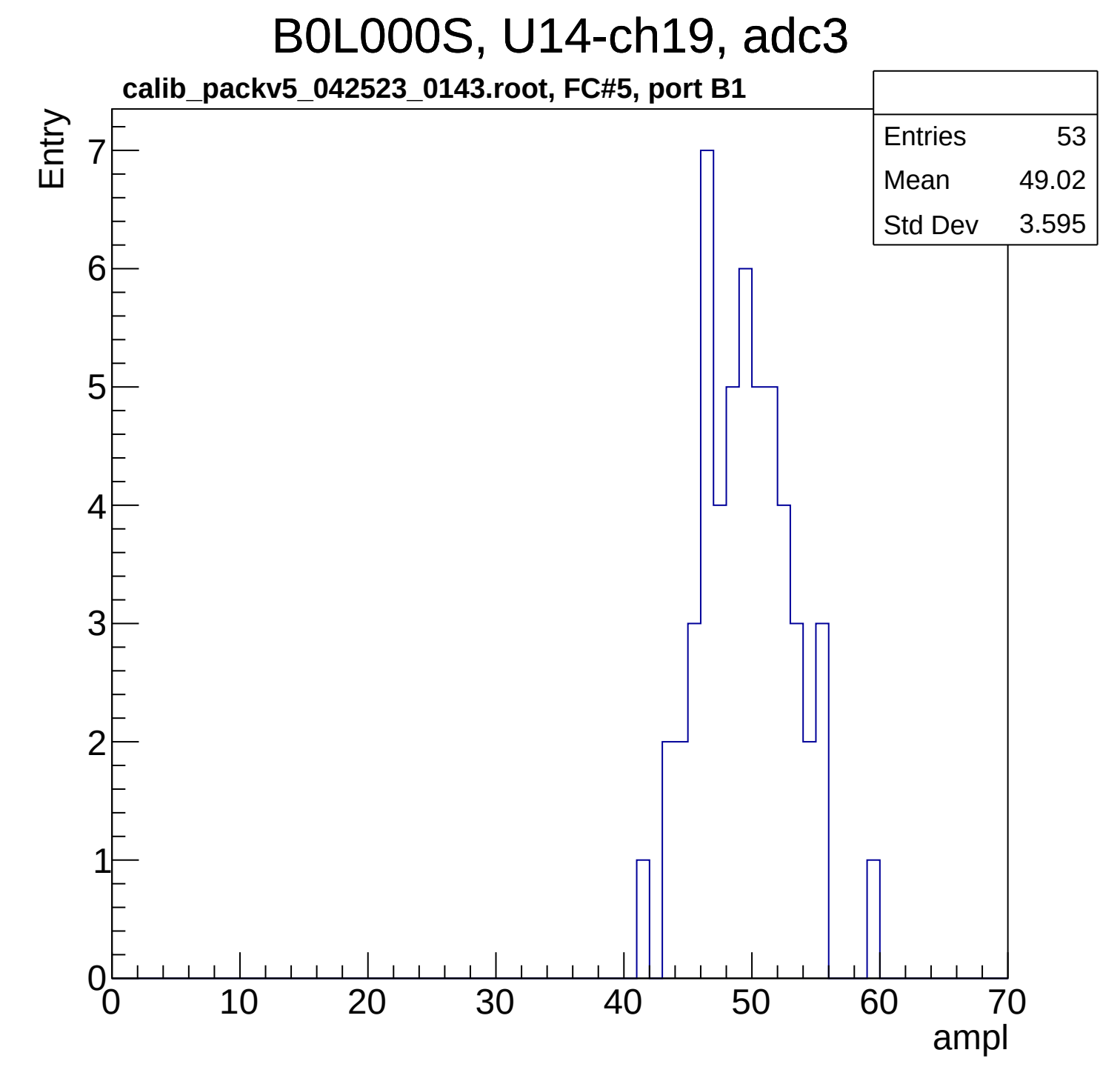
B0L000S, U14-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 49.02
Std Dev 3.595
Entry
ampl
0
1
2
3
4
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0
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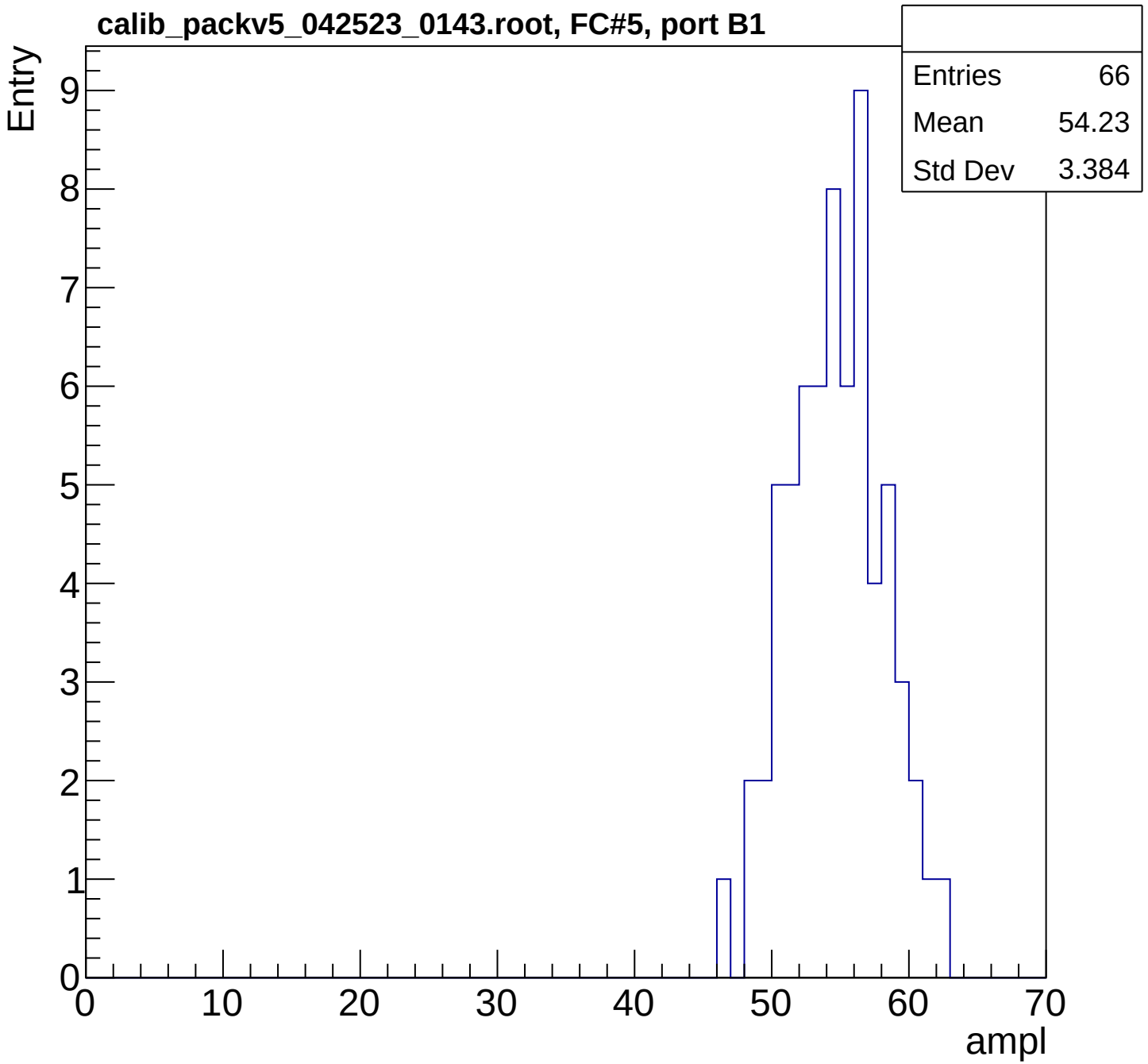
B0L000S, U14-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.23
Std Dev 3.384
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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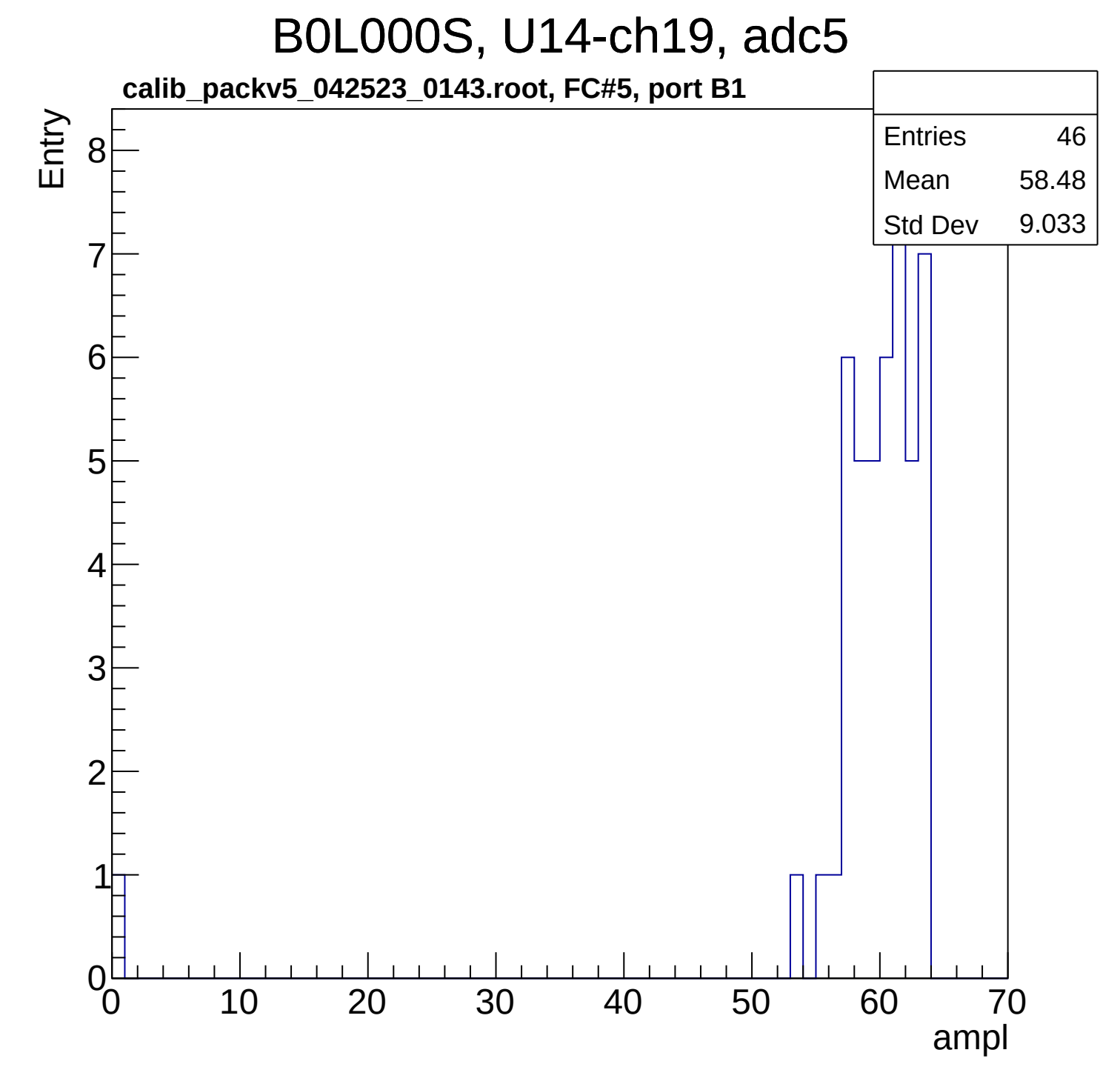
B0L000S, U14-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.48
Std Dev 9.033
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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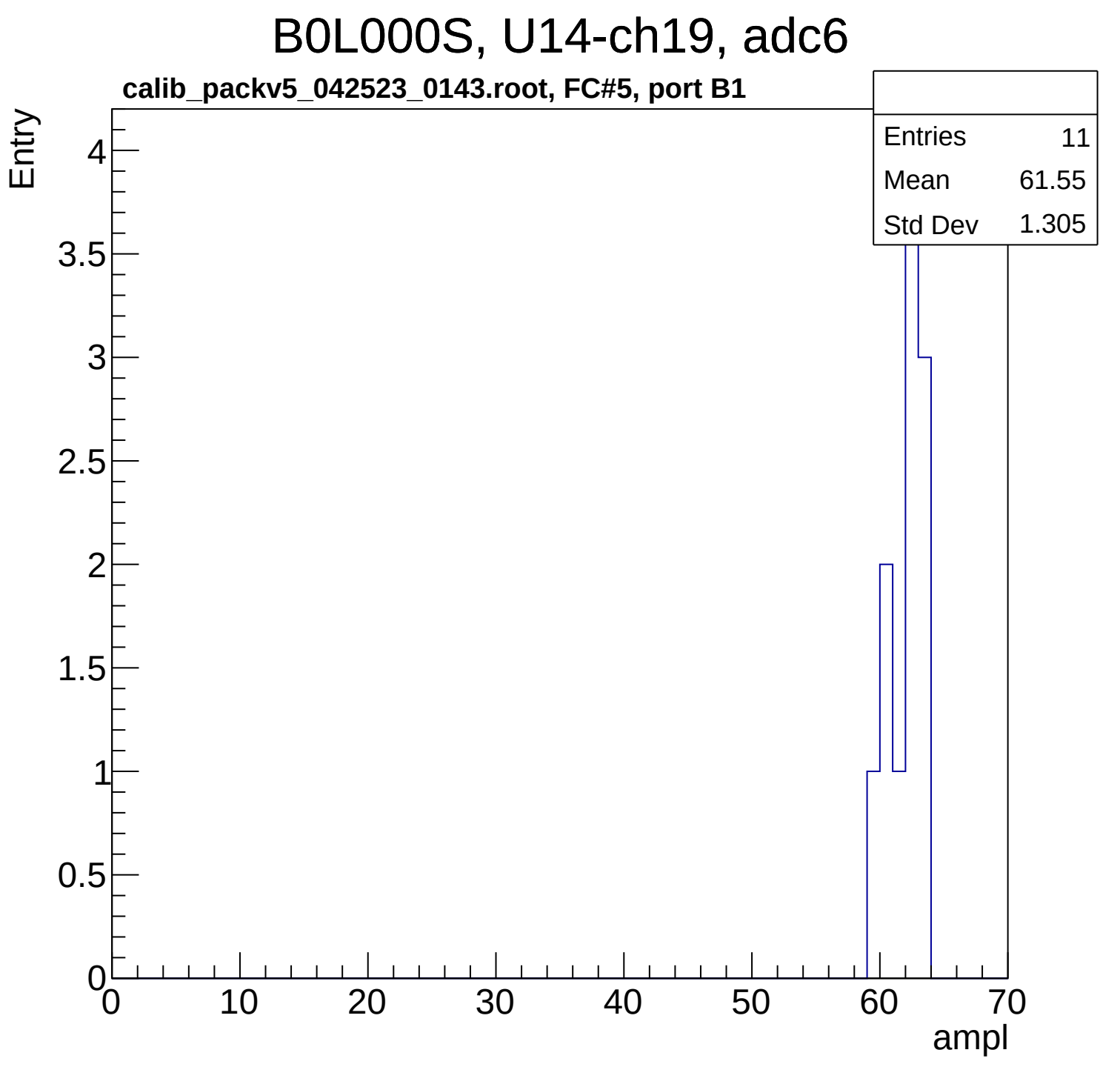
B0L000S, U14-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61.55
Std Dev 1.305
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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40
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60
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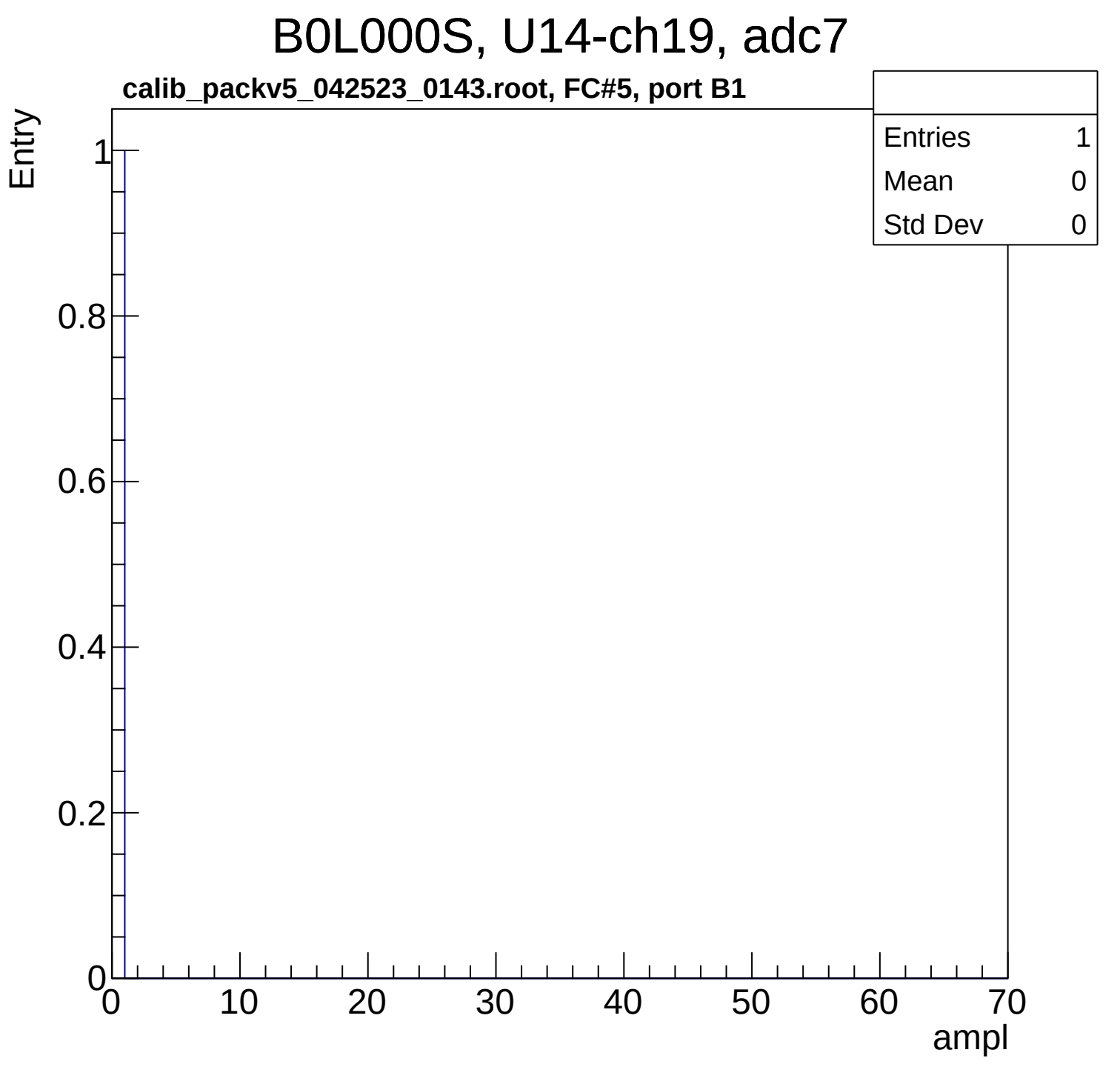
B0L000S, U14-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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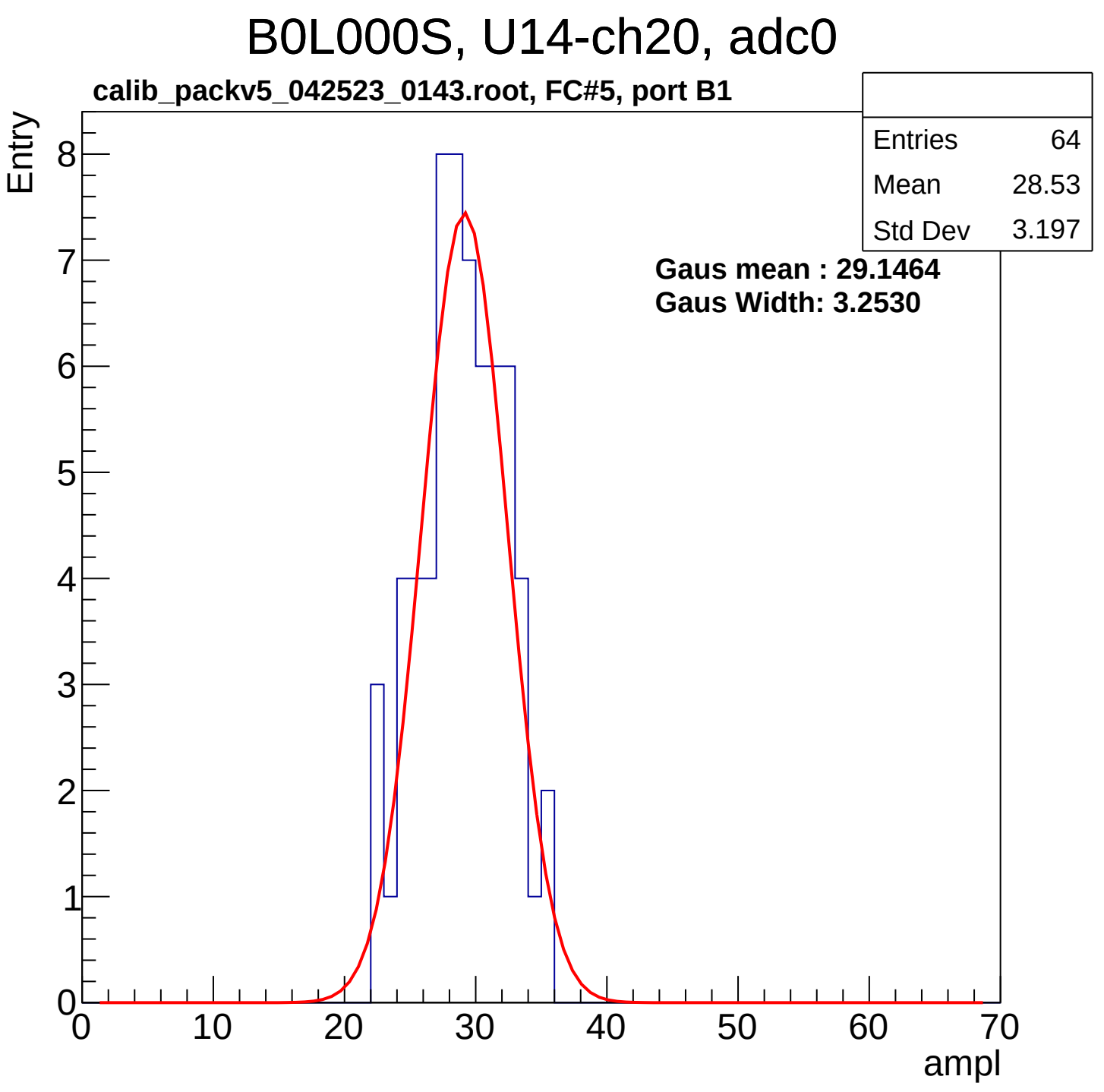
50
60
70
ampl


B0L000S, U14-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.53
Std Dev 3.197
Gaus mean : 29.1464
Gaus Width: 3.2530
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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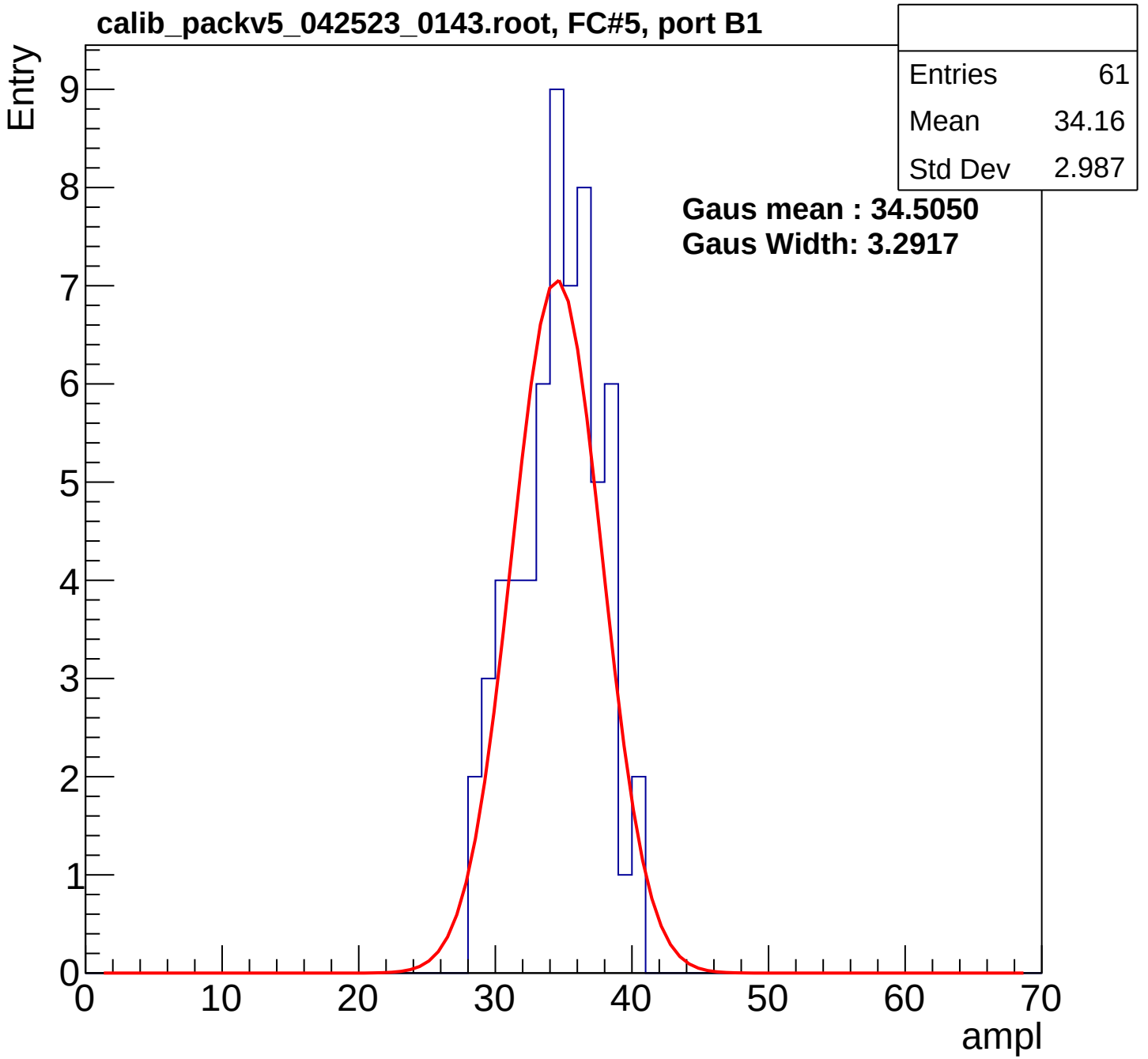
B0L000S, U14-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.16
Std Dev 2.987
Gaus mean : 34.5050
Gaus Width: 3.2917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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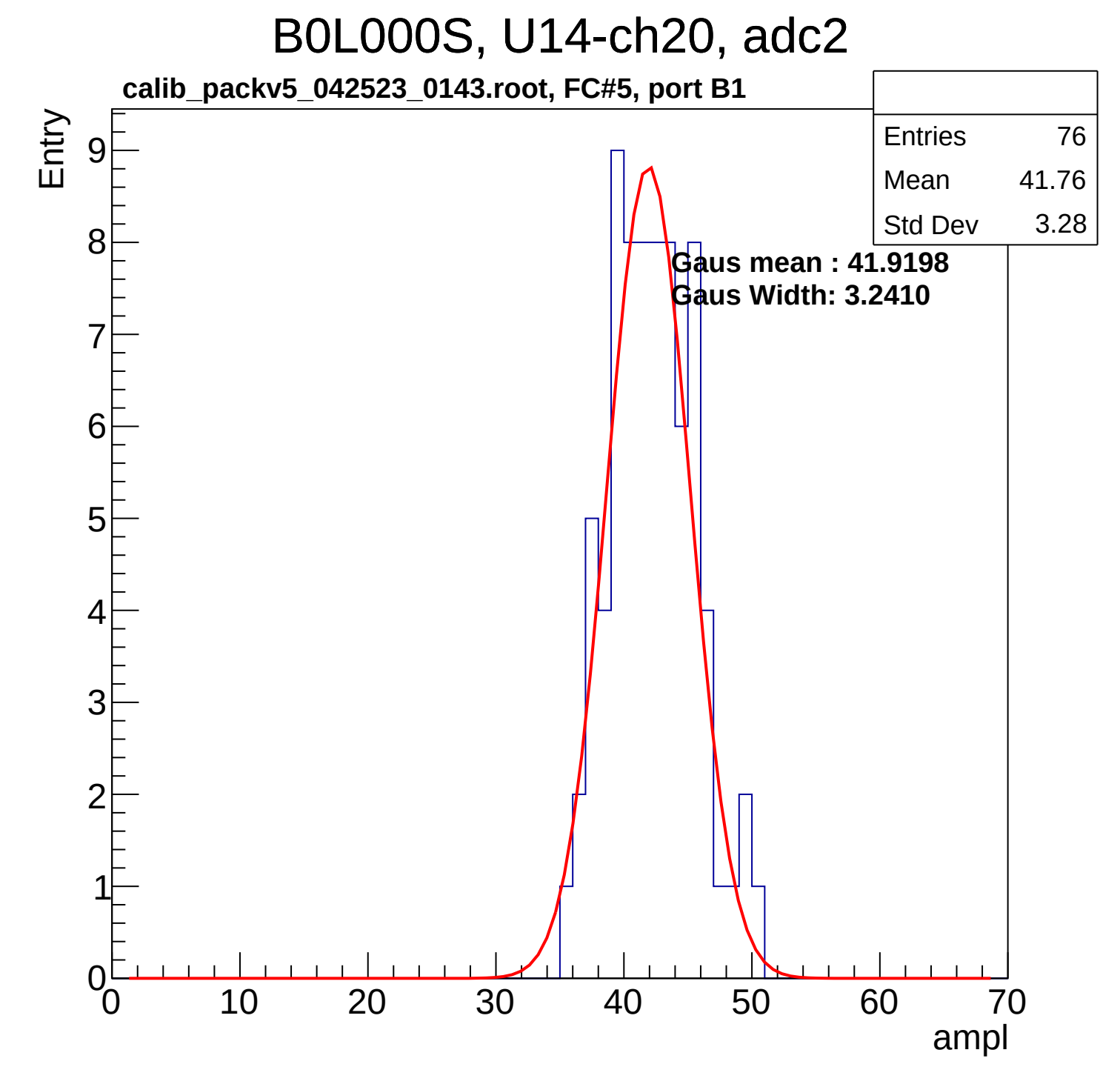
B0L000S, U14-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 41.76
Std Dev 3.28
Gaus mean : 41.9198
Gaus Width: 3.2410
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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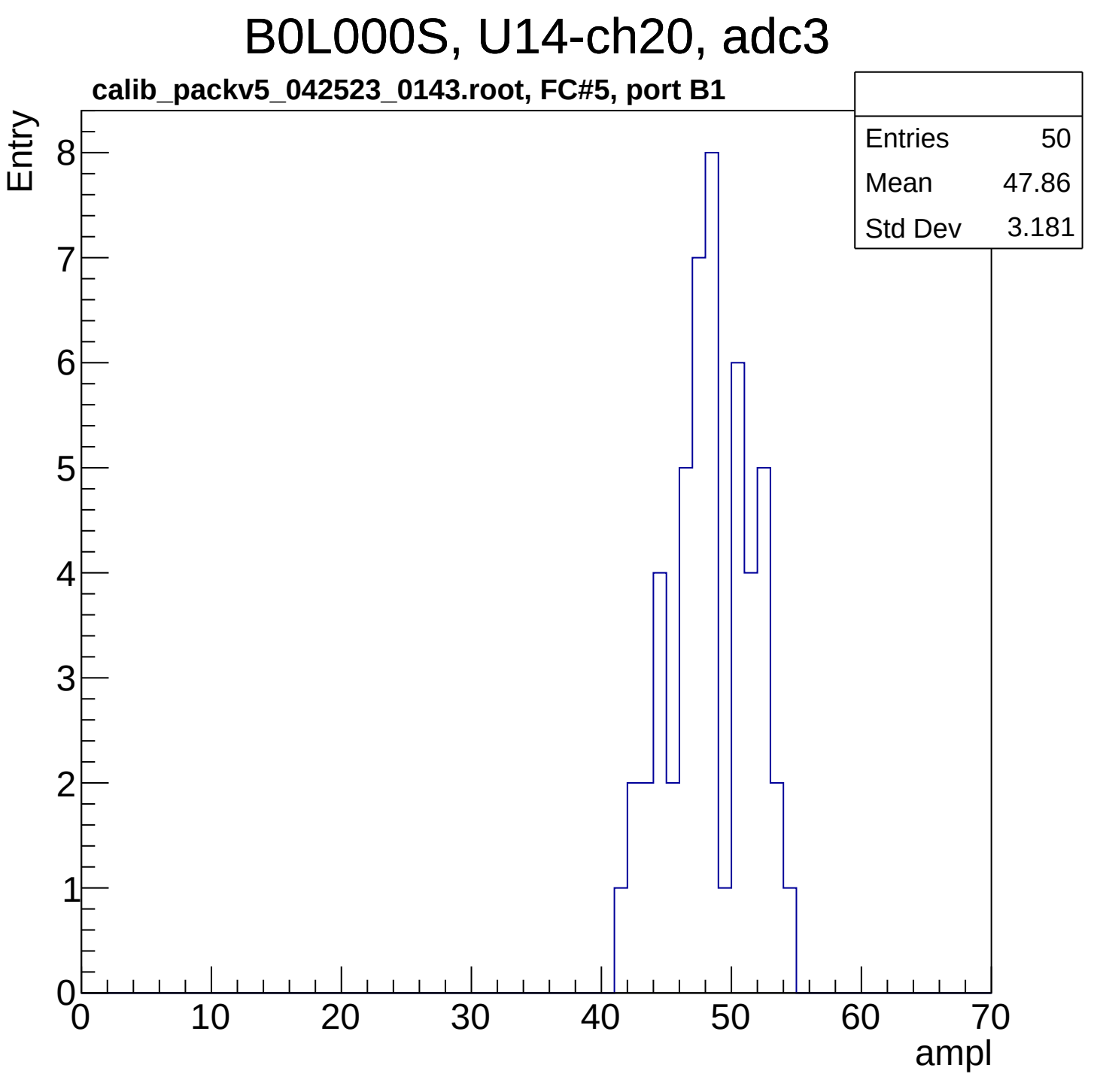
B0L000S, U14-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 47.86
Std Dev 3.181
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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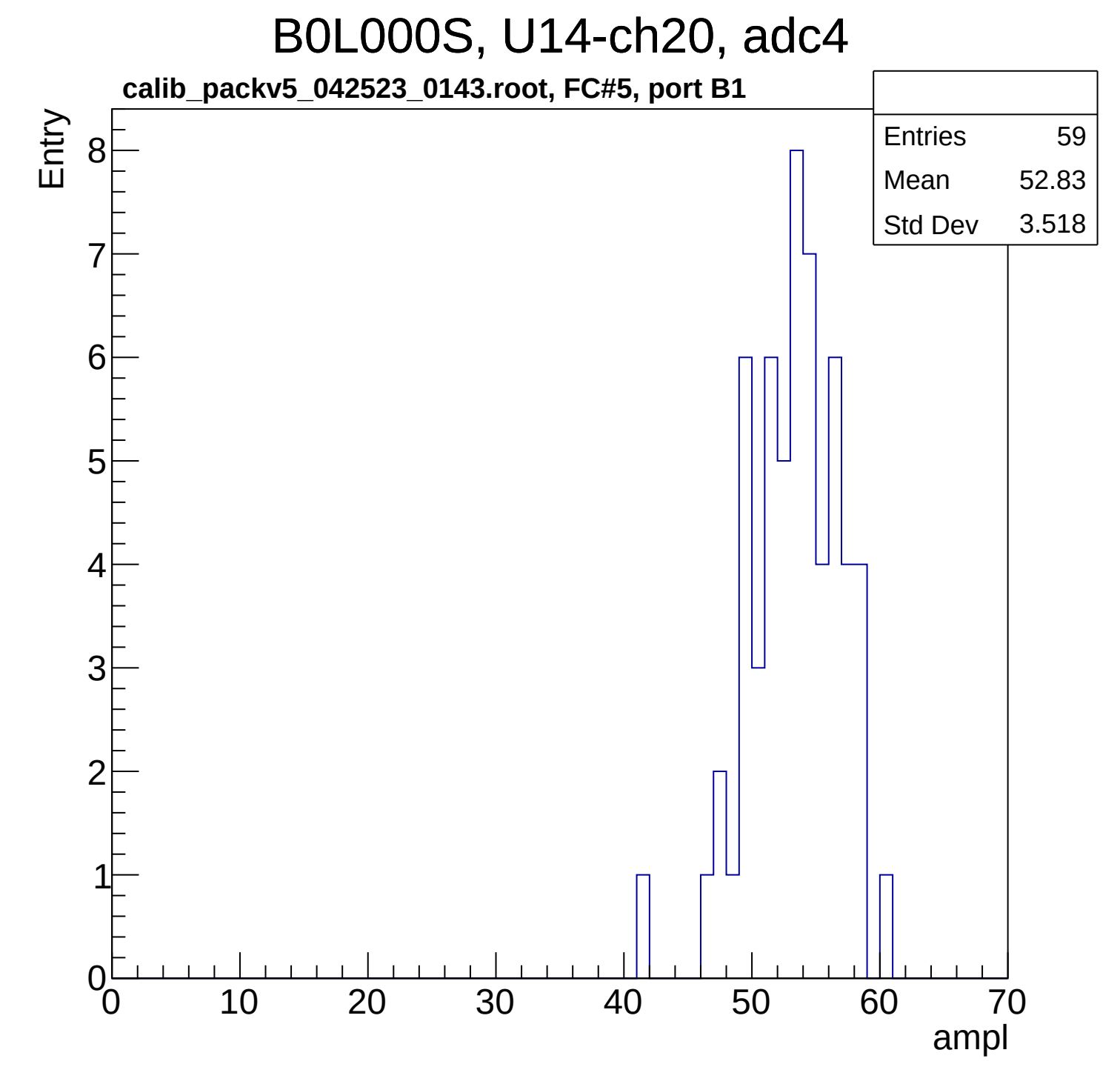
40
50
60
70


B0L000S, U14-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 52.83
Std Dev 3.518
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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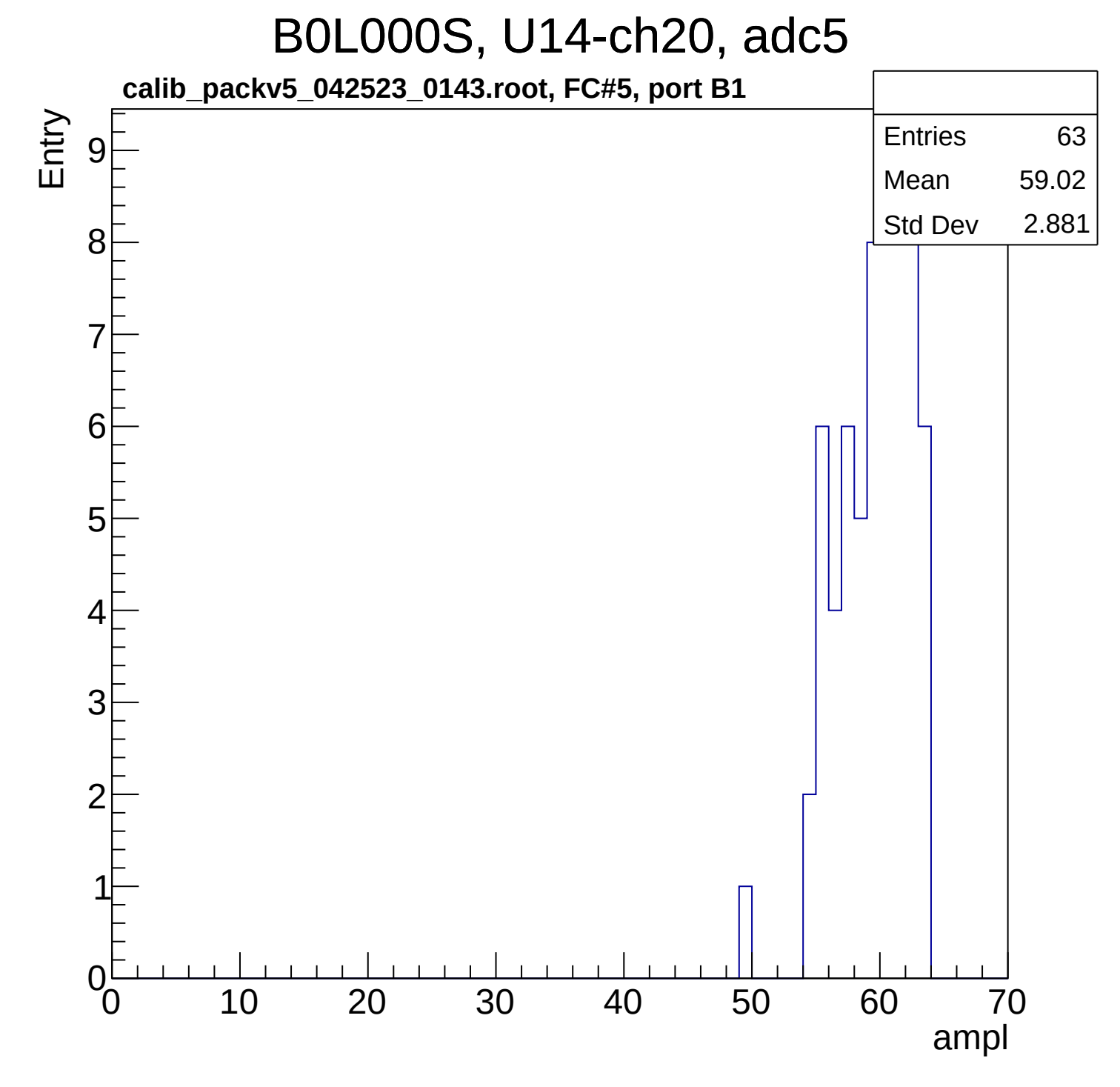
B0L000S, U14-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 59.02
Std Dev 2.881
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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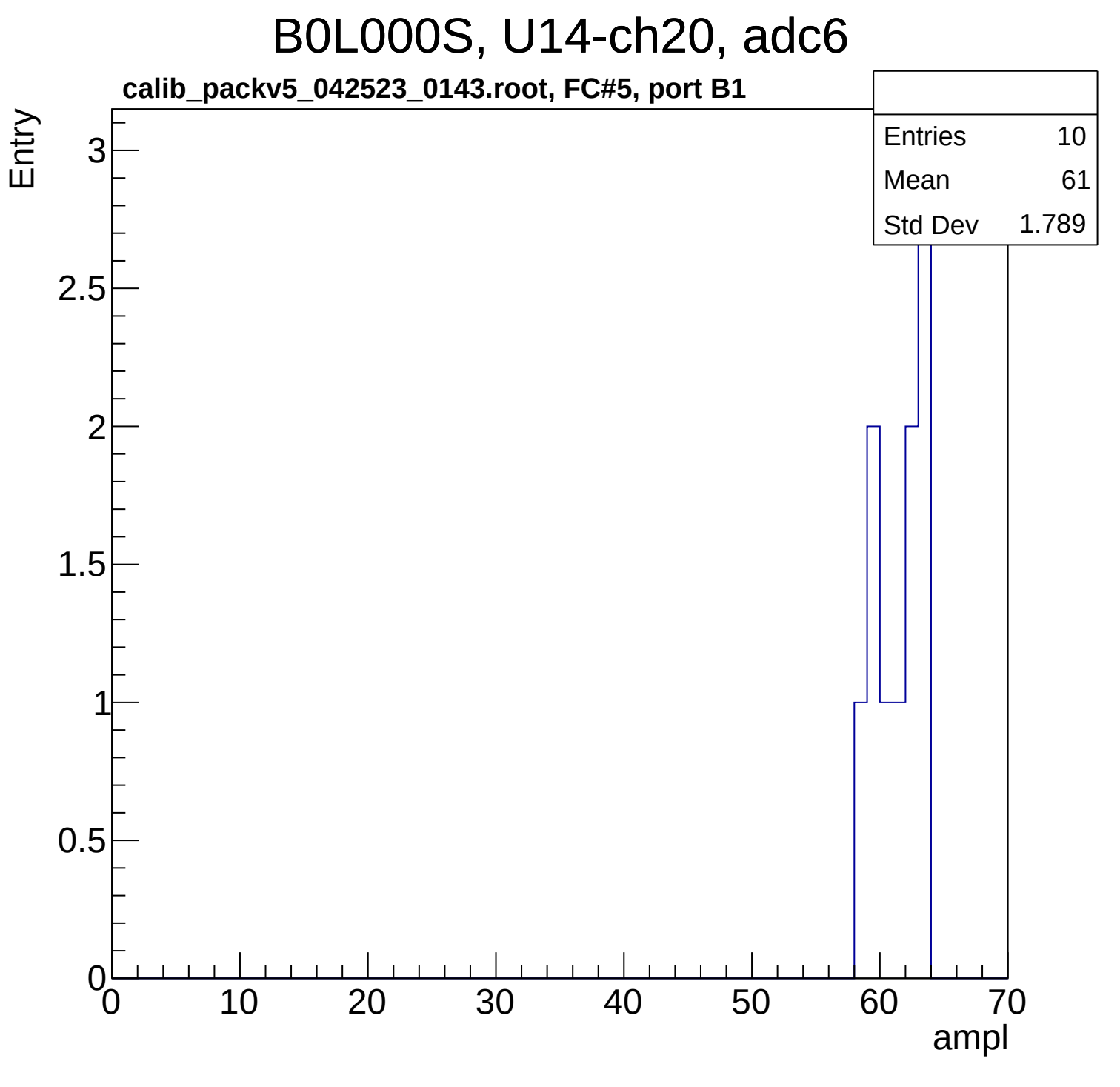
B0L000S, U14-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61
Std Dev 1.789
Entry
ampl
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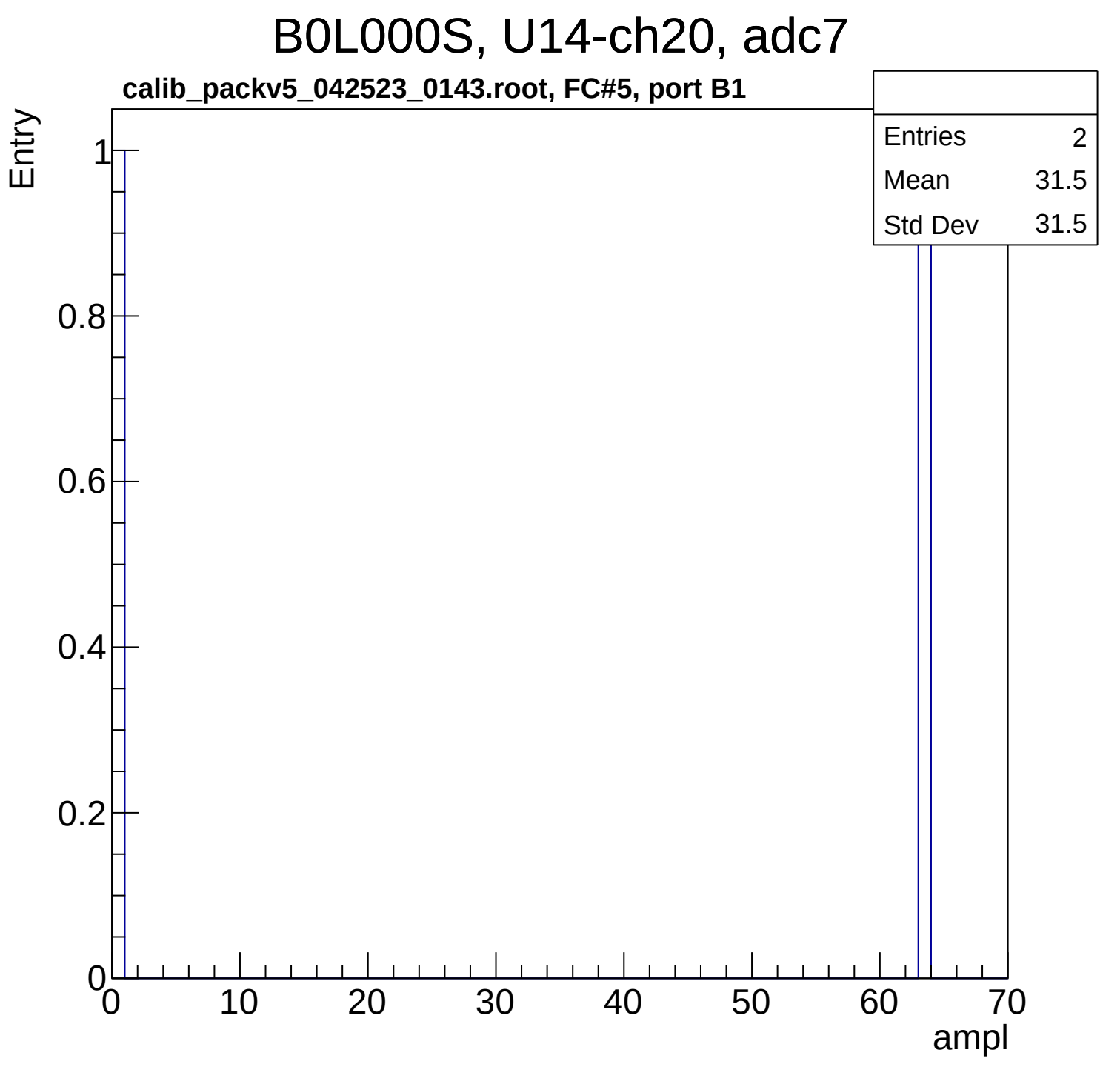
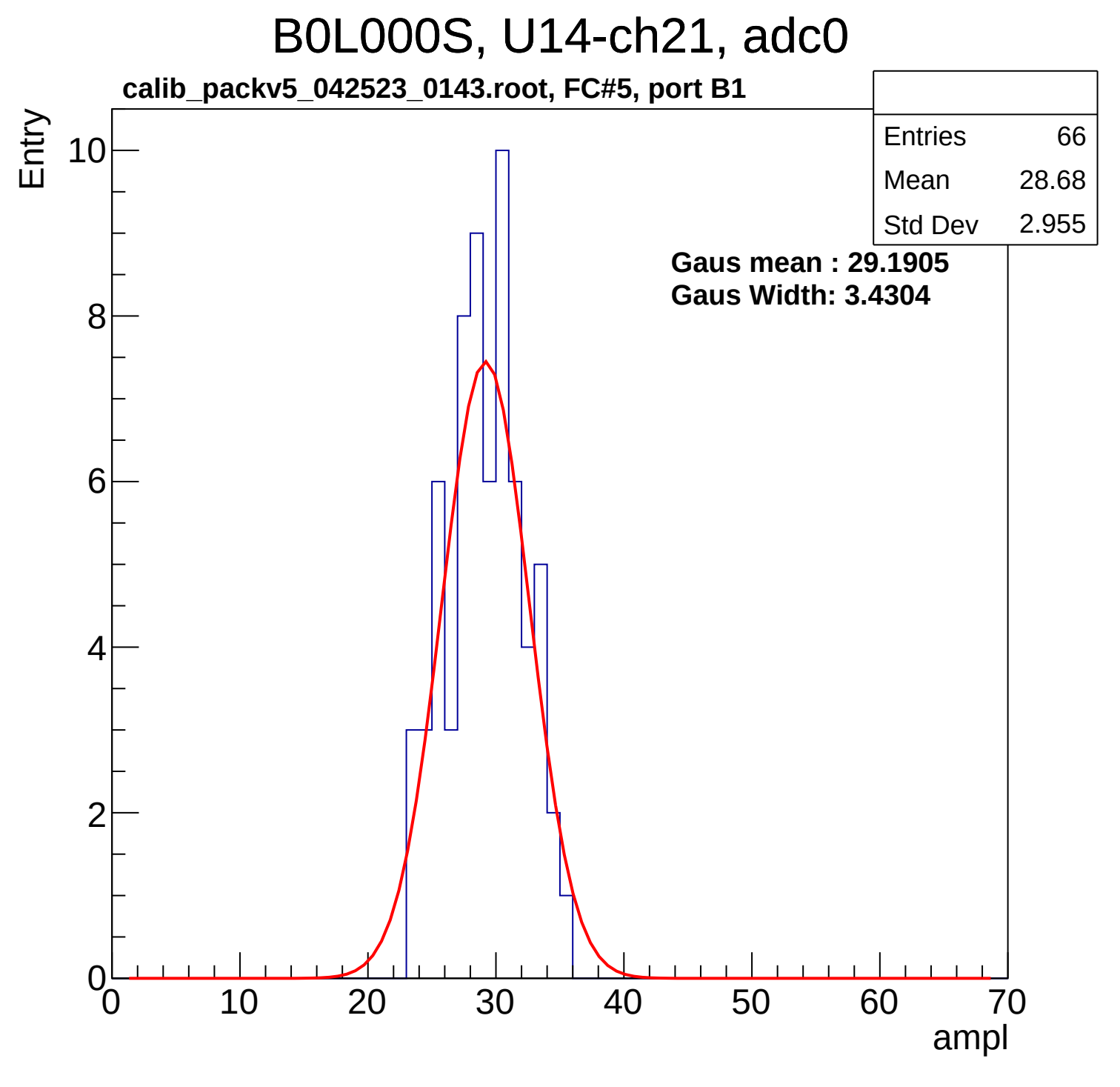
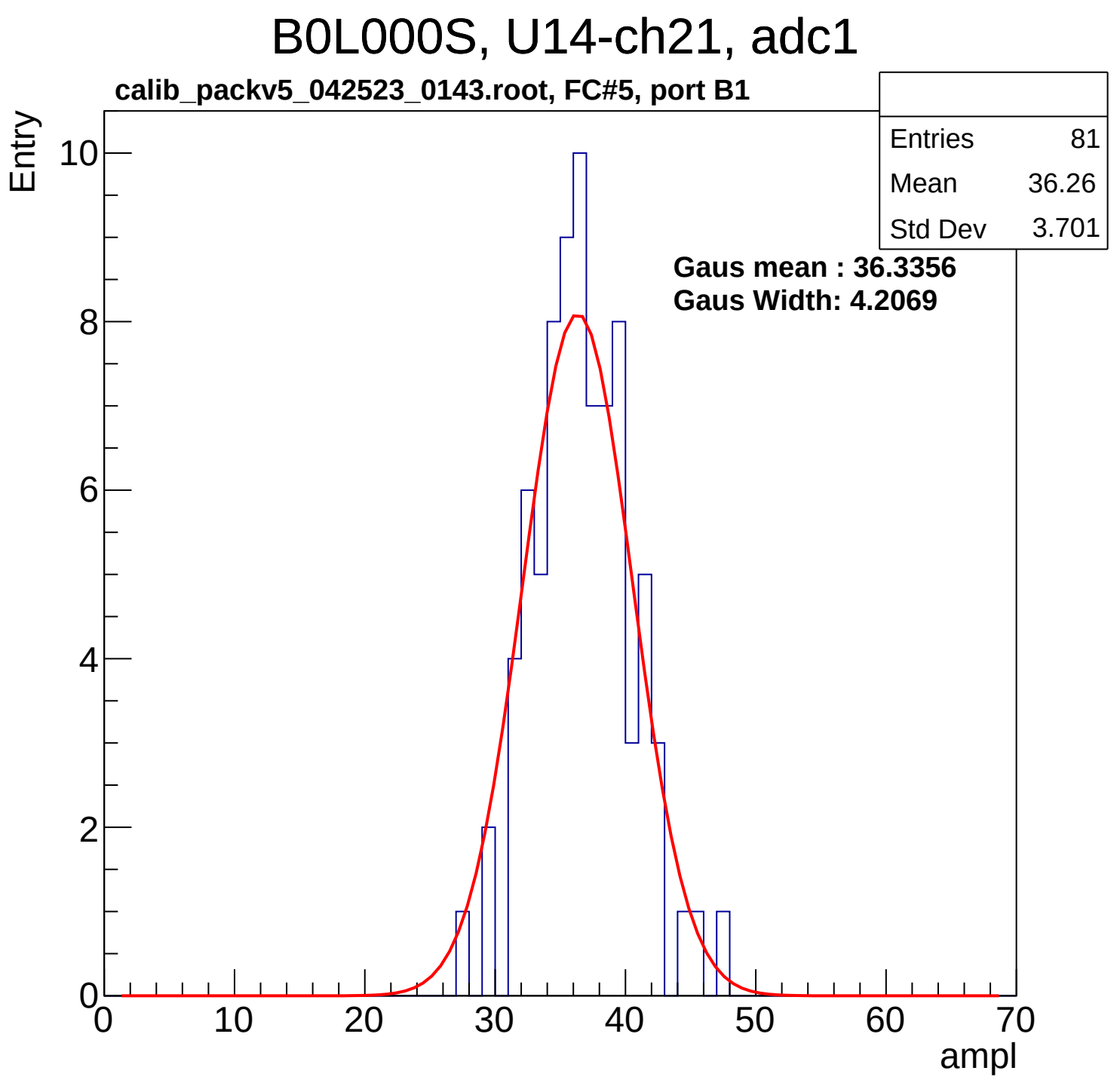
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.68
Std Dev 2.955
Gaus mean : 29.1905
Gaus Width: 3.4304
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 36.26
Std Dev 3.701
Gaus mean : 36.3356
Gaus Width: 4.2069
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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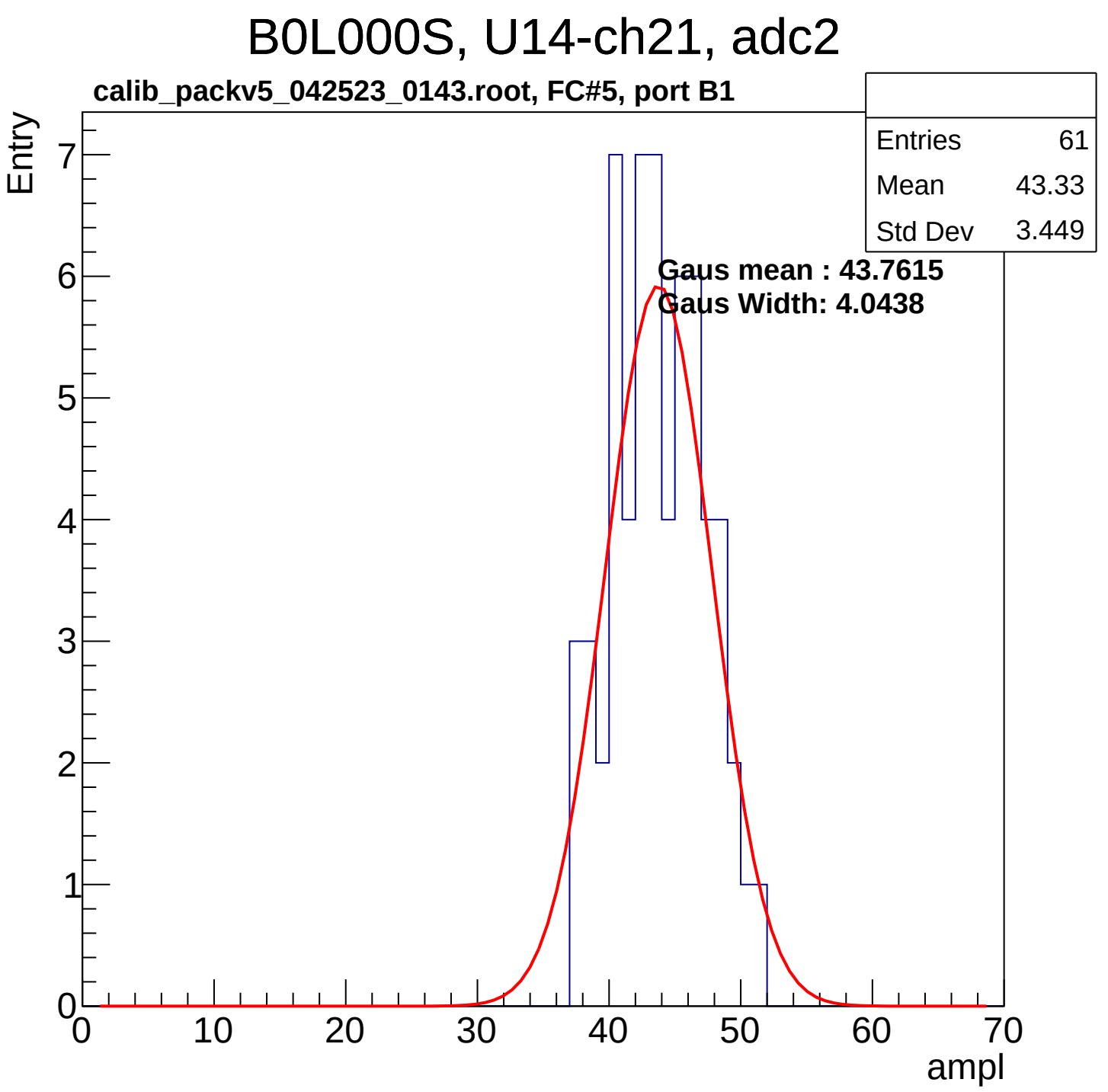
B0L000S, U14-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.33
Std Dev 3.449
Gaus mean : 43.7615
Gaus Width: 4.0438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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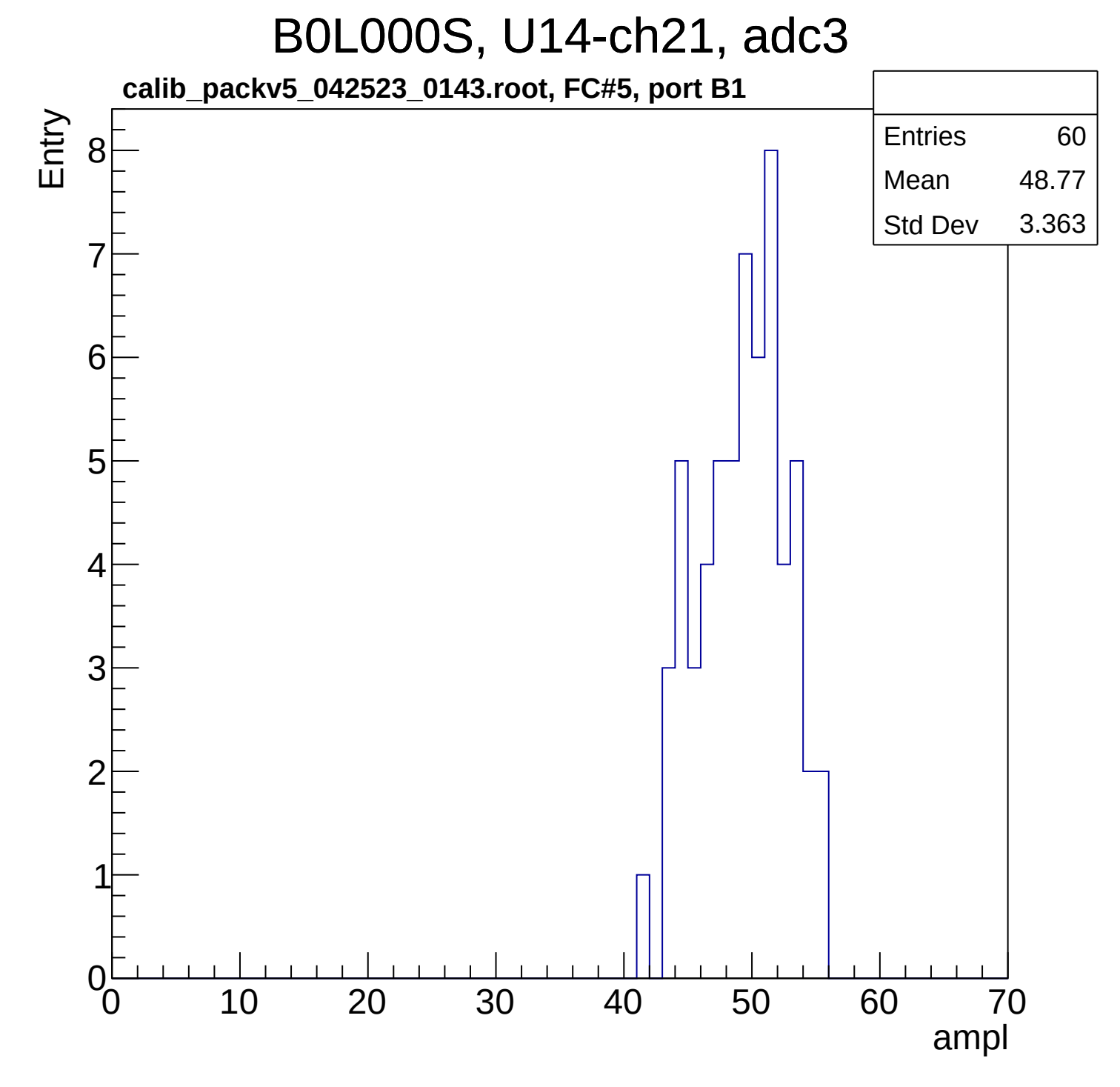
B0L000S, U14-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.77
Std Dev 3.363
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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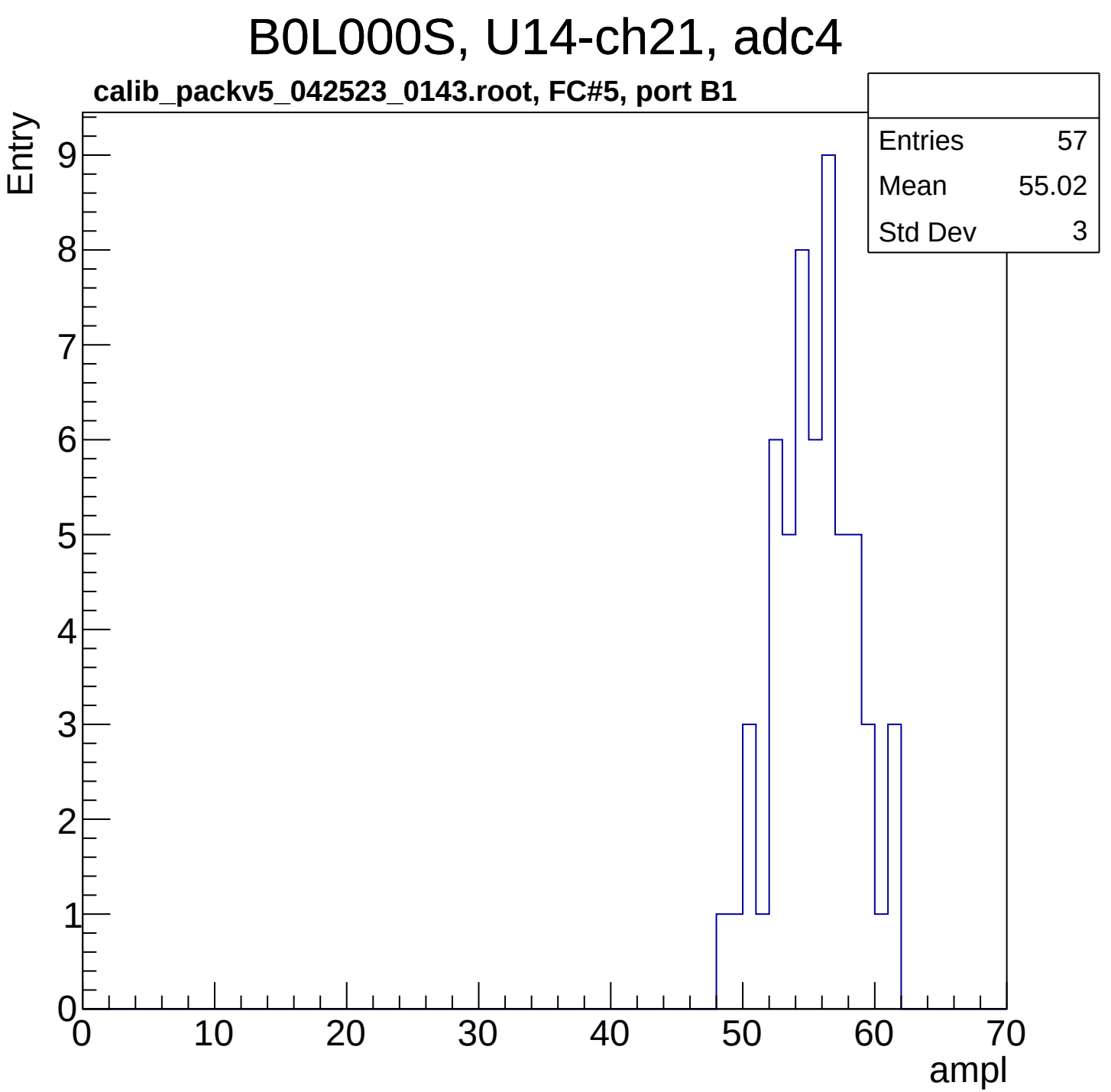
B0L000S, U14-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.02
Std Dev 3
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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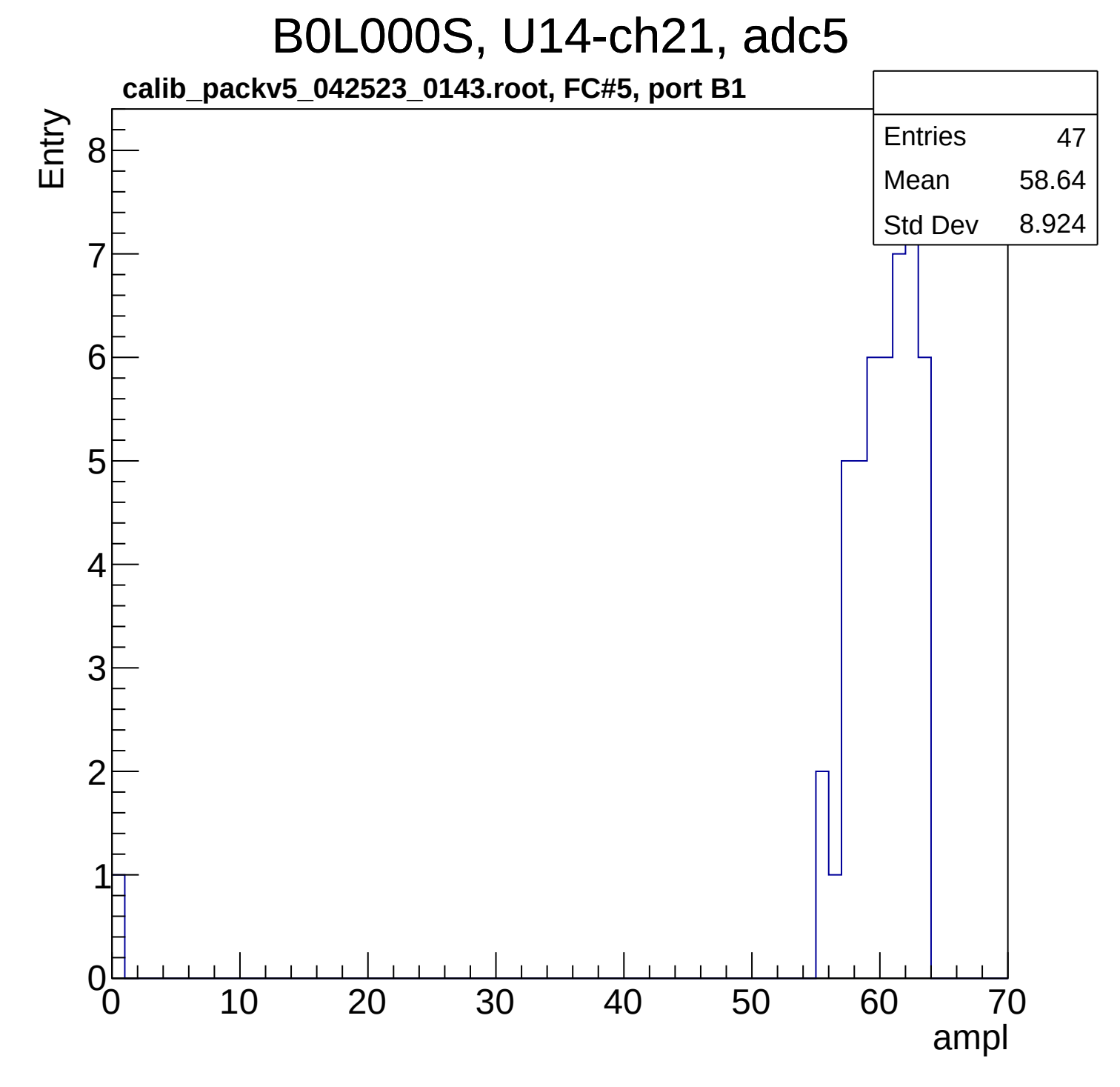
B0L000S, U14-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.64
Std Dev 8.924
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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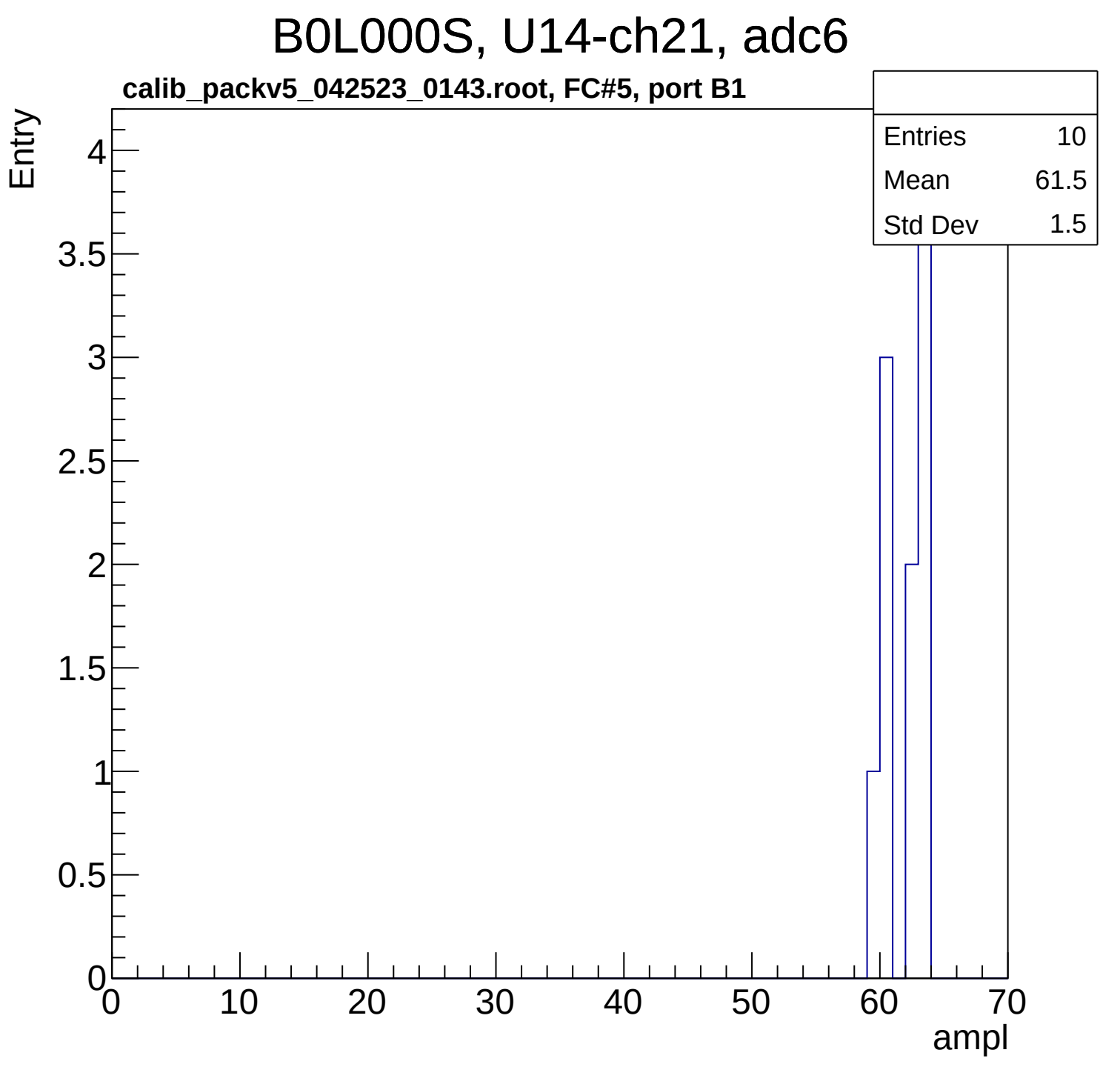
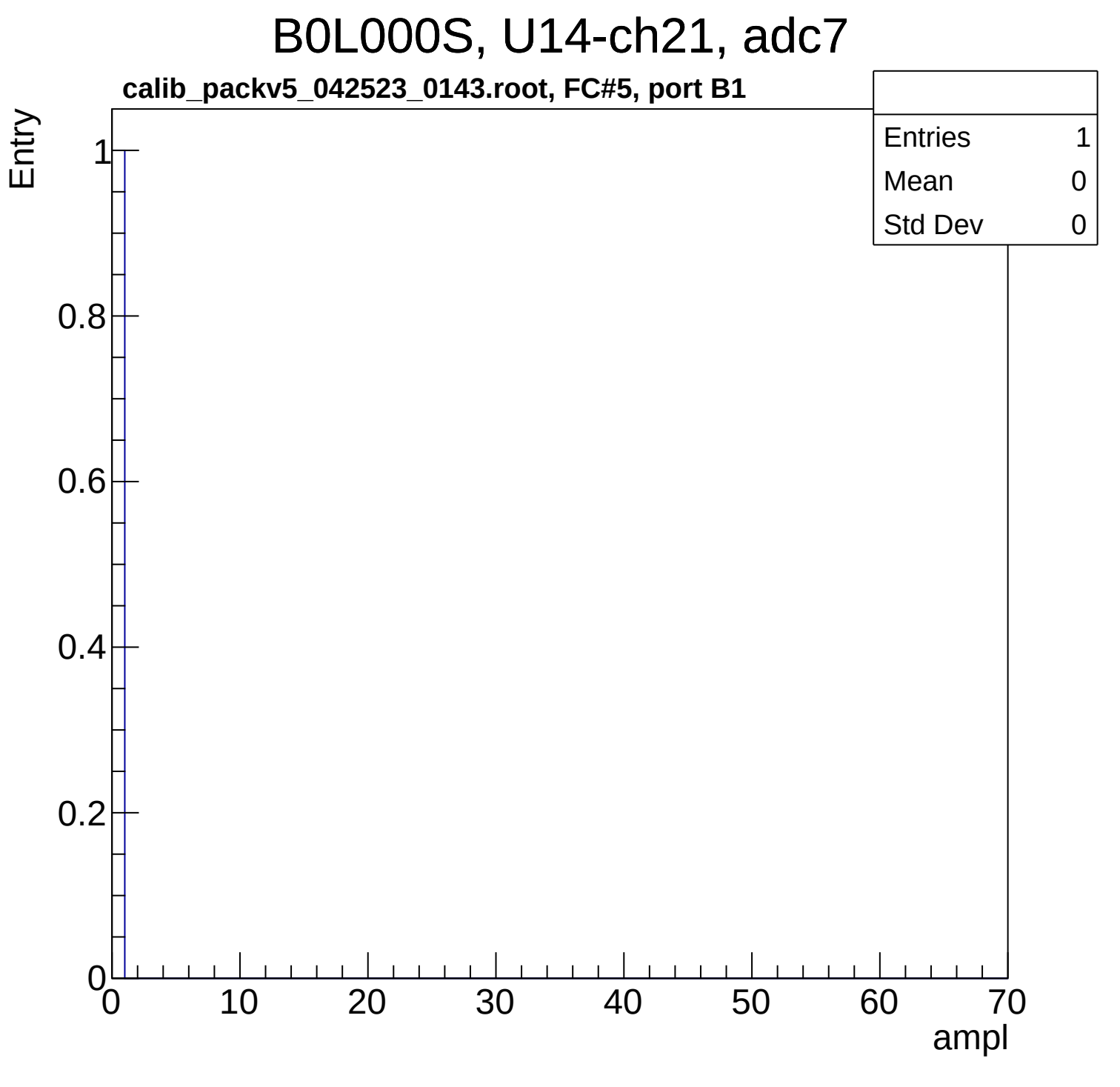

B0L000S, U14-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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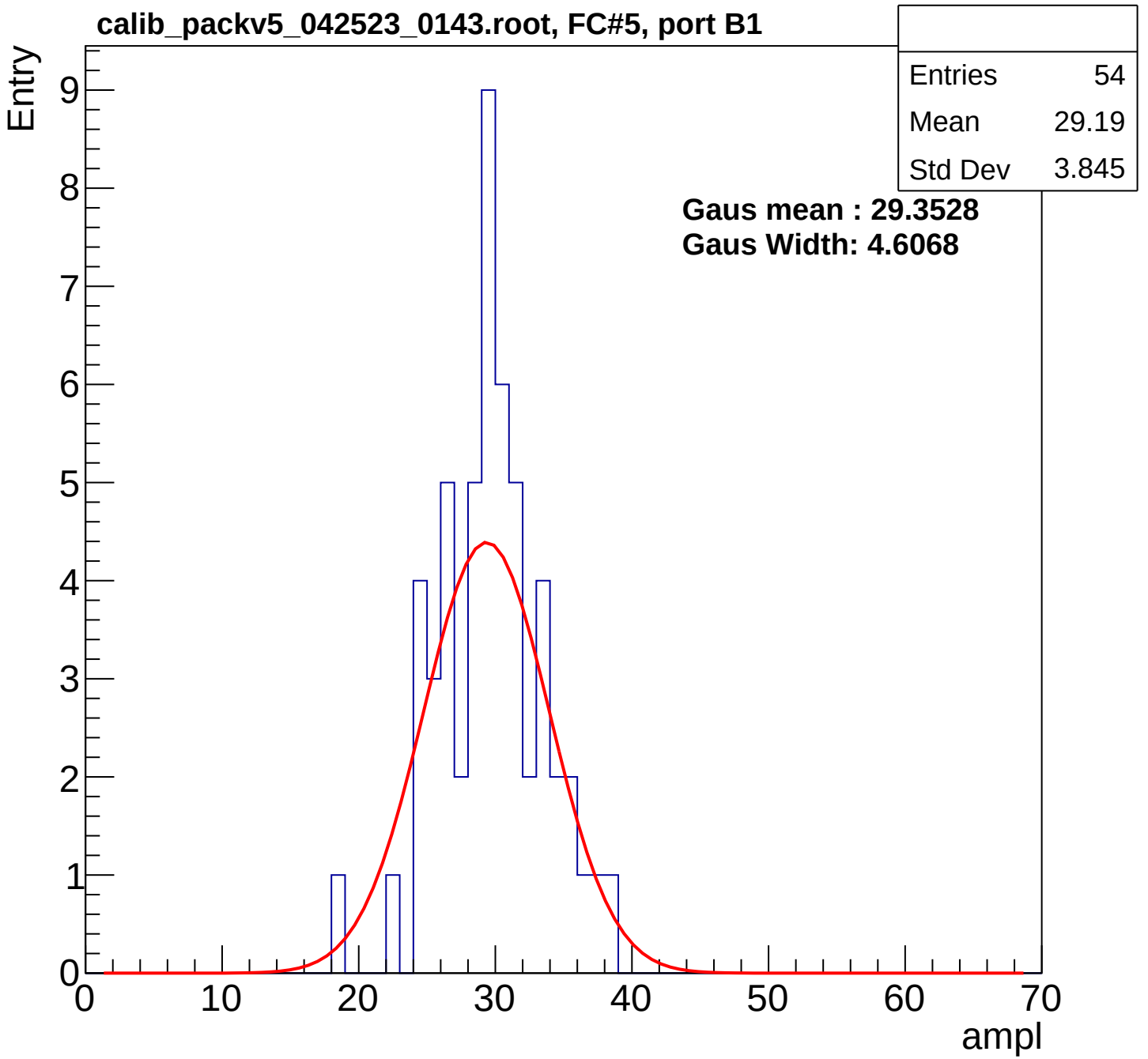
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.19
Std Dev 3.845
Gaus mean : 29.3528
Gaus Width: 4.6068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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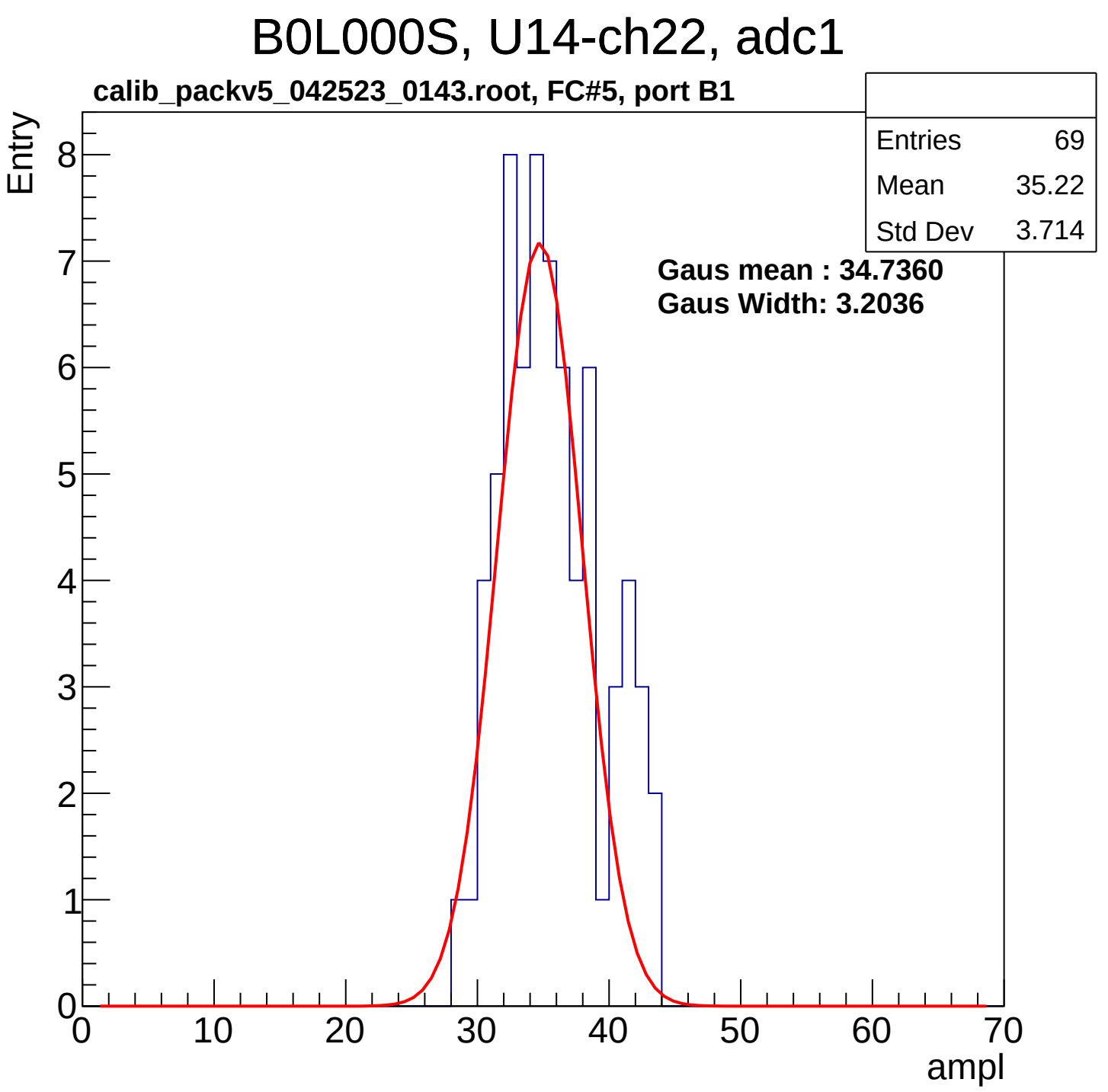
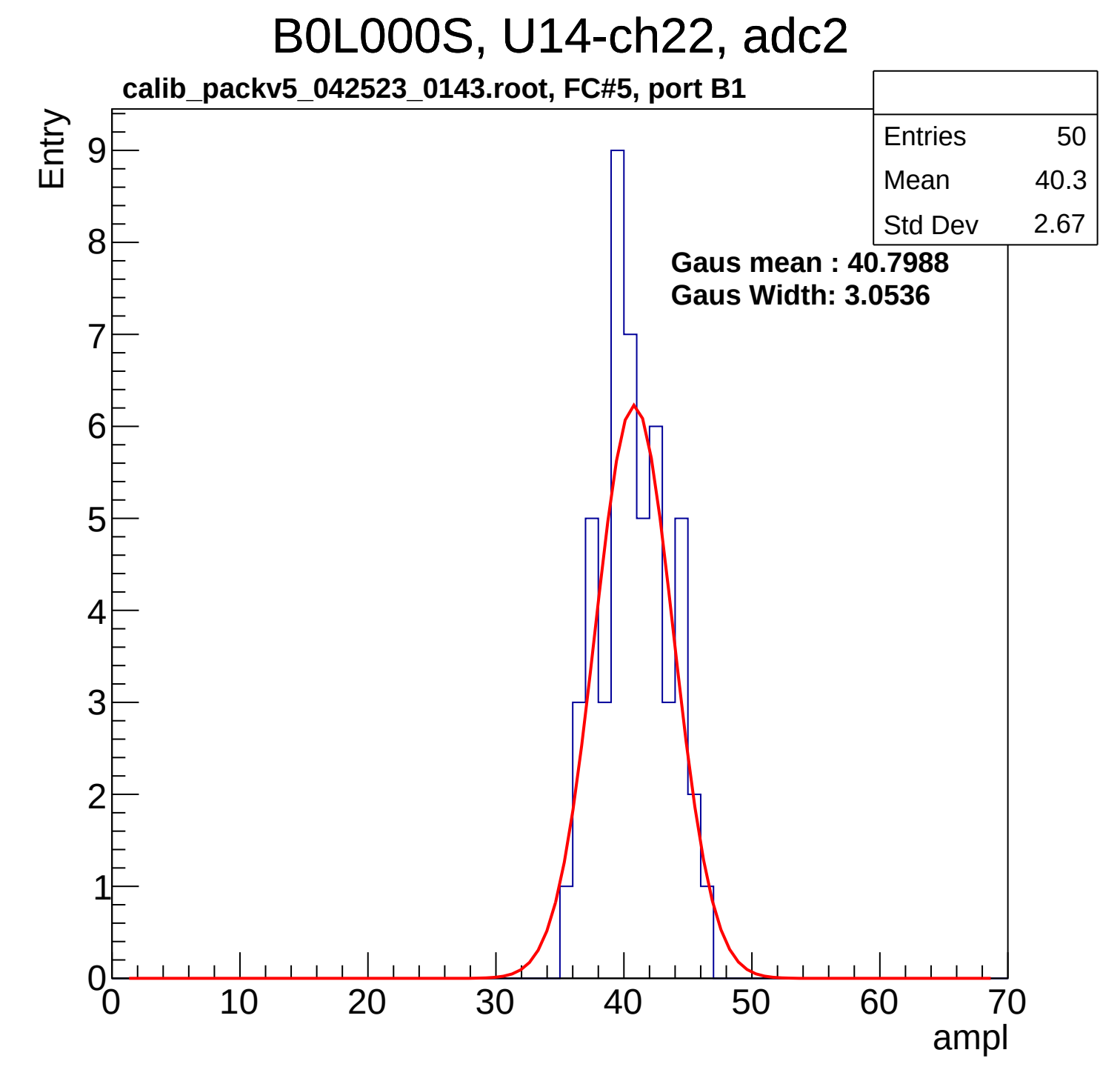
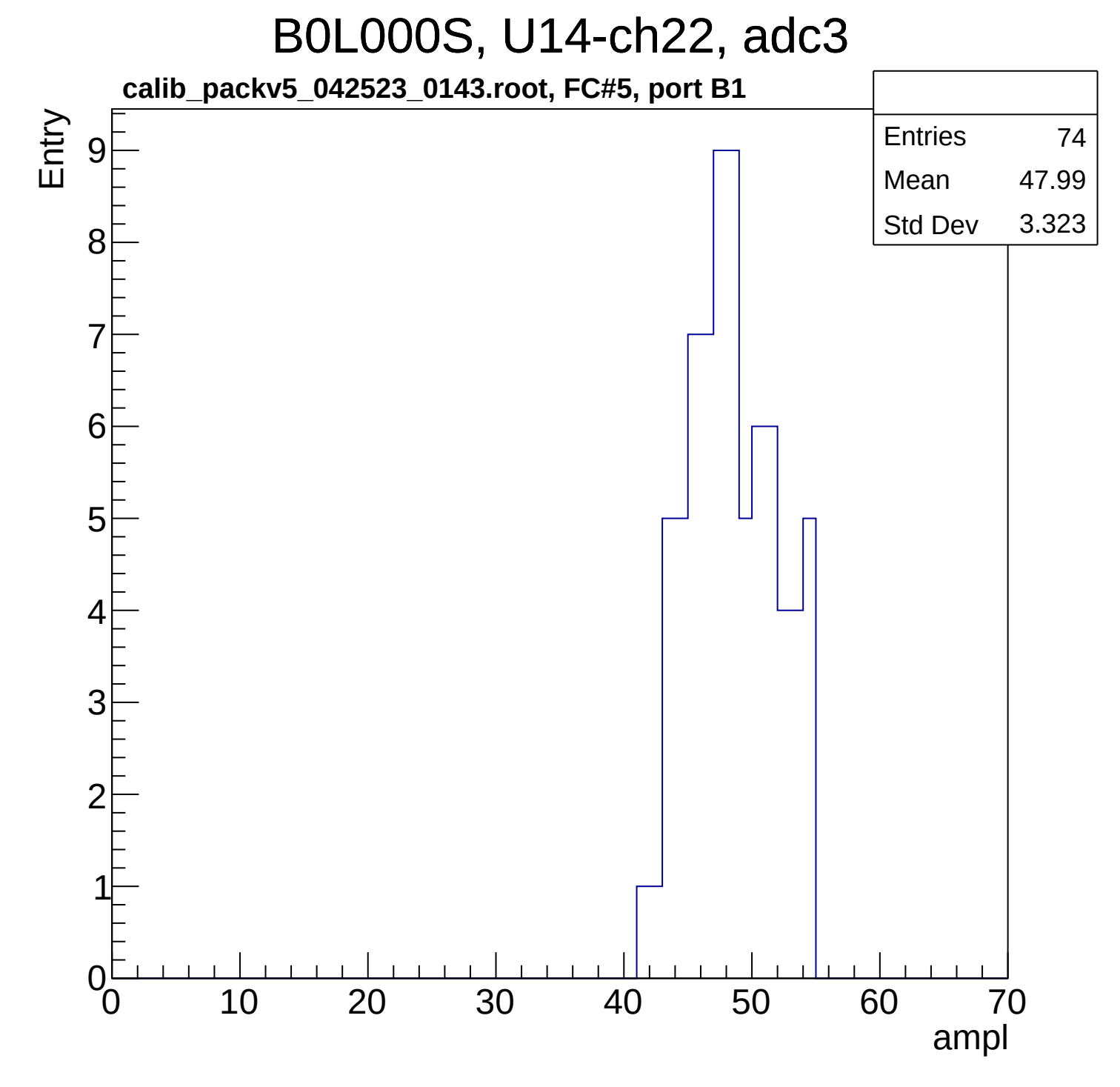

B0L000S, U14-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.22
Std Dev 3.714
Gaus mean : 34.7360
Gaus Width: 3.2036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 40.3
Std Dev 2.67
Gaus mean : 40.7988
Gaus Width: 3.0536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 47.99
Std Dev 3.323
Entry
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3
4
5
6
7
8
9
0
10
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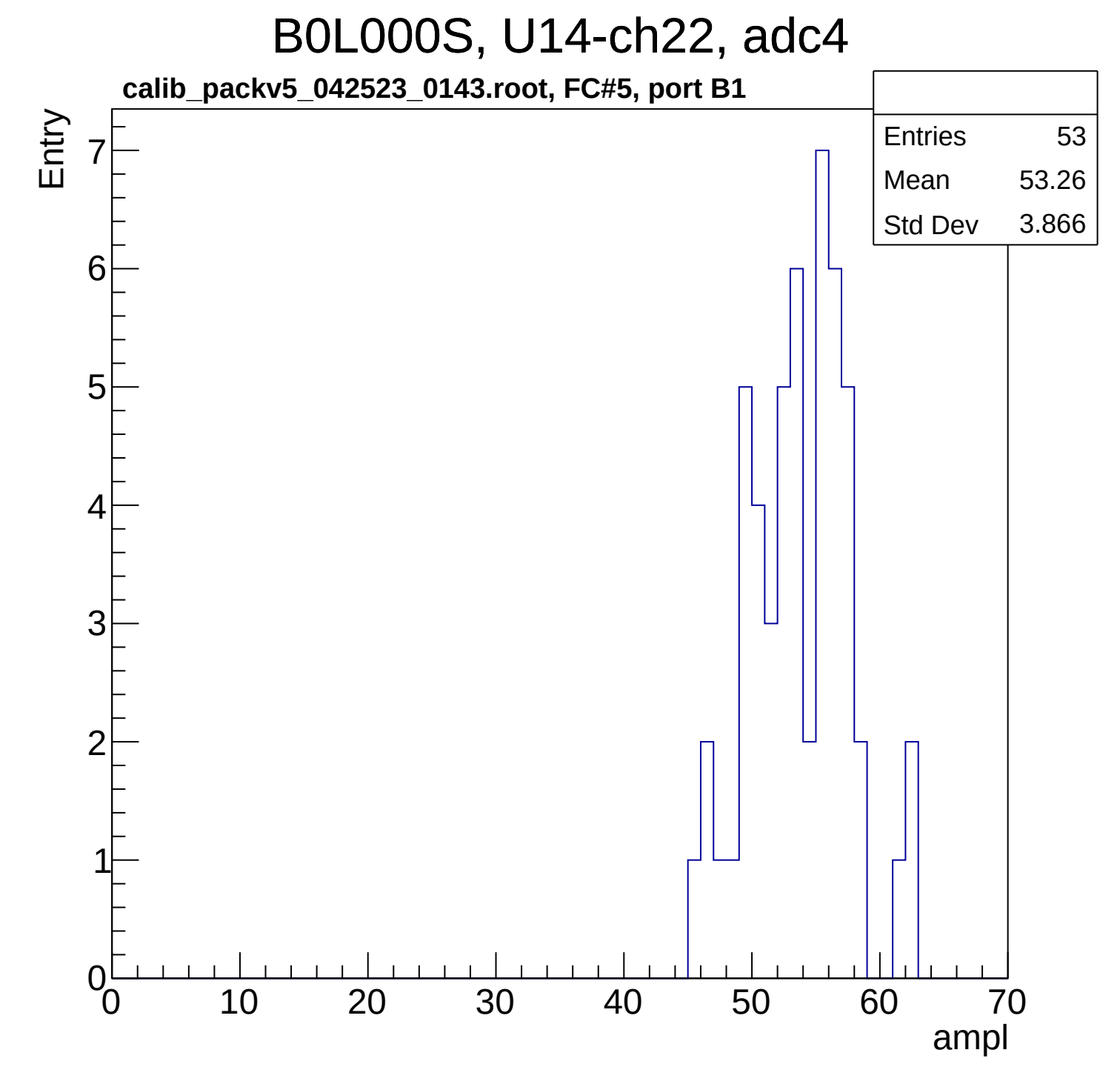
50
60
70
ampl


B0L000S, U14-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.26
Std Dev 3.866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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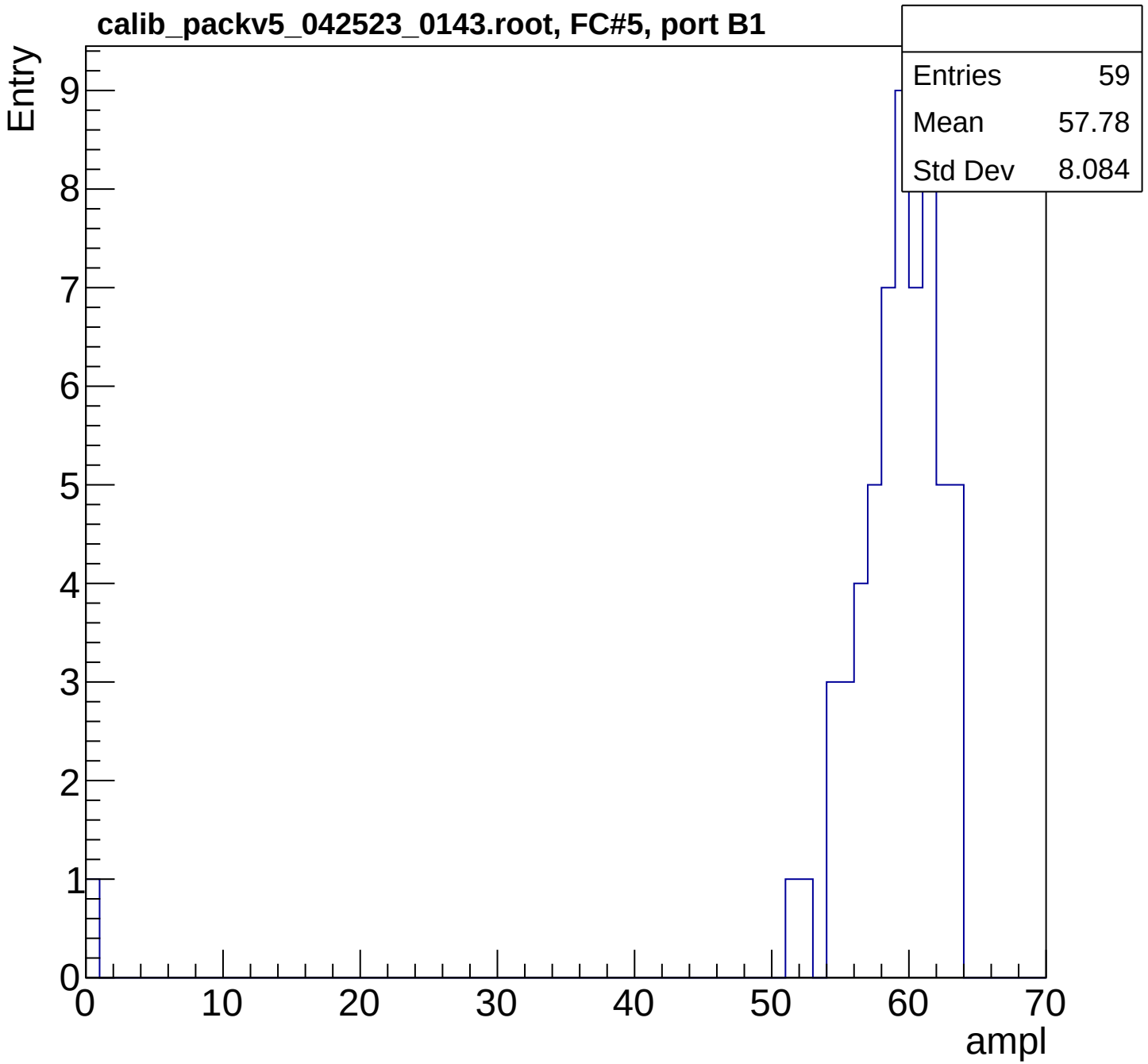
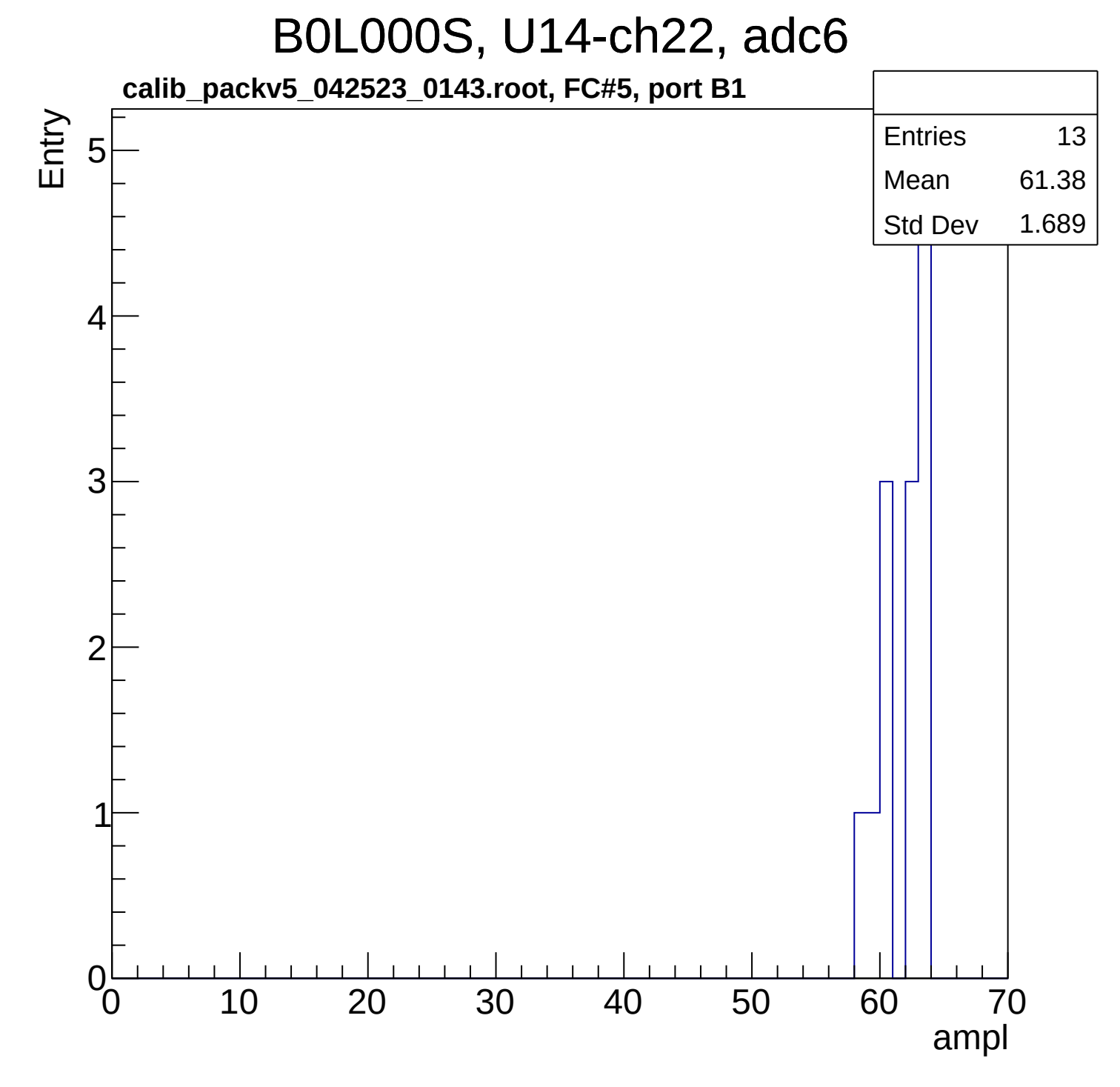
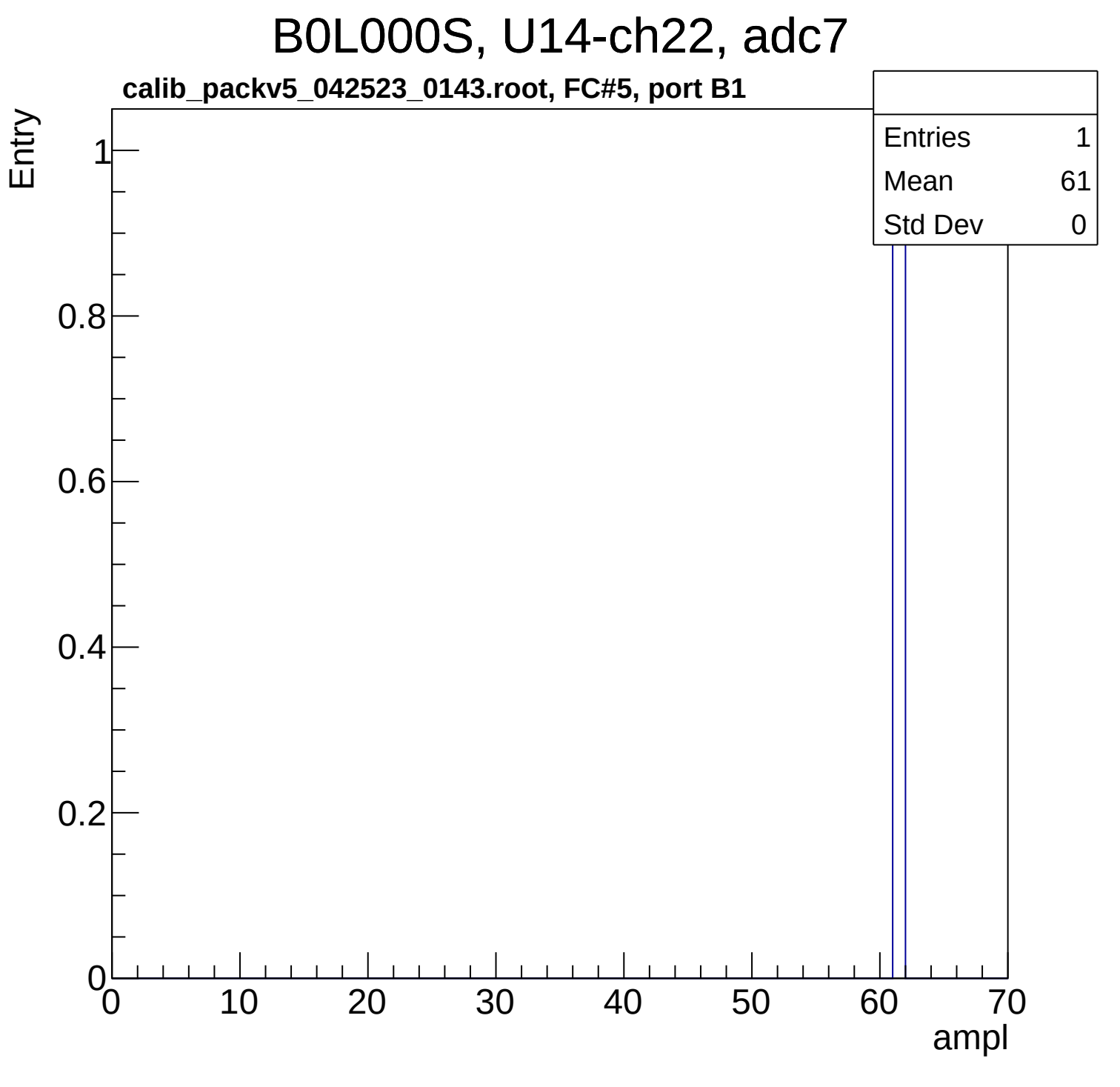

B0L000S, U14-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.78
Std Dev 8.084
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.38
Std Dev 1.689
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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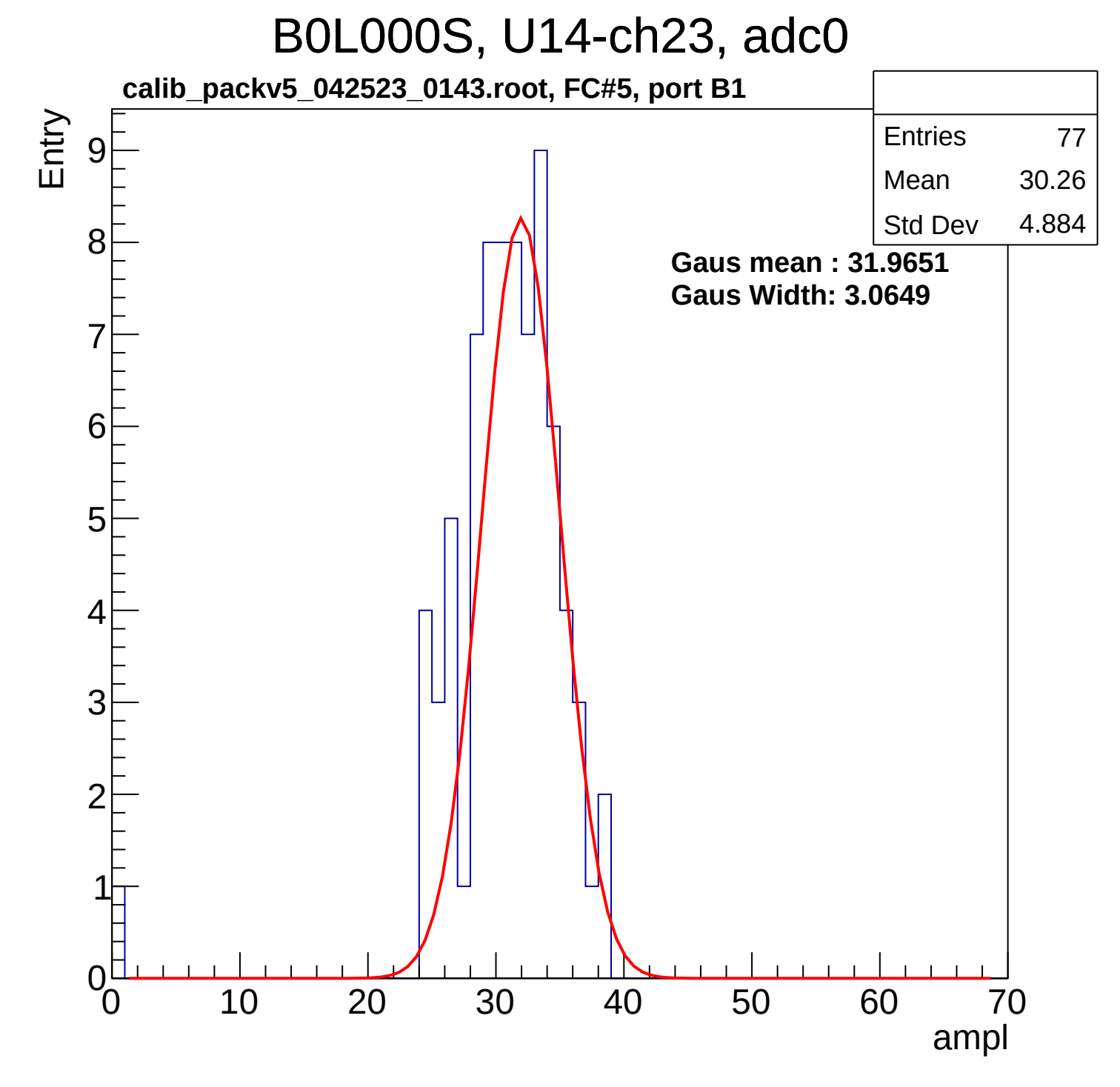
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 30.26
Std Dev 4.884
Gaus mean : 31.9651
Gaus Width: 3.0649
Entry
ampl
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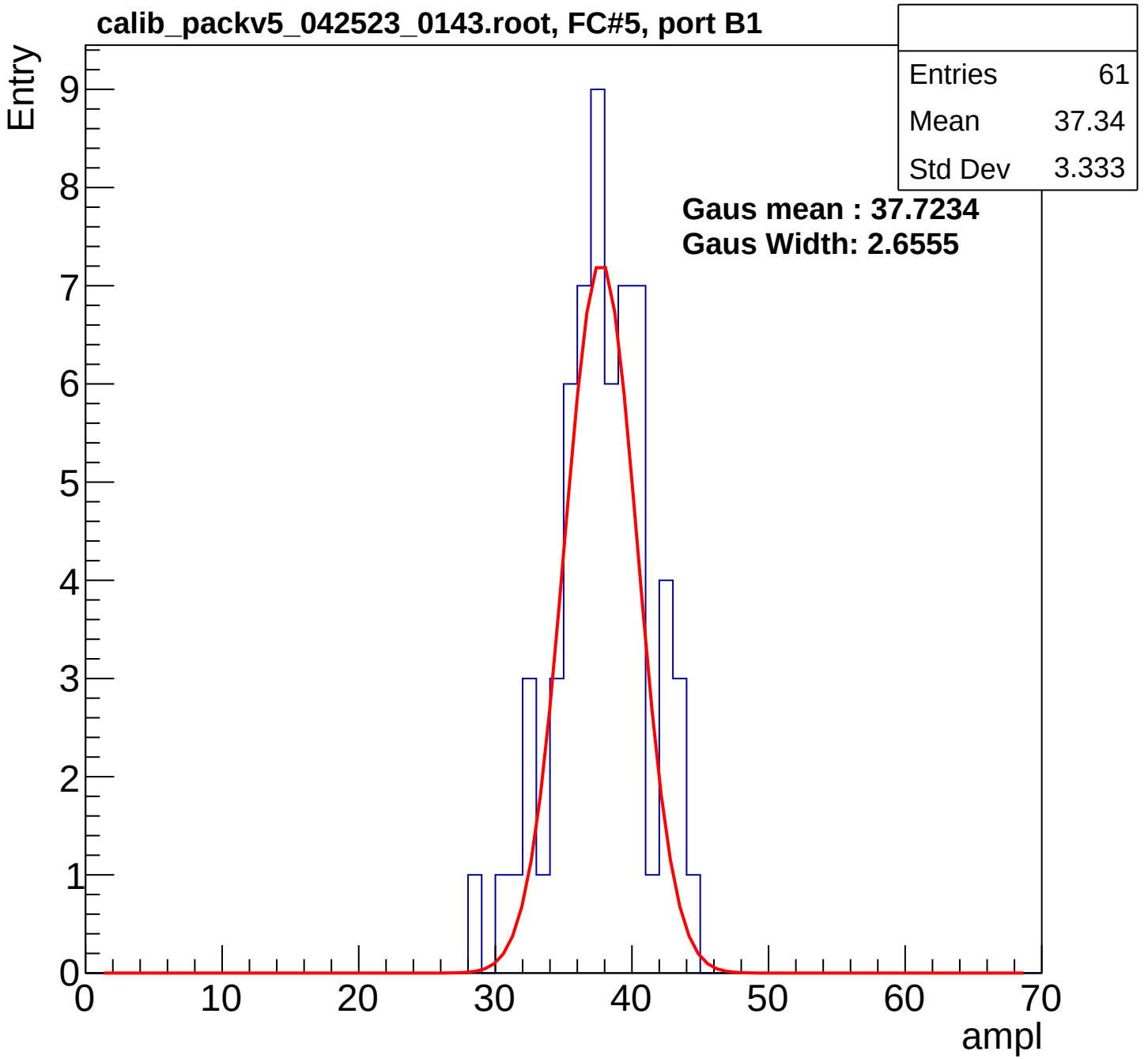
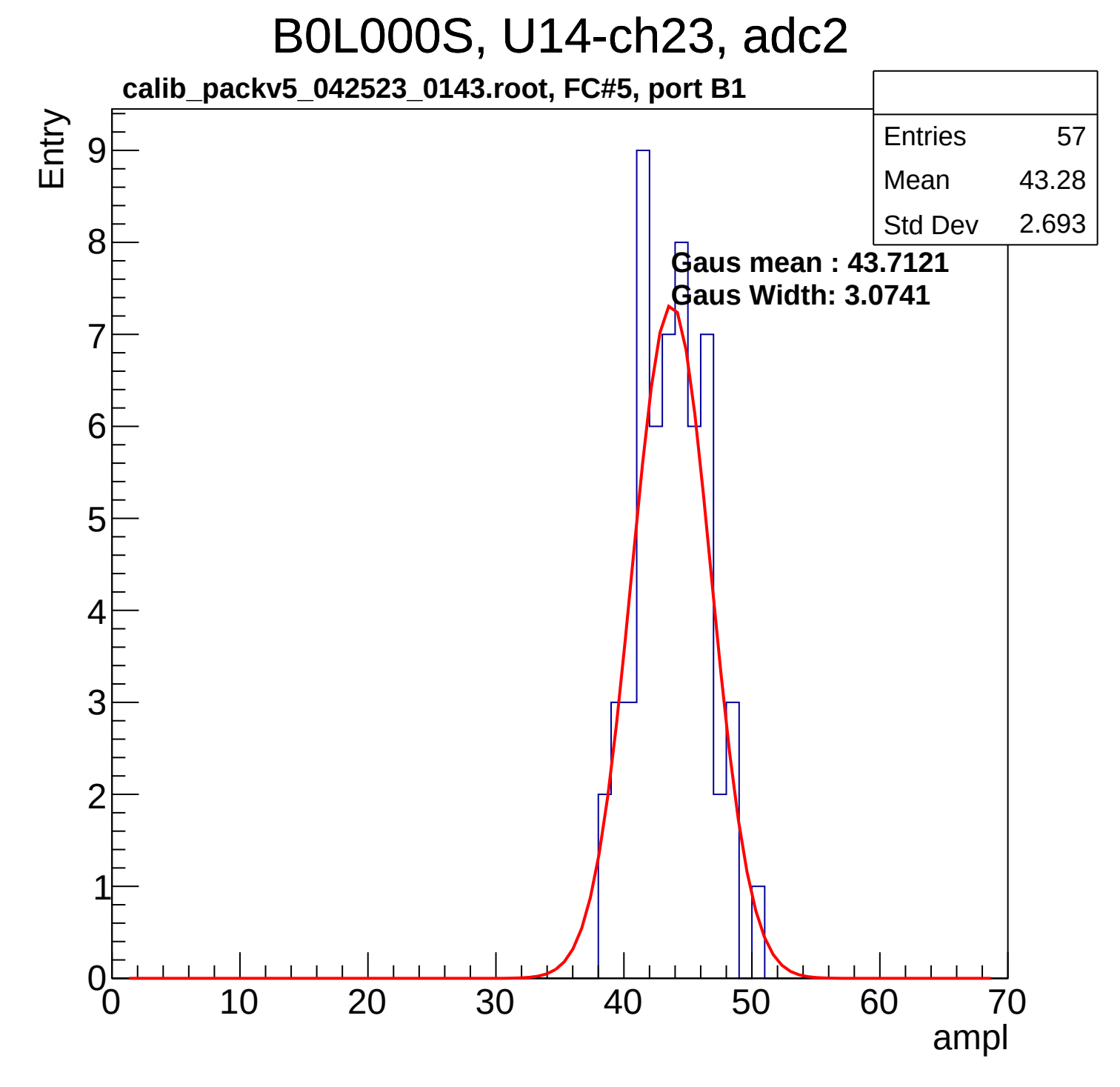
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 37.34
Std Dev 3.333
Gaus mean : 37.7234
Gaus Width: 2.6555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.28
Std Dev 2.693
Gaus mean : 43.7121
Gaus Width: 3.0741
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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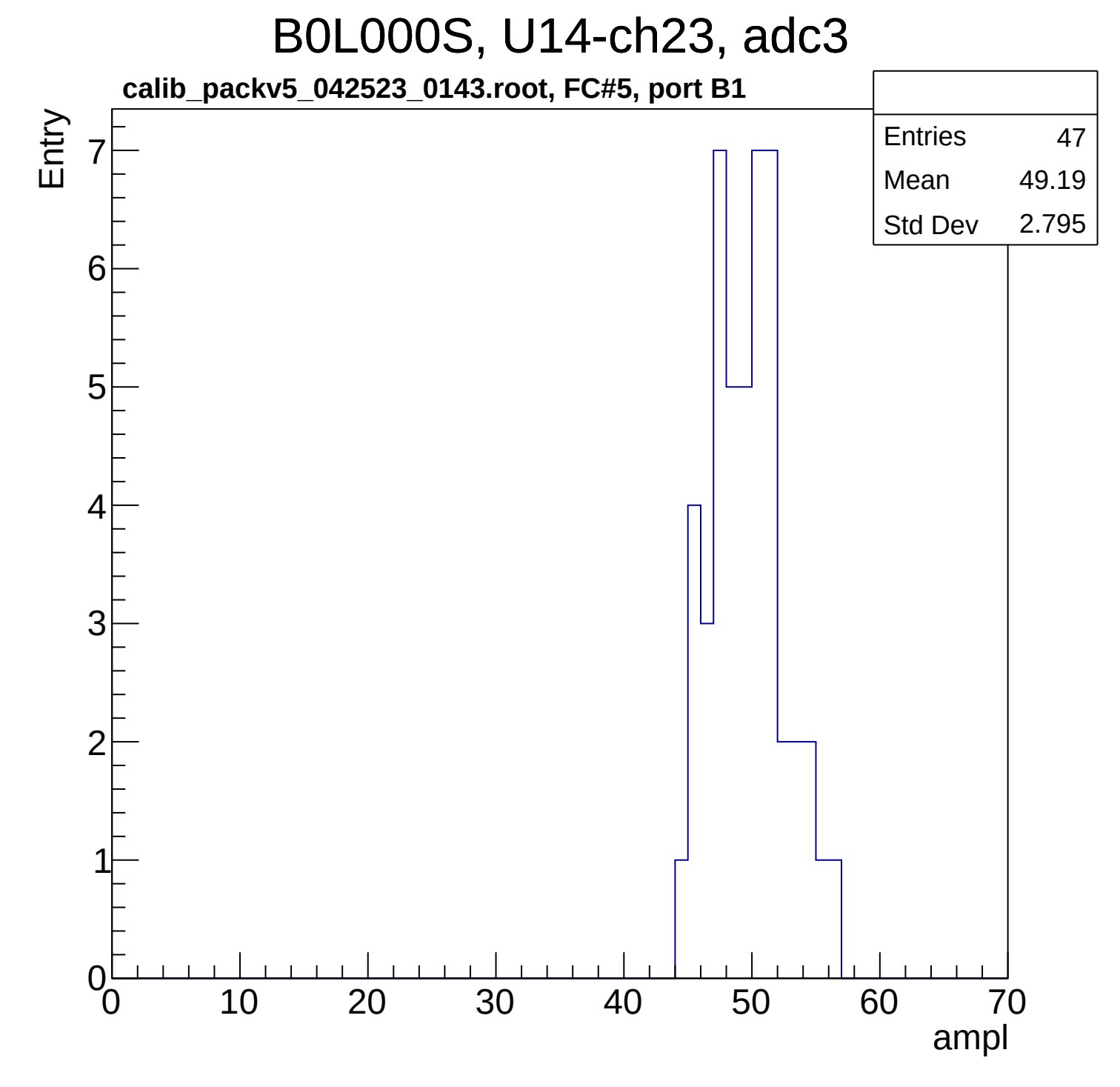
B0L000S, U14-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 49.19
Std Dev 2.795
Entry
ampl
0
1
2
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4
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0
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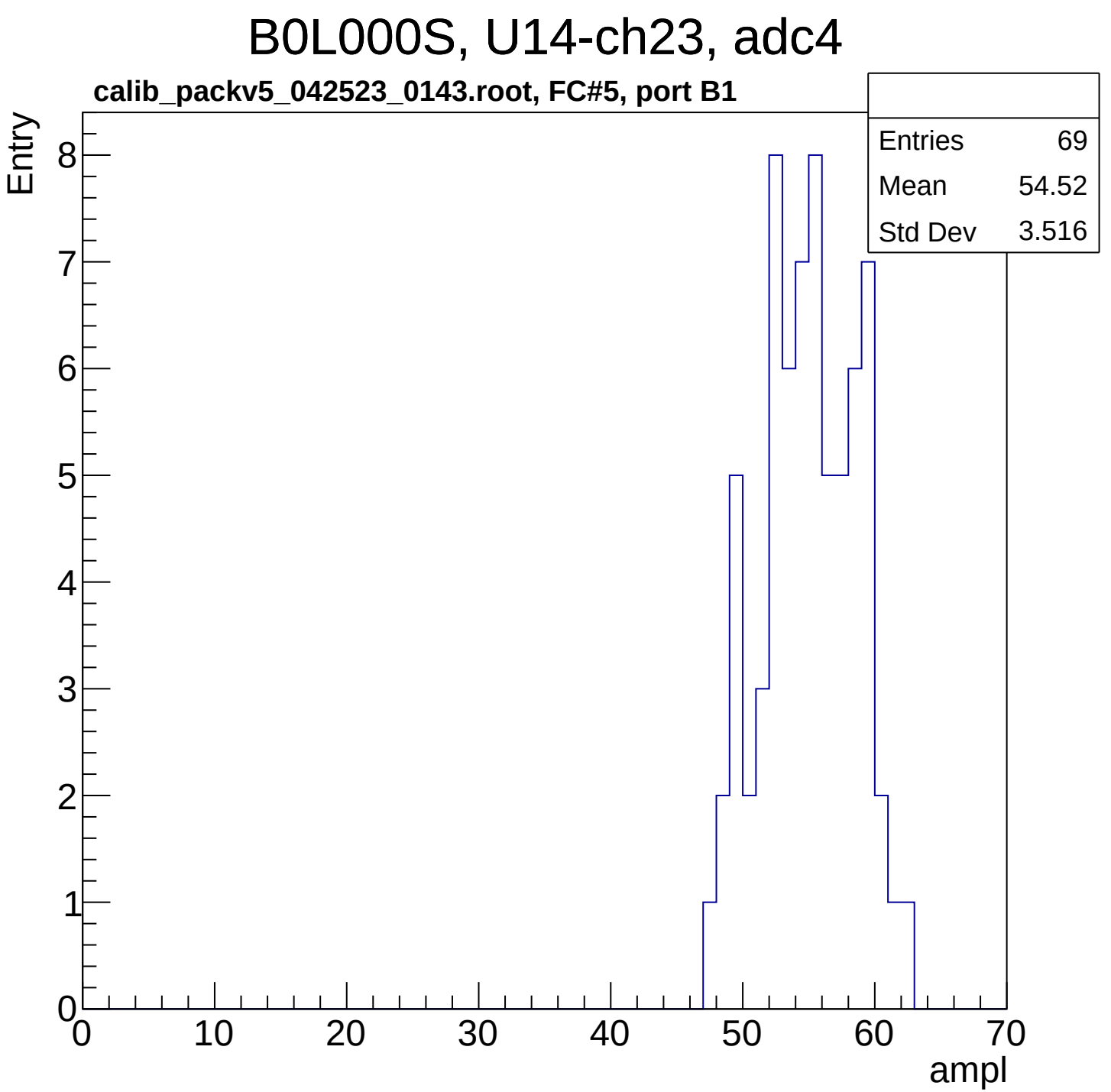
B0L000S, U14-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 54.52
Std Dev 3.516
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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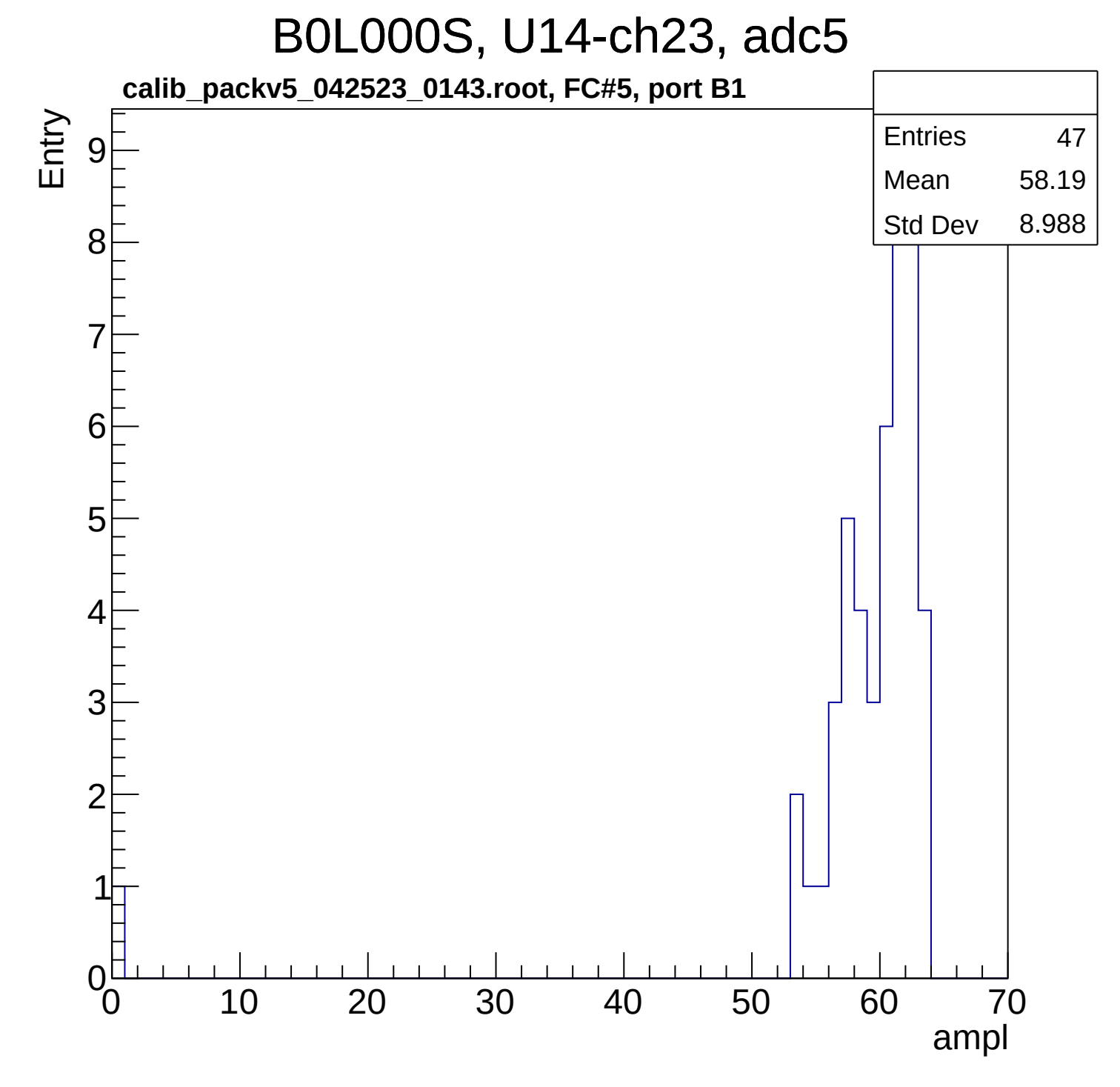
B0L000S, U14-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.19
Std Dev 8.988
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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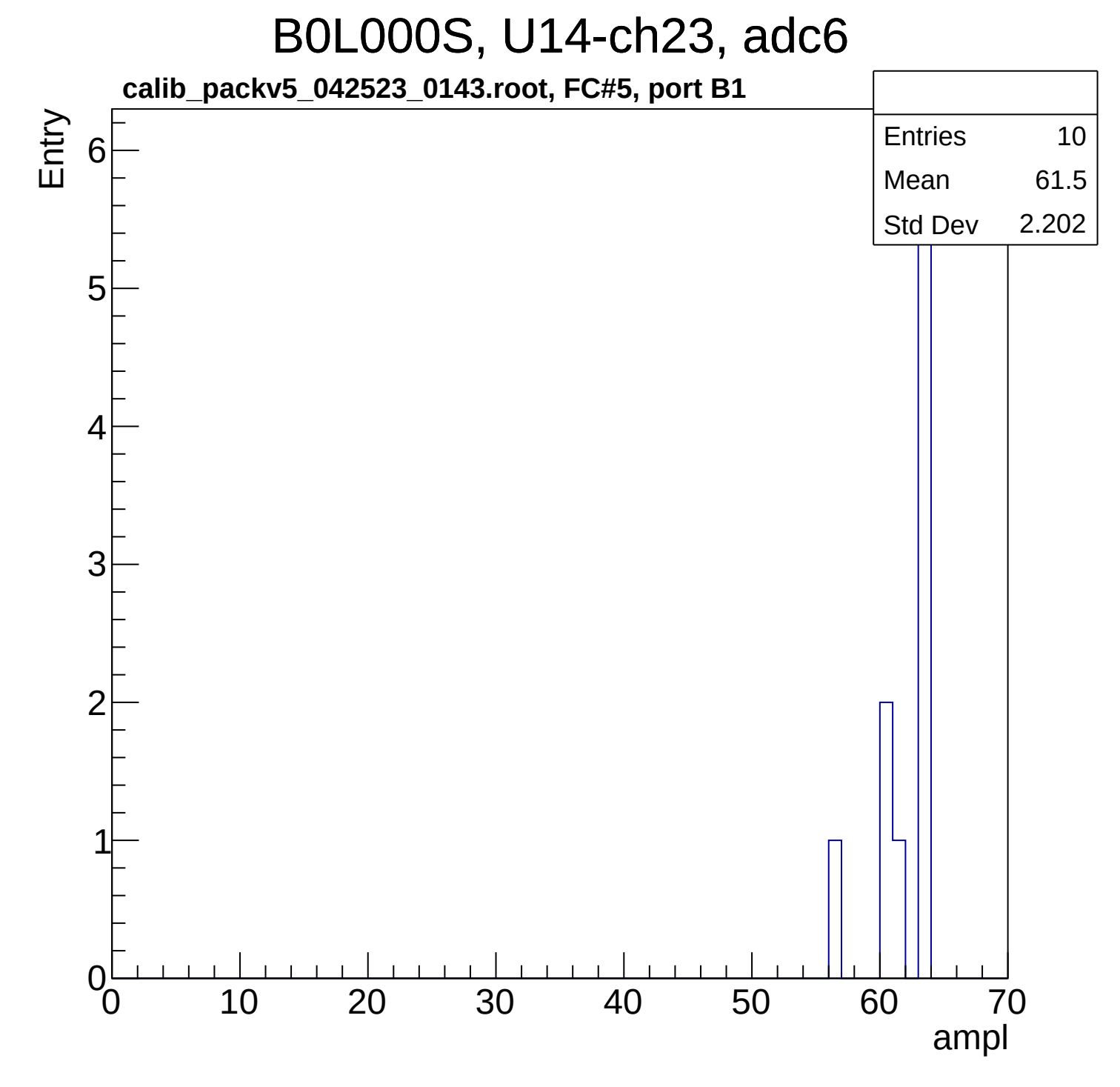
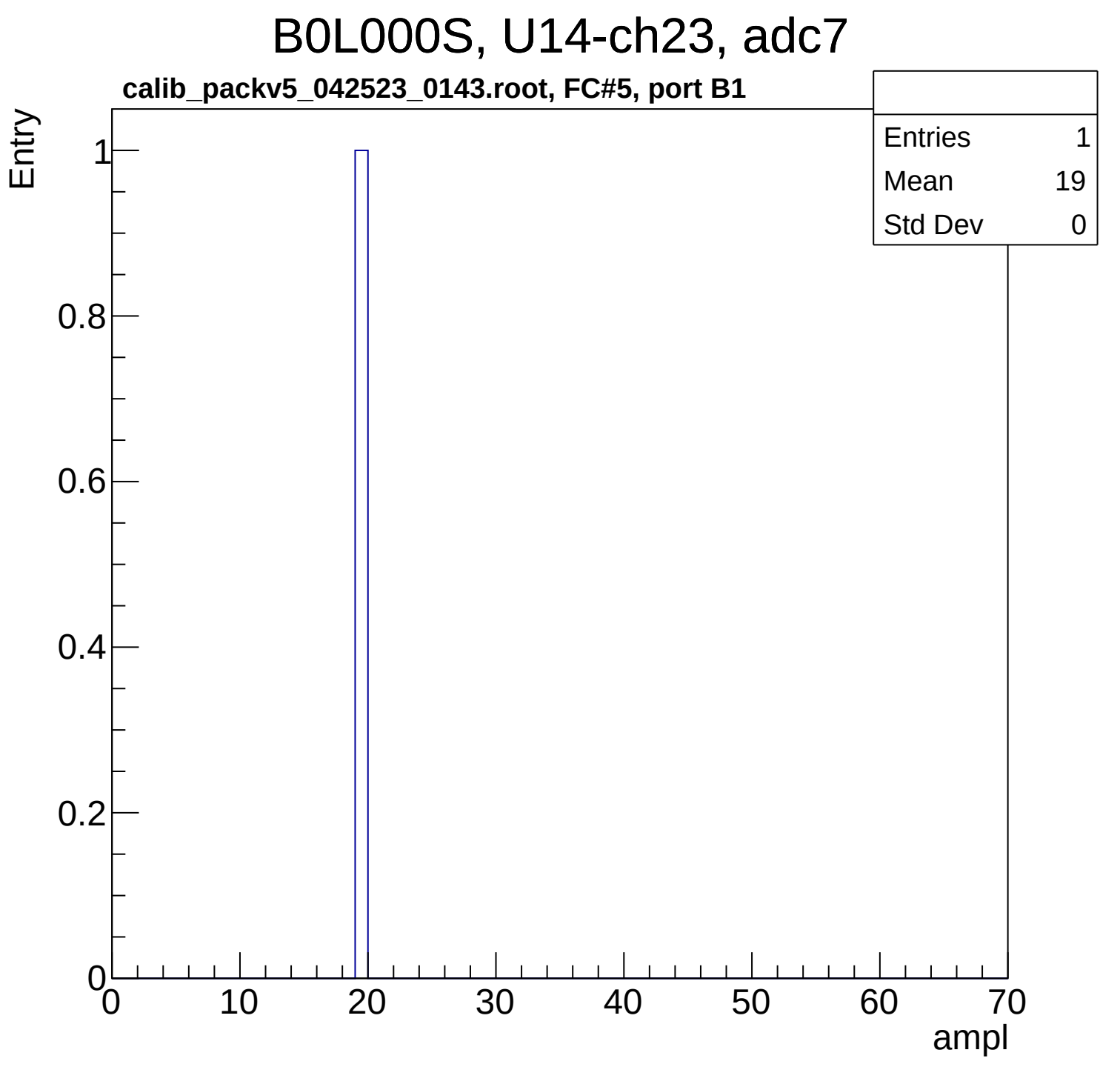
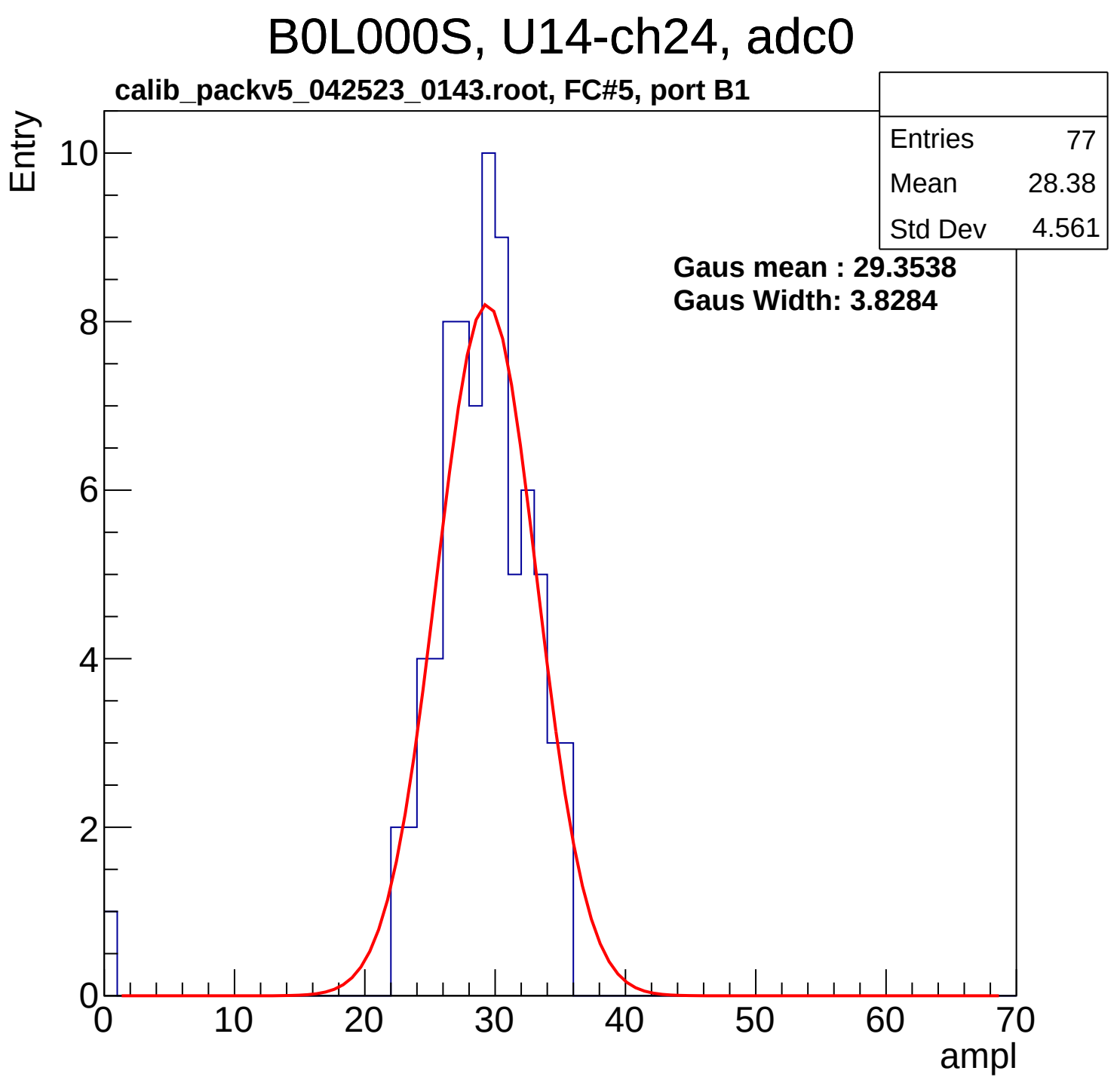
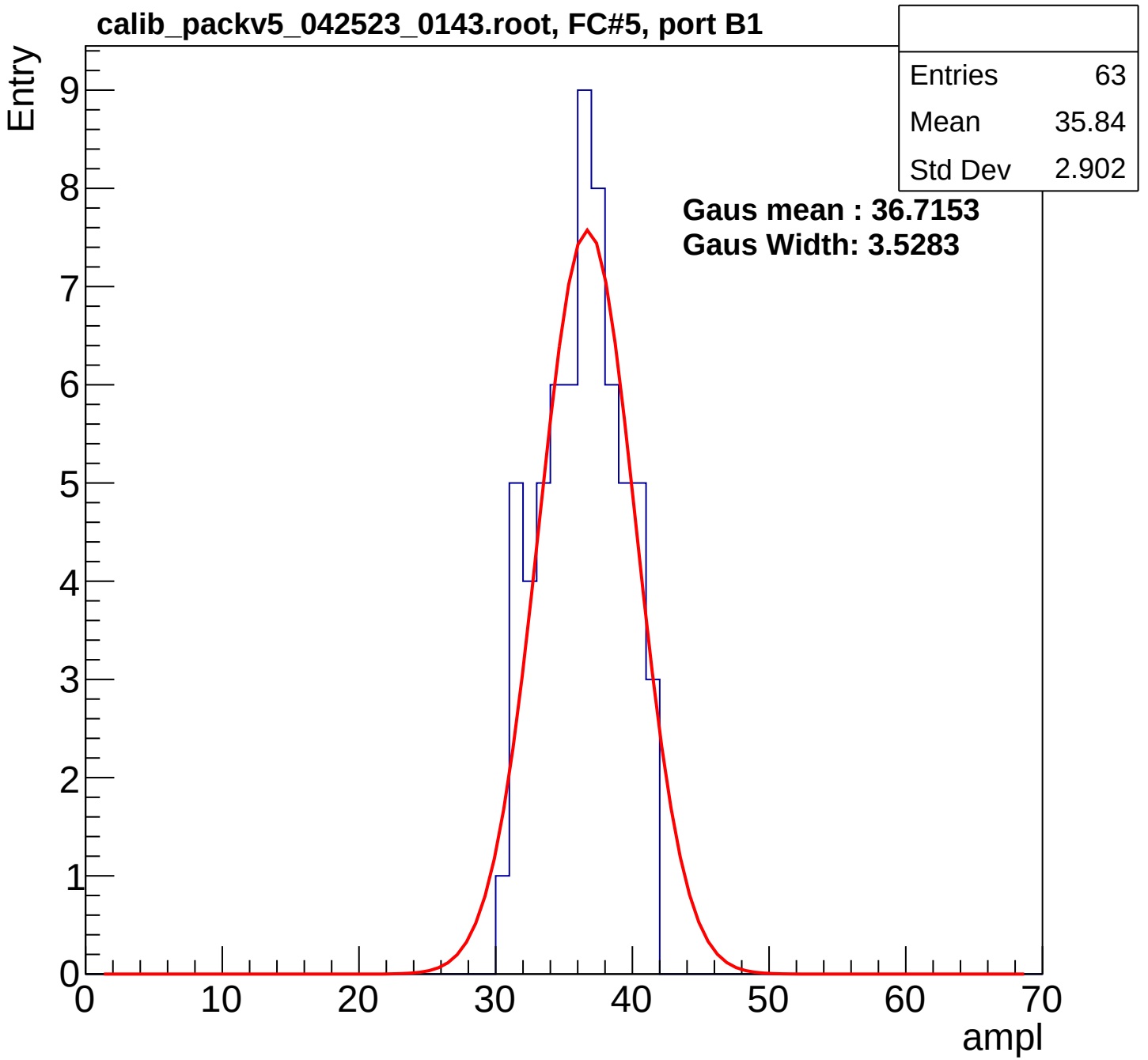

B0L000S, U14-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 2.202
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.38
Std Dev 4.561
Gaus mean : 29.3538
Gaus Width: 3.8284
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.84
Std Dev 2.902
Gaus mean : 36.7153
Gaus Width: 3.5283
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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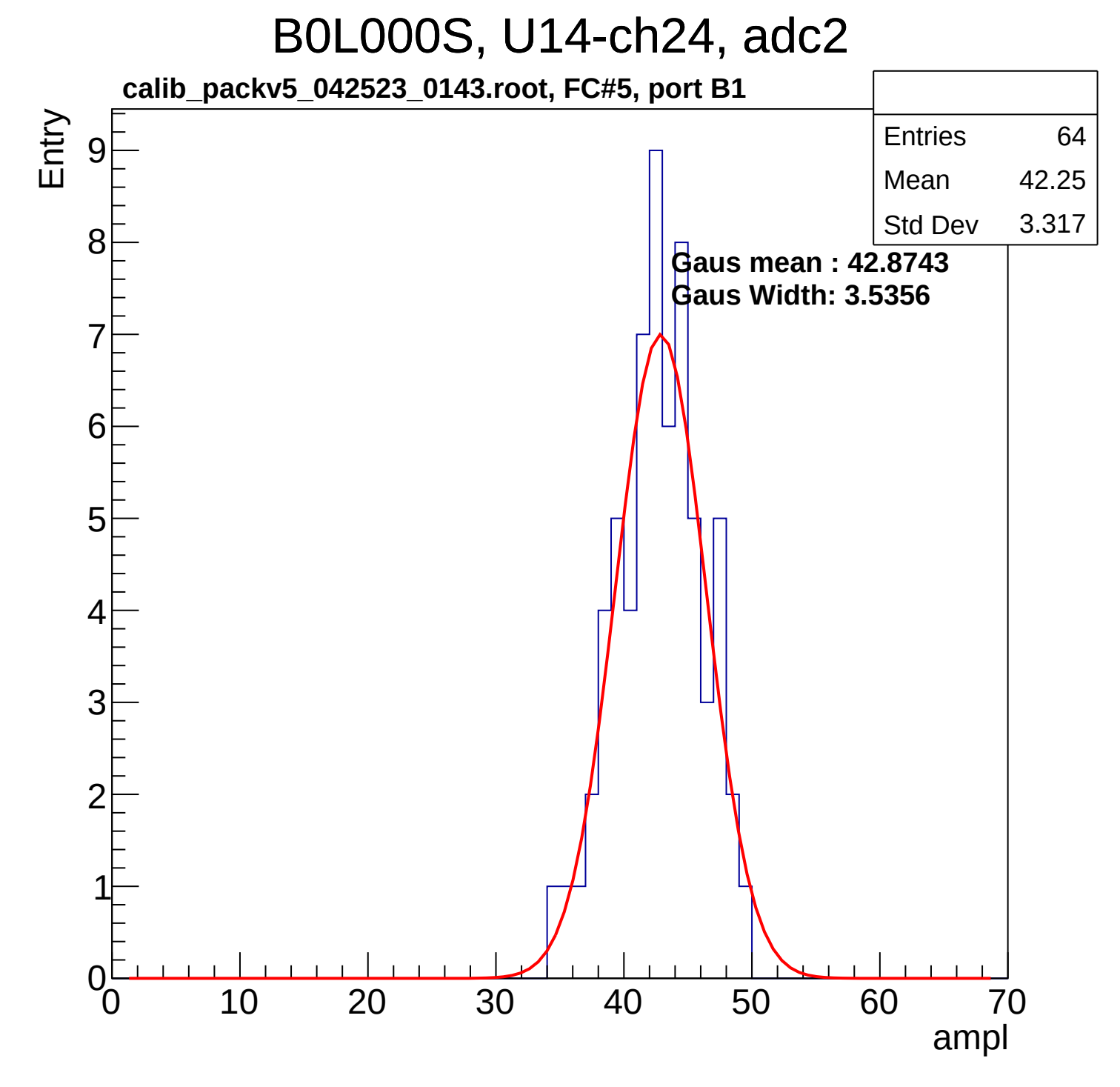
B0L000S, U14-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 42.25
Std Dev 3.317
Gaus mean : 42.8743
Gaus Width: 3.5356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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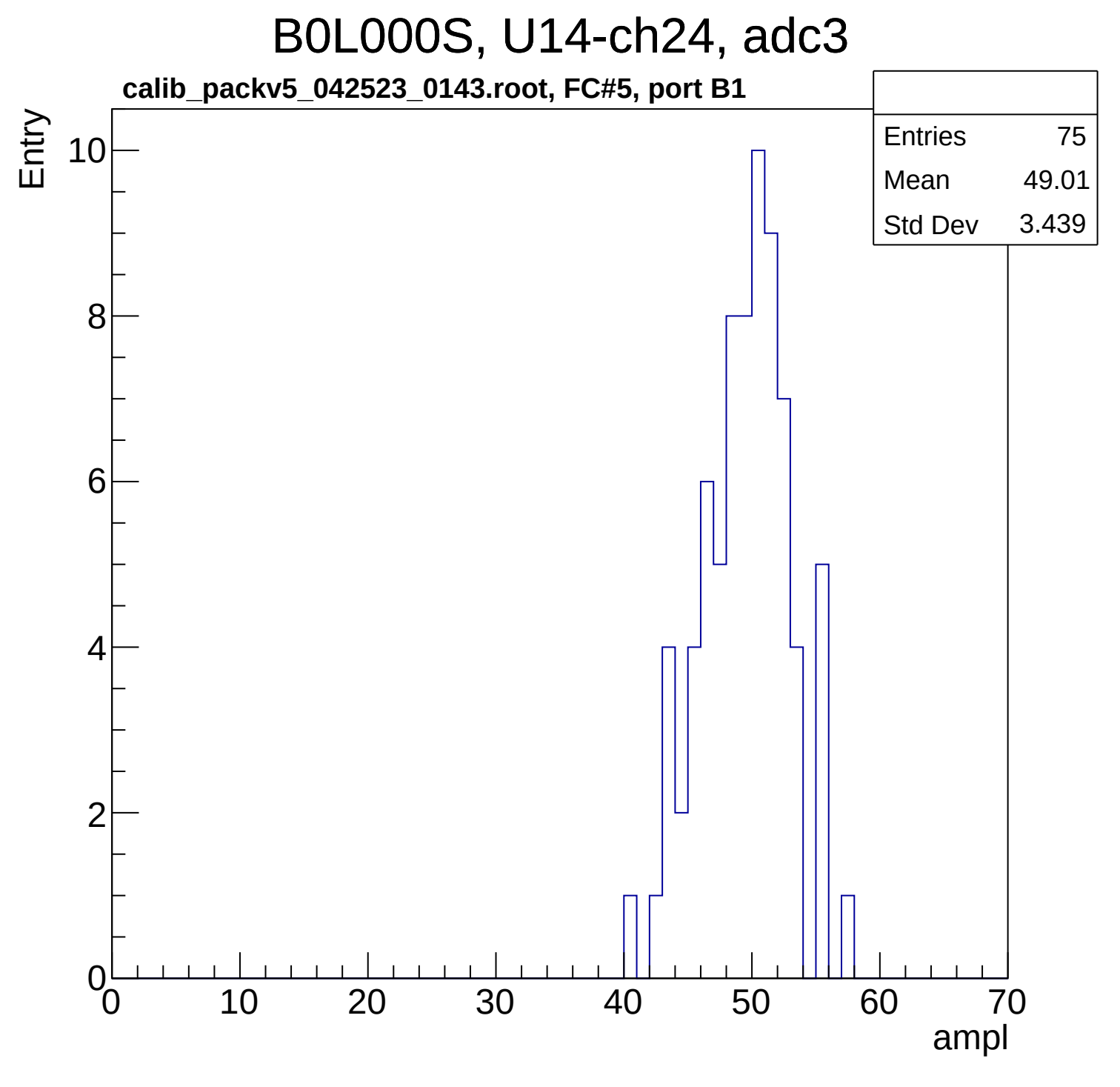
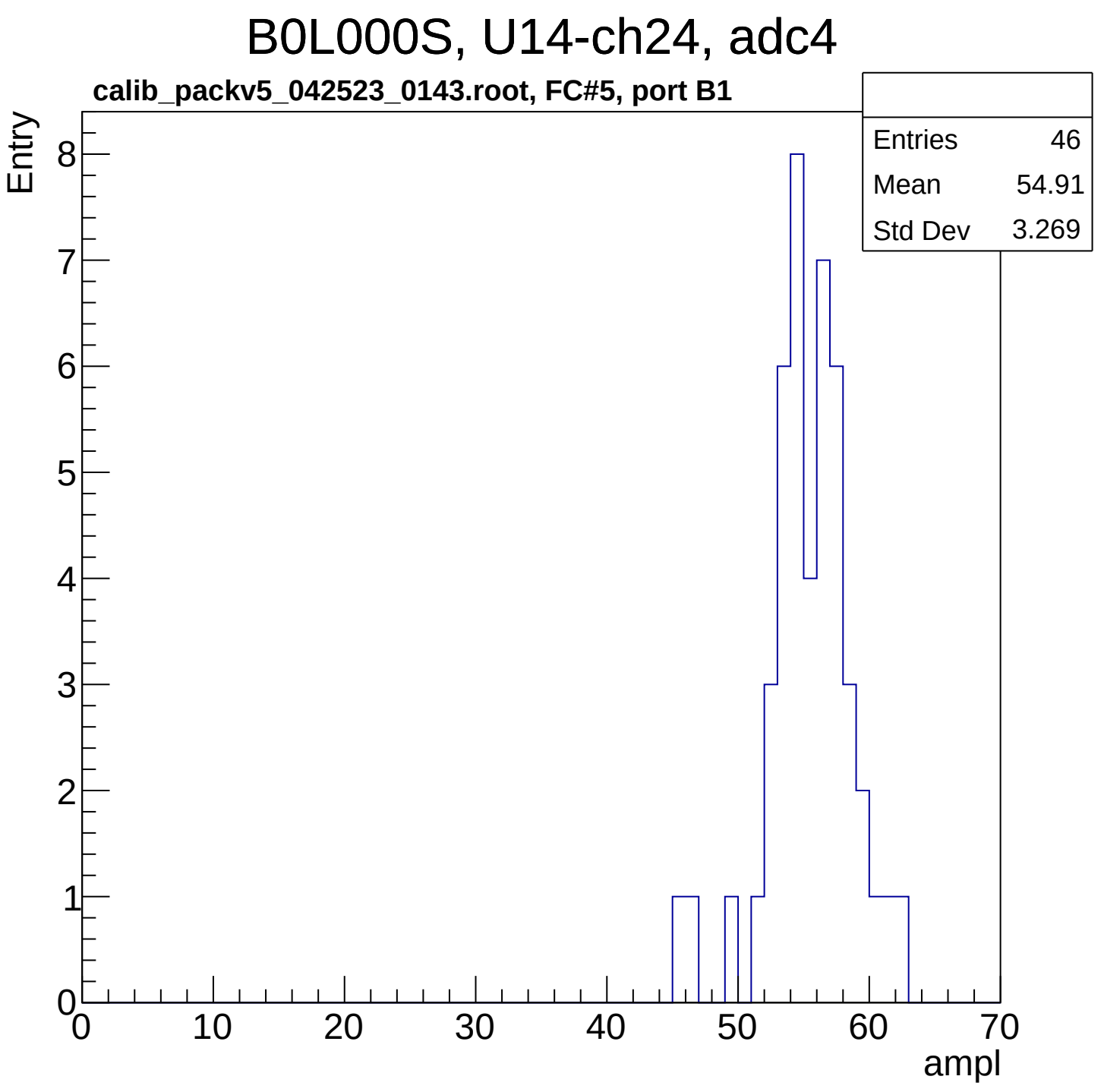

B0L000S, U14-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 49.01
Std Dev 3.439
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.91
Std Dev 3.269
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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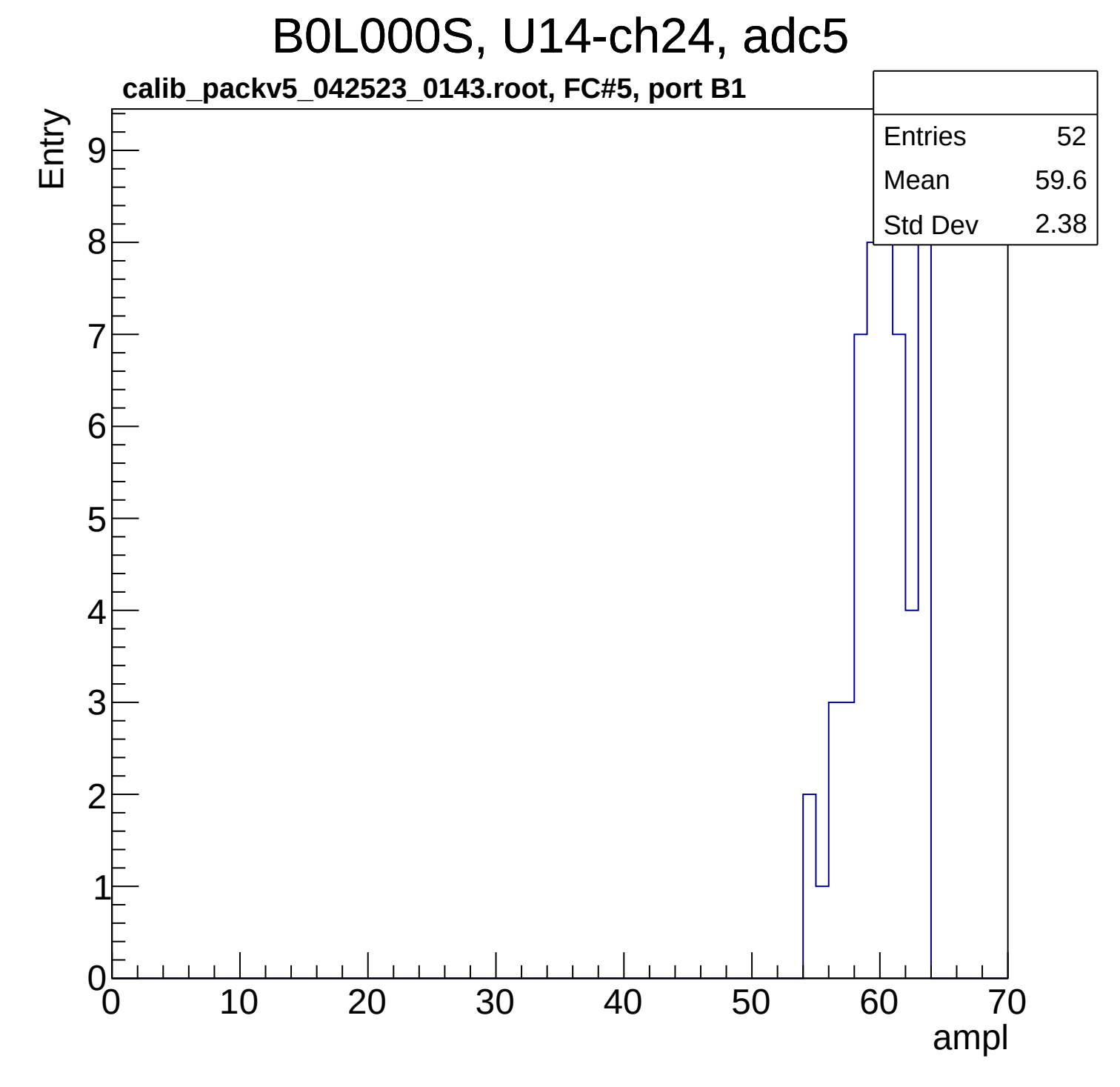
B0L000S, U14-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.6
Std Dev 2.38
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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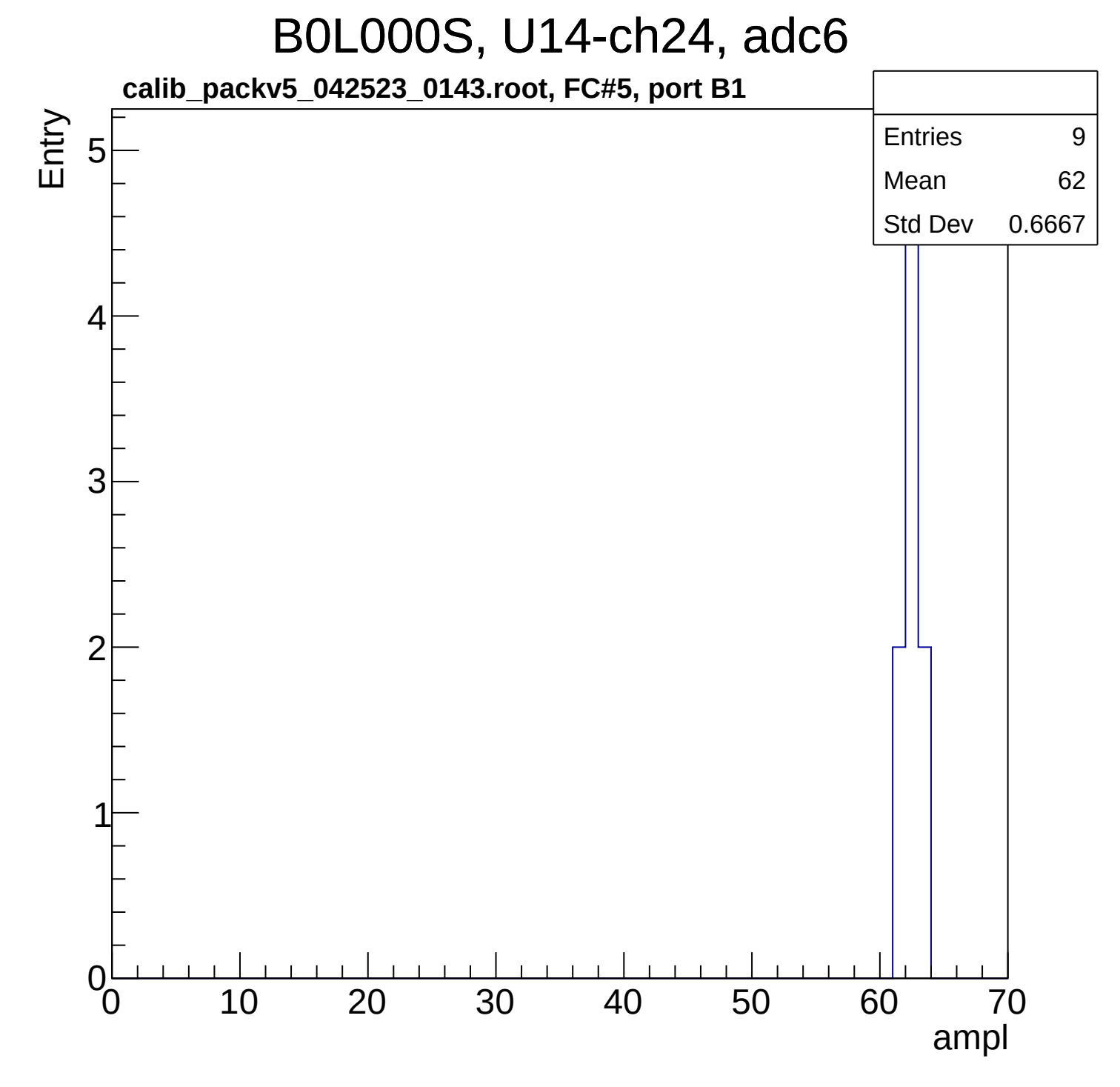
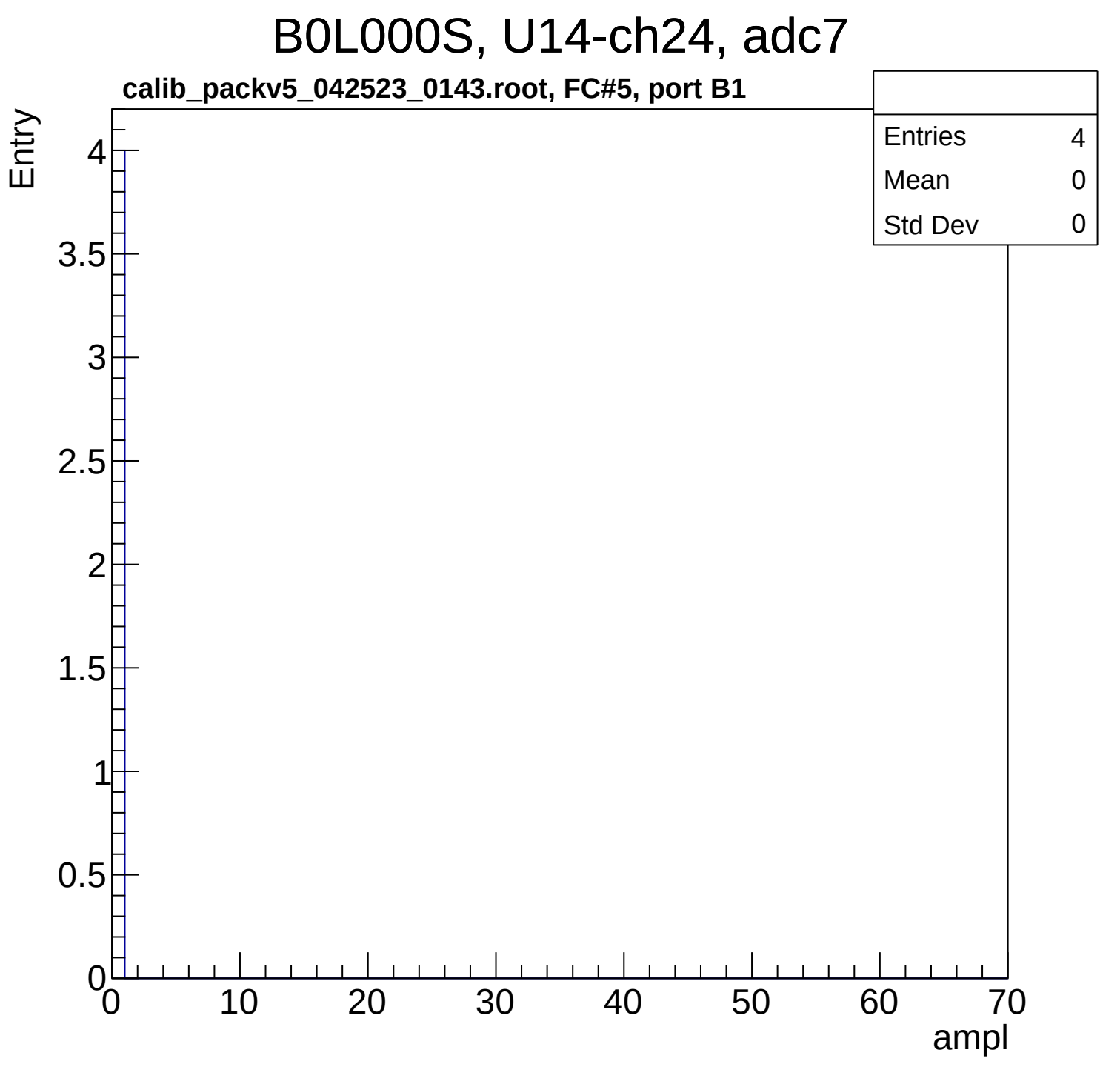
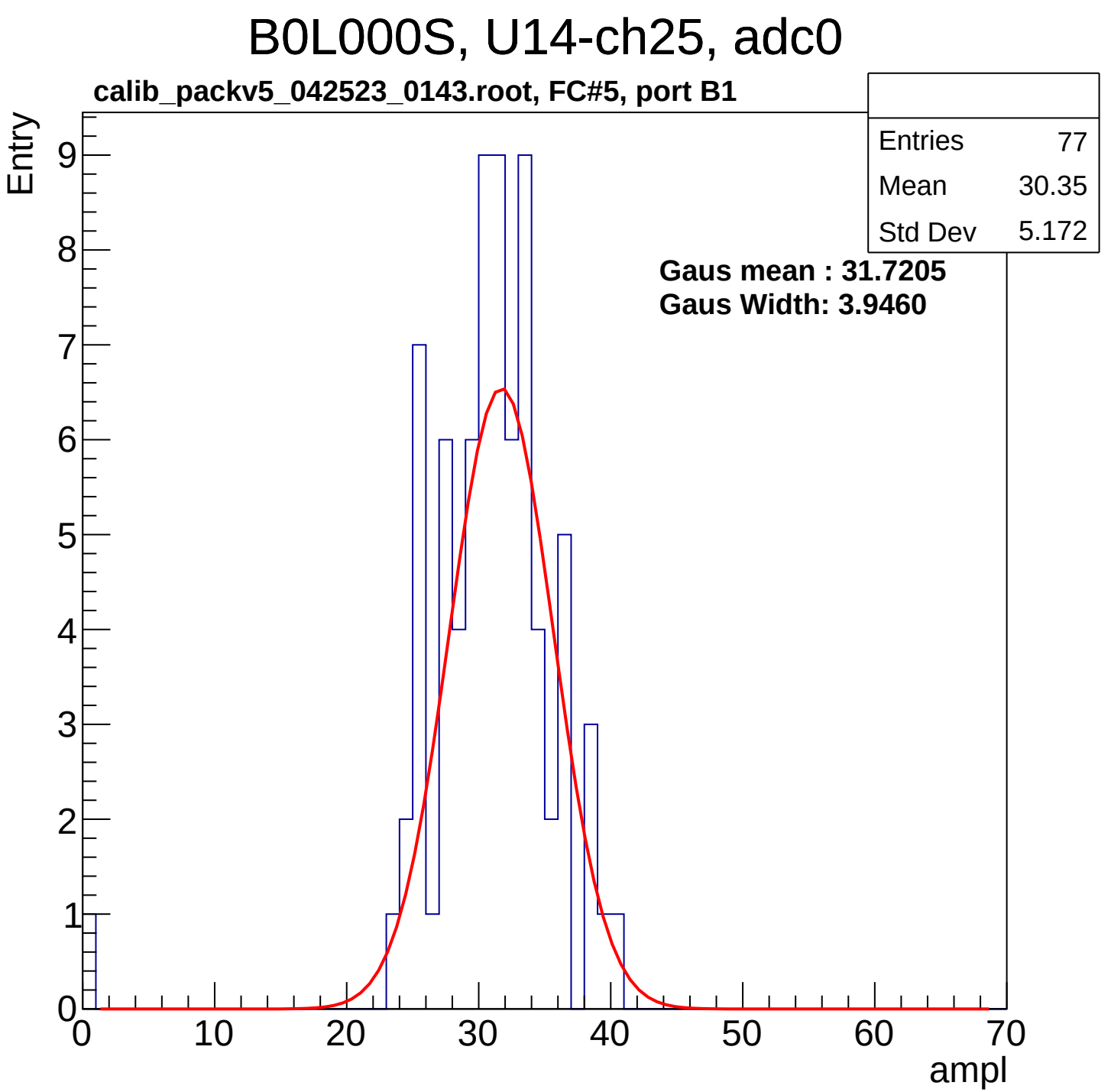

B0L000S, U14-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62
Std Dev 0.6667
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 0
Std Dev 0
Entry
4
3.5
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 30.35
Std Dev 5.172
Gaus mean : 31.7205
Gaus Width: 3.9460
Entry
ampl
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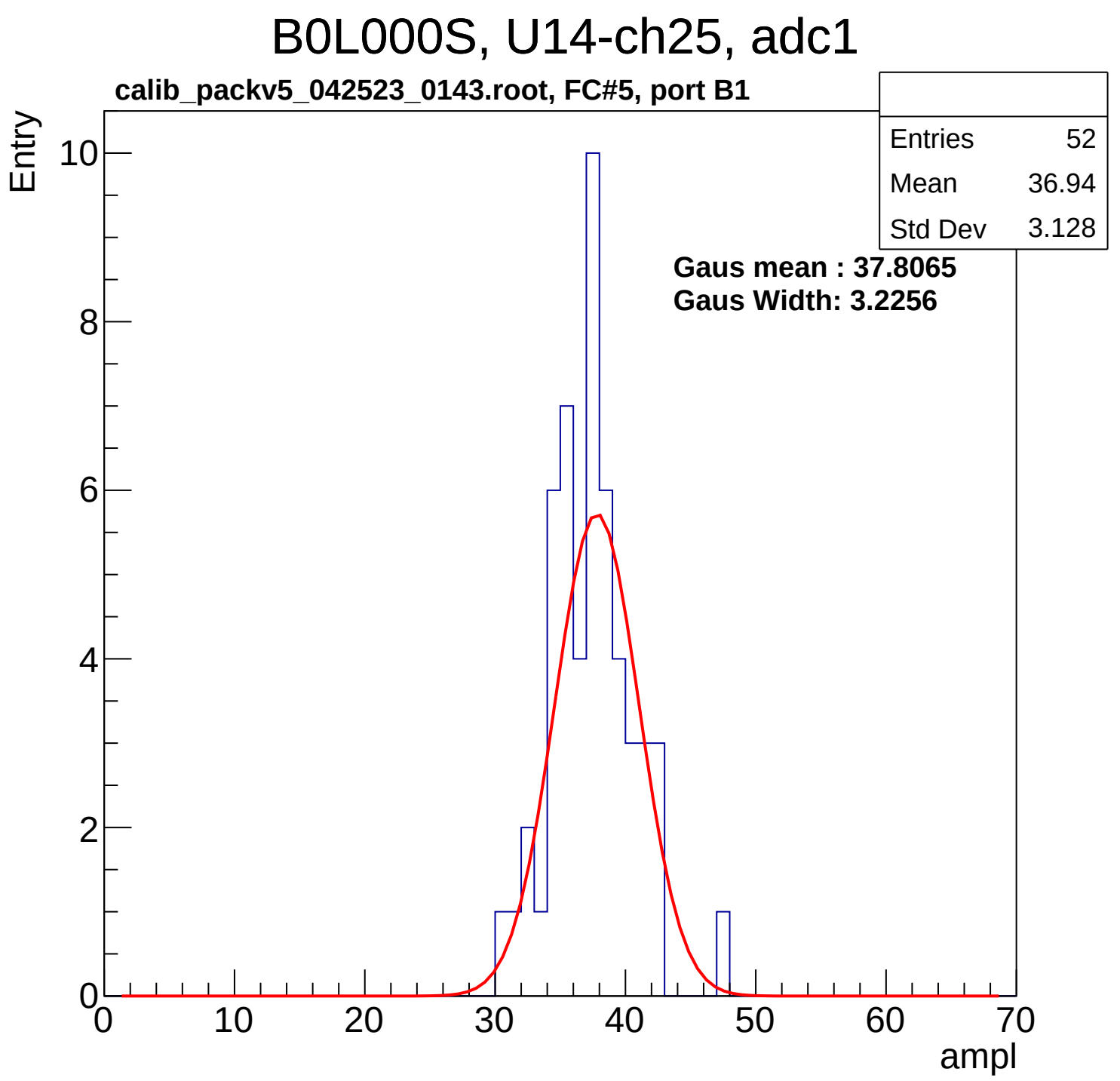
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 36.94
Std Dev 3.128
Gaus mean : 37.8065
Gaus Width: 3.2256
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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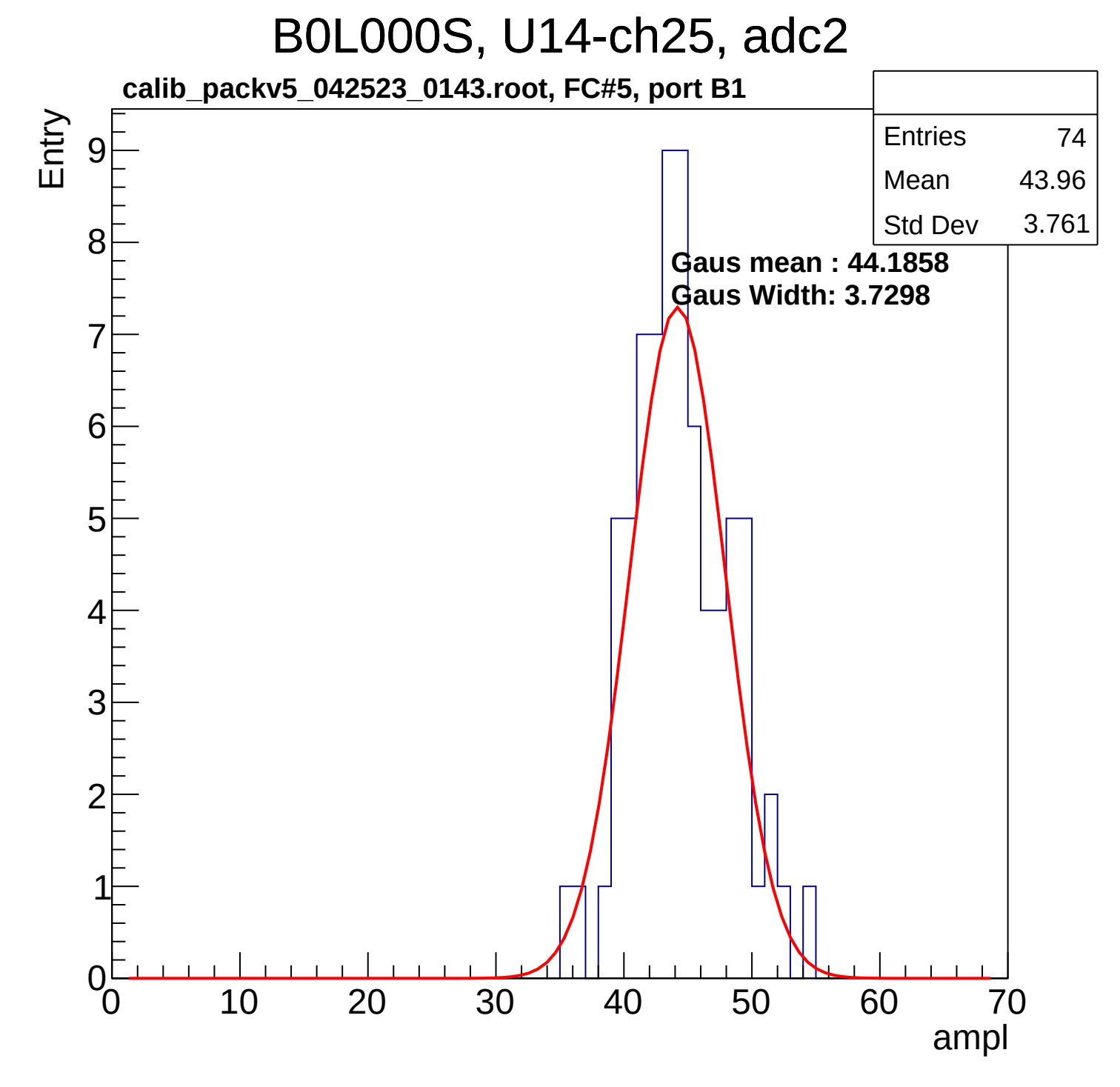
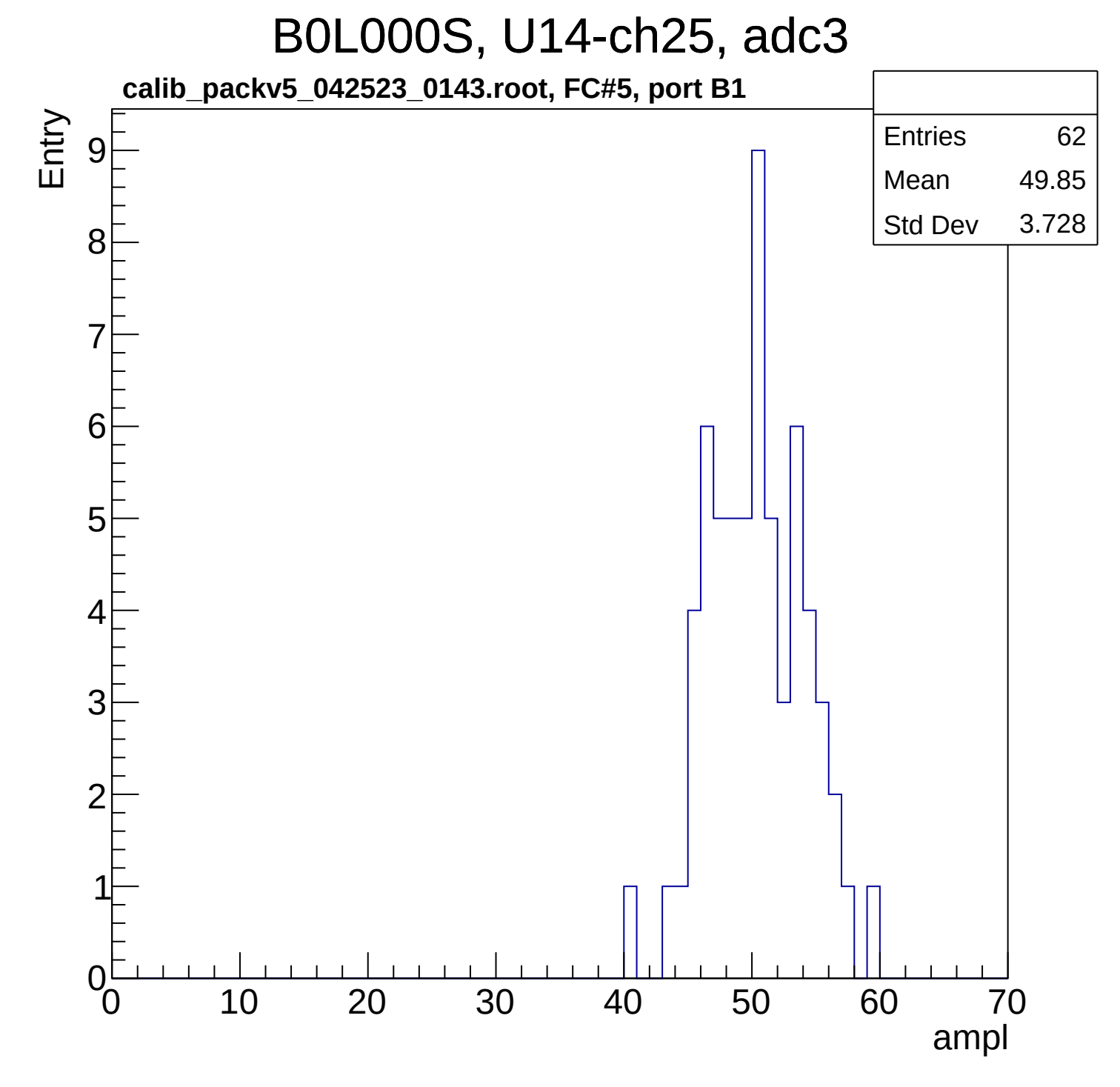

B0L000S, U14-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.96
Std Dev 3.761
Gaus mean : 44.1858
Gaus Width: 3.7298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.85
Std Dev 3.728
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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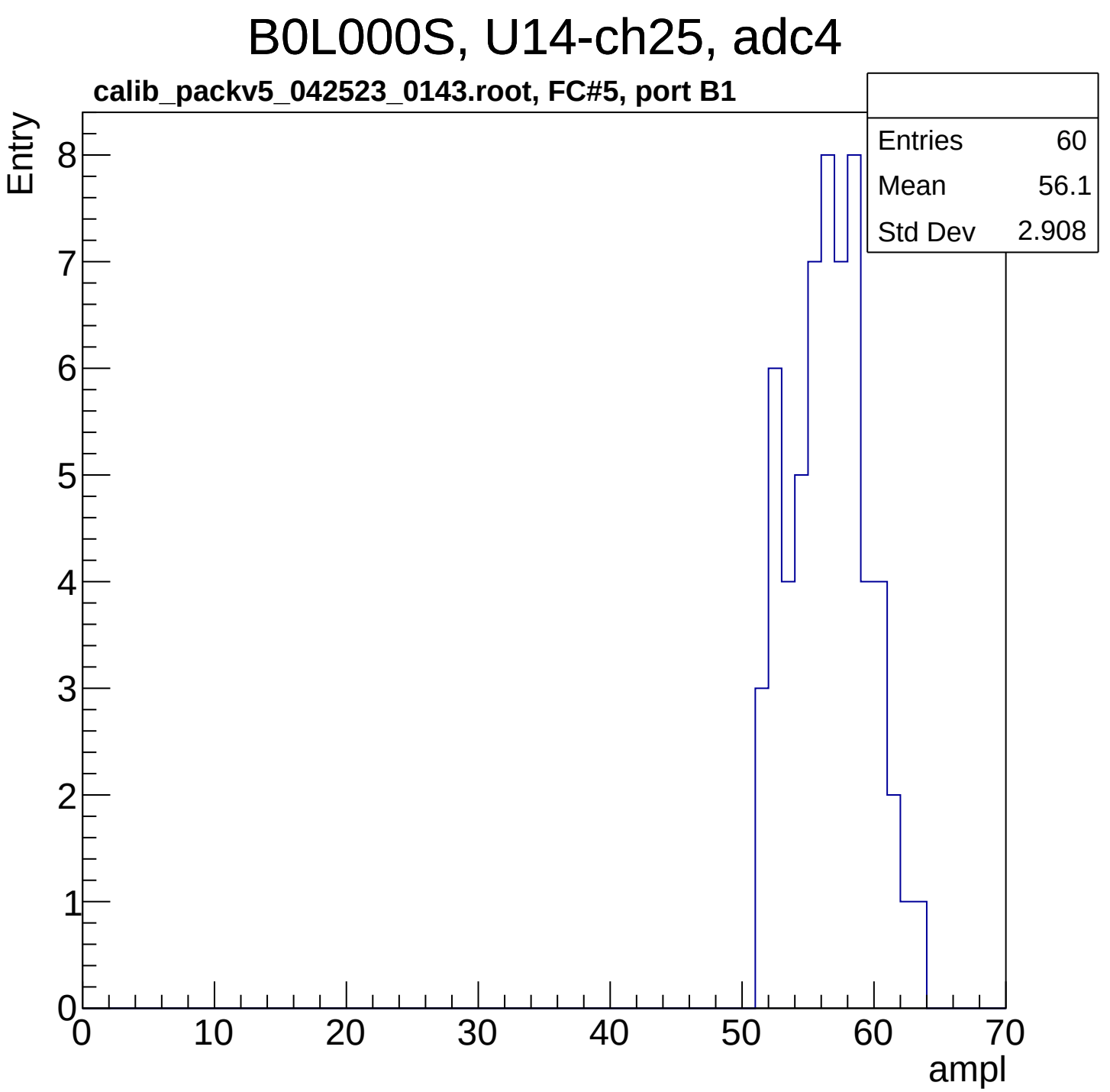
B0L000S, U14-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.1
Std Dev 2.908
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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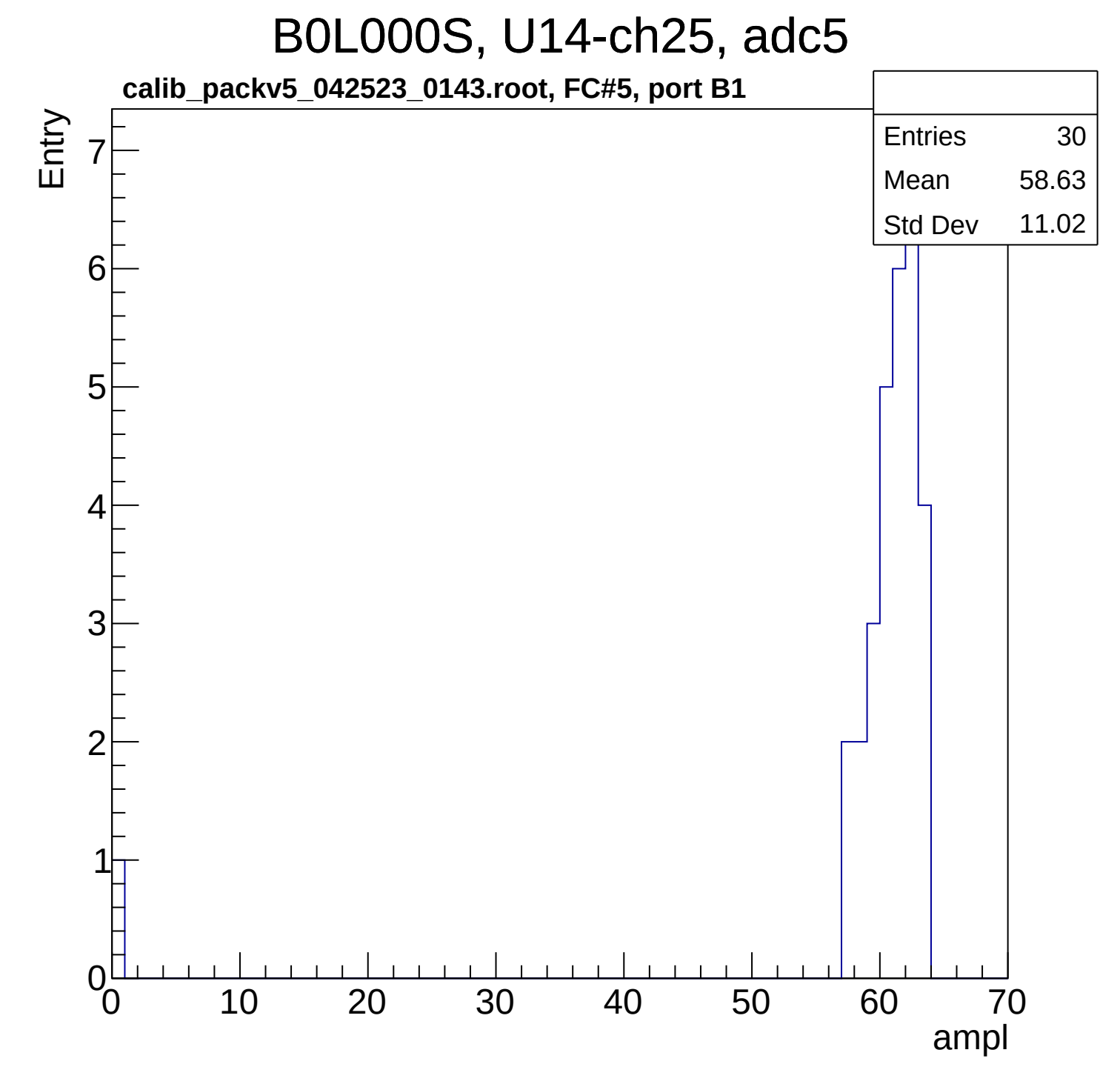
B0L000S, U14-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.63
Std Dev 11.02
Entry
ampl
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4
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6
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0
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60
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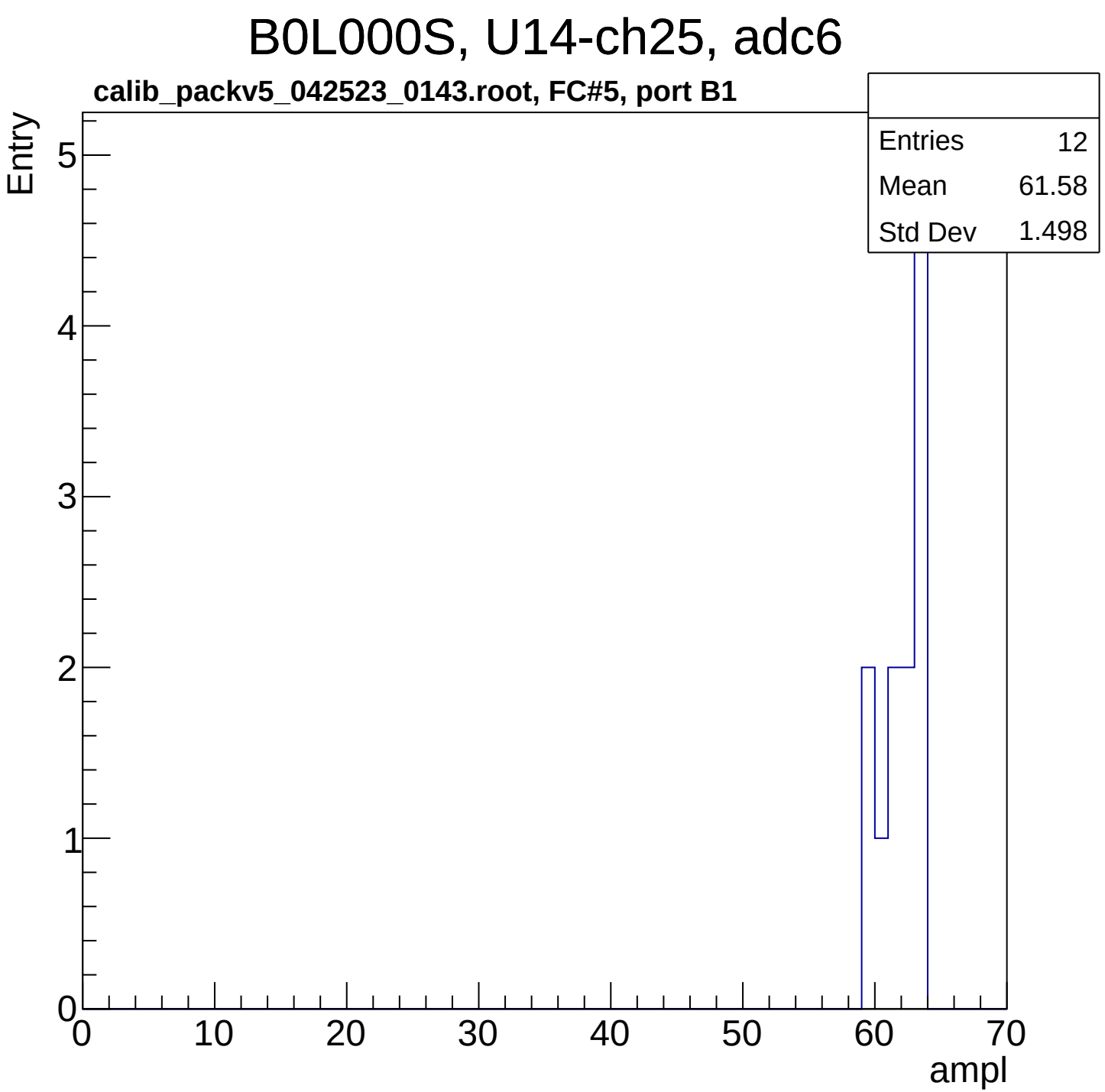
B0L000S, U14-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.58
Std Dev 1.498
Entry
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1
2
3
4
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10
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30
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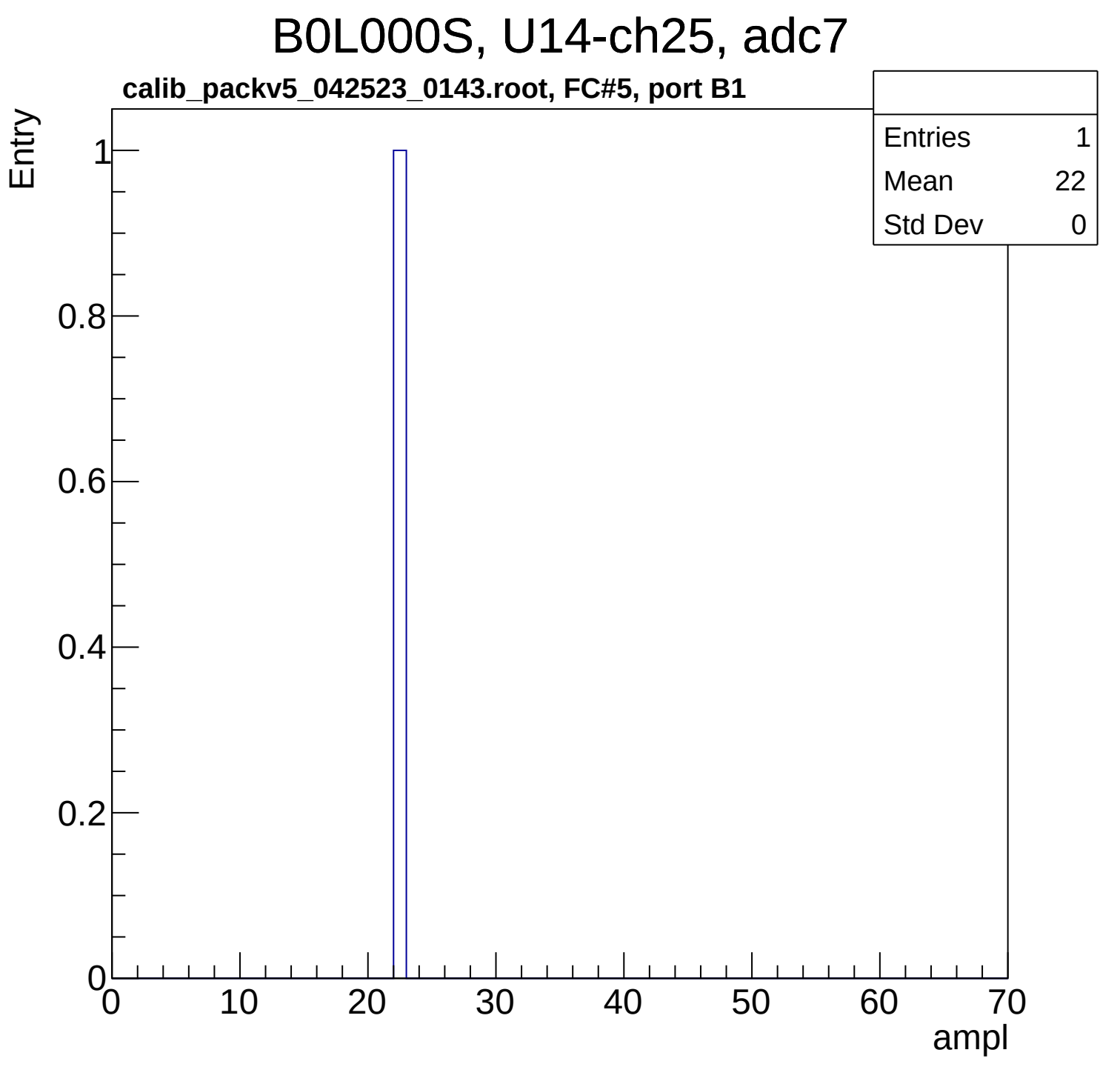
50
60
70
ampl


B0L000S, U14-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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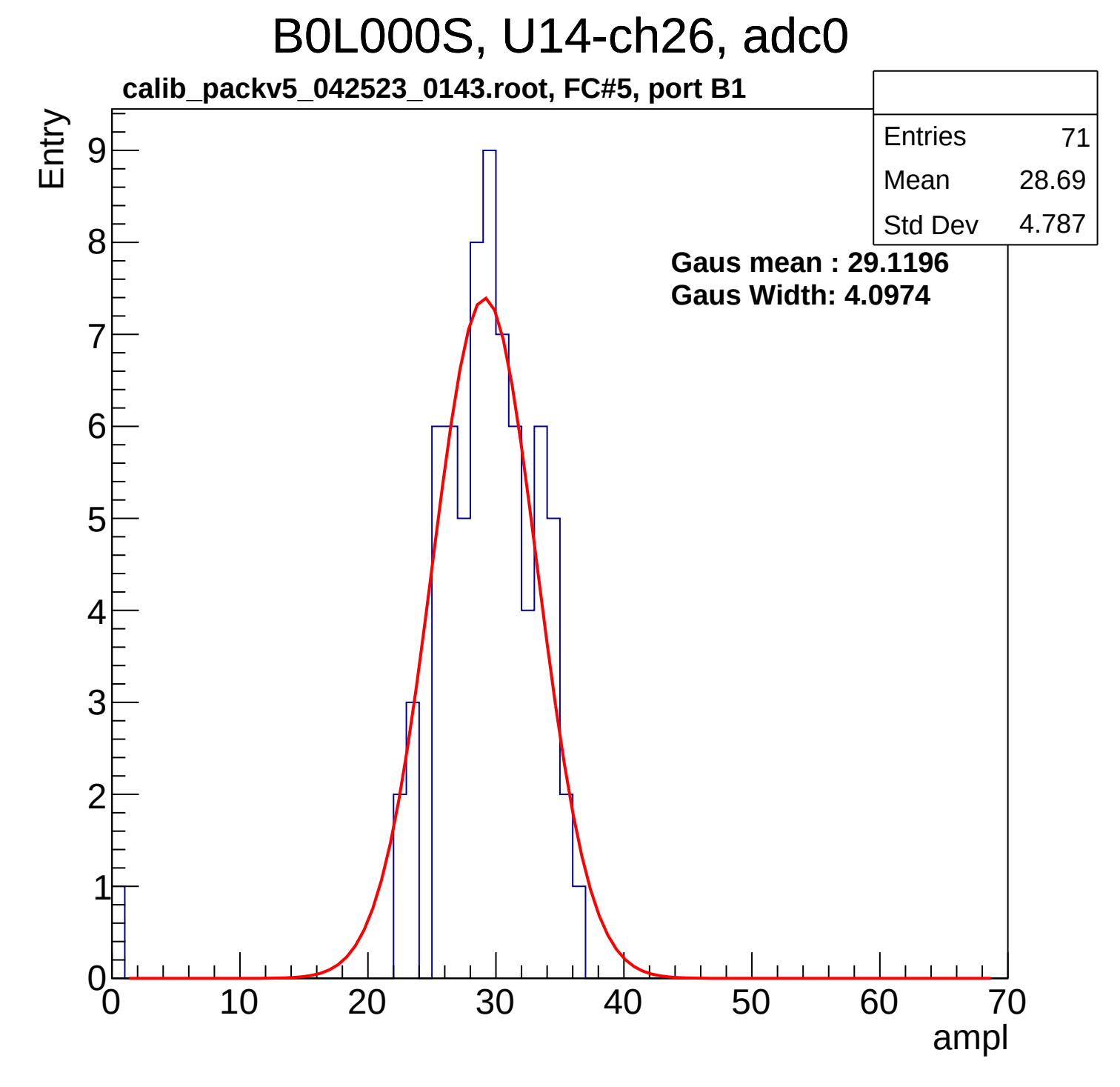
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.69
Std Dev 4.787
Gaus mean : 29.1196
Gaus Width: 4.0974
Entry
ampl
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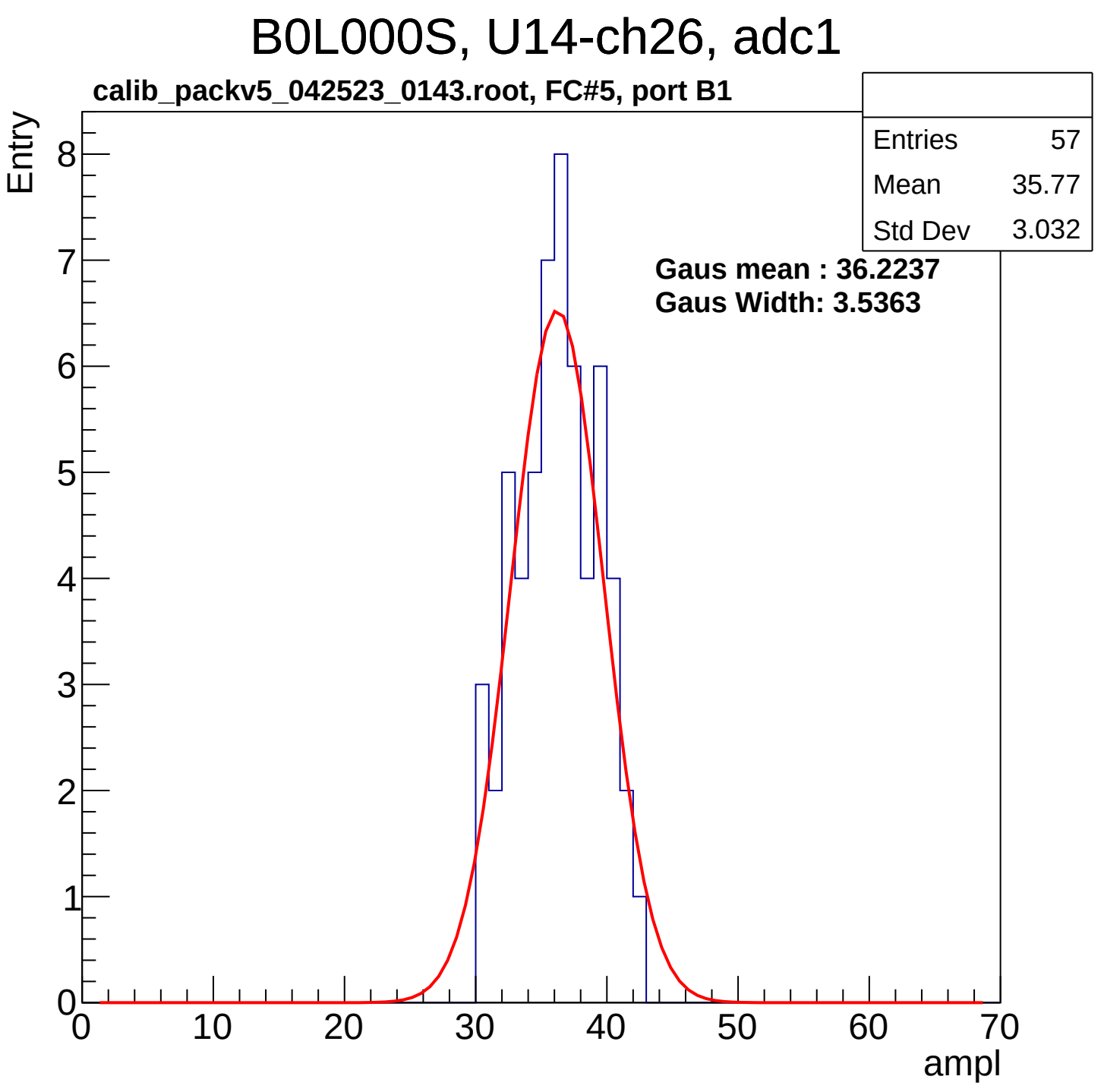
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.77
Std Dev 3.032
Gaus mean : 36.2237
Gaus Width: 3.5363
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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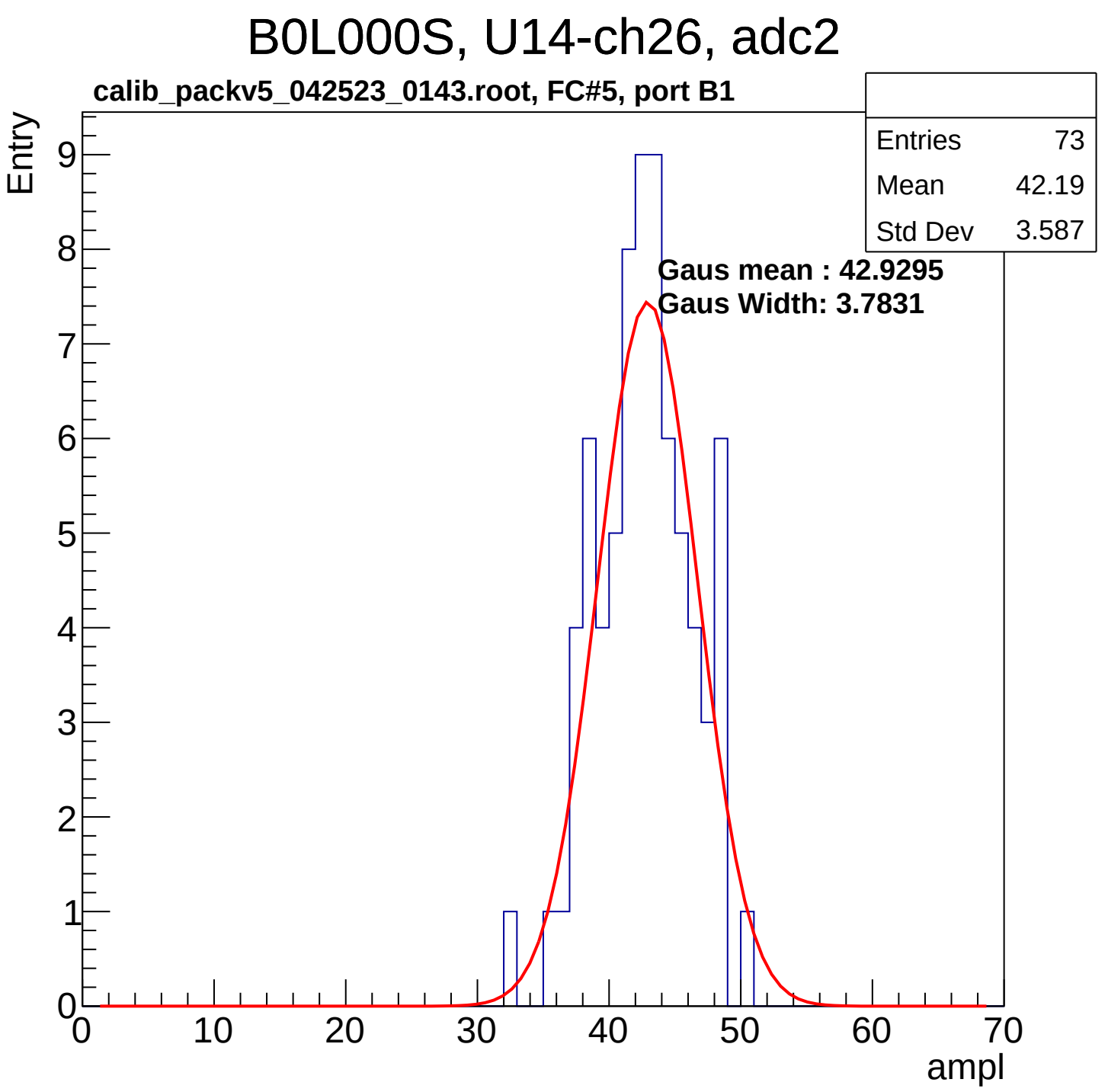
40
50
60
70


B0L000S, U14-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.19
Std Dev 3.587
Gaus mean : 42.9295
Gaus Width: 3.7831
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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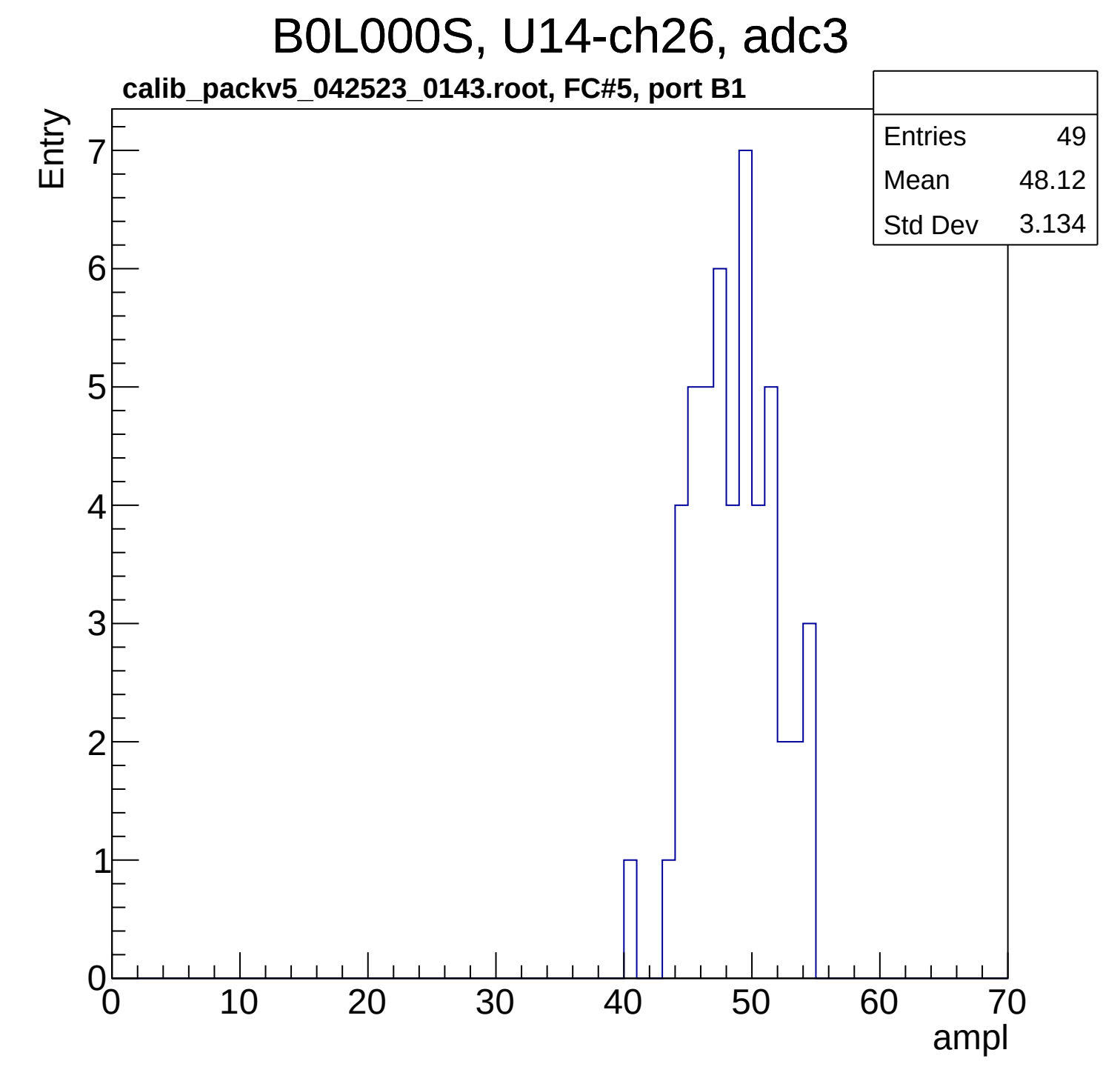
B0L000S, U14-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 48.12
Std Dev 3.134
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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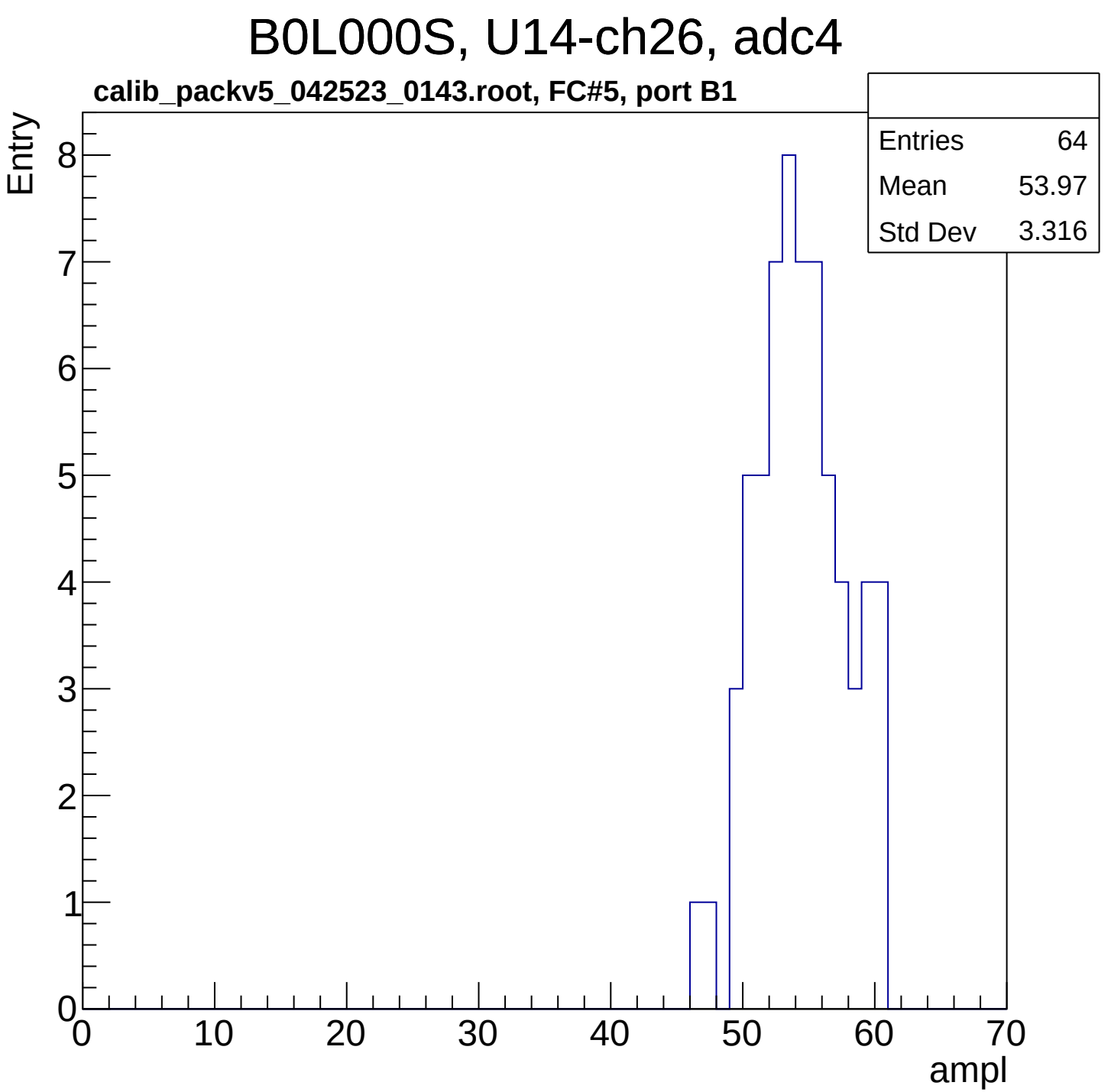
B0L000S, U14-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 53.97
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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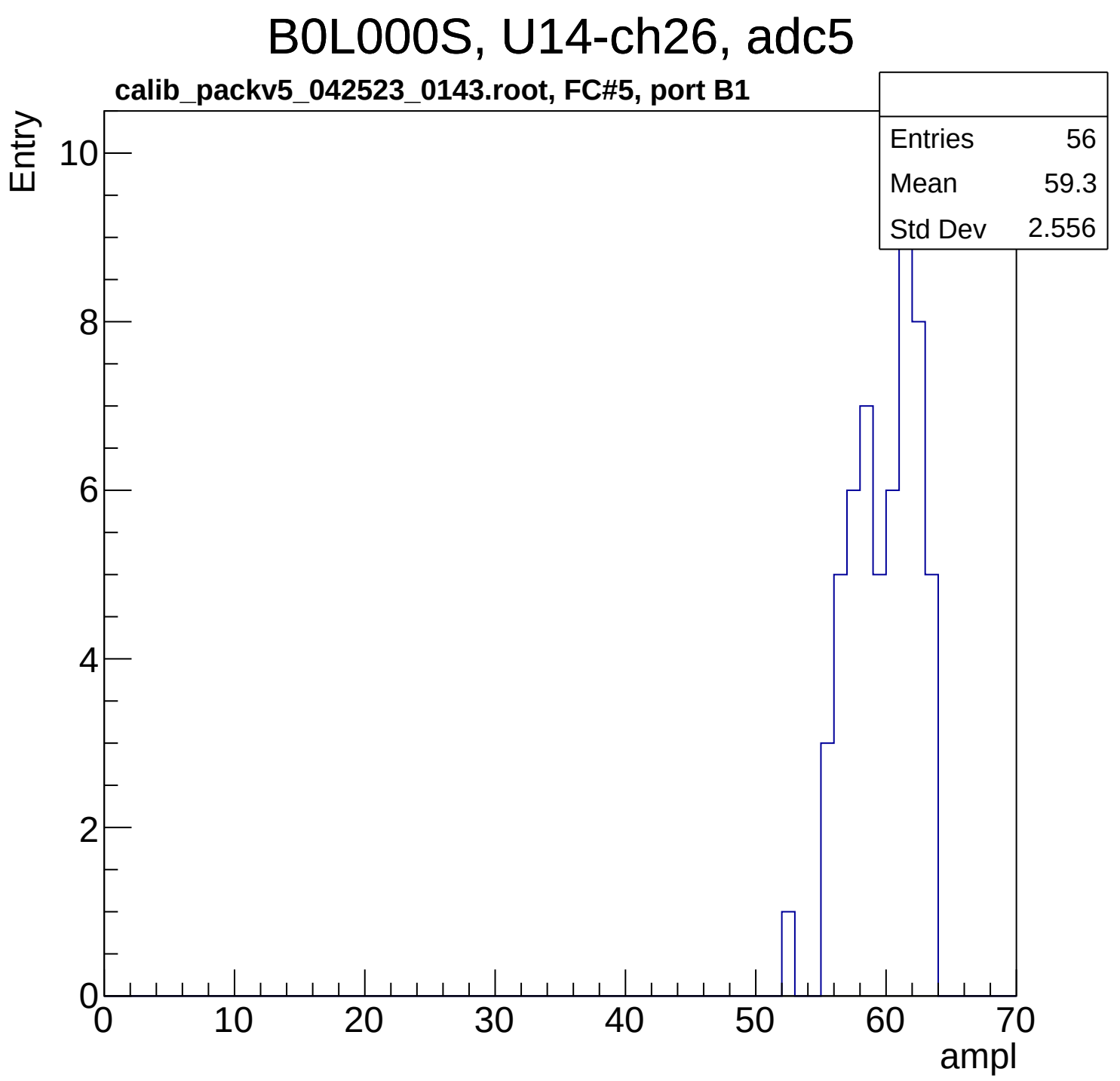
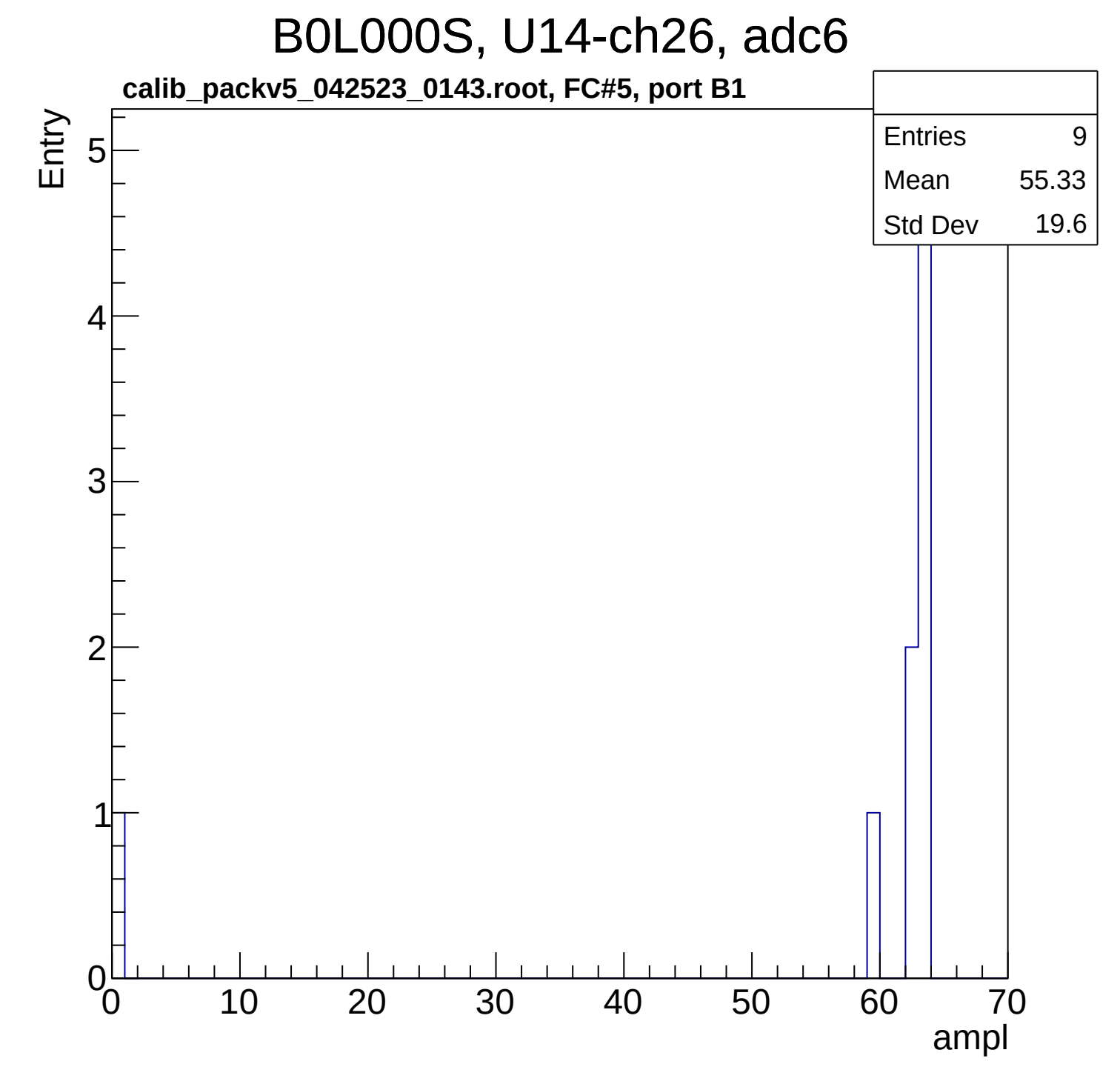
40
50
60
70


B0L000S, U14-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.3
Std Dev 2.556
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 55.33
Std Dev 19.6
Entry
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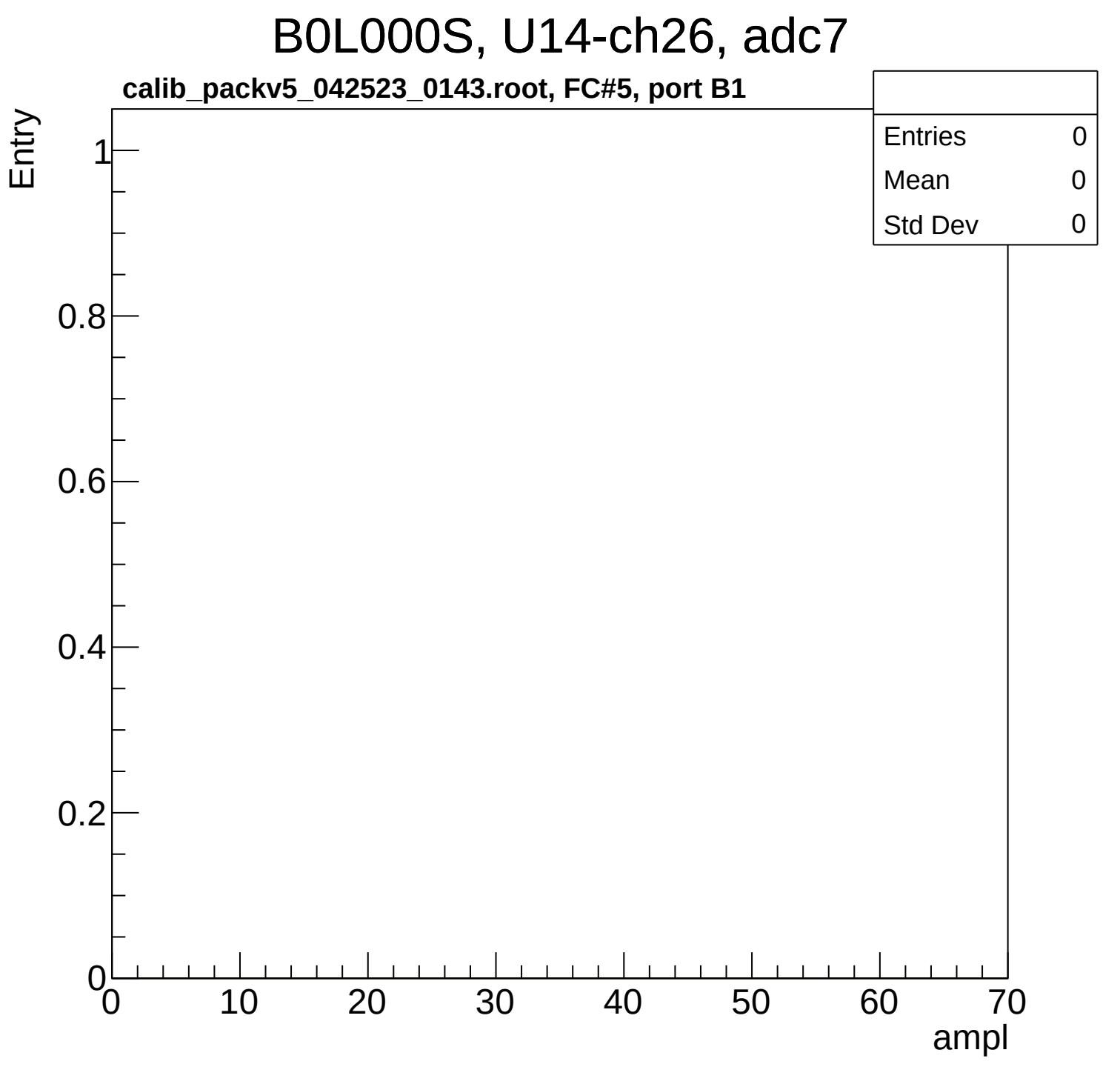
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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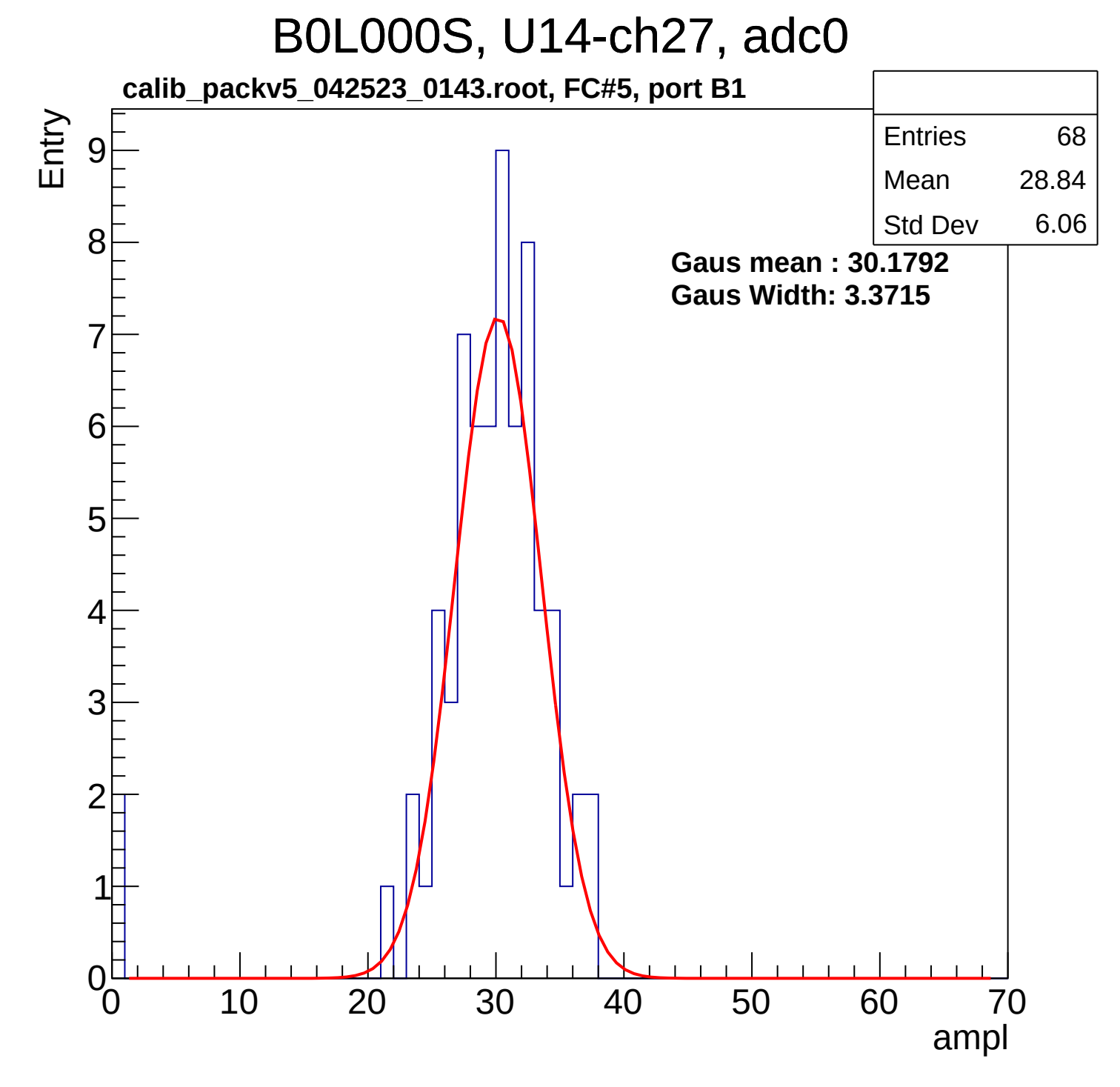
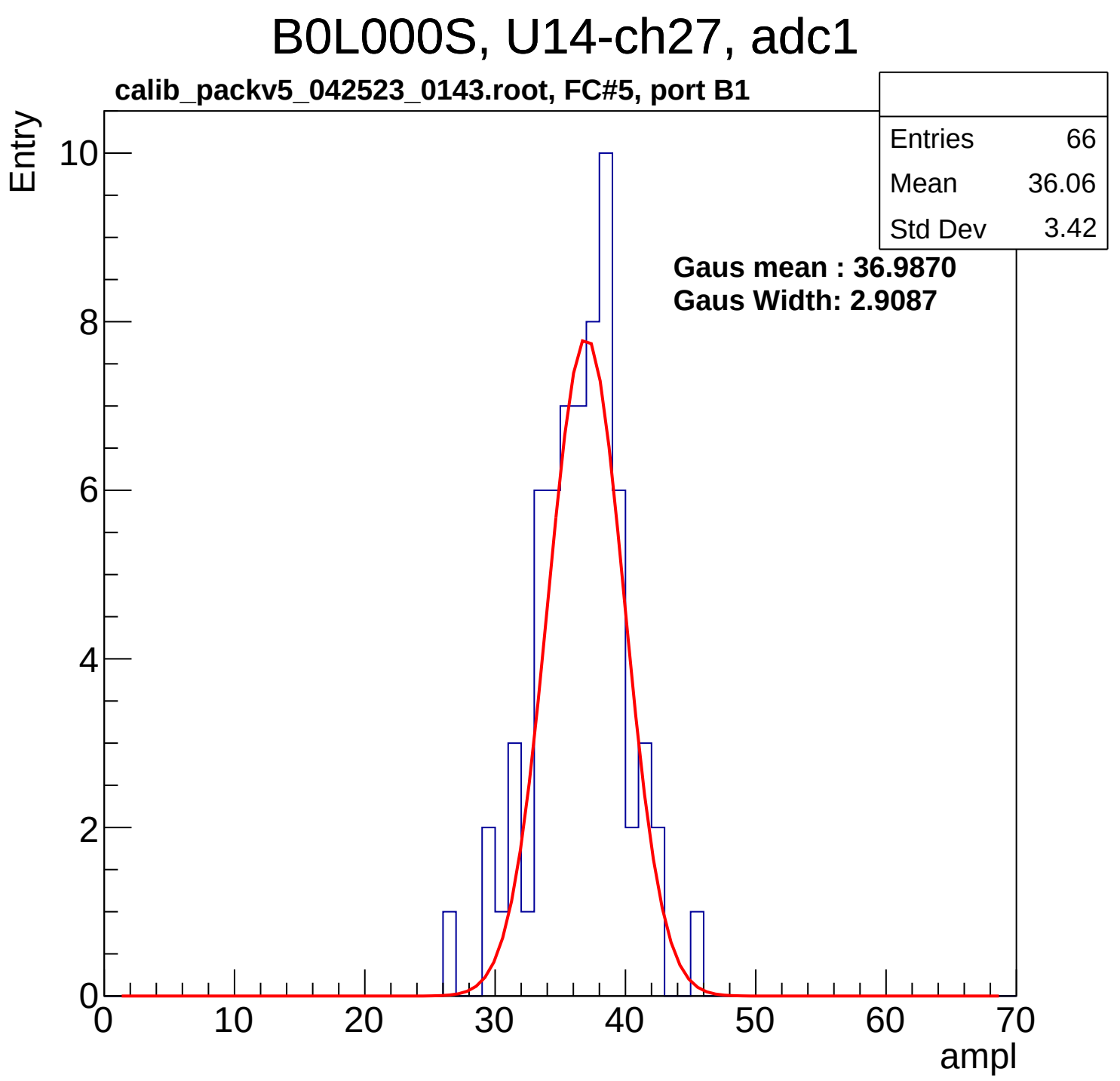
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.84
Std Dev 6.06
Gaus mean : 30.1792
Gaus Width: 3.3715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.06
Std Dev 3.42
Gaus mean : 36.9870
Gaus Width: 2.9087
Entry
ampl
0
2
4
6
8
10
0
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40
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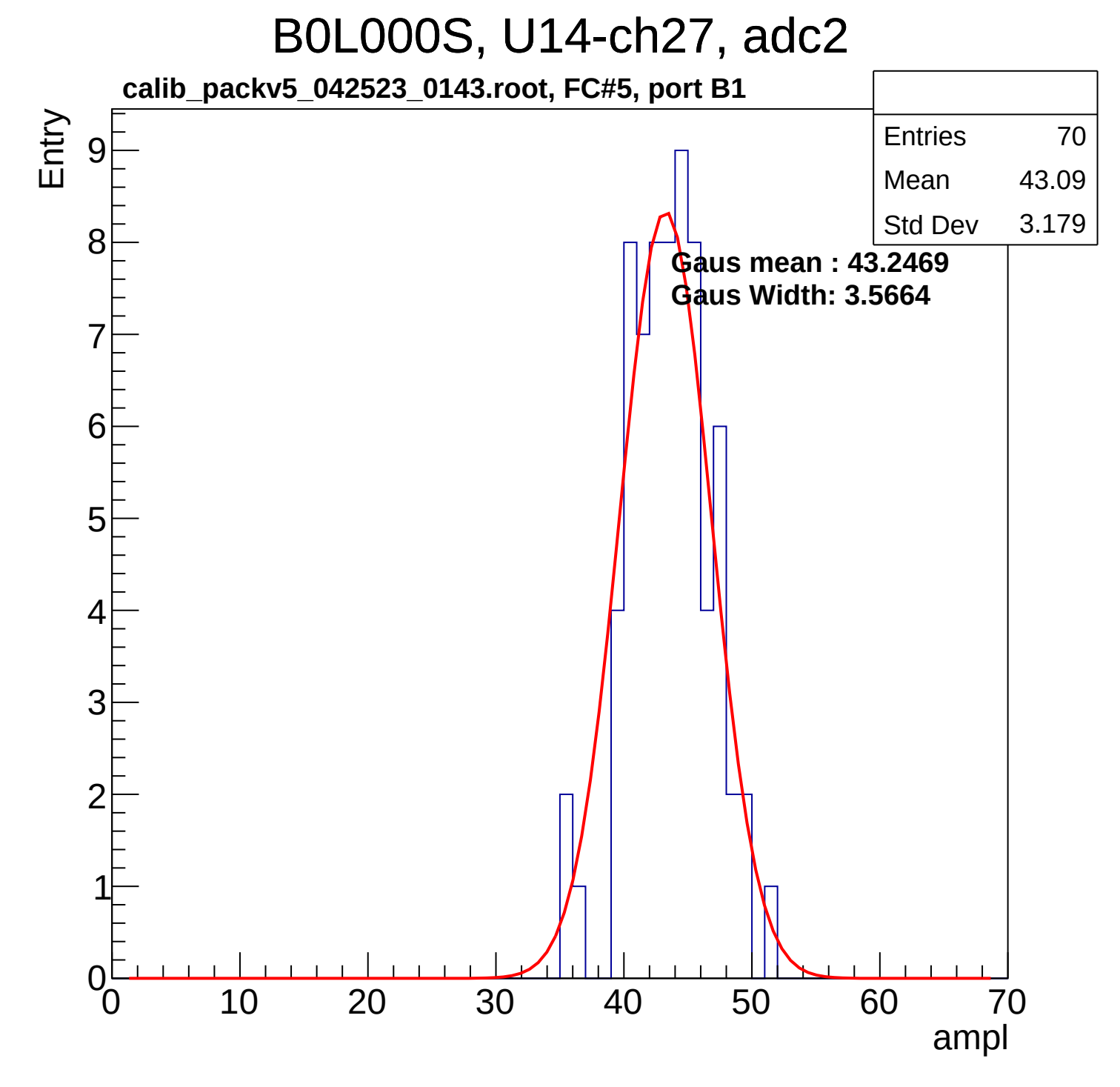
B0L000S, U14-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 43.09
Std Dev 3.179
Gaus mean : 43.2469
Gaus Width: 3.5664
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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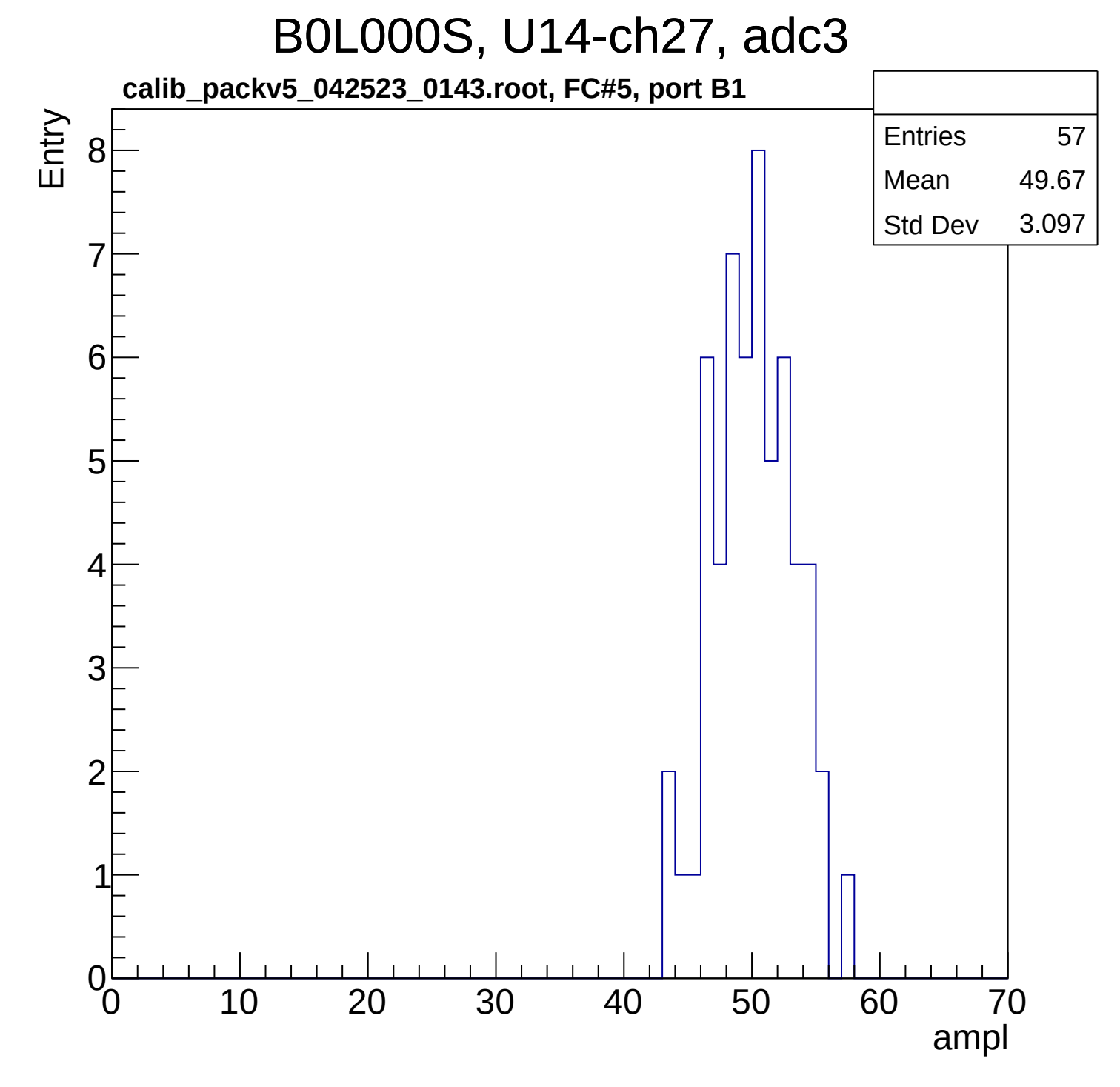
B0L000S, U14-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.67
Std Dev 3.097
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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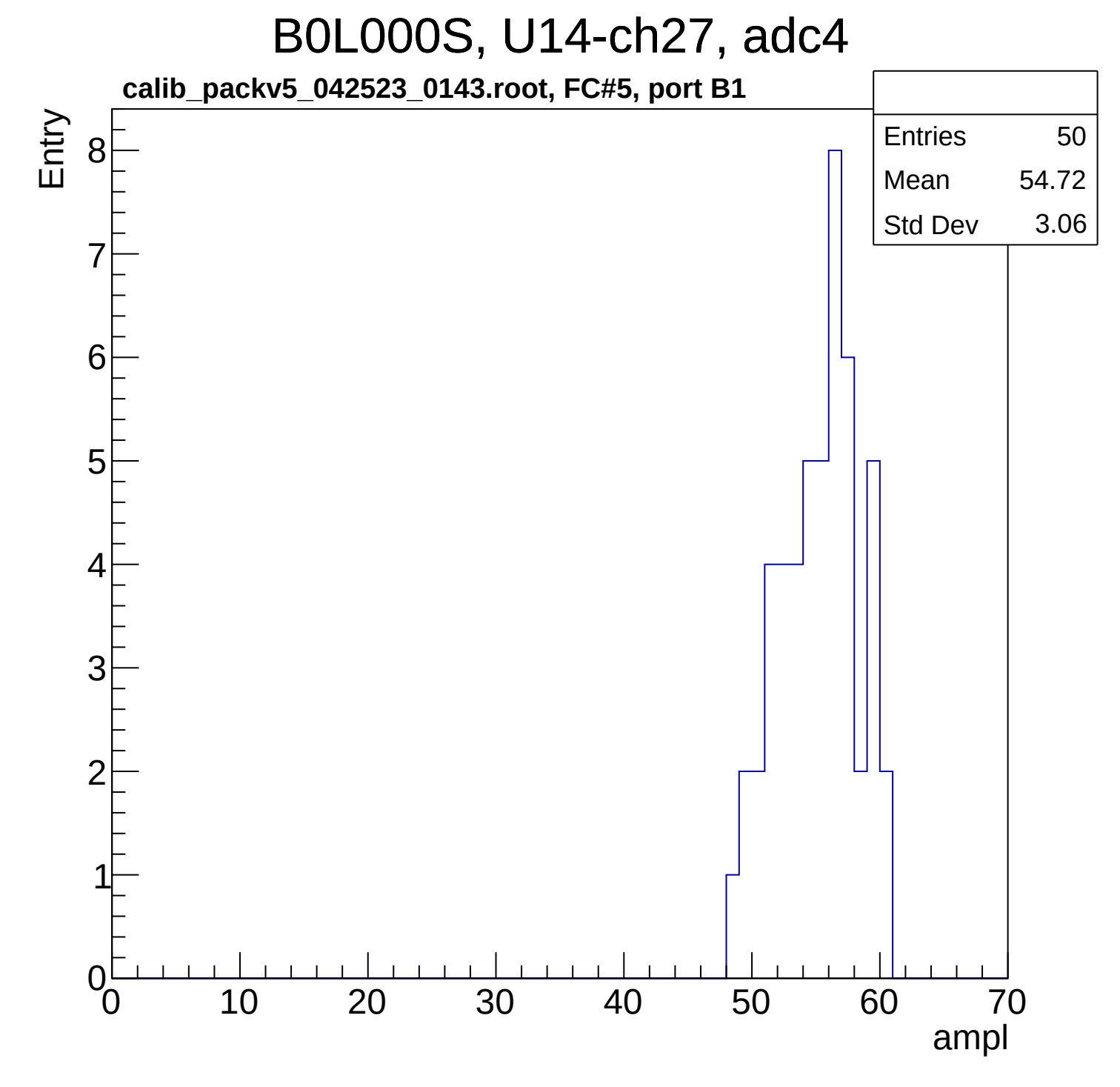
B0L000S, U14-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.72
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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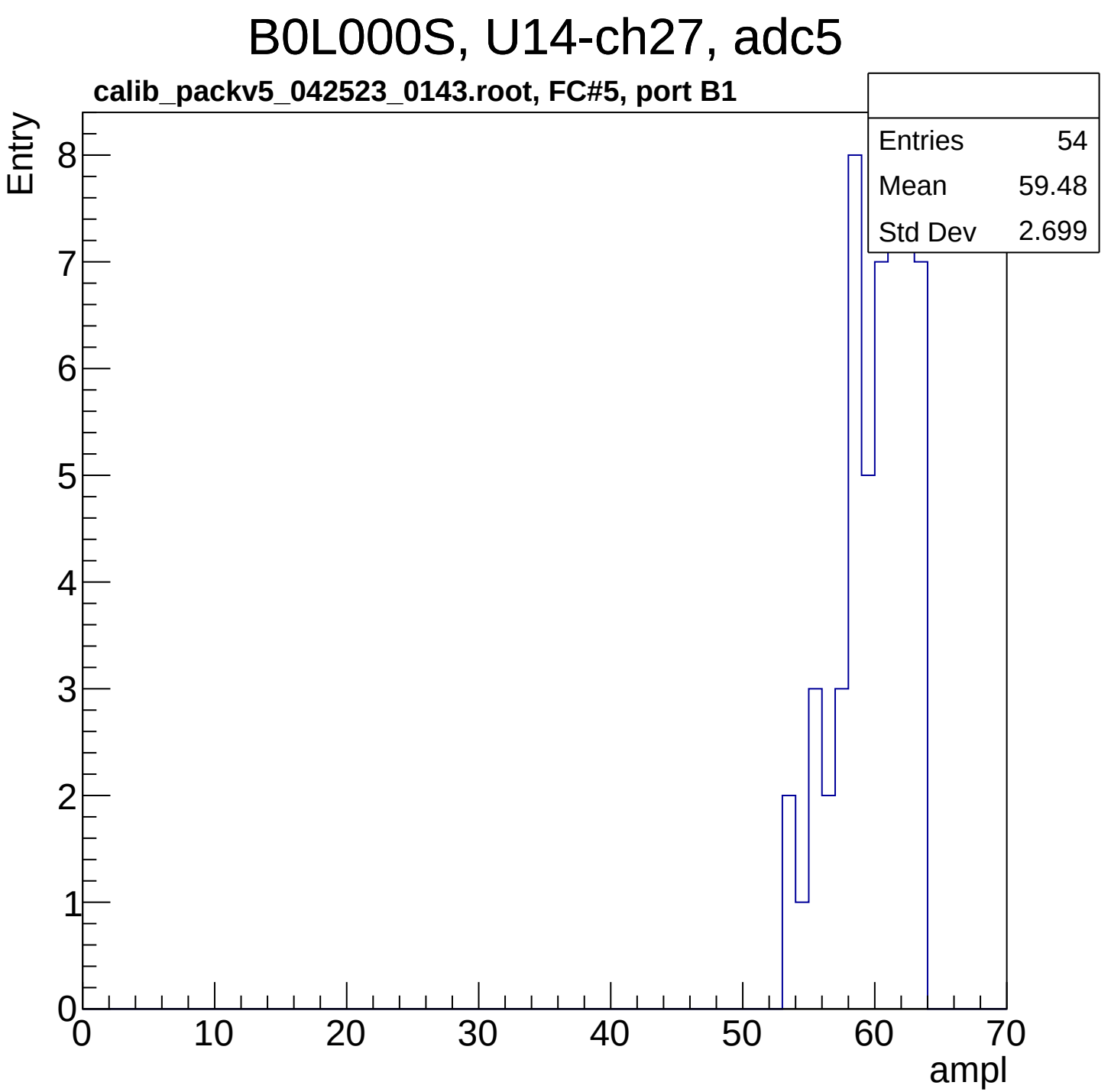
B0L000S, U14-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.48
Std Dev 2.699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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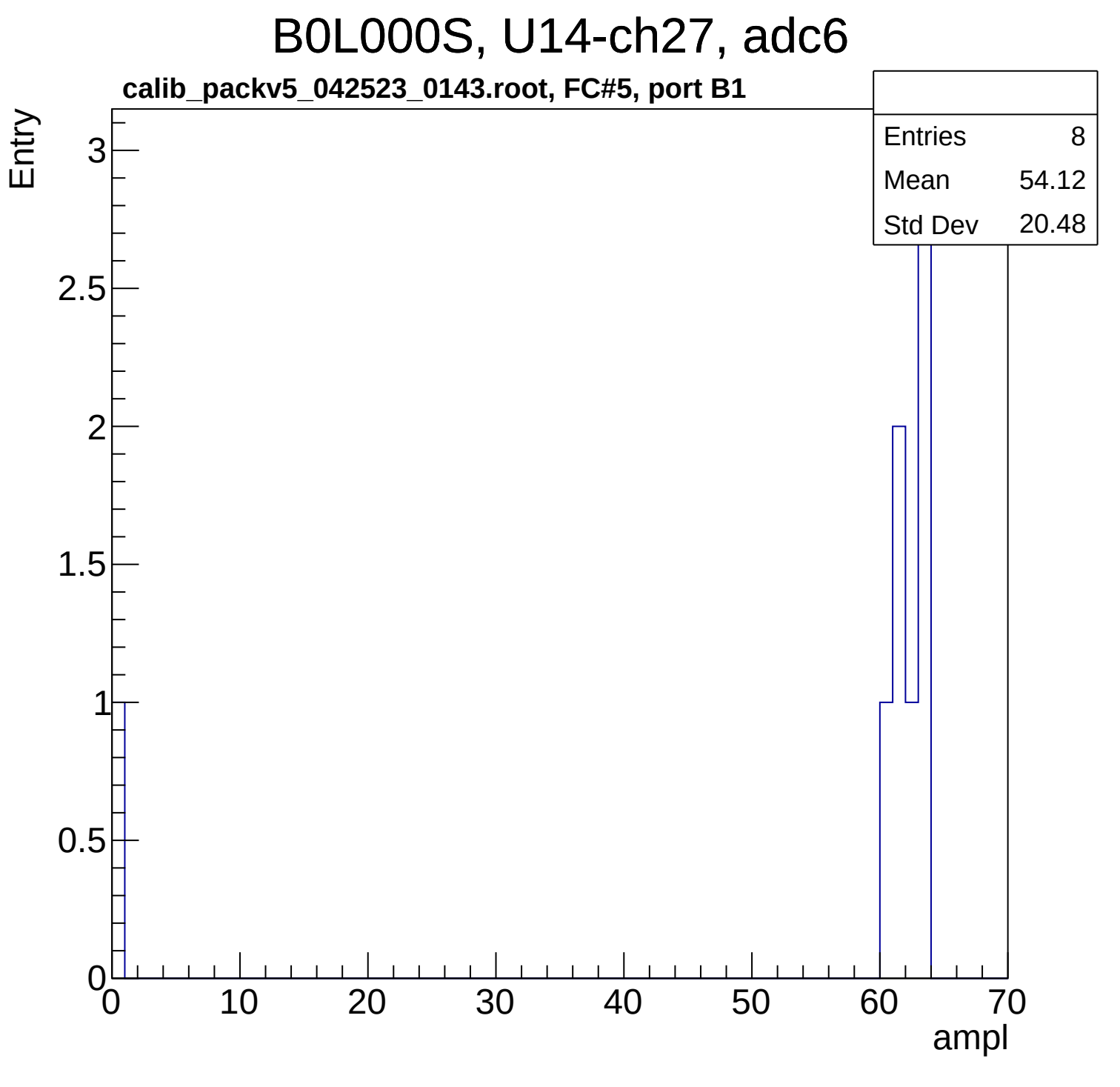
B0L000S, U14-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.12
Std Dev 20.48
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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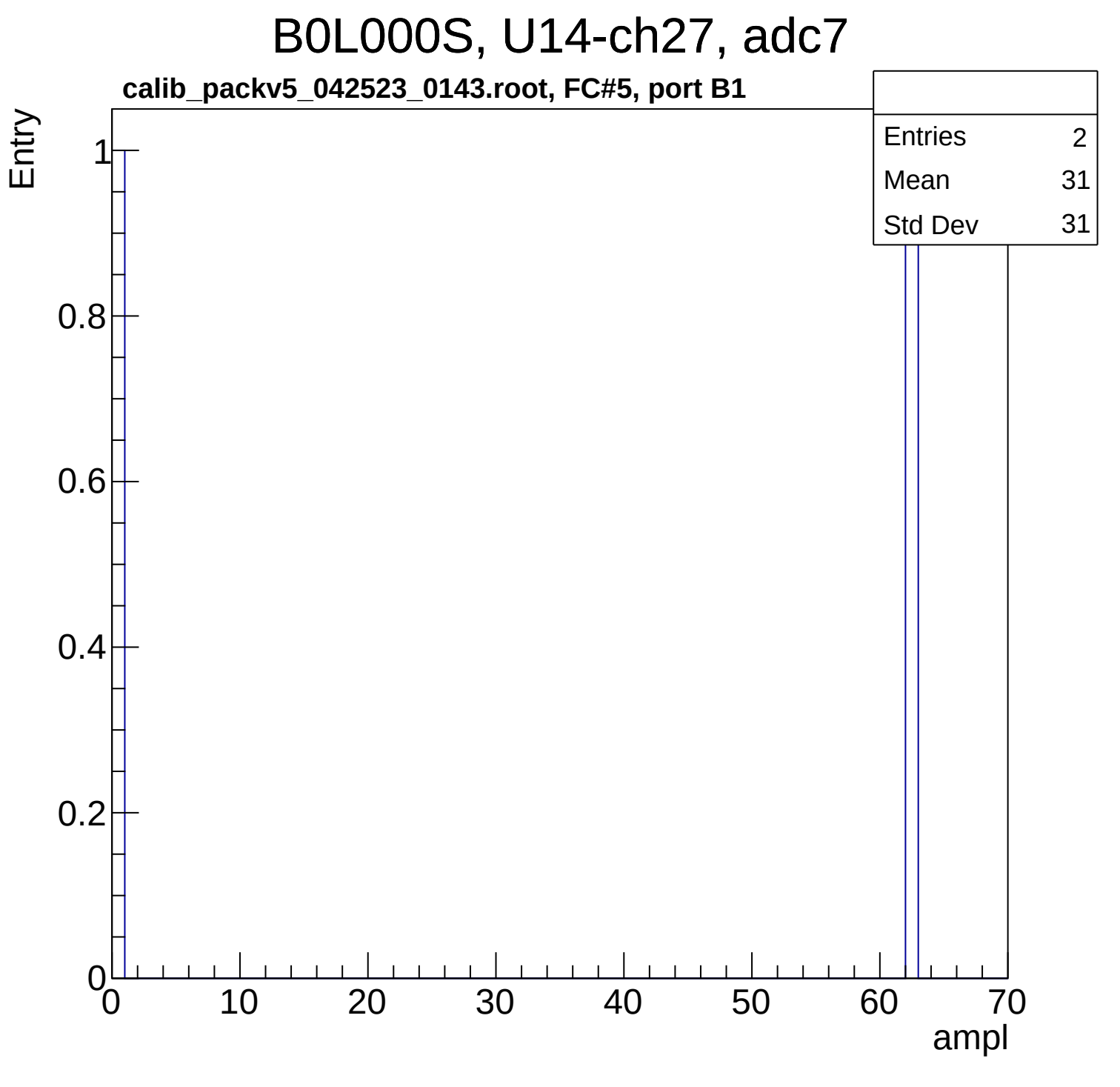
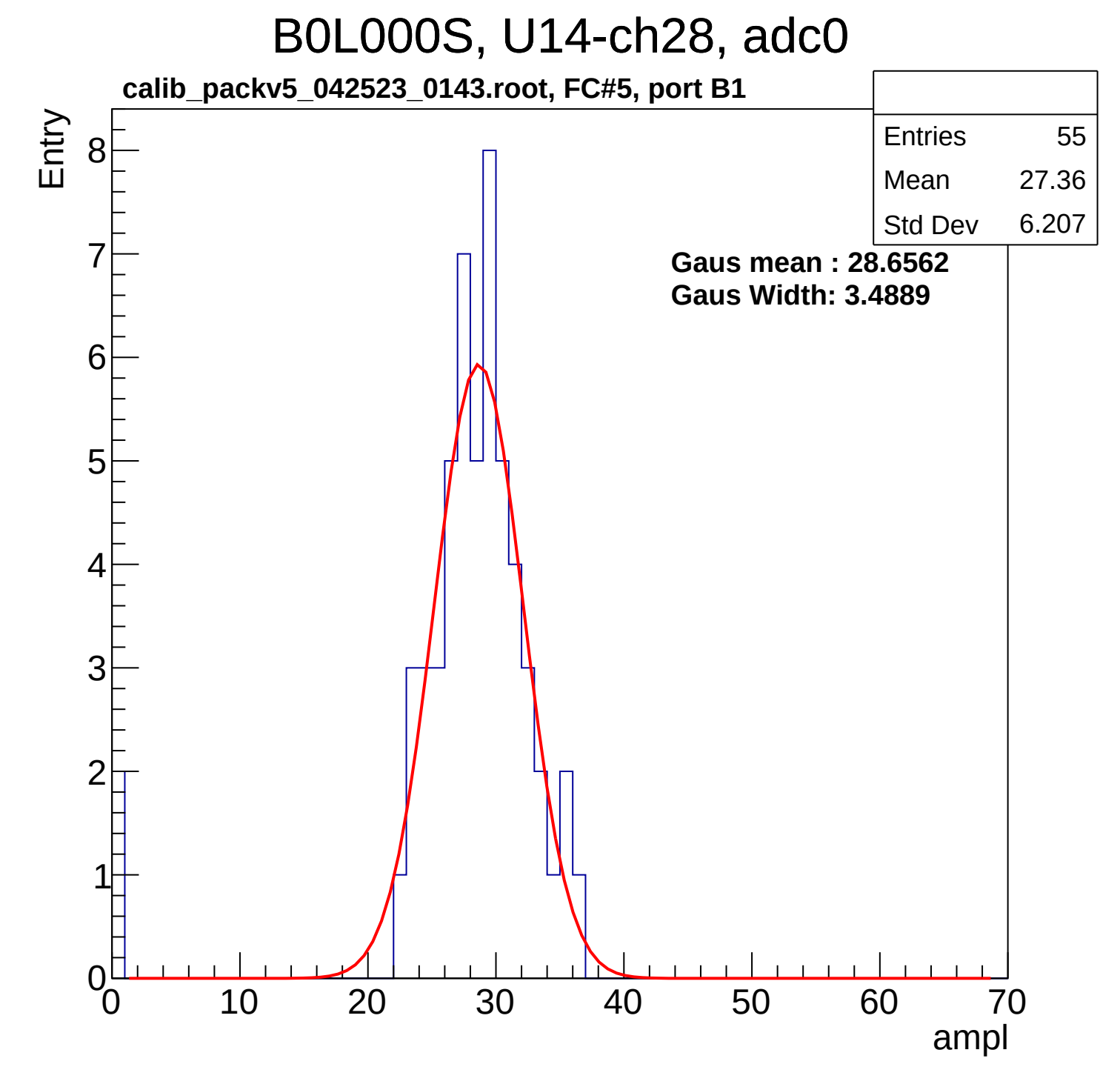

B0L000S, U14-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 27.36
Std Dev 6.207
Gaus mean : 28.6562
Gaus Width: 3.4889
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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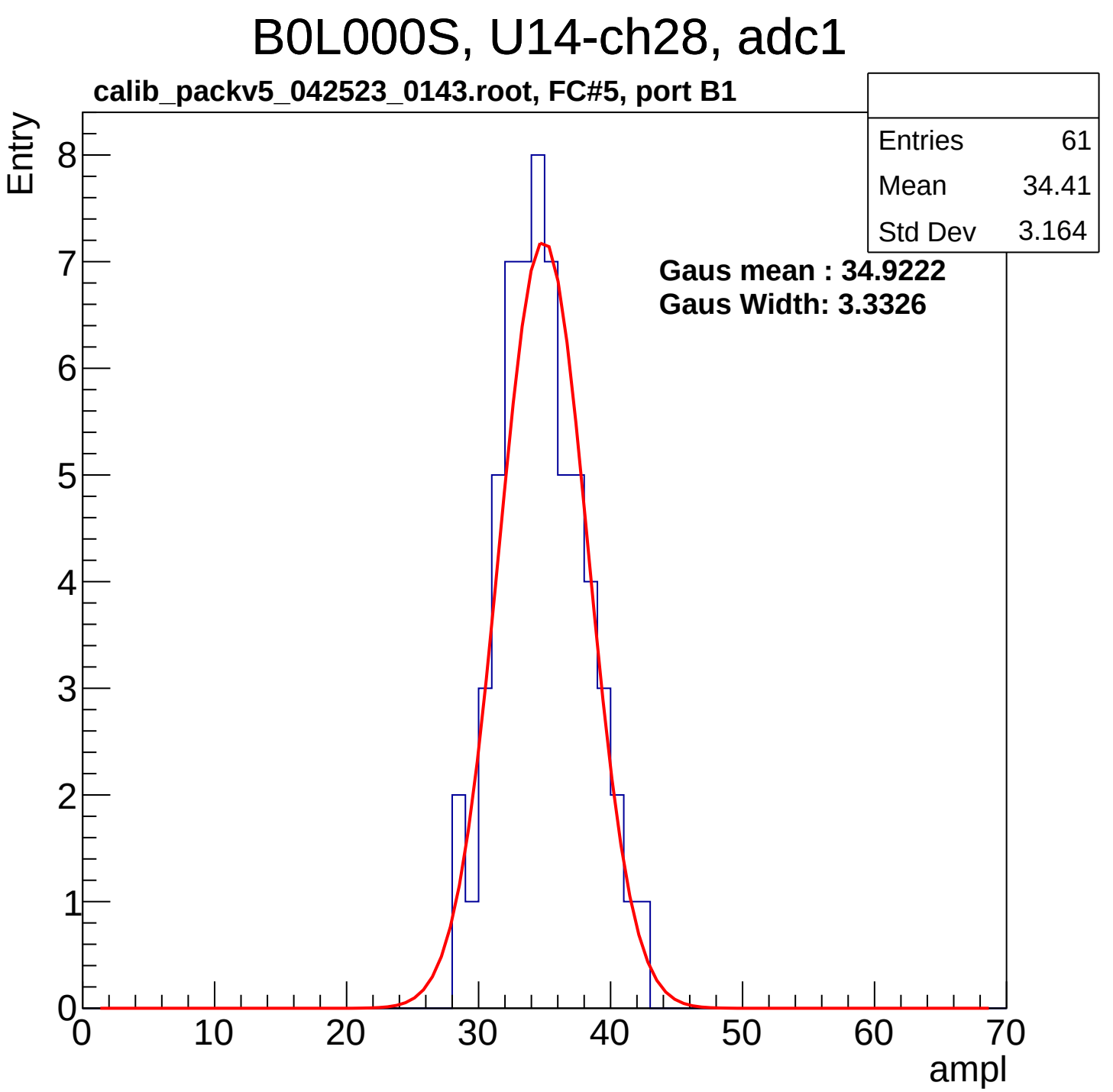
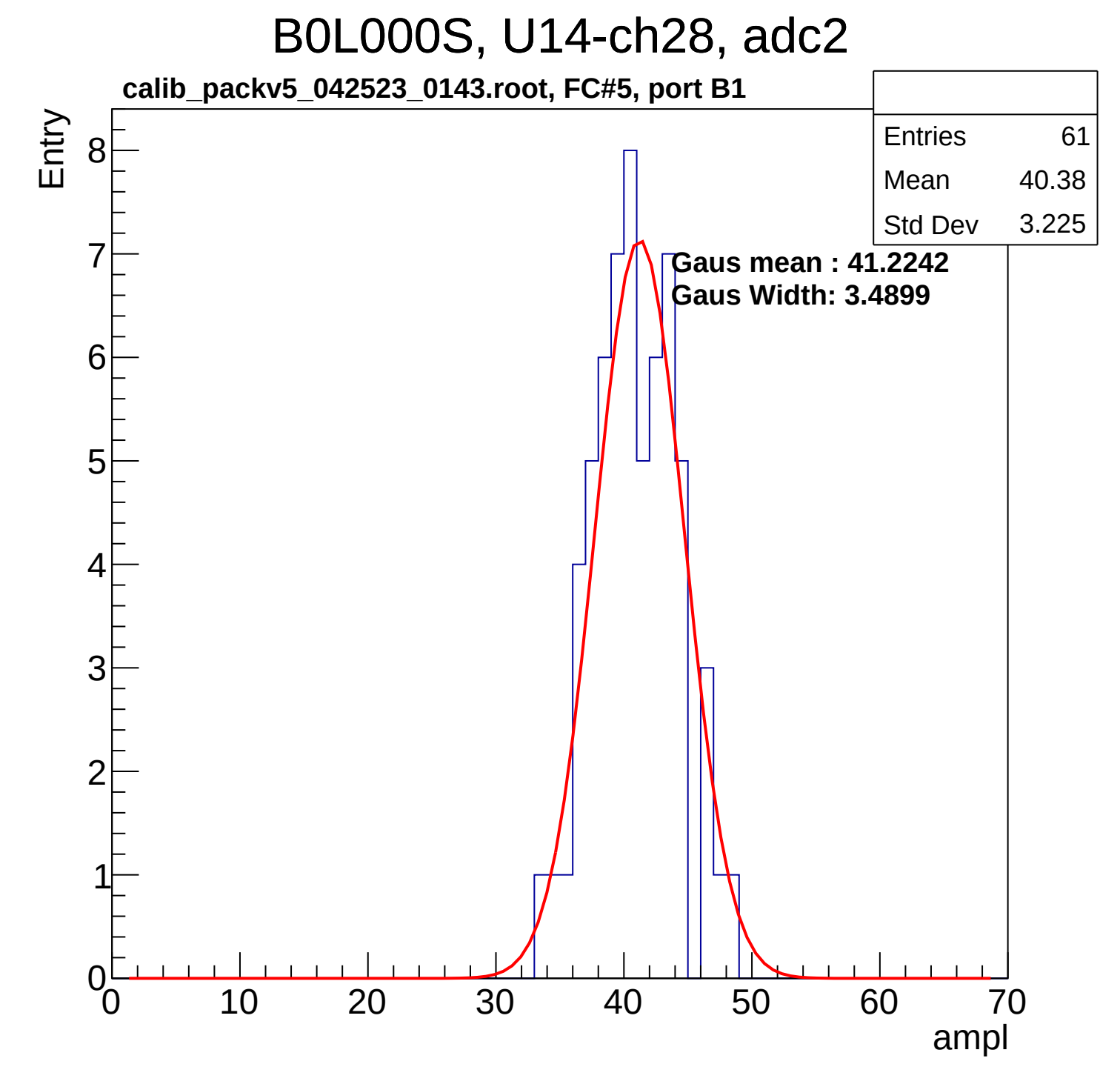
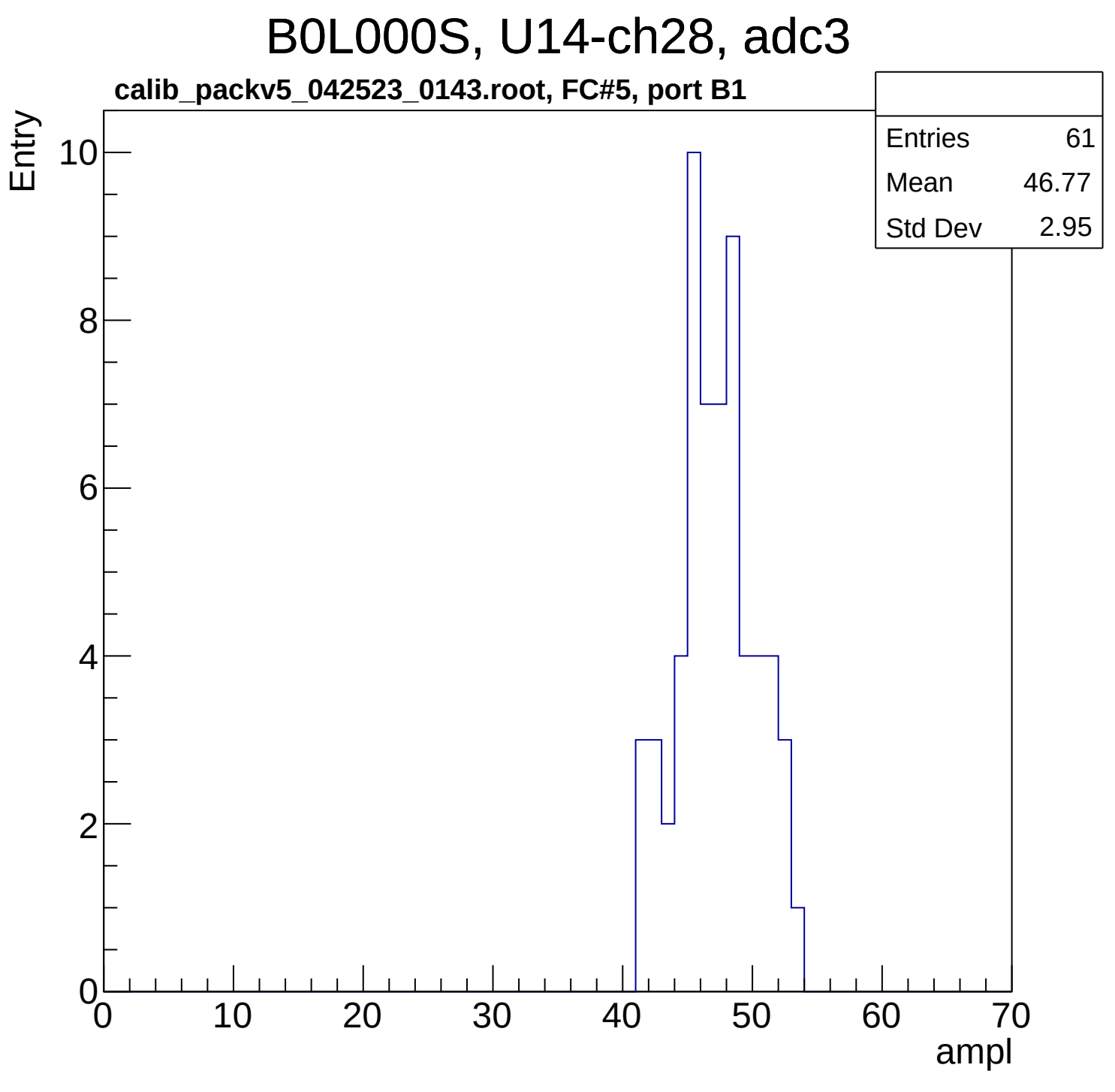
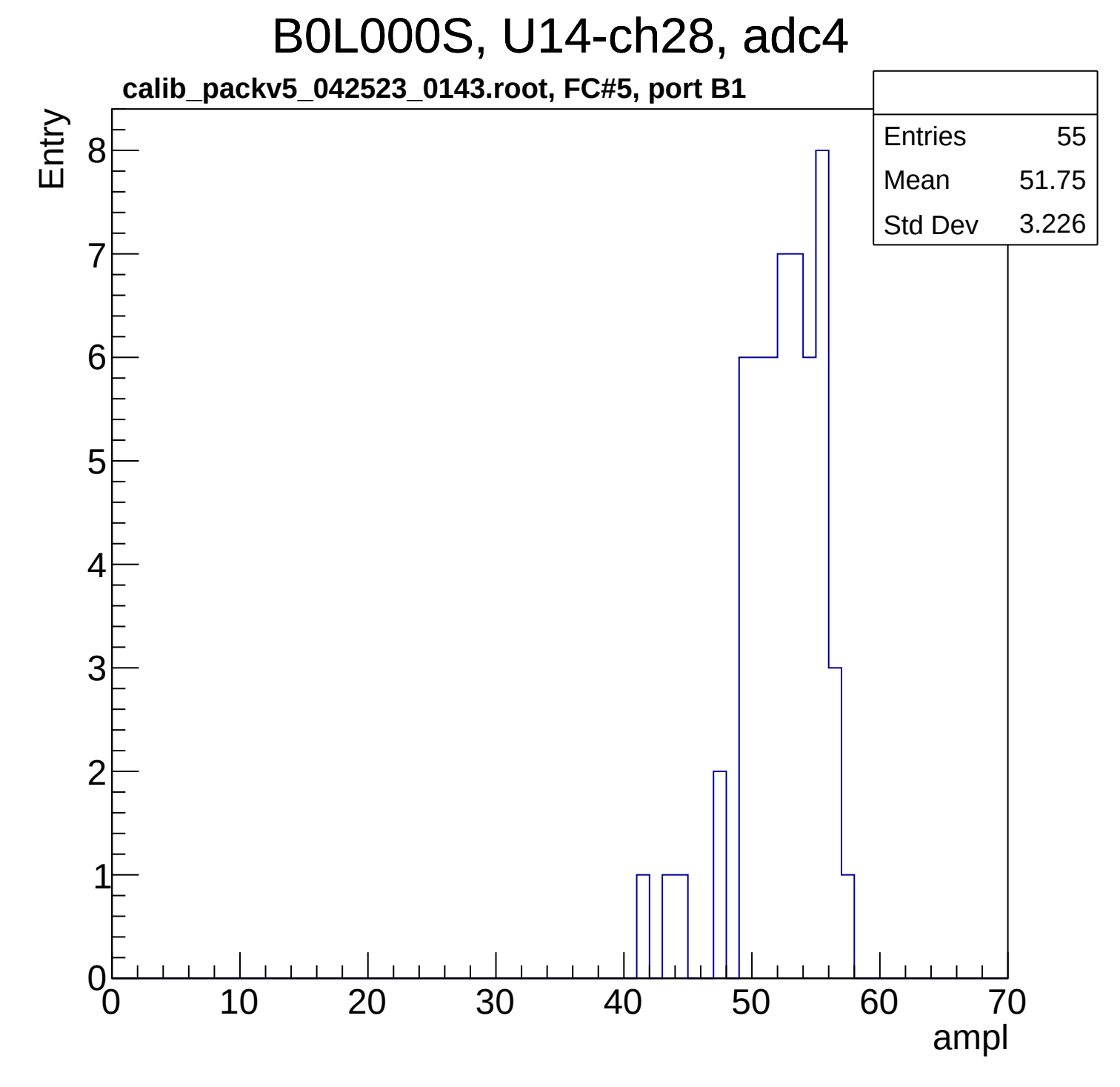

B0L000S, U14-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 34.41
Std Dev 3.164
Gaus mean : 34.9222
Gaus Width: 3.3326
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 40.38
Std Dev 3.225
Gaus mean : 41.2242
Gaus Width: 3.4899
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 46.77
Std Dev 2.95
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 51.75
Std Dev 3.226
Entry
ampl
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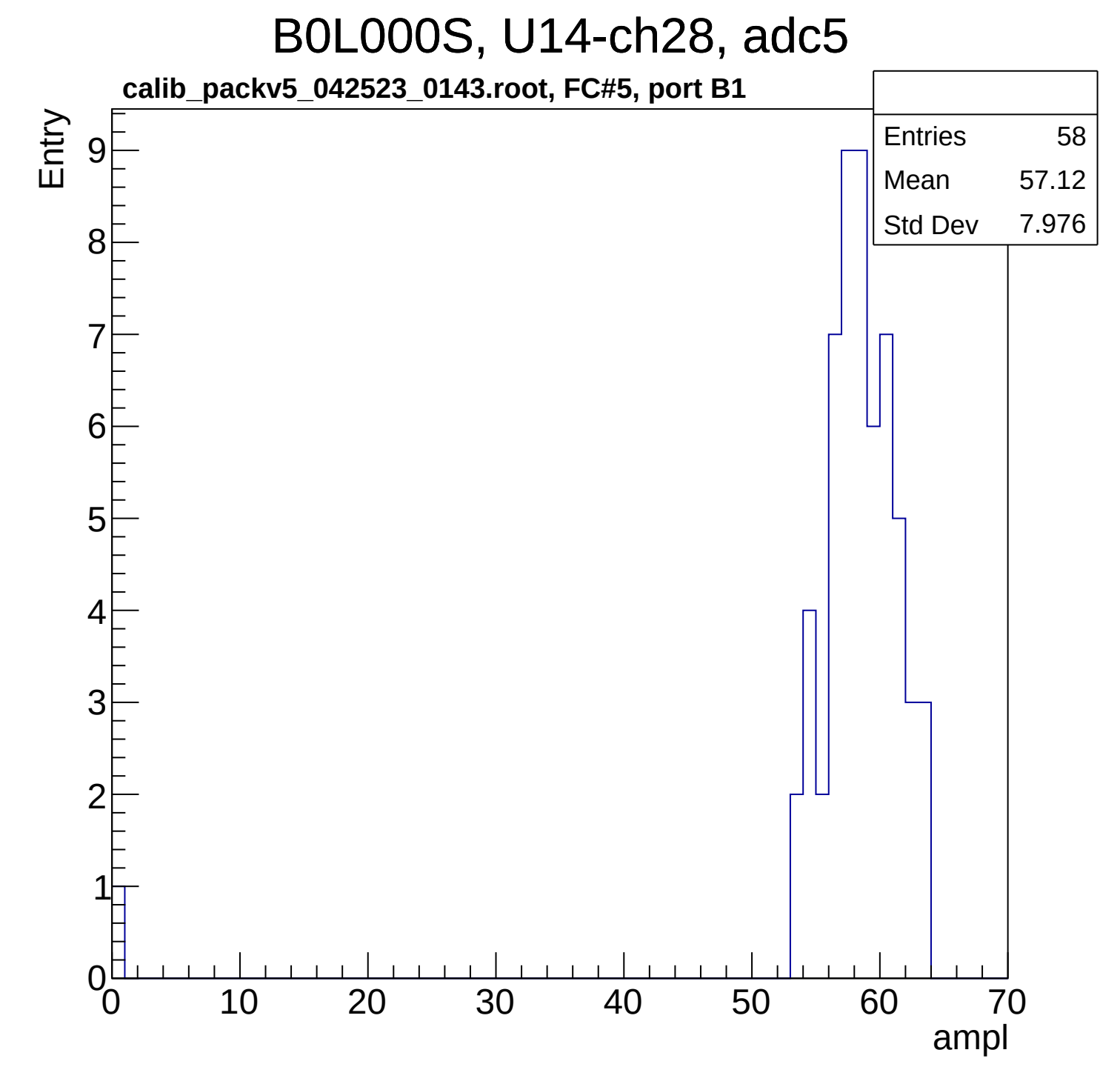
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.12
Std Dev 7.976
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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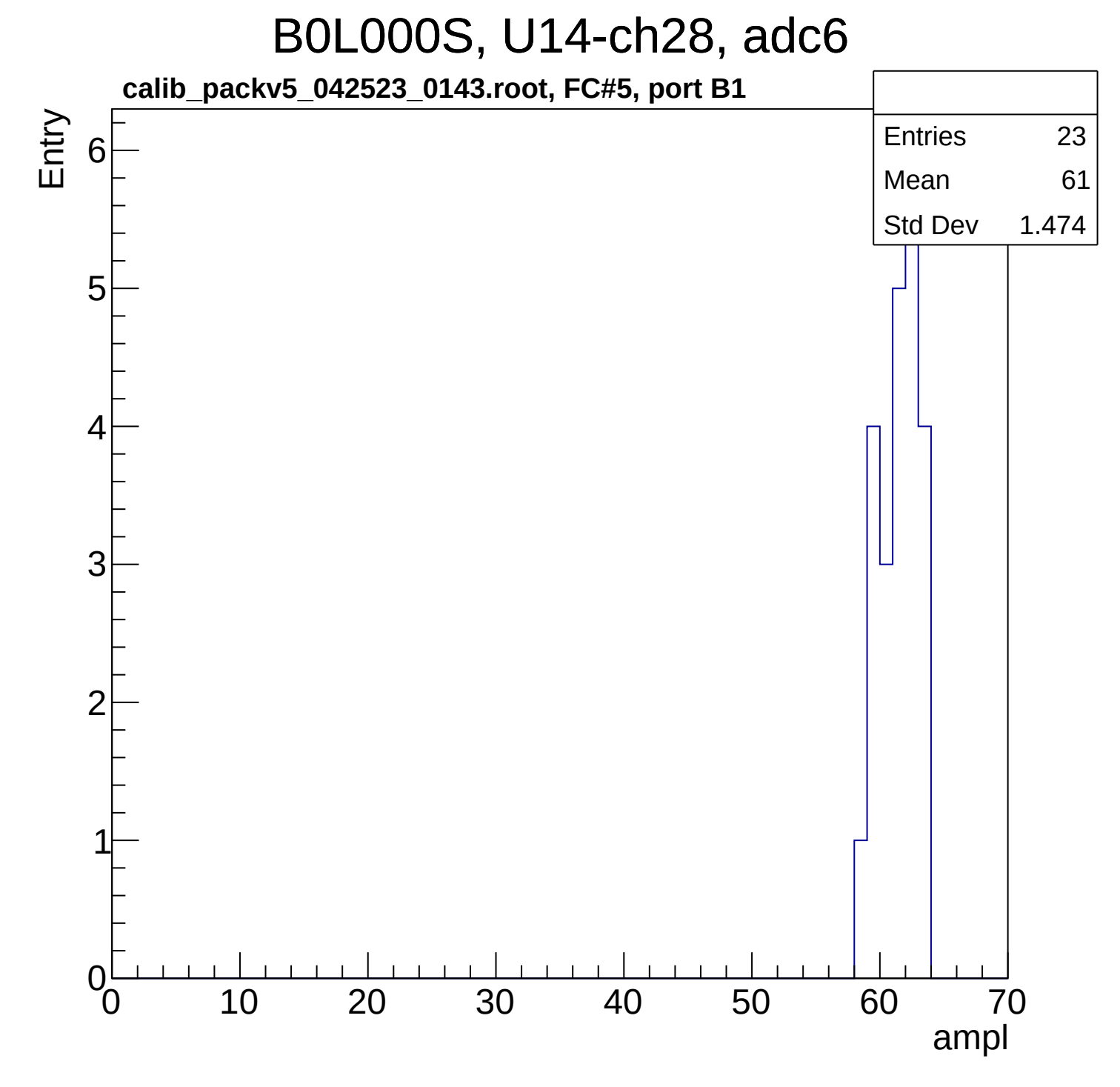
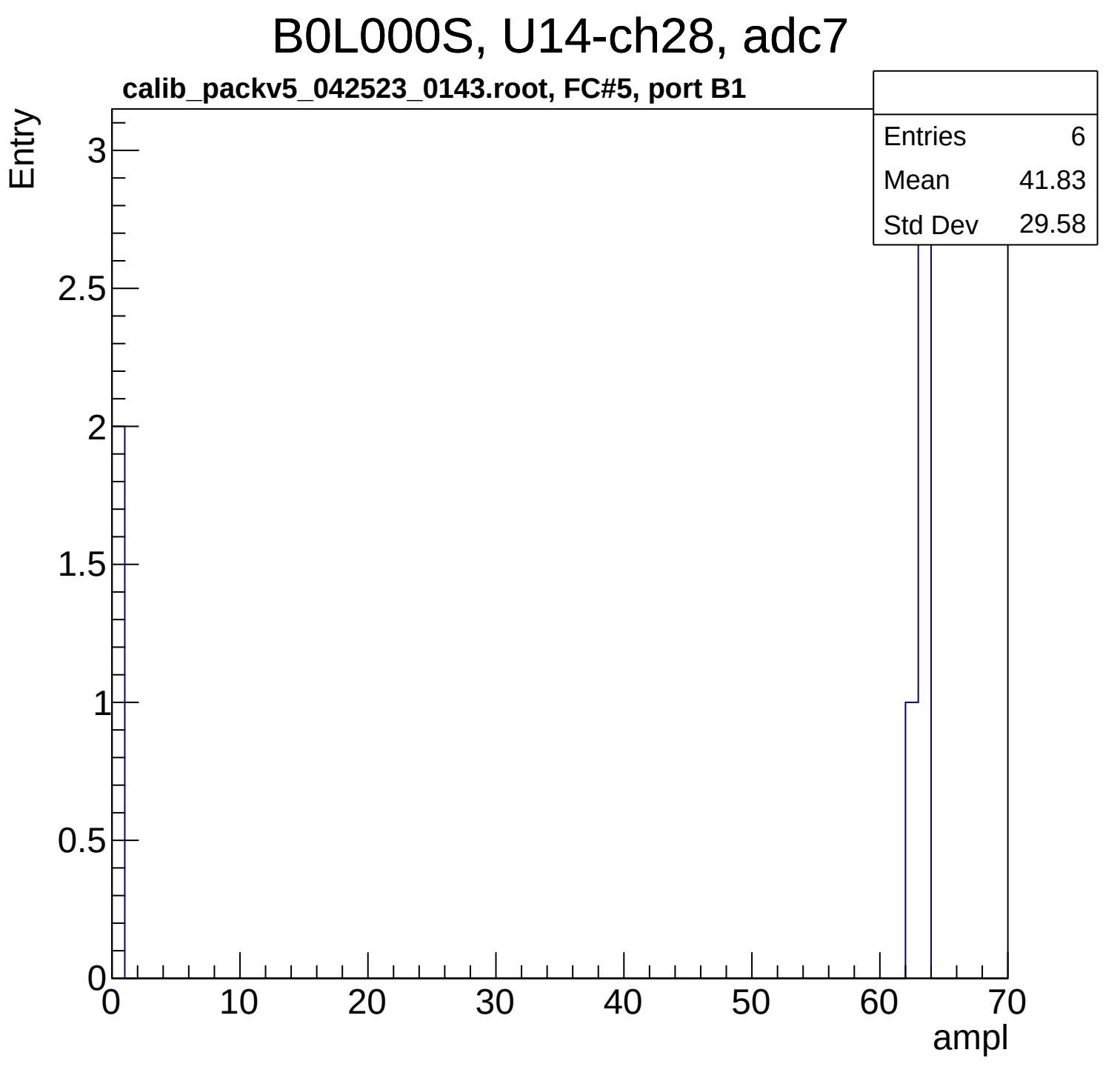

B0L000S, U14-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 61
Std Dev 1.474
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 41.83
Std Dev 29.58
Entry
ampl
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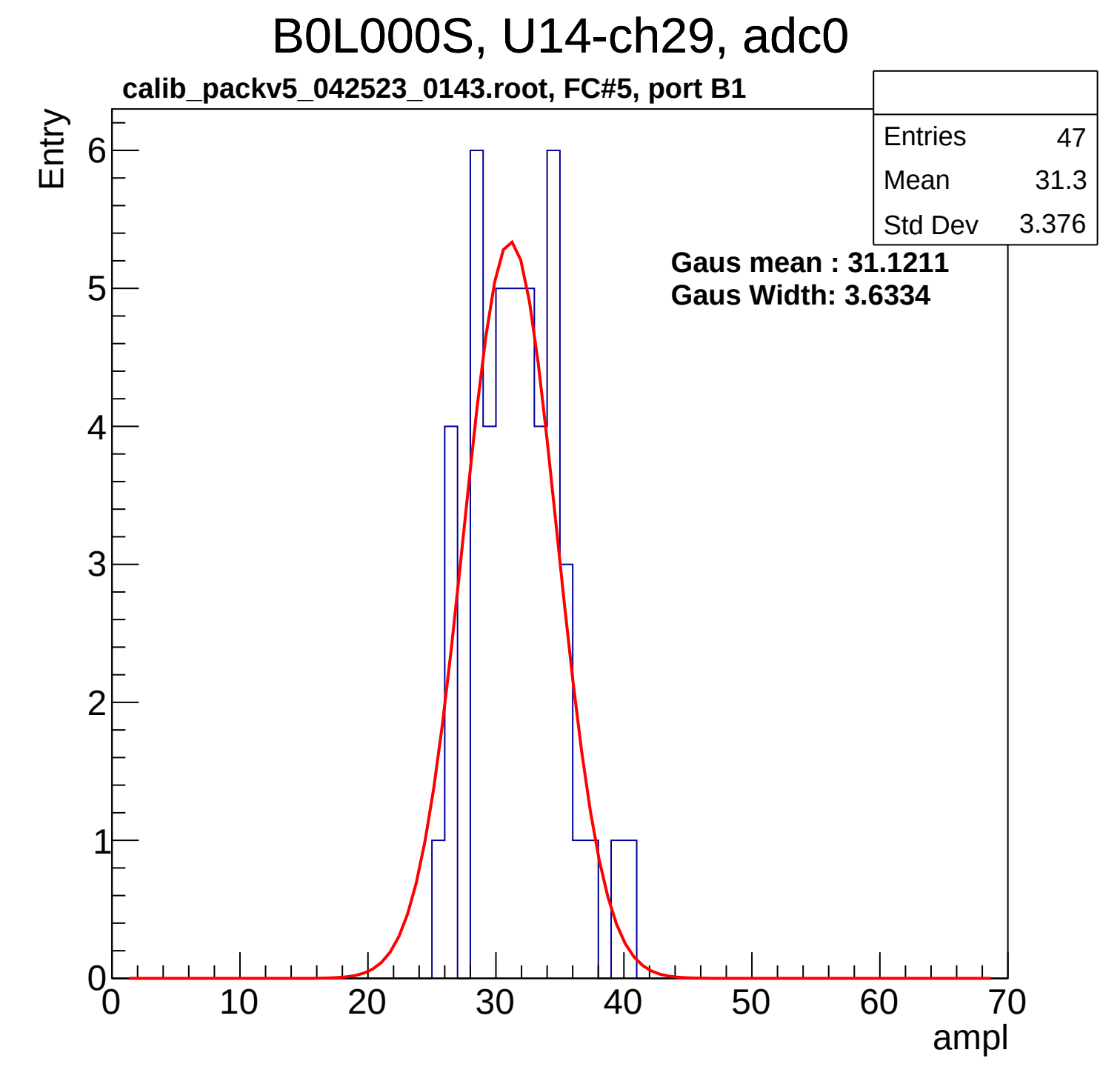
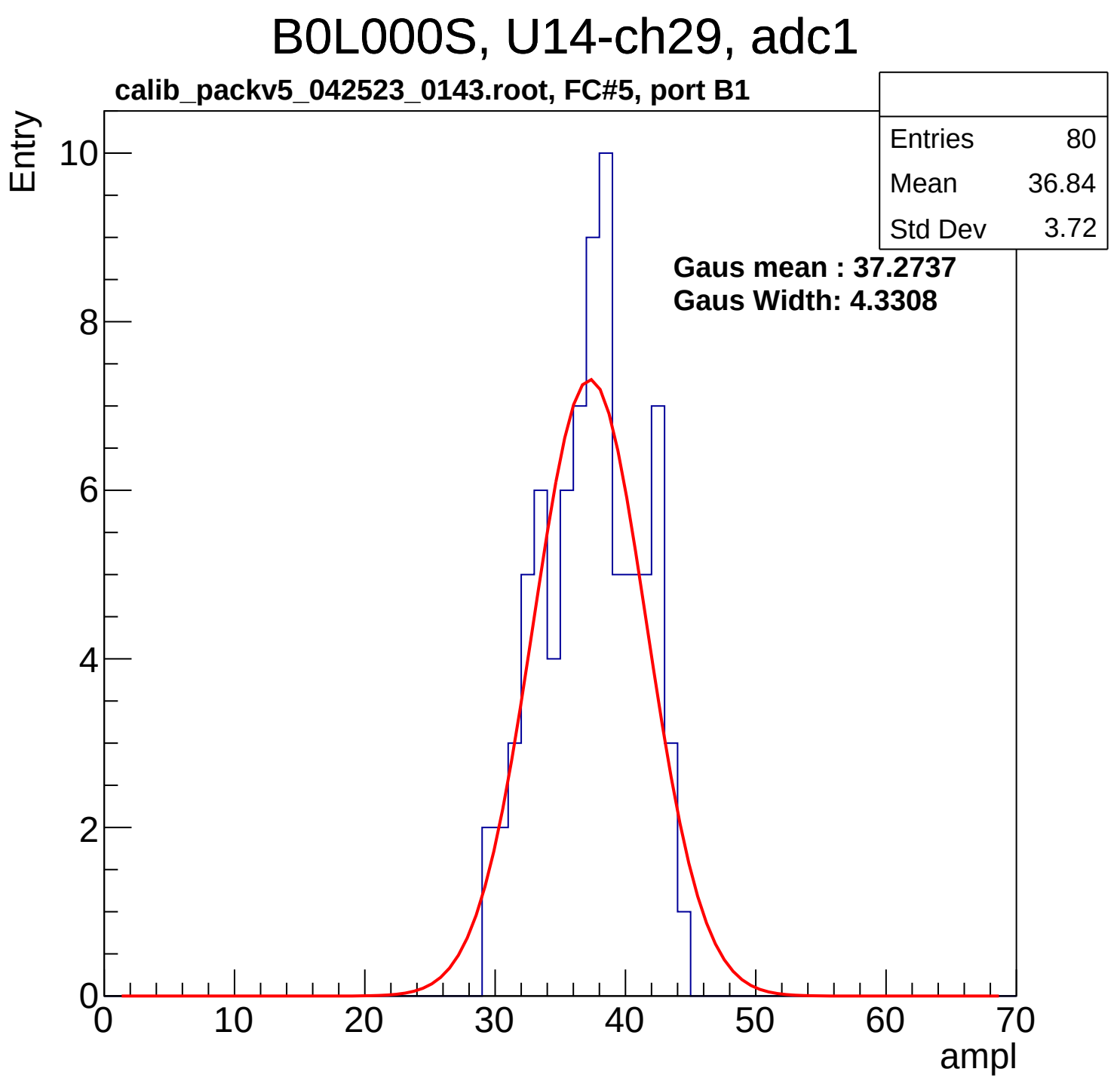
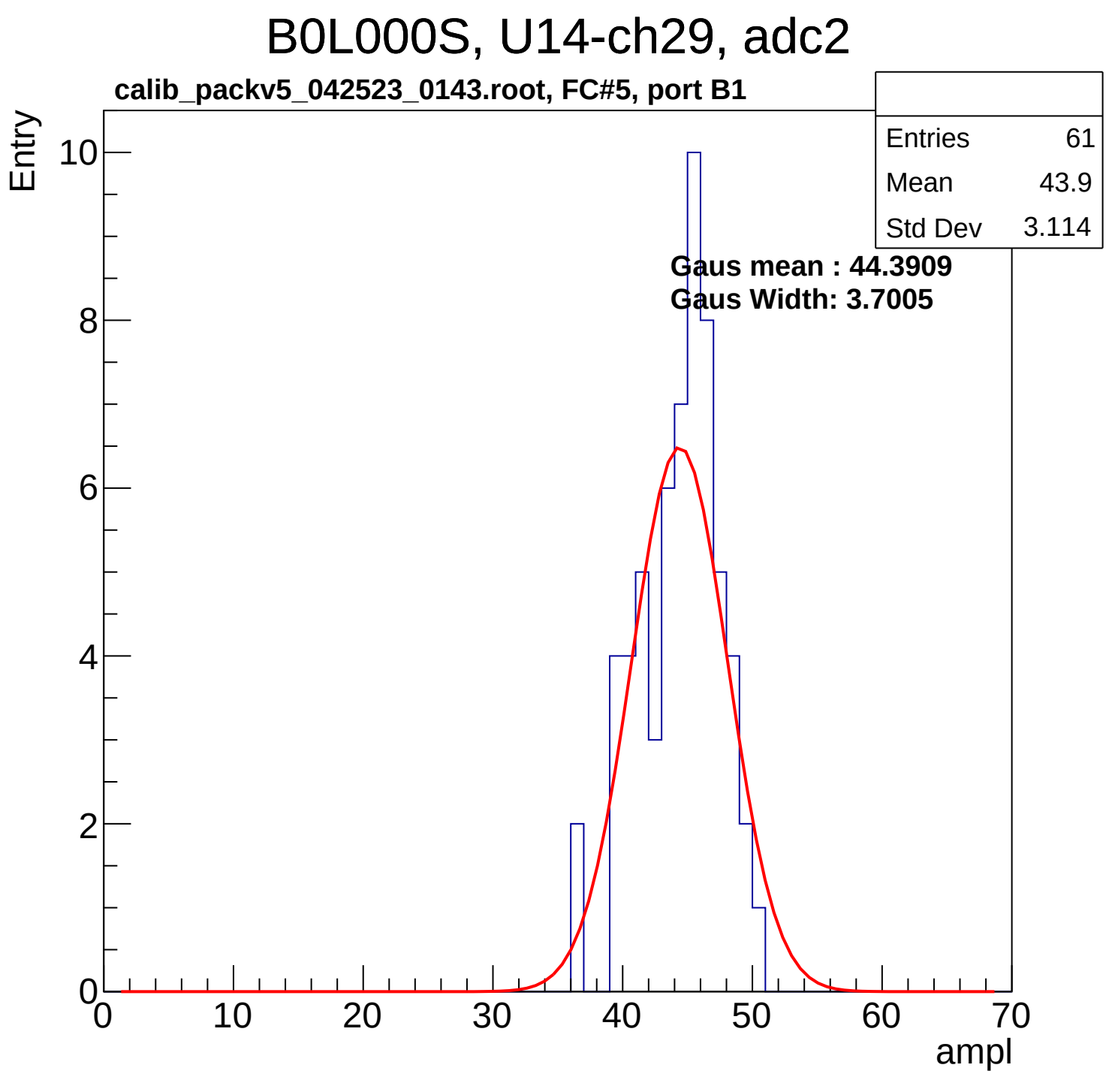
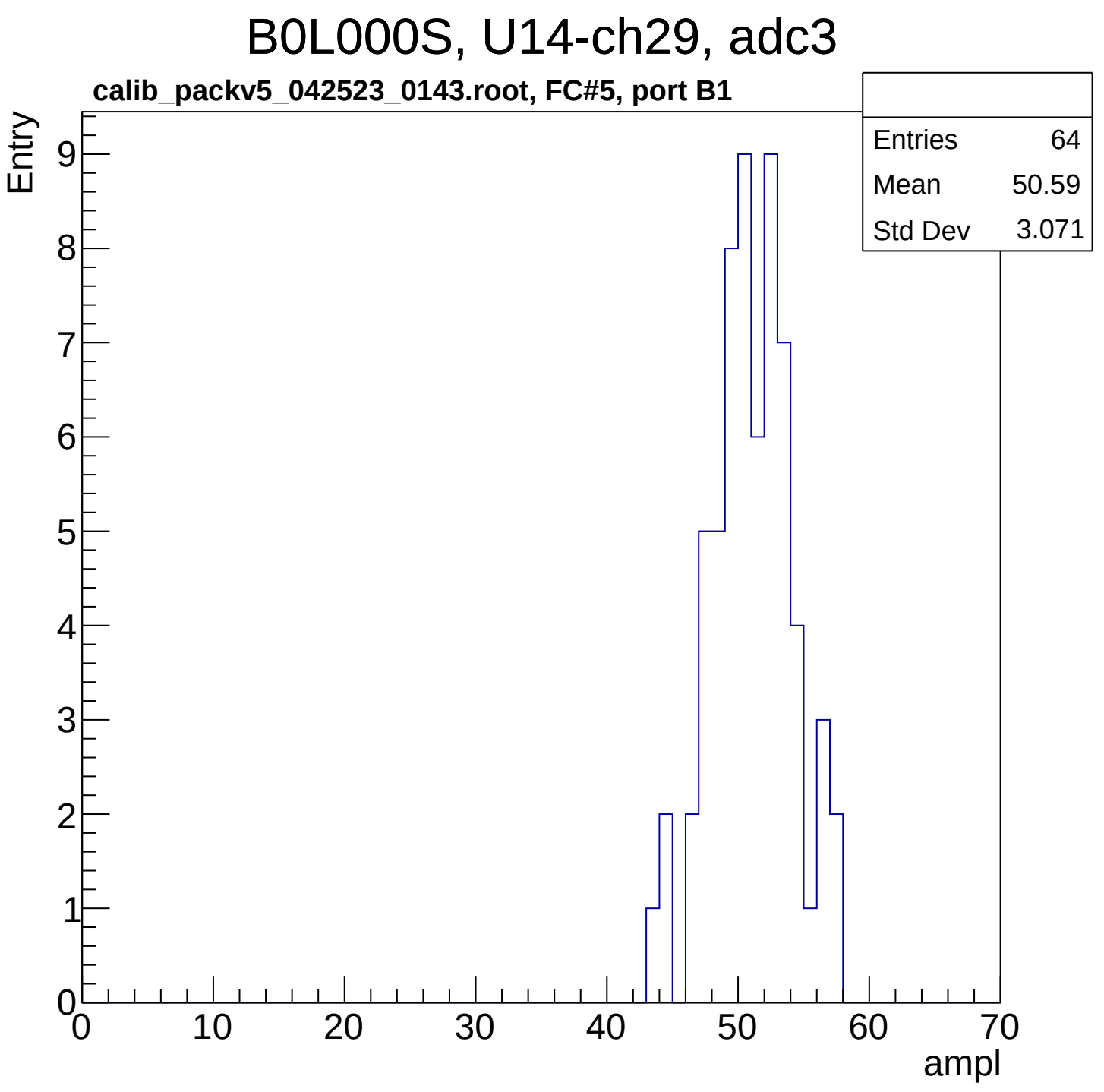
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 31.3
Std Dev 3.376
Gaus mean : 31.1211
Gaus Width: 3.6334
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.84
Std Dev 3.72
Gaus mean : 37.2737
Gaus Width: 4.3308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 43.9
Std Dev 3.114
Gaus mean : 44.3909
Gaus Width: 3.7005
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50.59
Std Dev 3.071
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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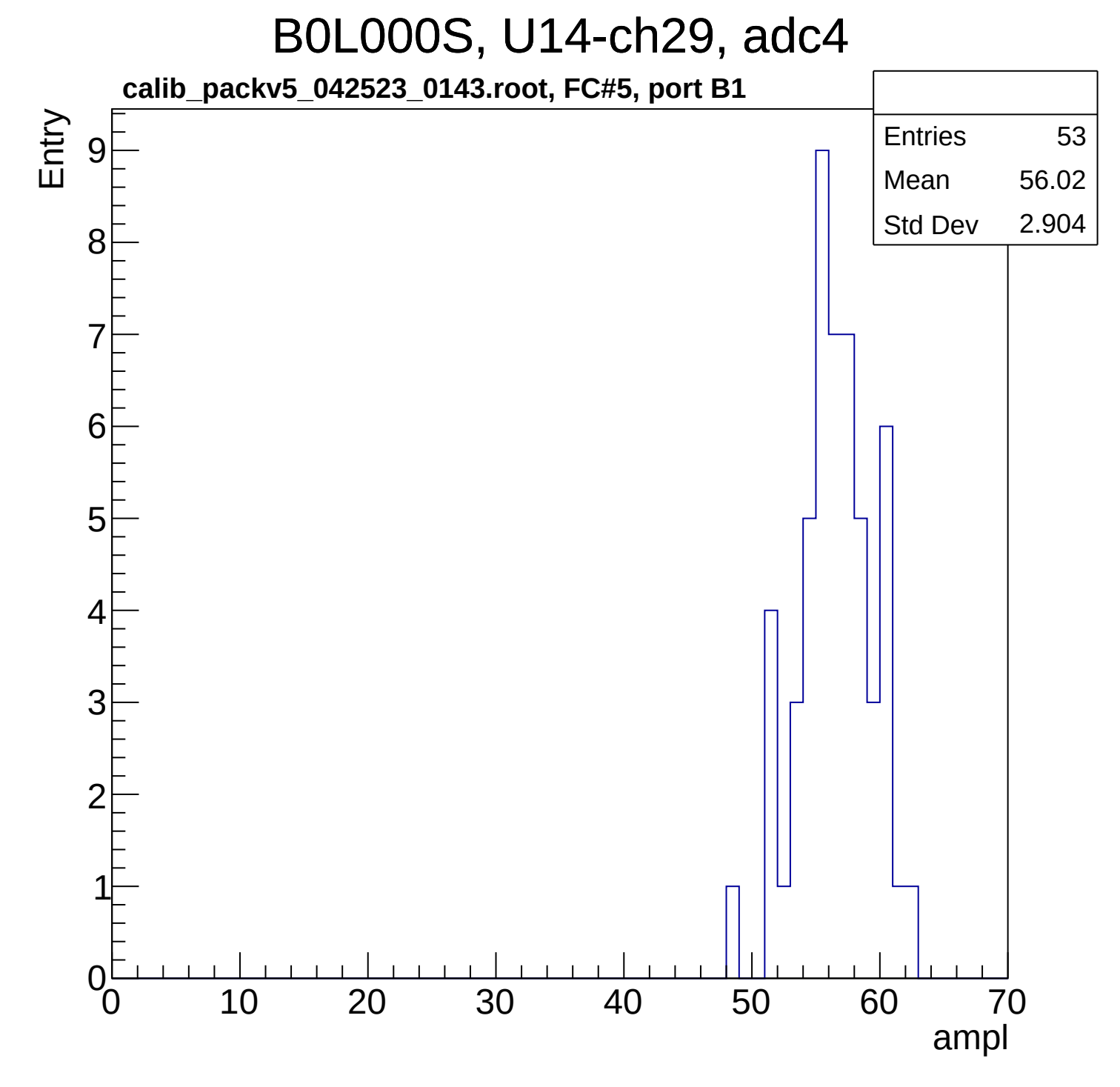
B0L000S, U14-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.02
Std Dev 2.904
Entry
ampl
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6
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0
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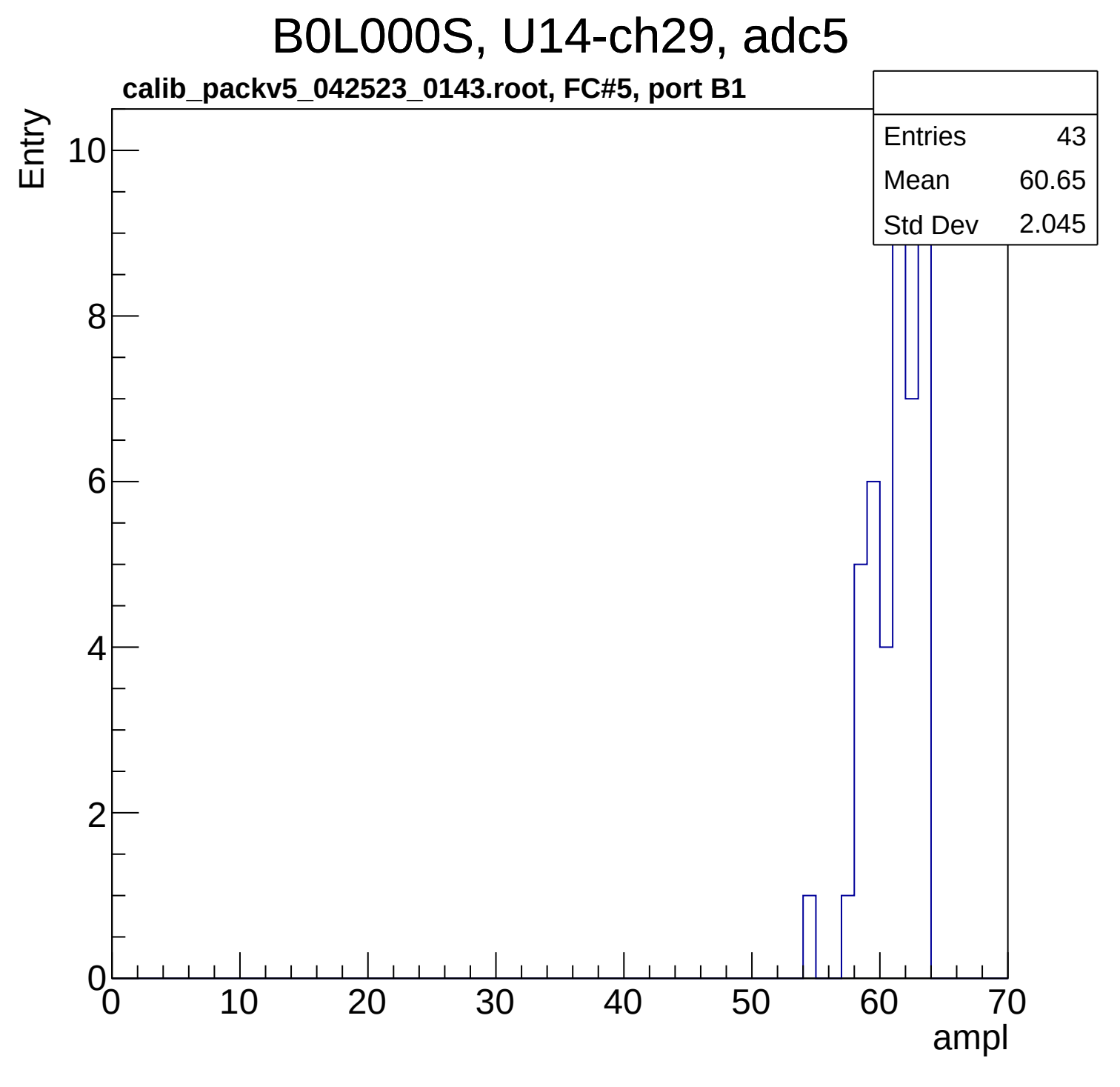
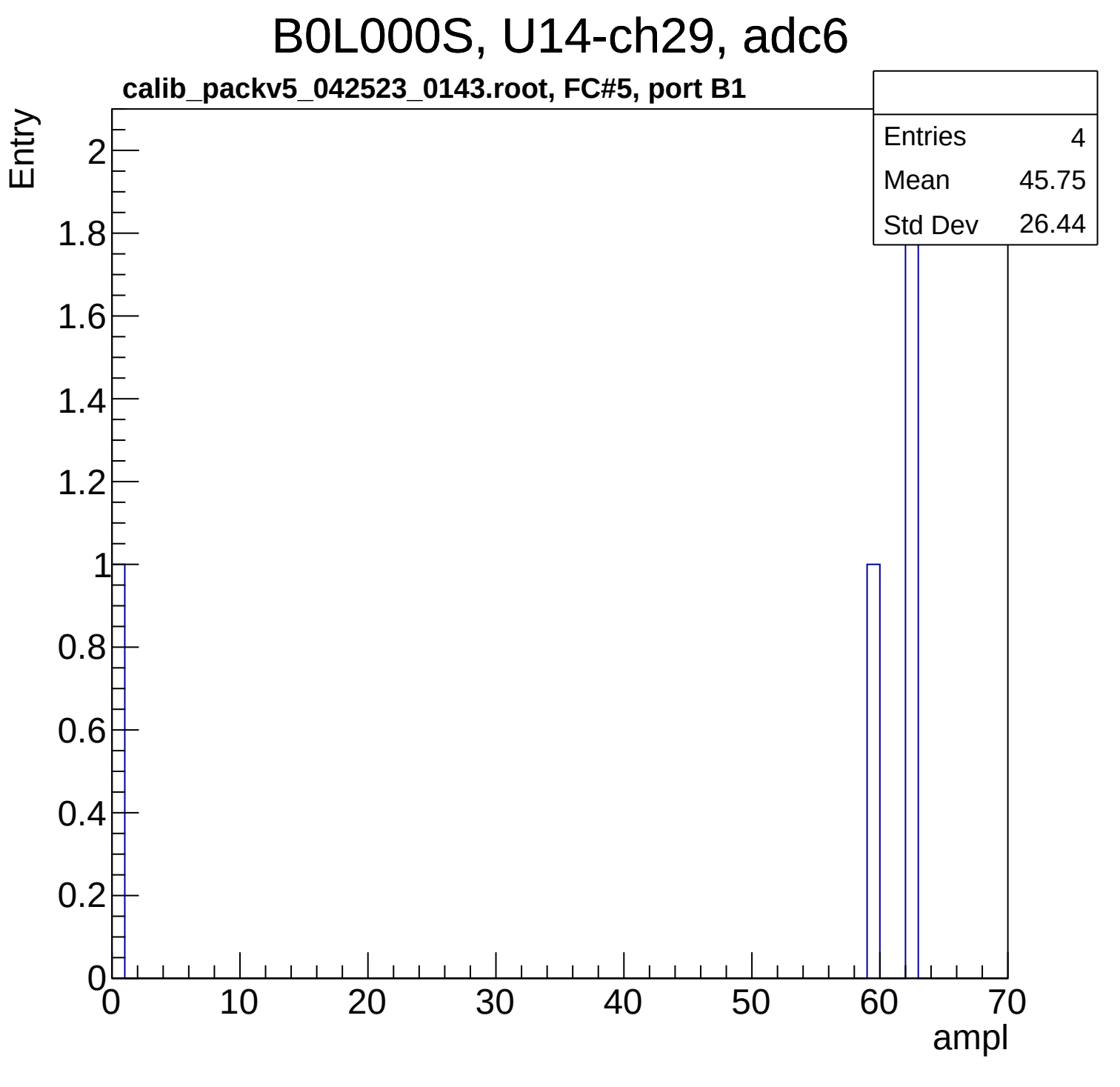

B0L000S, U14-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.65
Std Dev 2.045
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 45.75
Std Dev 26.44
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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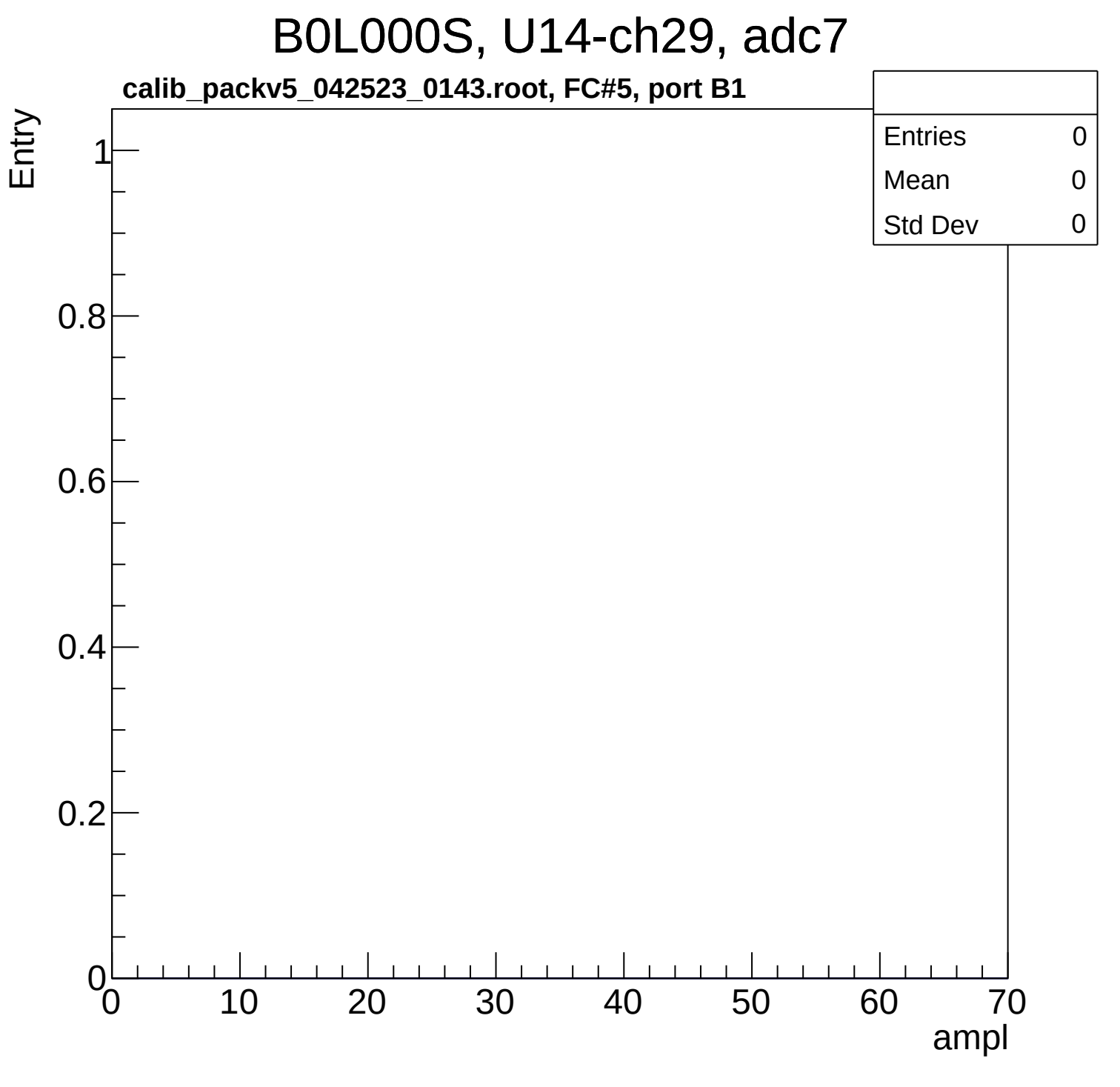
B0L000S, U14-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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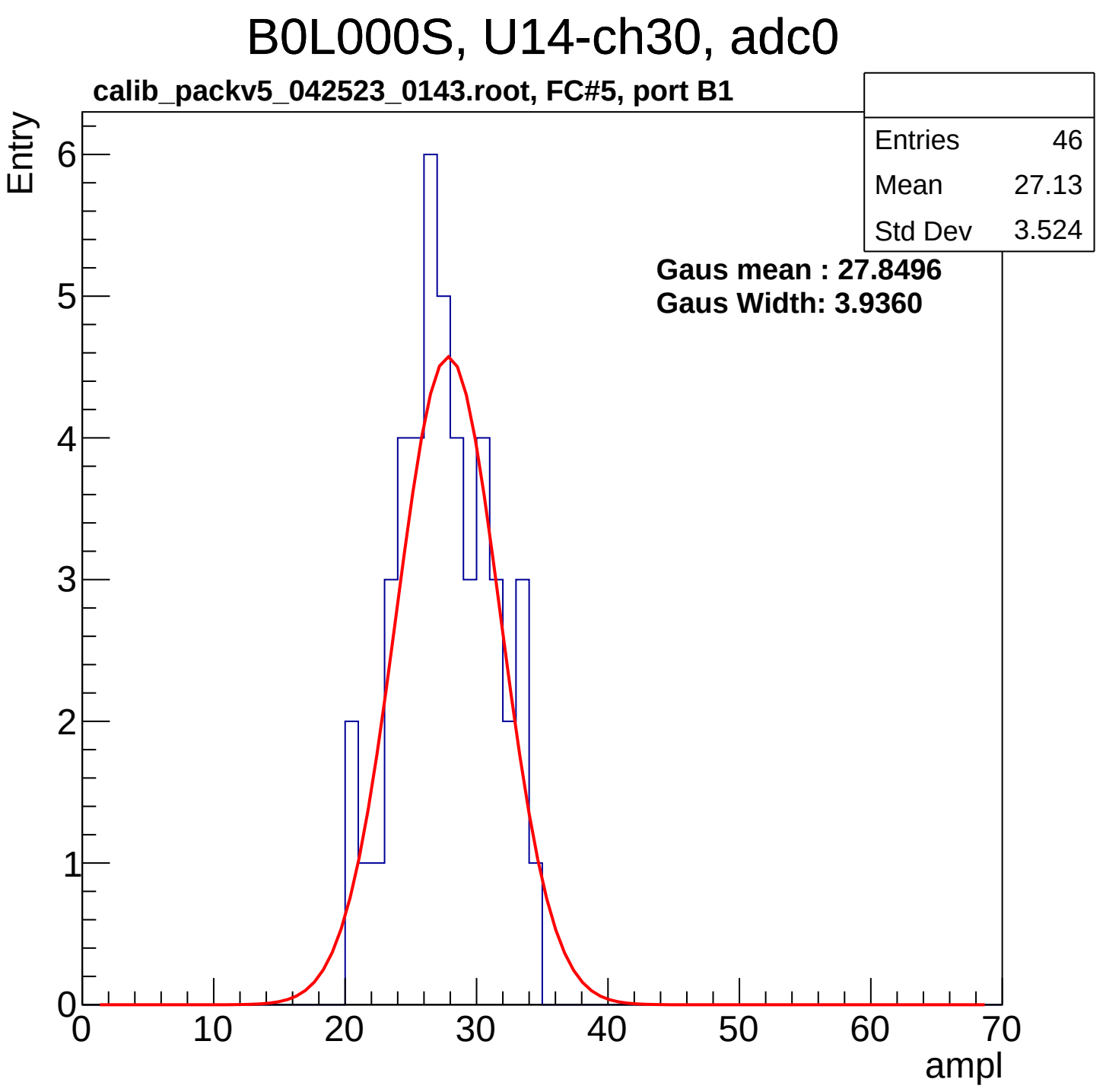
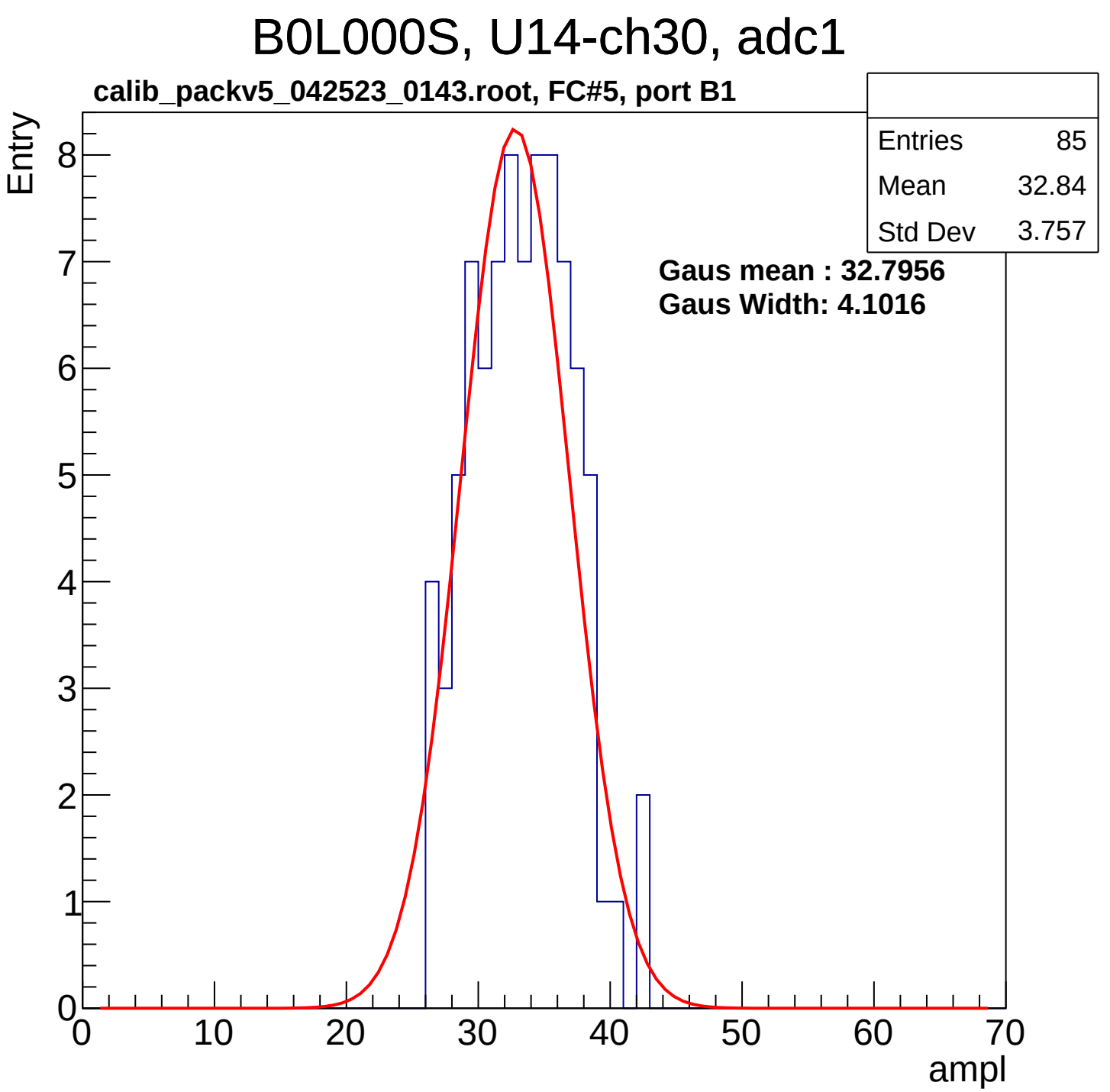
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 27.13
Std Dev 3.524
Gaus mean : 27.8496
Gaus Width: 3.9360
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 32.84
Std Dev 3.757
Gaus mean : 32.7956
Gaus Width: 4.1016
Entry
ampl
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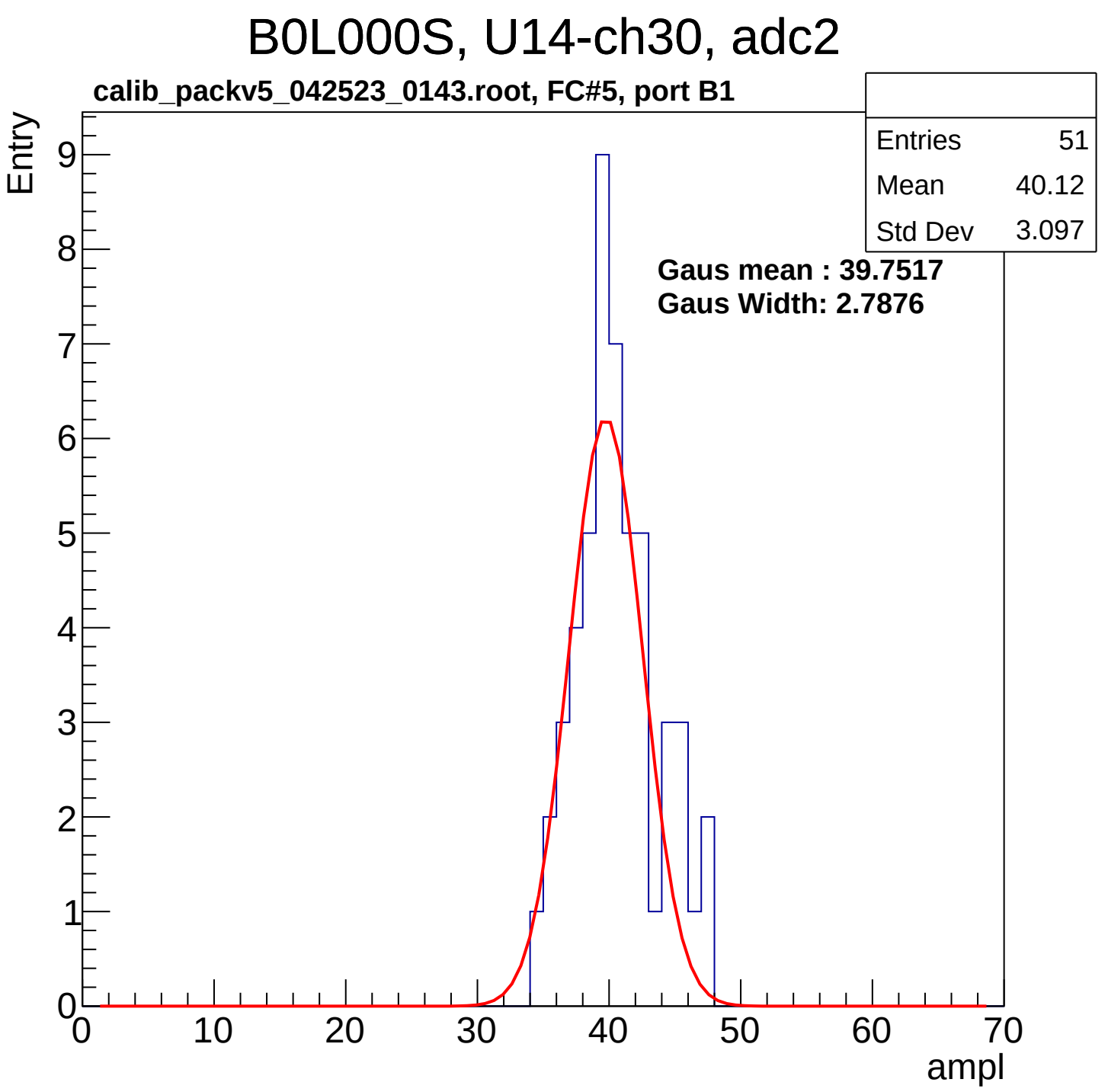
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 40.12
Std Dev 3.097
Gaus mean : 39.7517
Gaus Width: 2.7876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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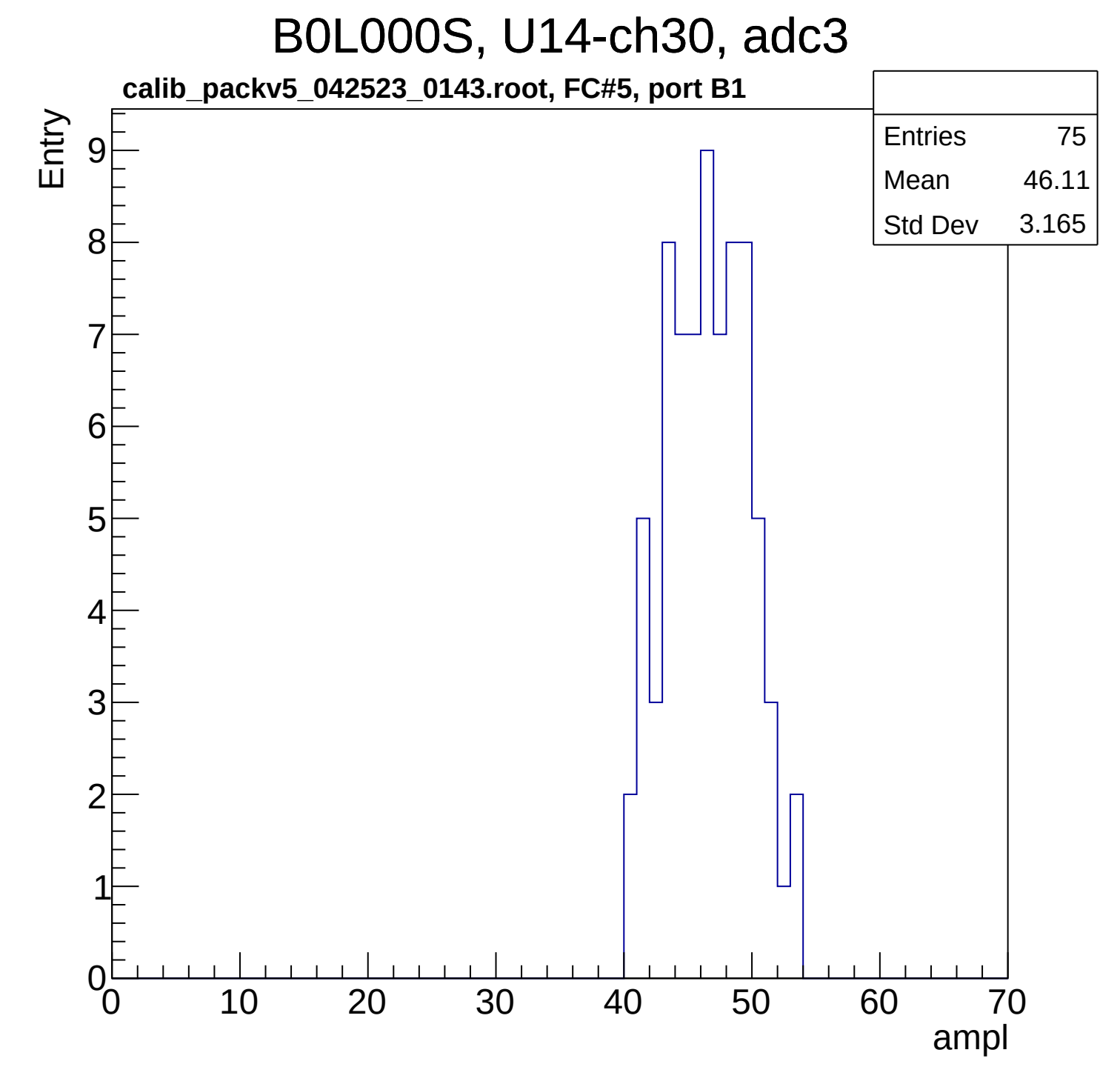
B0L000S, U14-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 46.11
Std Dev 3.165
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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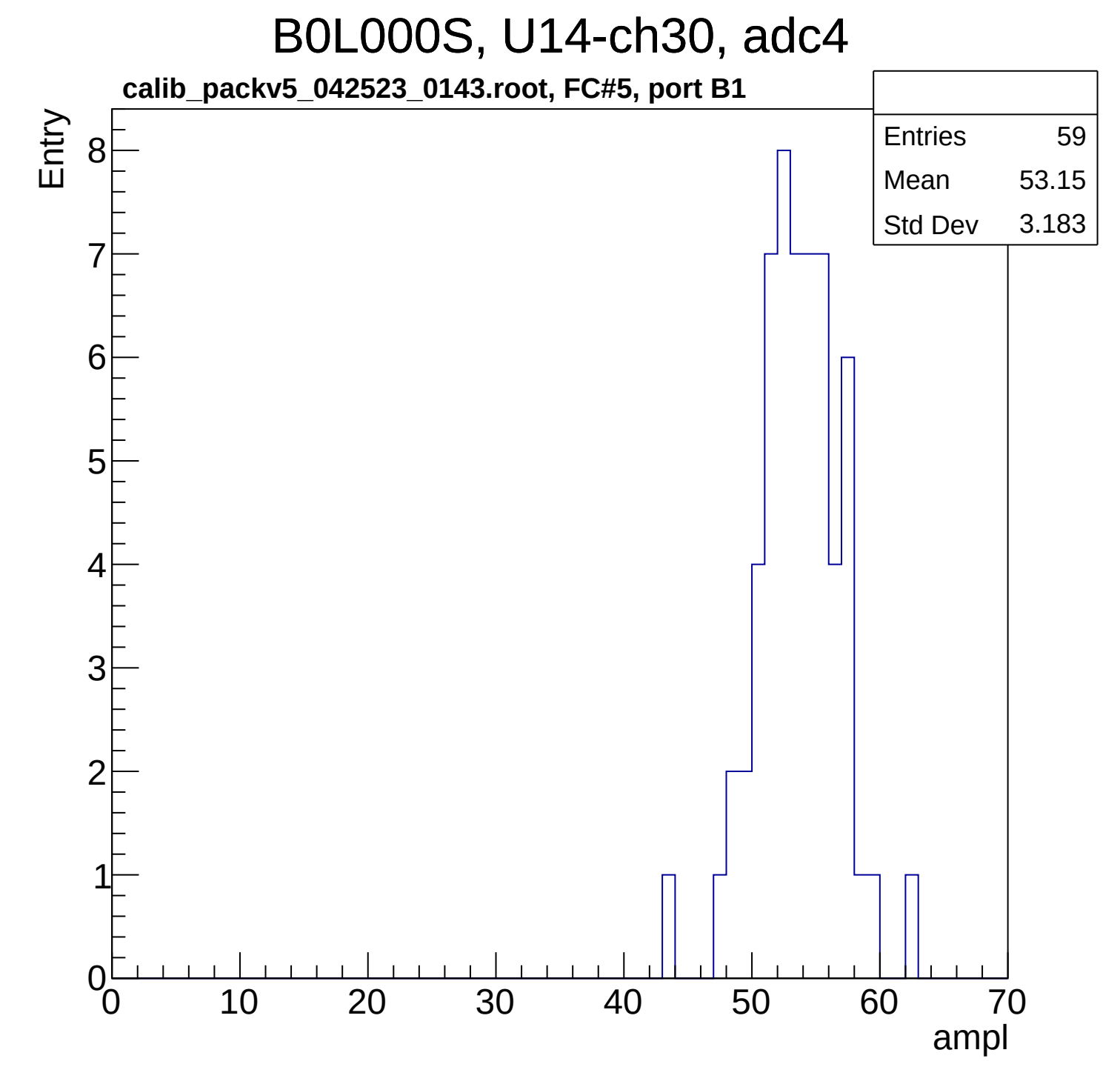
40
50
60
70


B0L000S, U14-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.15
Std Dev 3.183
Entry
ampl
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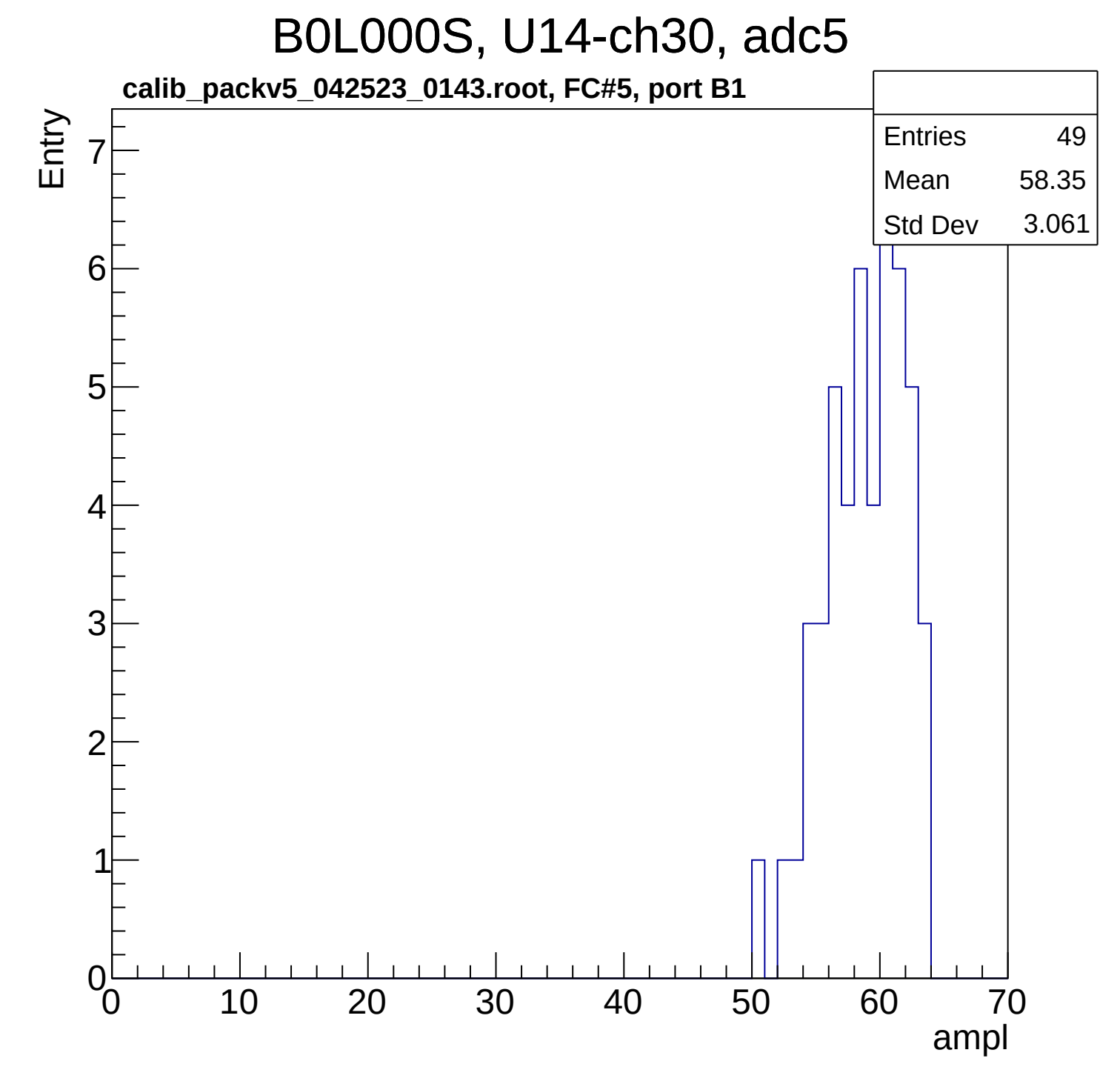
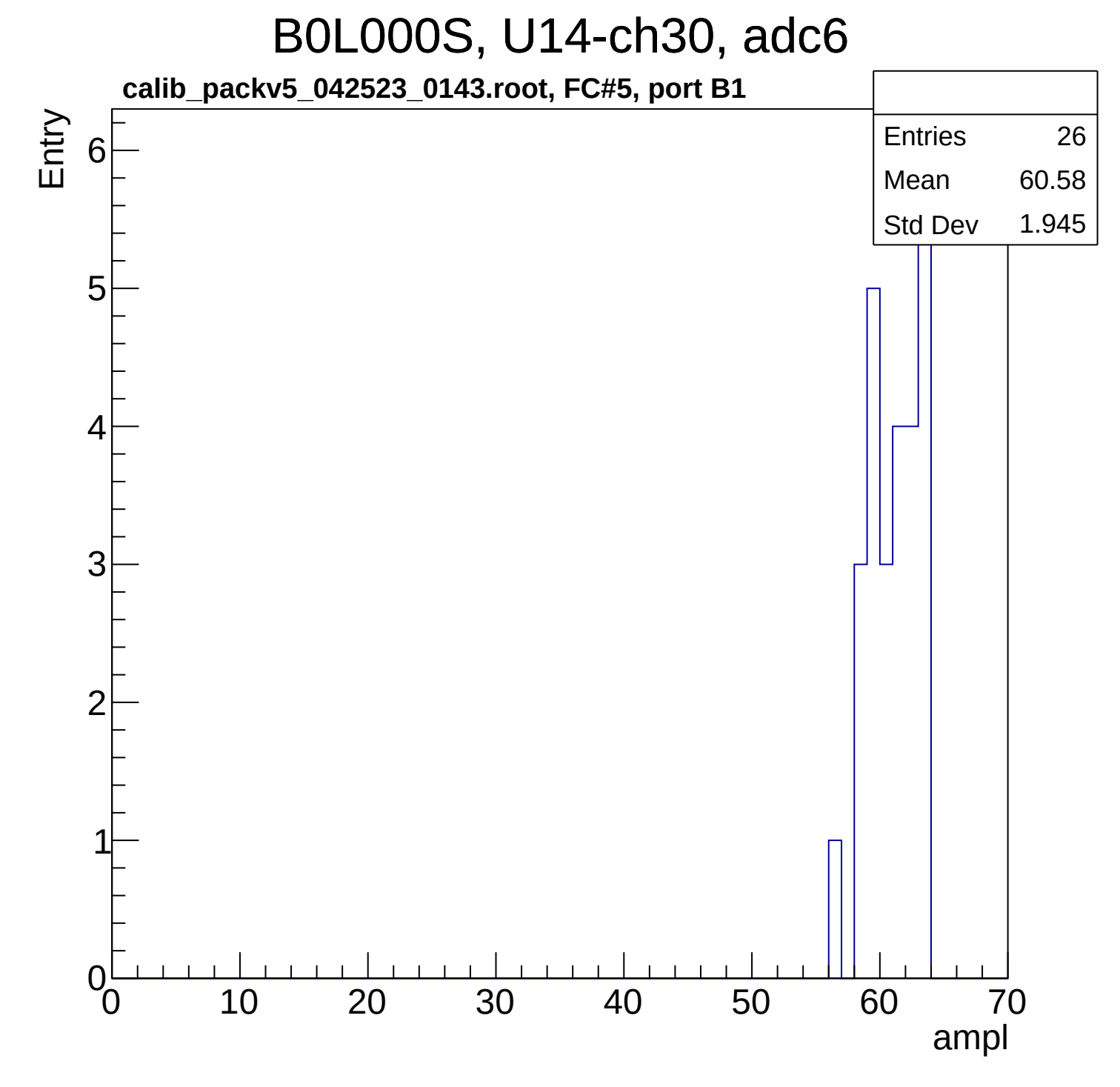
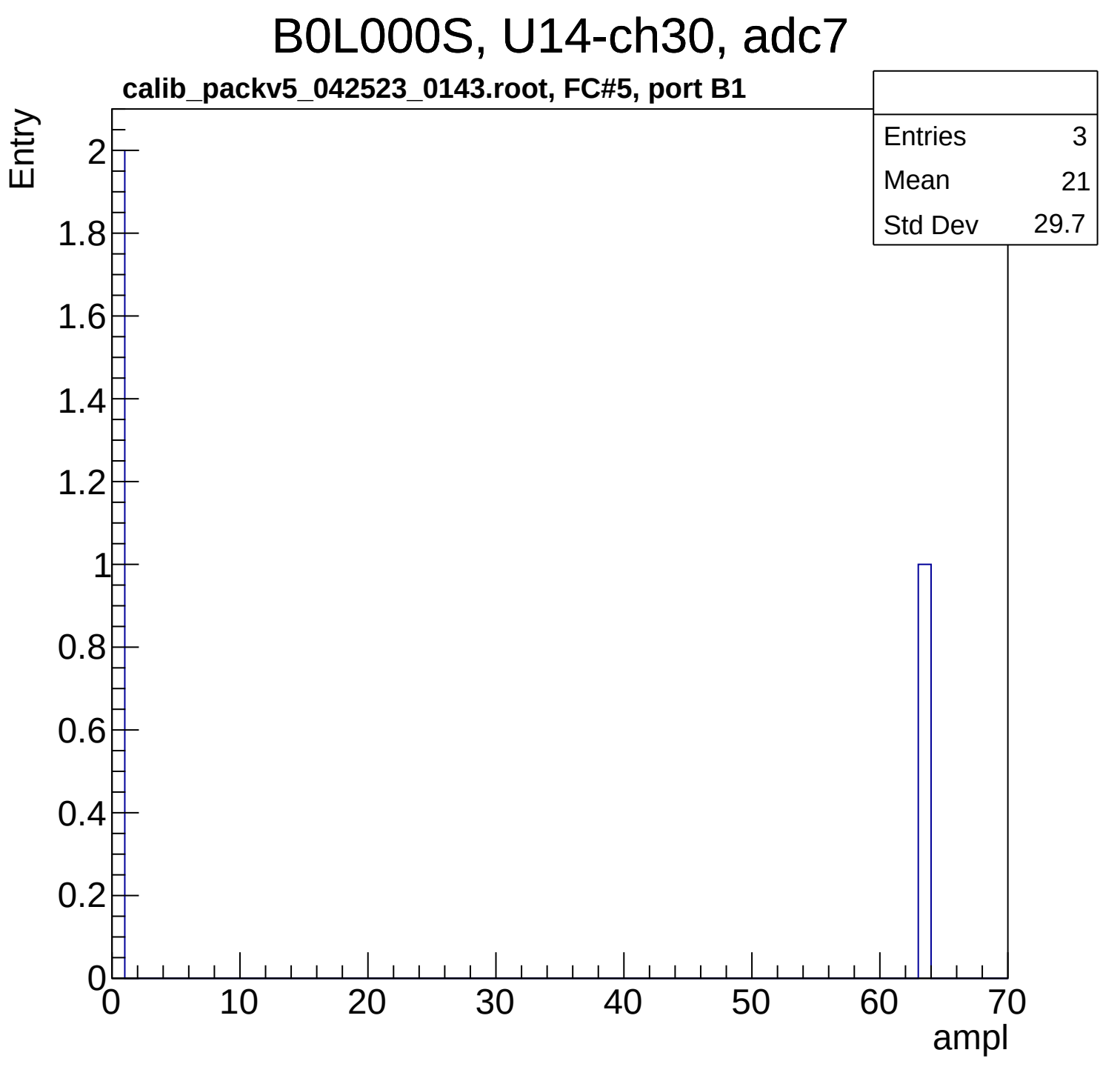
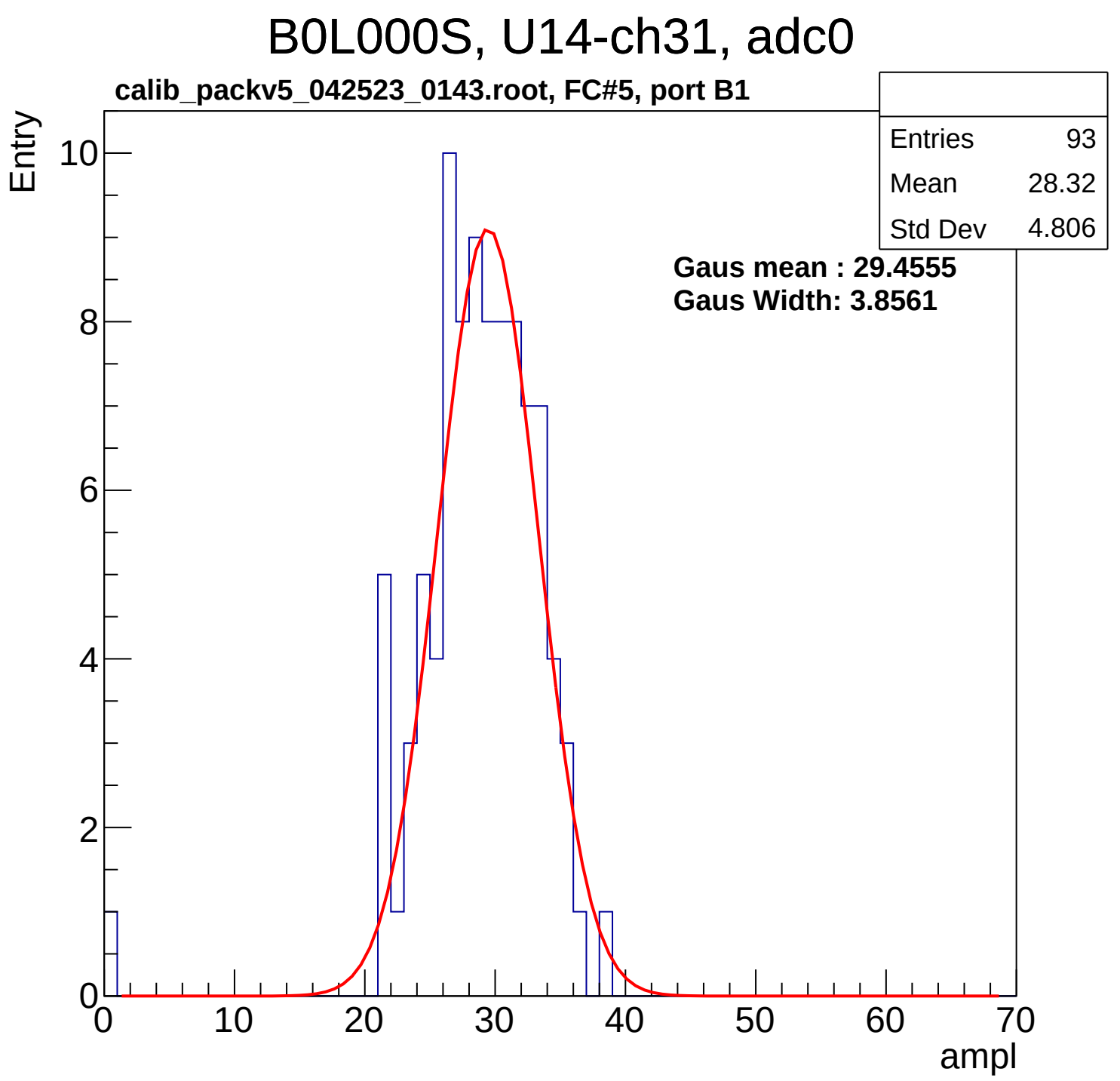
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.35
Std Dev 3.061
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 60.58
Std Dev 1.945
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 93
Mean 28.32
Std Dev 4.806
Gaus mean : 29.4555
Gaus Width: 3.8561
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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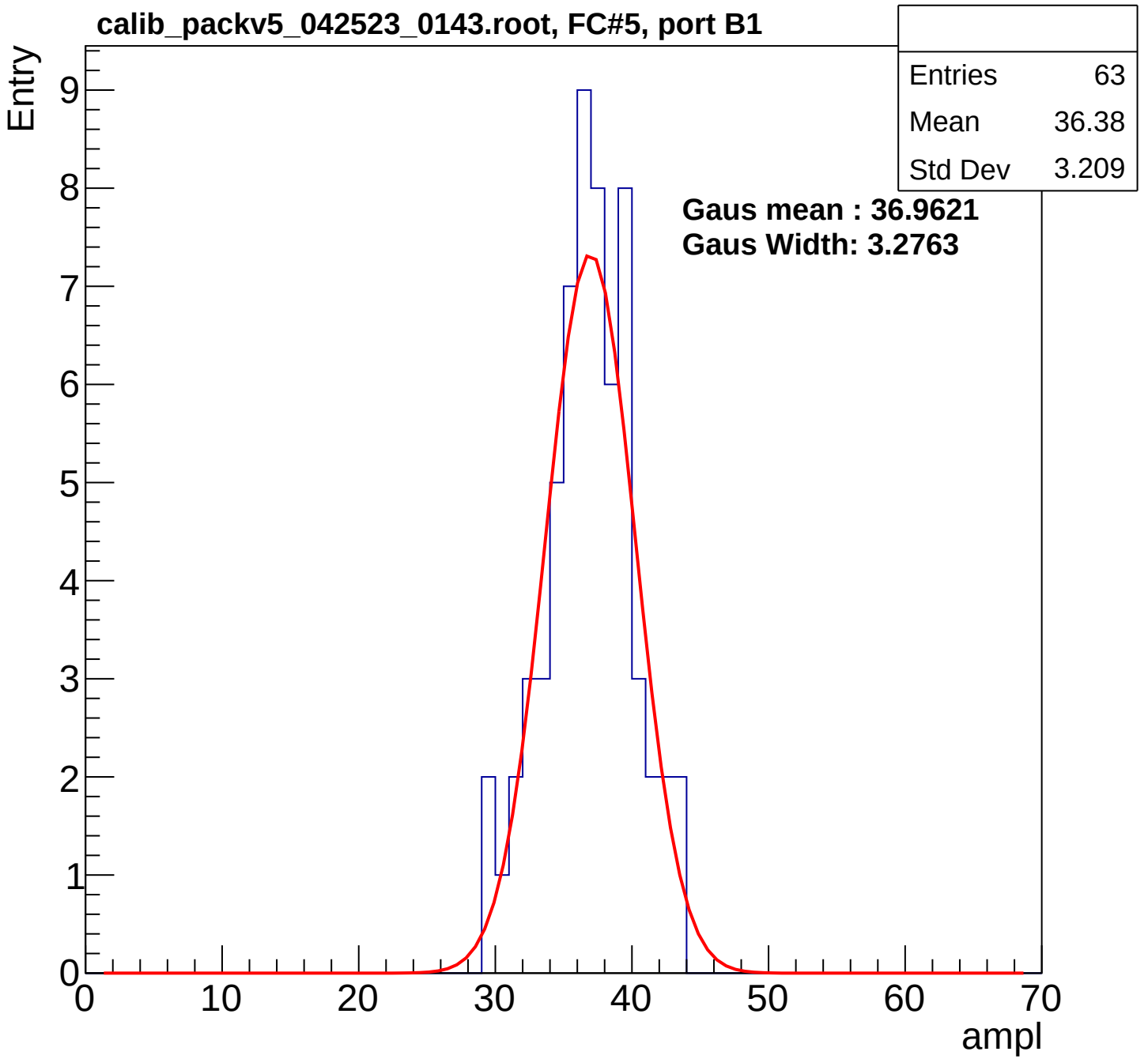
B0L000S, U14-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.38
Std Dev 3.209
Gaus mean : 36.9621
Gaus Width: 3.2763
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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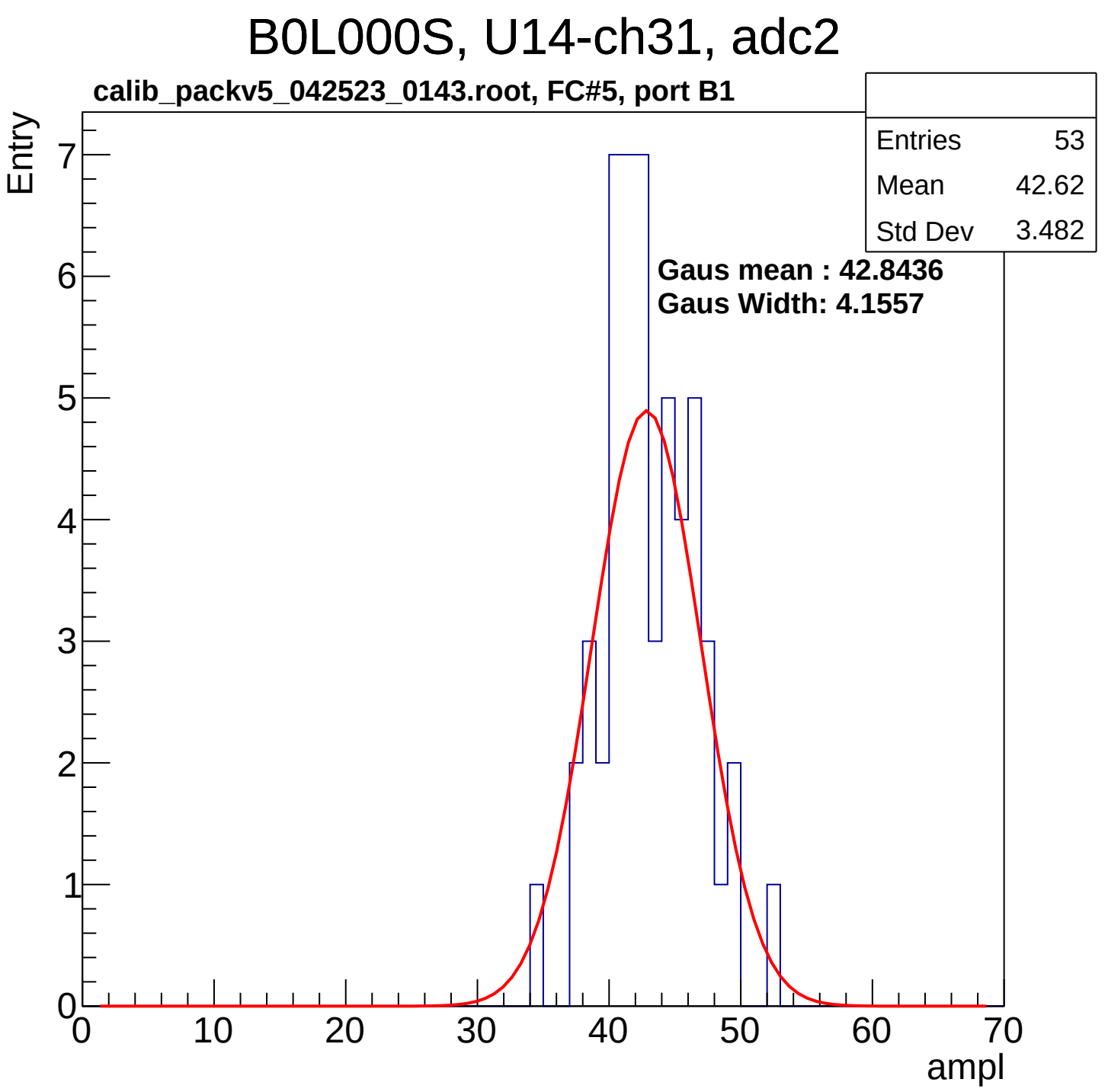
B0L000S, U14-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.62
Std Dev 3.482
Gaus mean : 42.8436
Gaus Width: 4.1557
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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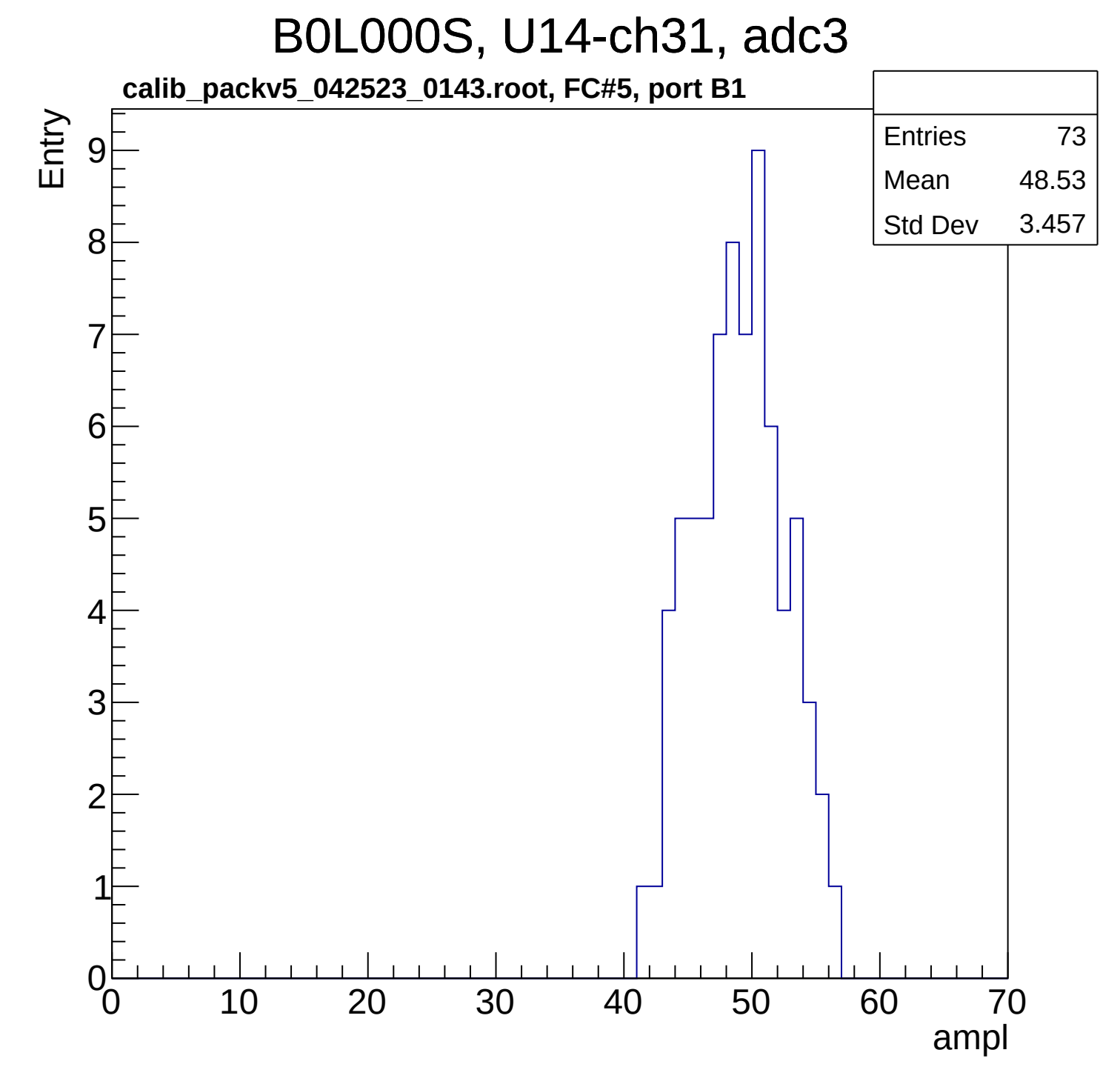
B0L000S, U14-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.53
Std Dev 3.457
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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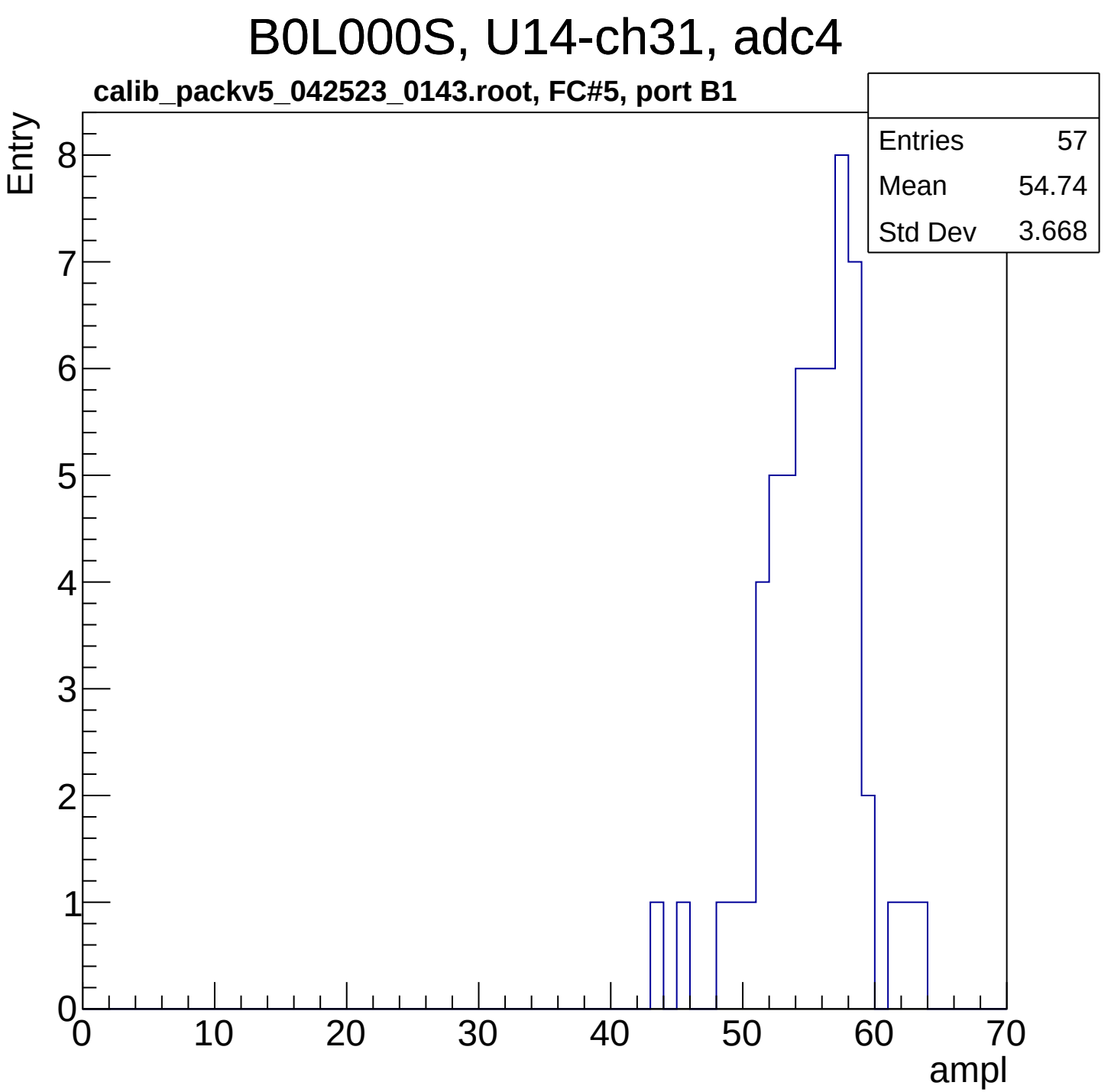
B0L000S, U14-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.74
Std Dev 3.668
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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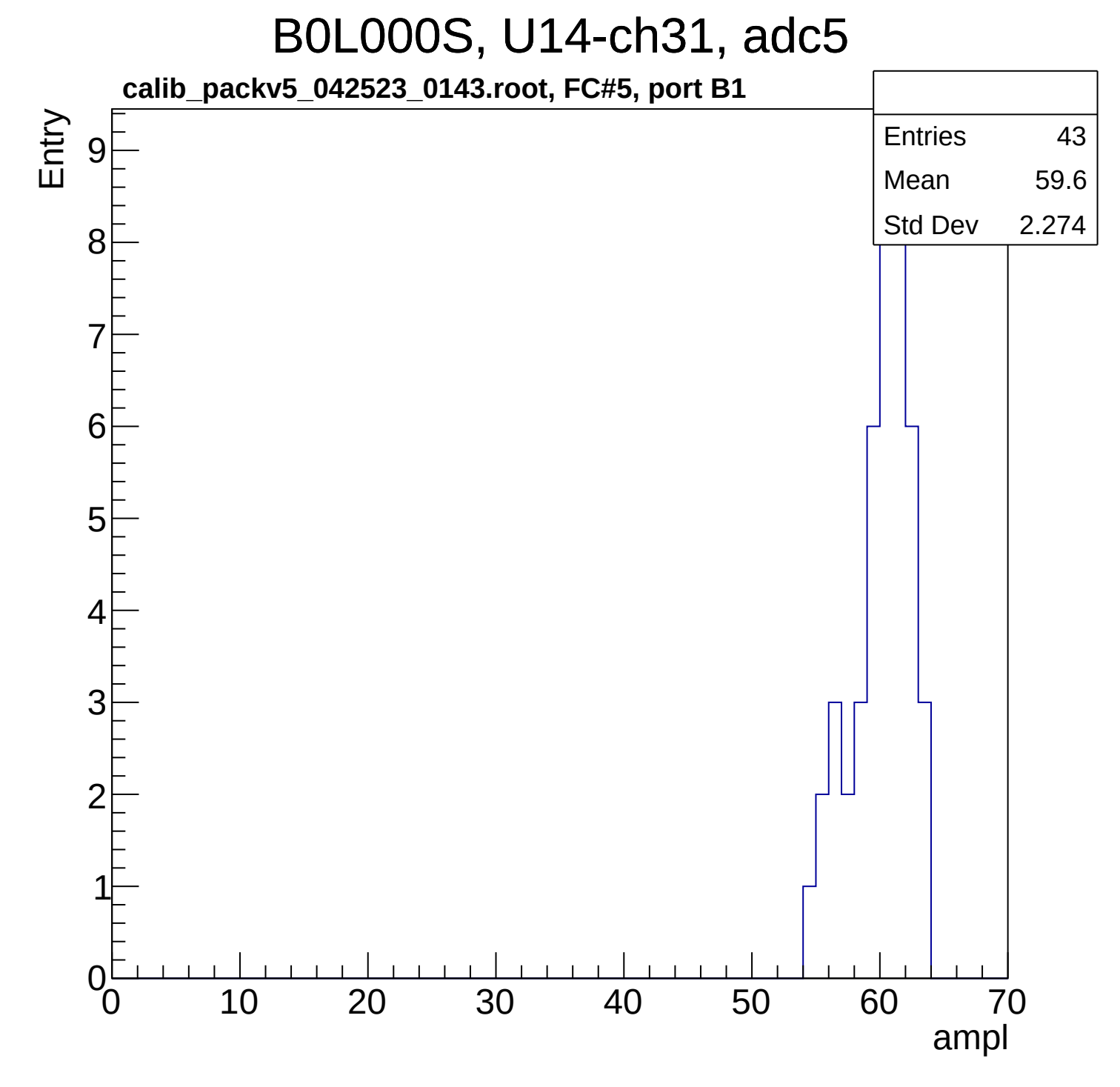
B0L000S, U14-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.6
Std Dev 2.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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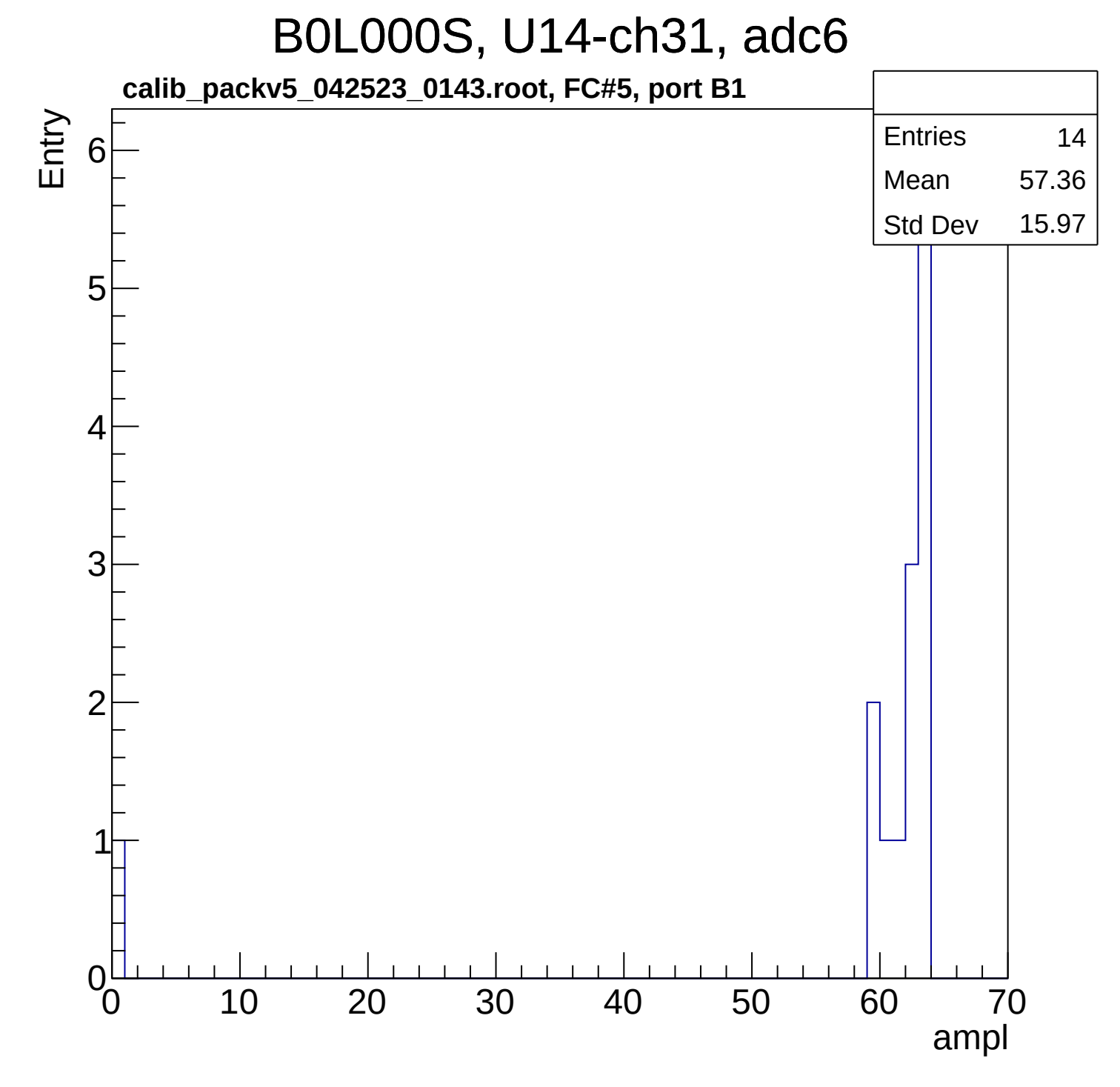
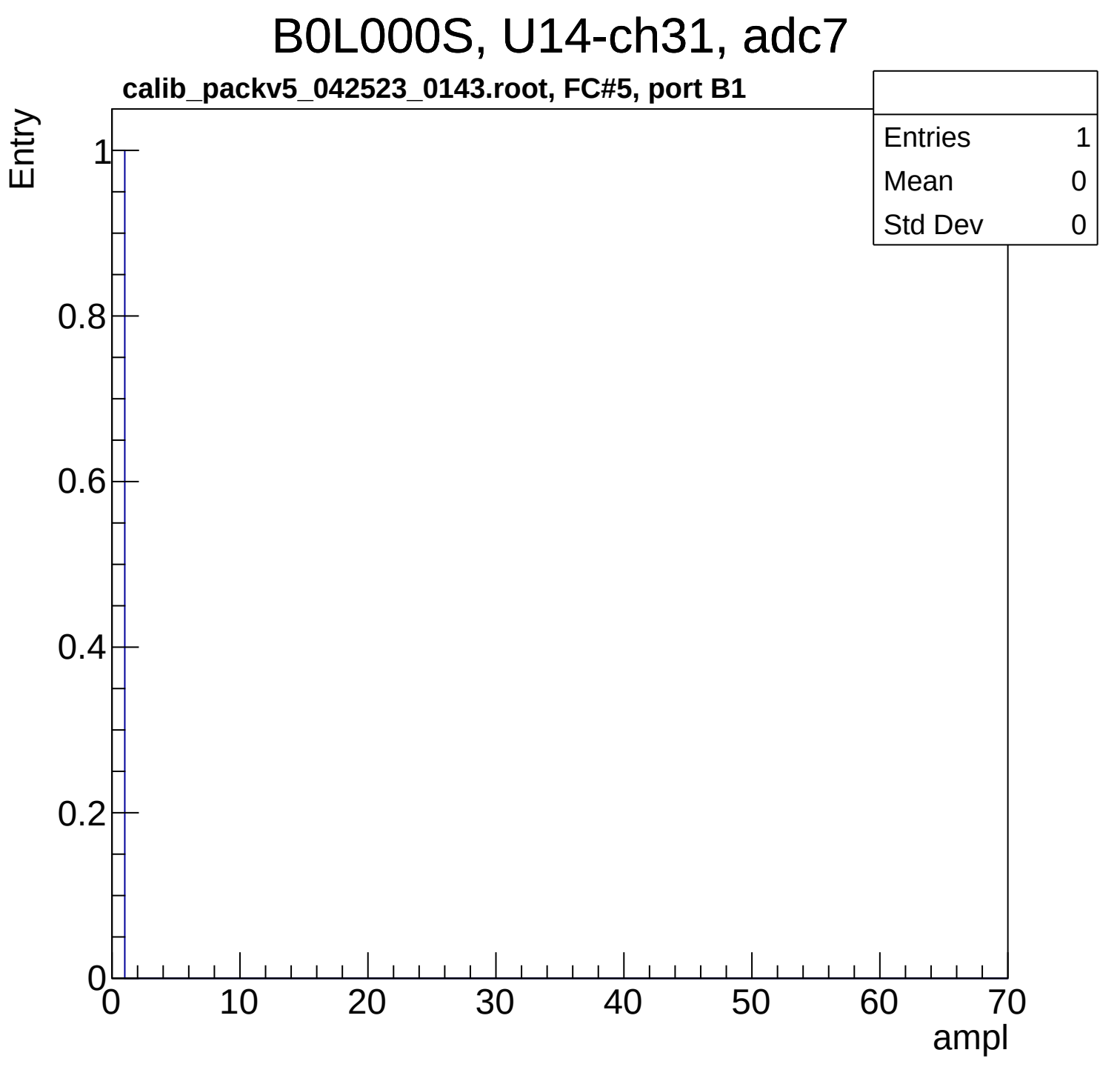

B0L000S, U14-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 57.36
Std Dev 15.97
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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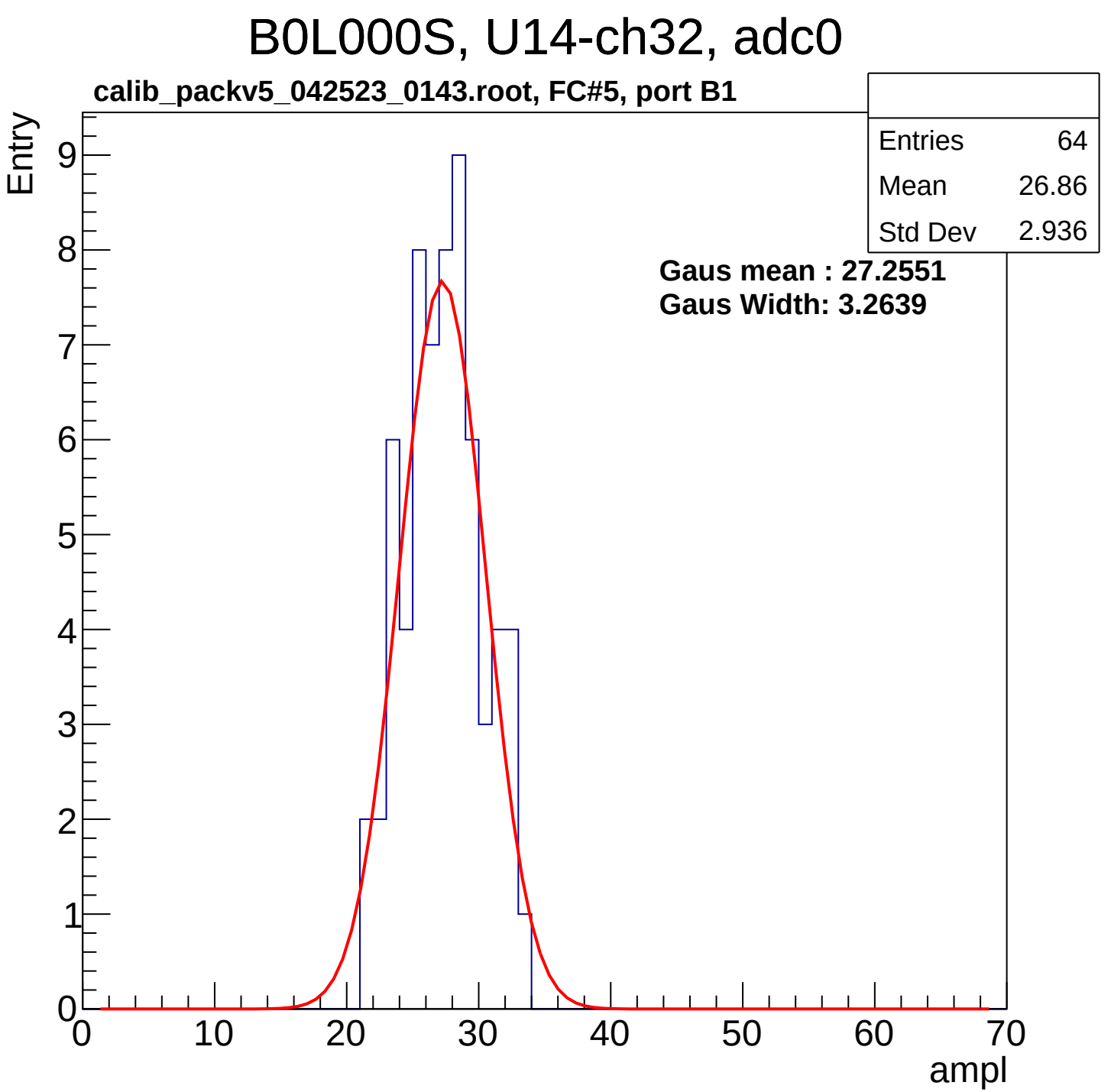
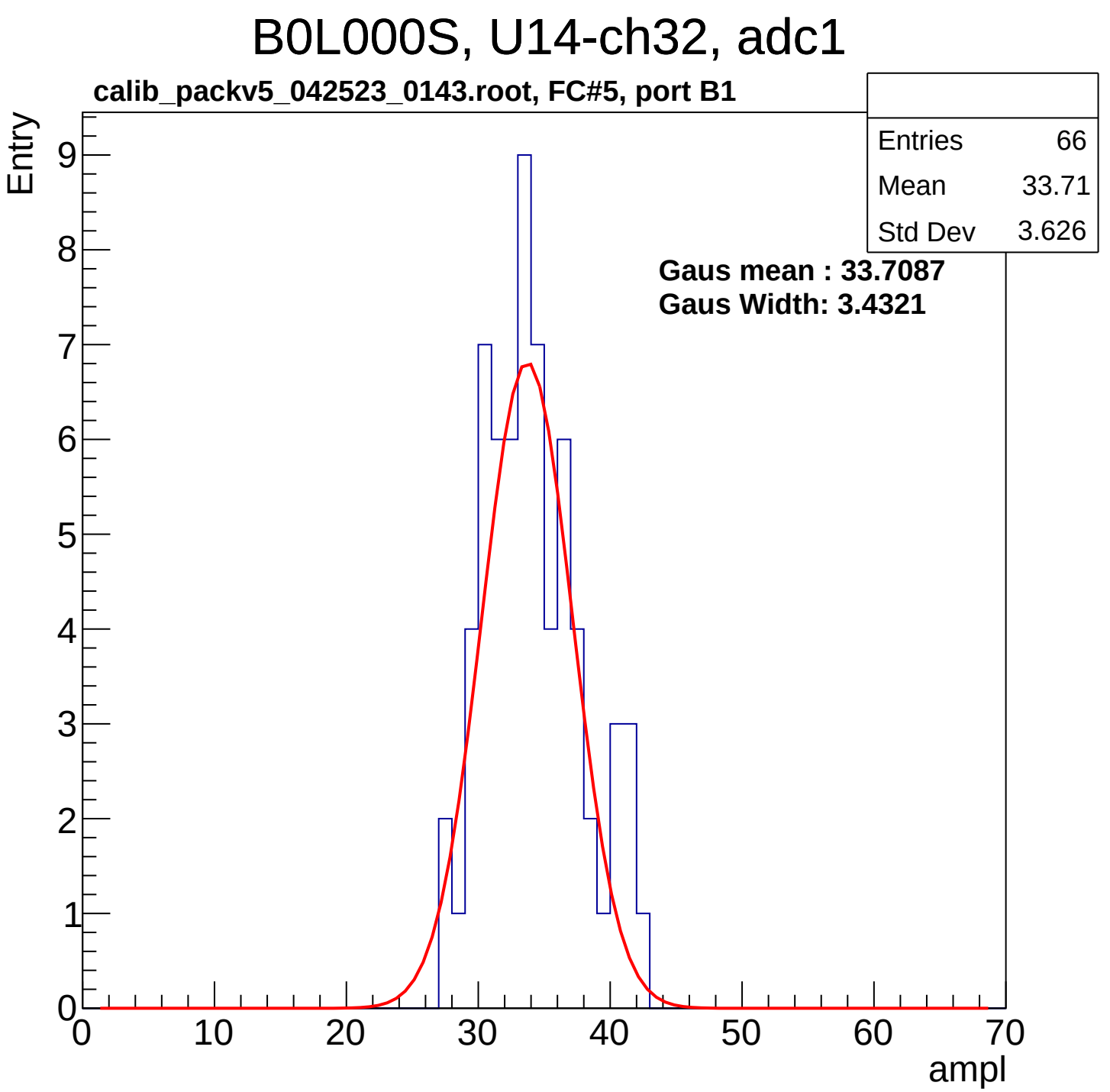
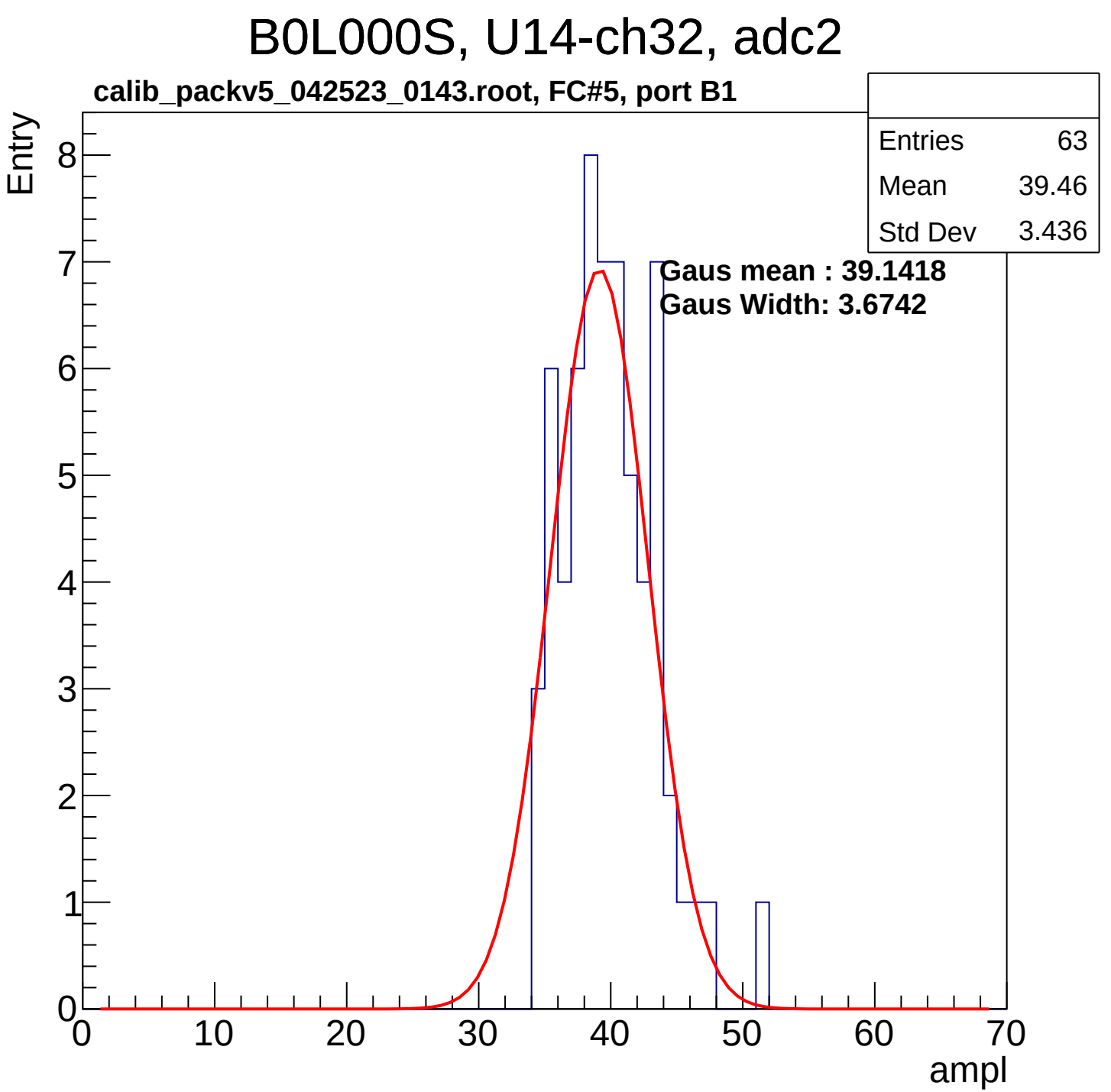
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 26.86
Std Dev 2.936
Gaus mean : 27.2551
Gaus Width: 3.2639
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 33.71
Std Dev 3.626
Gaus mean : 33.7087
Gaus Width: 3.4321
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 39.46
Std Dev 3.436
Gaus mean : 39.1418
Gaus Width: 3.6742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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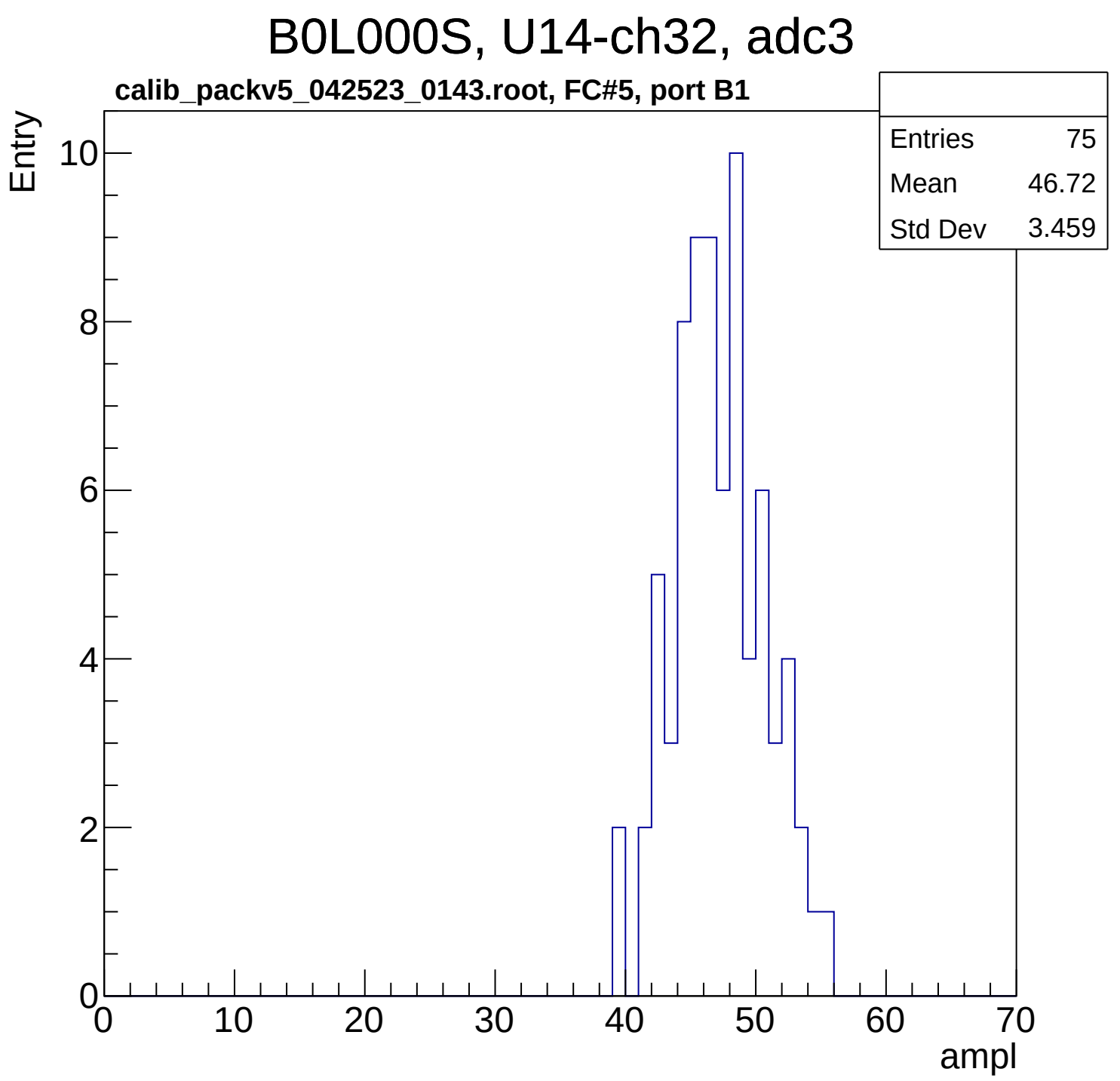
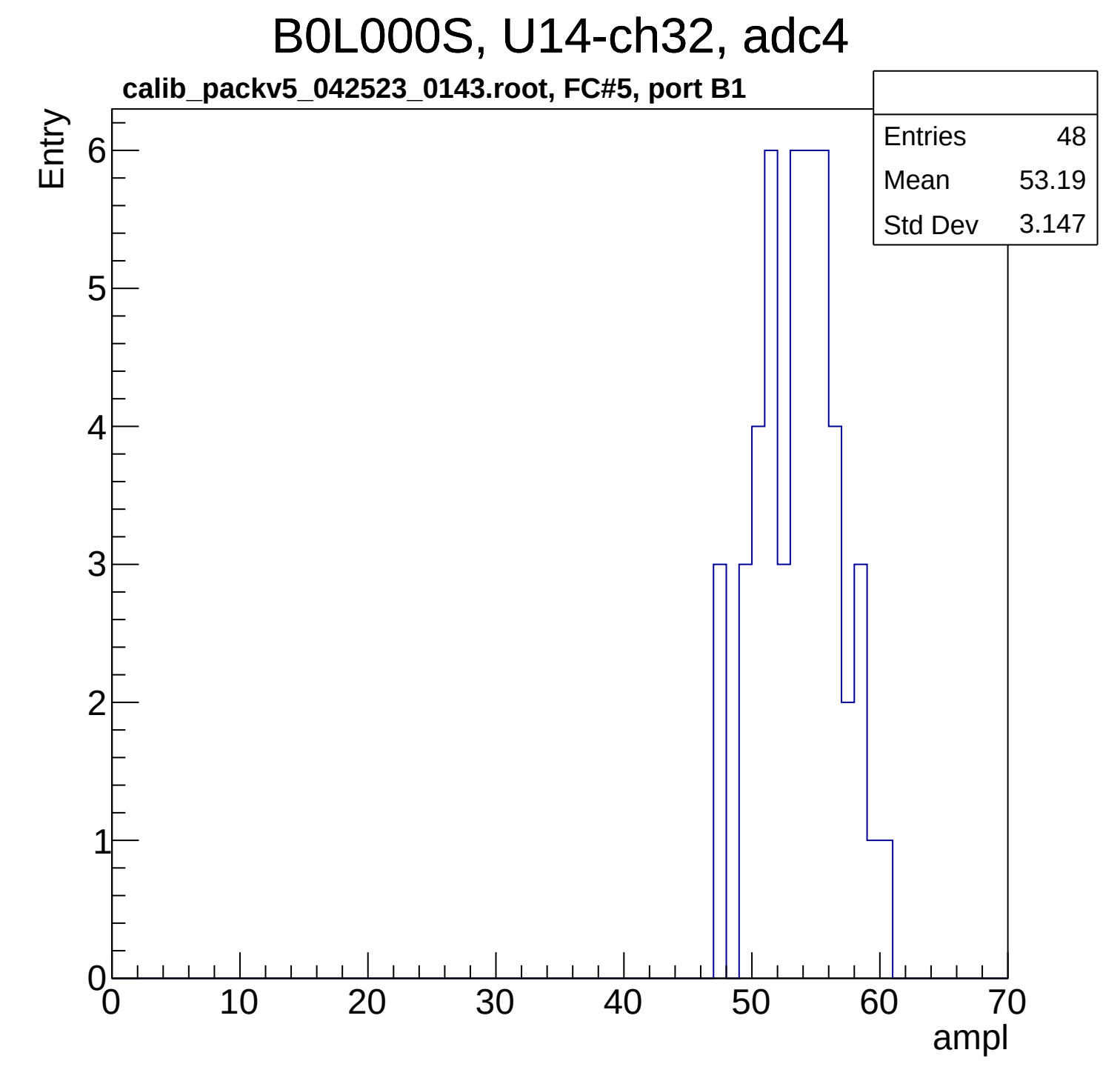
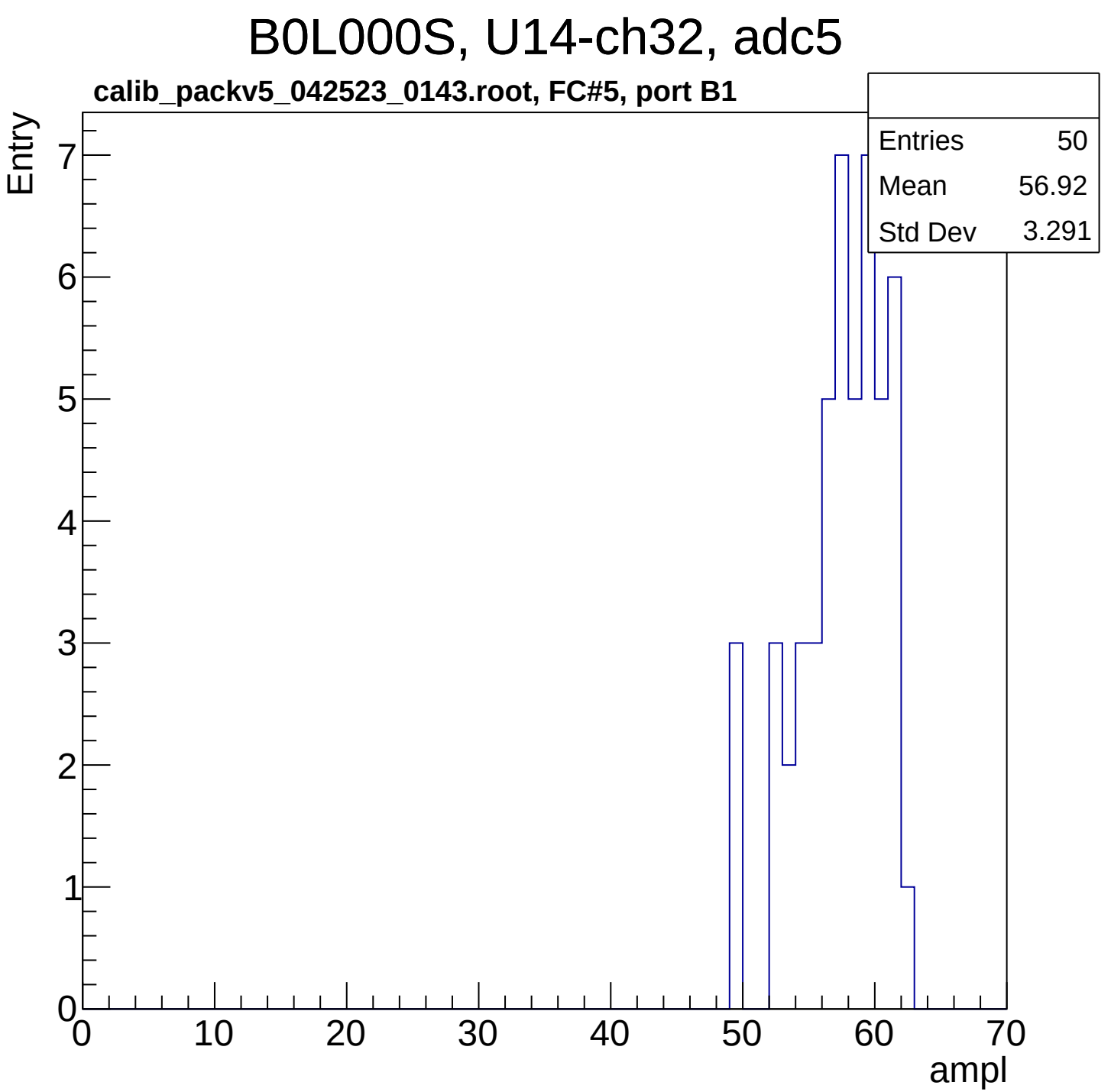
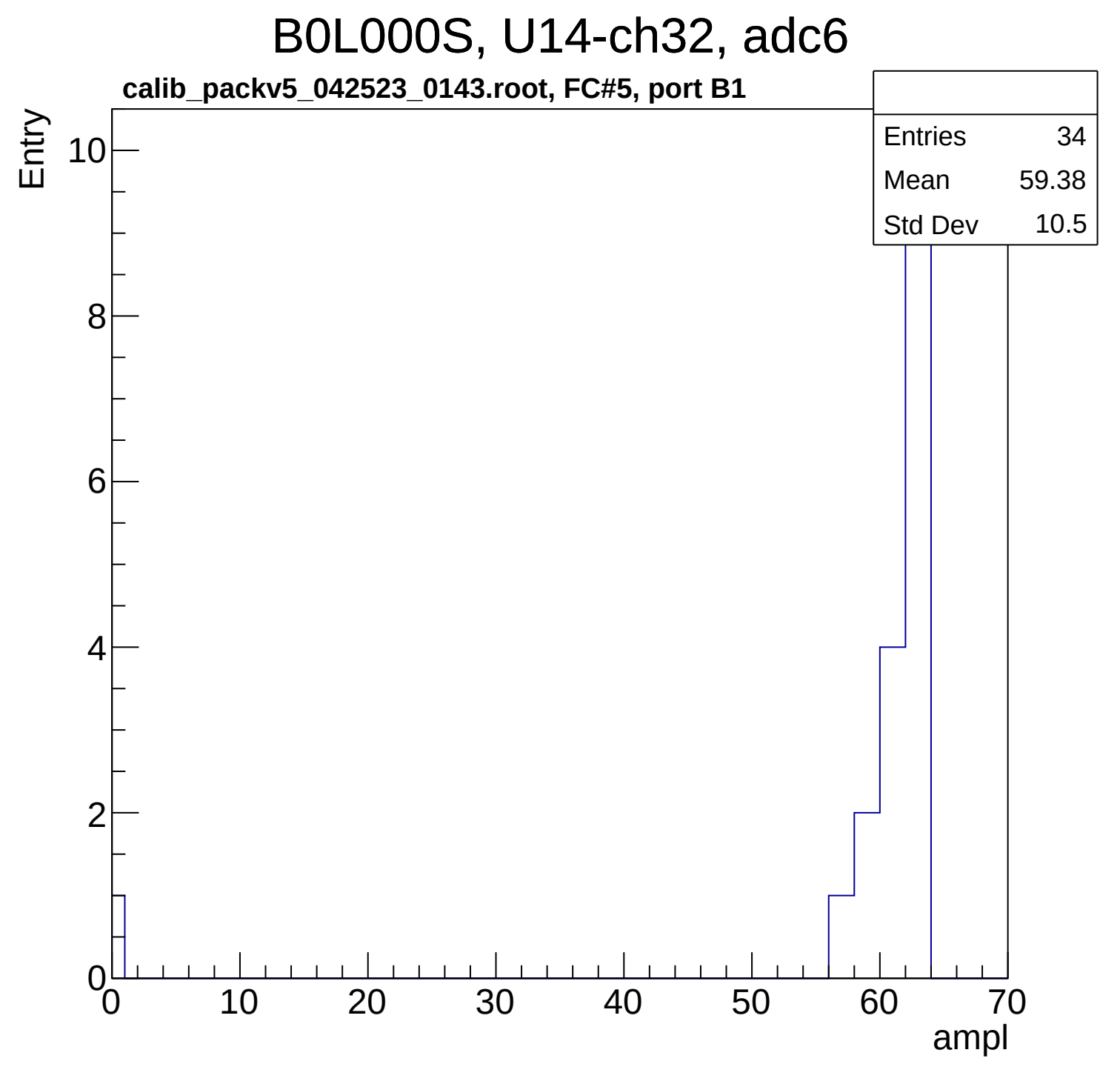
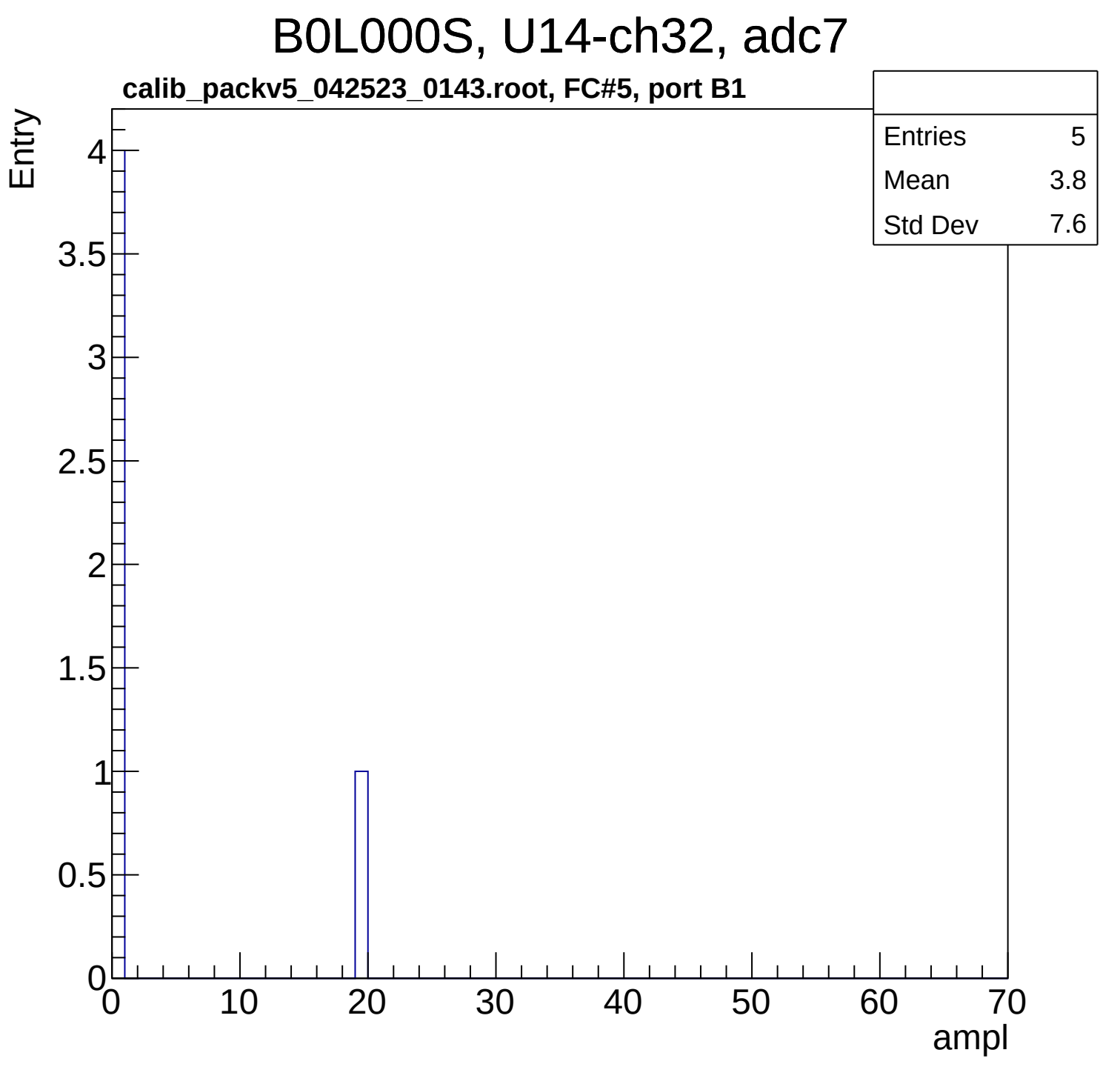

B0L000S, U14-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 46.72
Std Dev 3.459
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 53.19
Std Dev 3.147
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 56.92
Std Dev 3.291
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.38
Std Dev 10.5
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 3.8
Std Dev 7.6
Entry
ampl
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0.5
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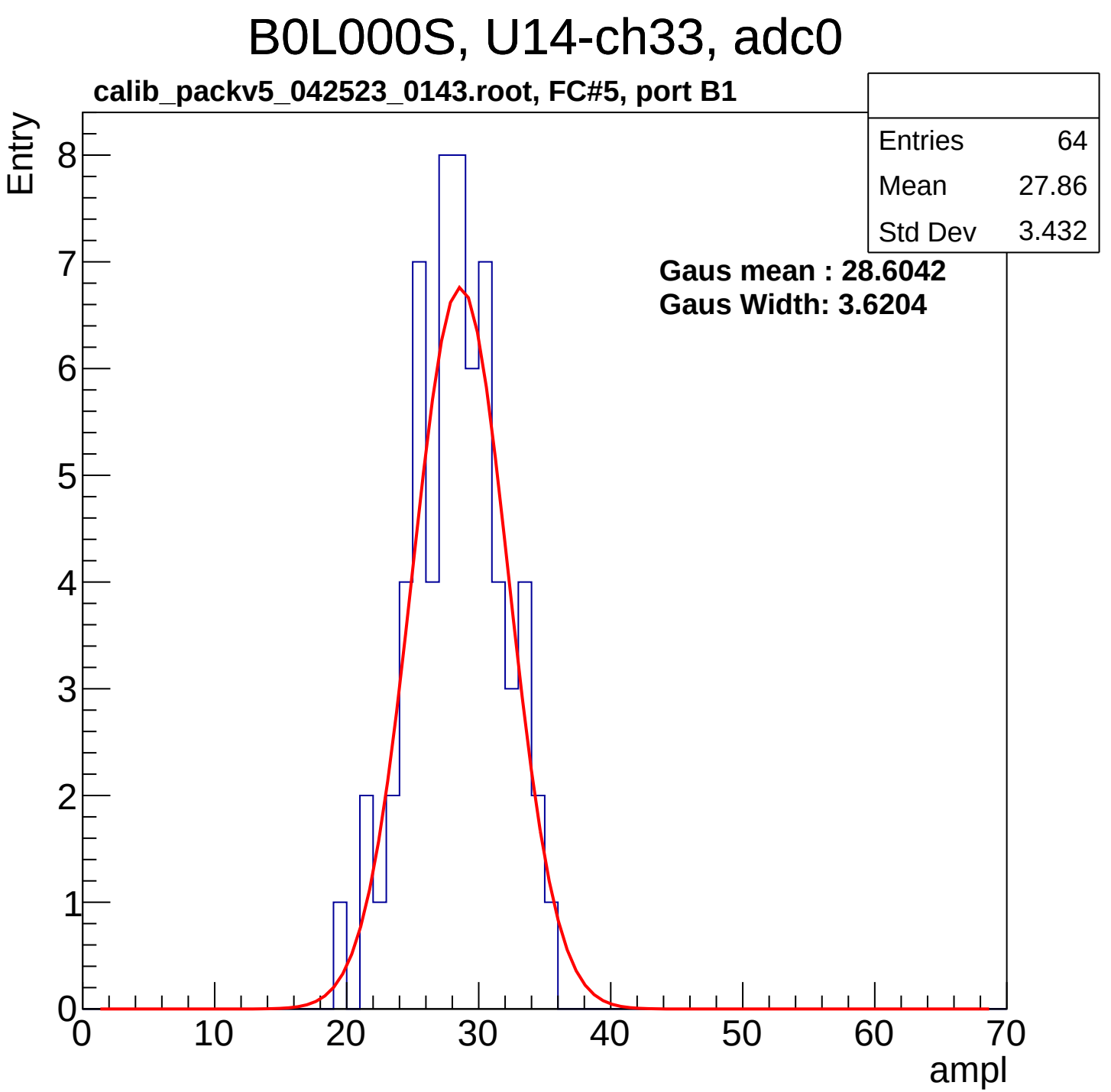
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 27.86
Std Dev 3.432
Gaus mean : 28.6042
Gaus Width: 3.6204
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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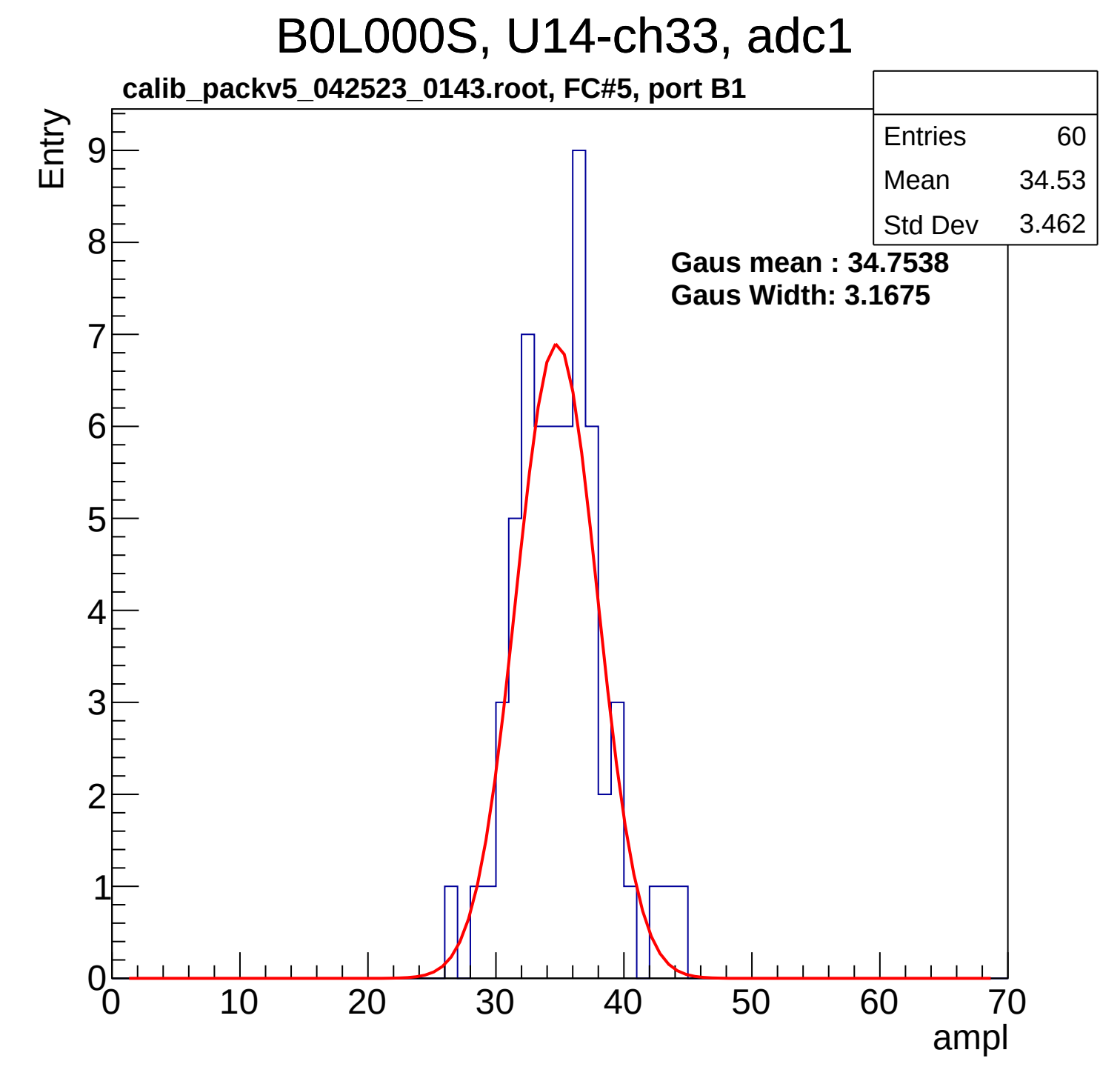
B0L000S, U14-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 34.53
Std Dev 3.462
Gaus mean : 34.7538
Gaus Width: 3.1675
Entry
ampl
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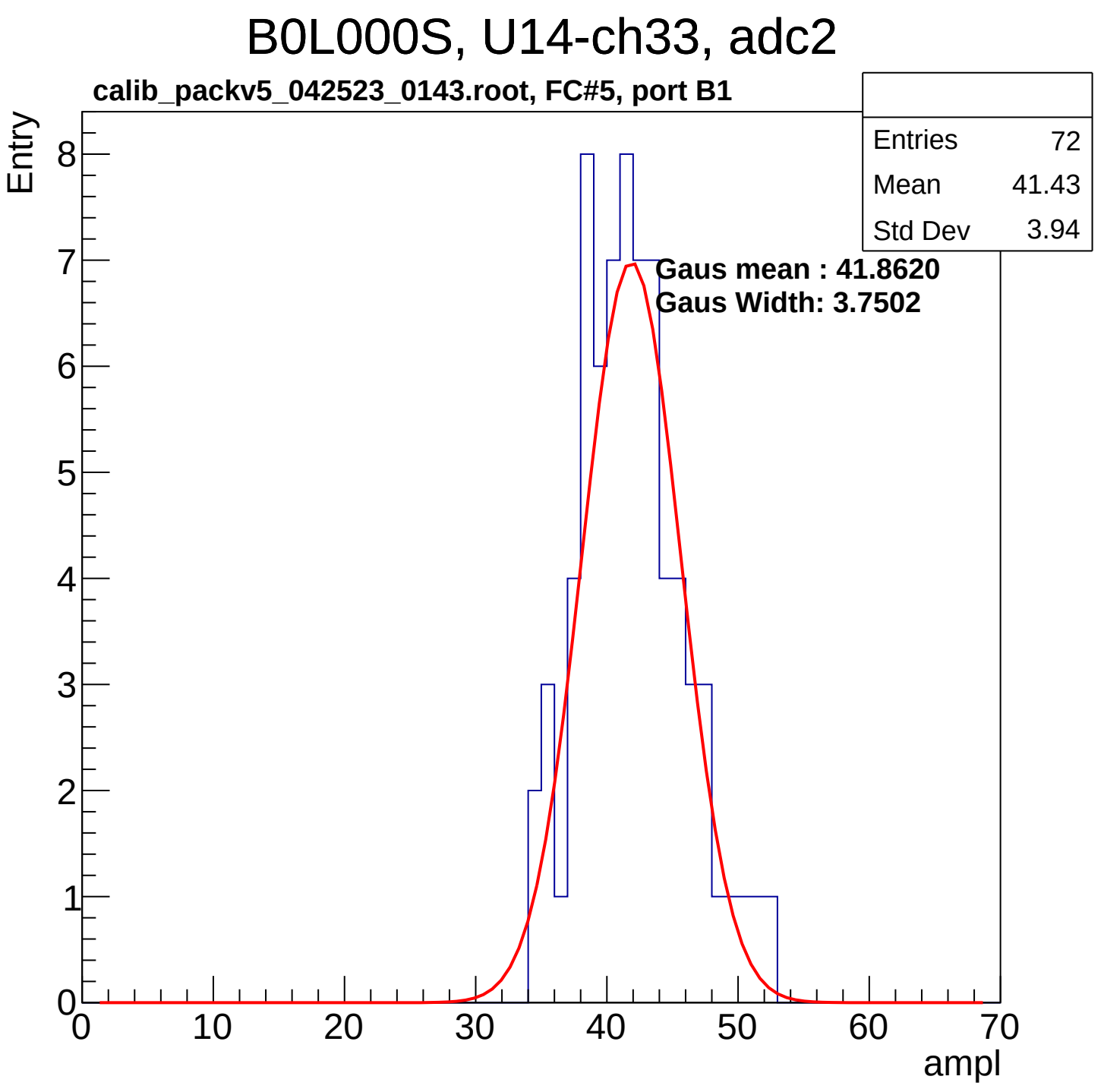
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 41.43
Std Dev 3.94
Gaus mean : 41.8620
Gaus Width: 3.7502
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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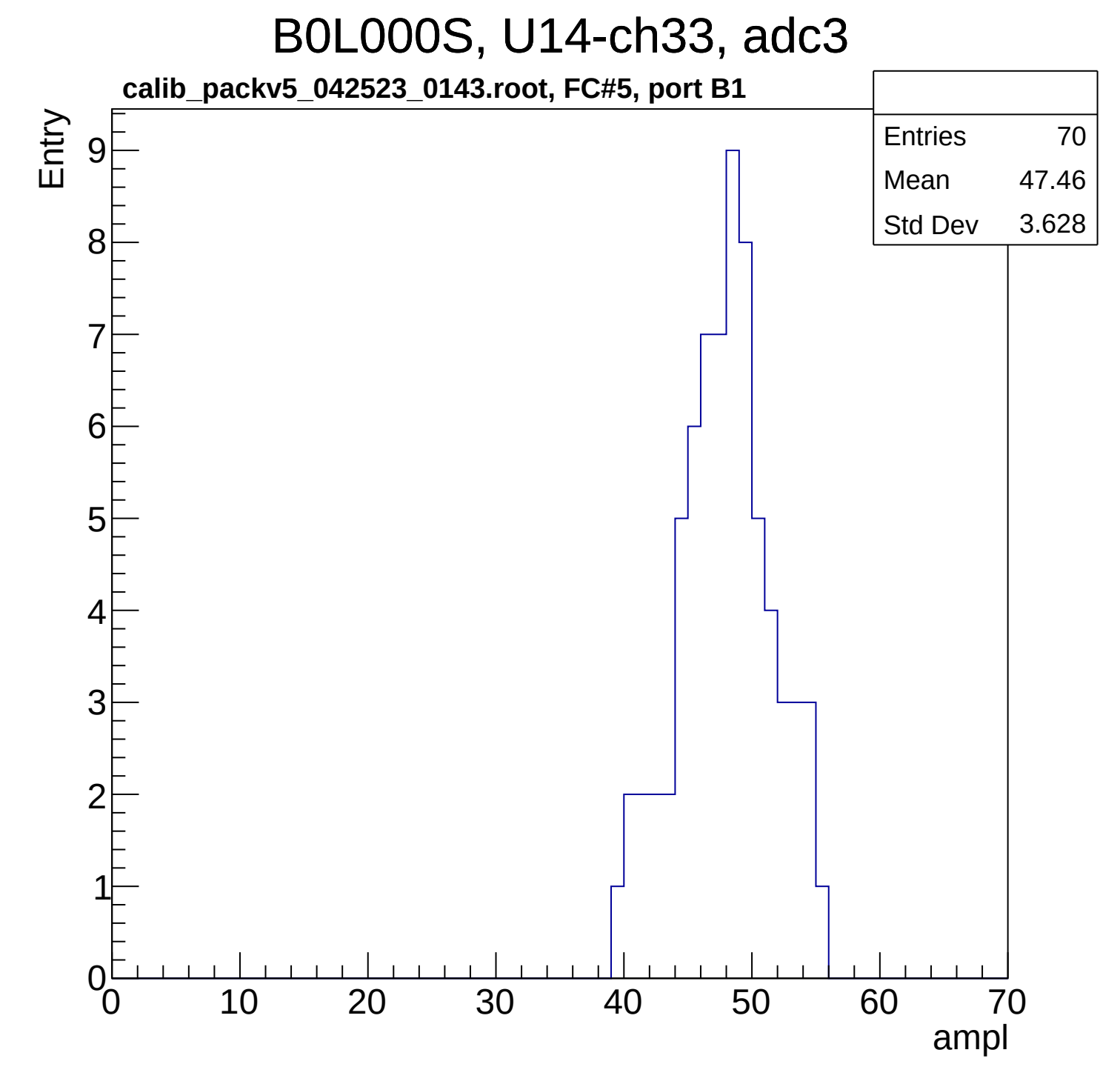
B0L000S, U14-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 47.46
Std Dev 3.628
Entry
ampl
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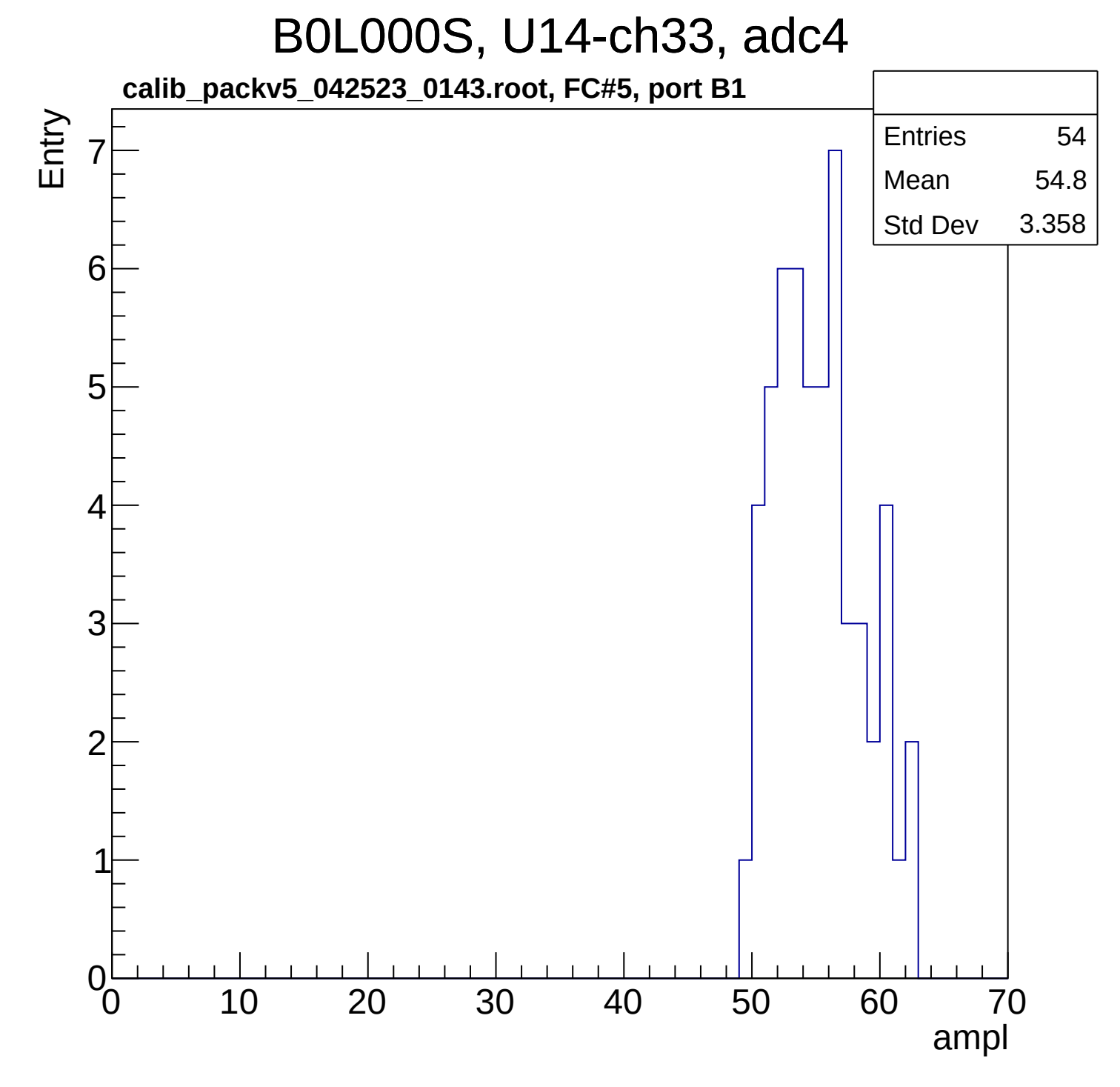
B0L000S, U14-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.8
Std Dev 3.358
Entry
ampl
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2
3
4
5
6
7
0
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60
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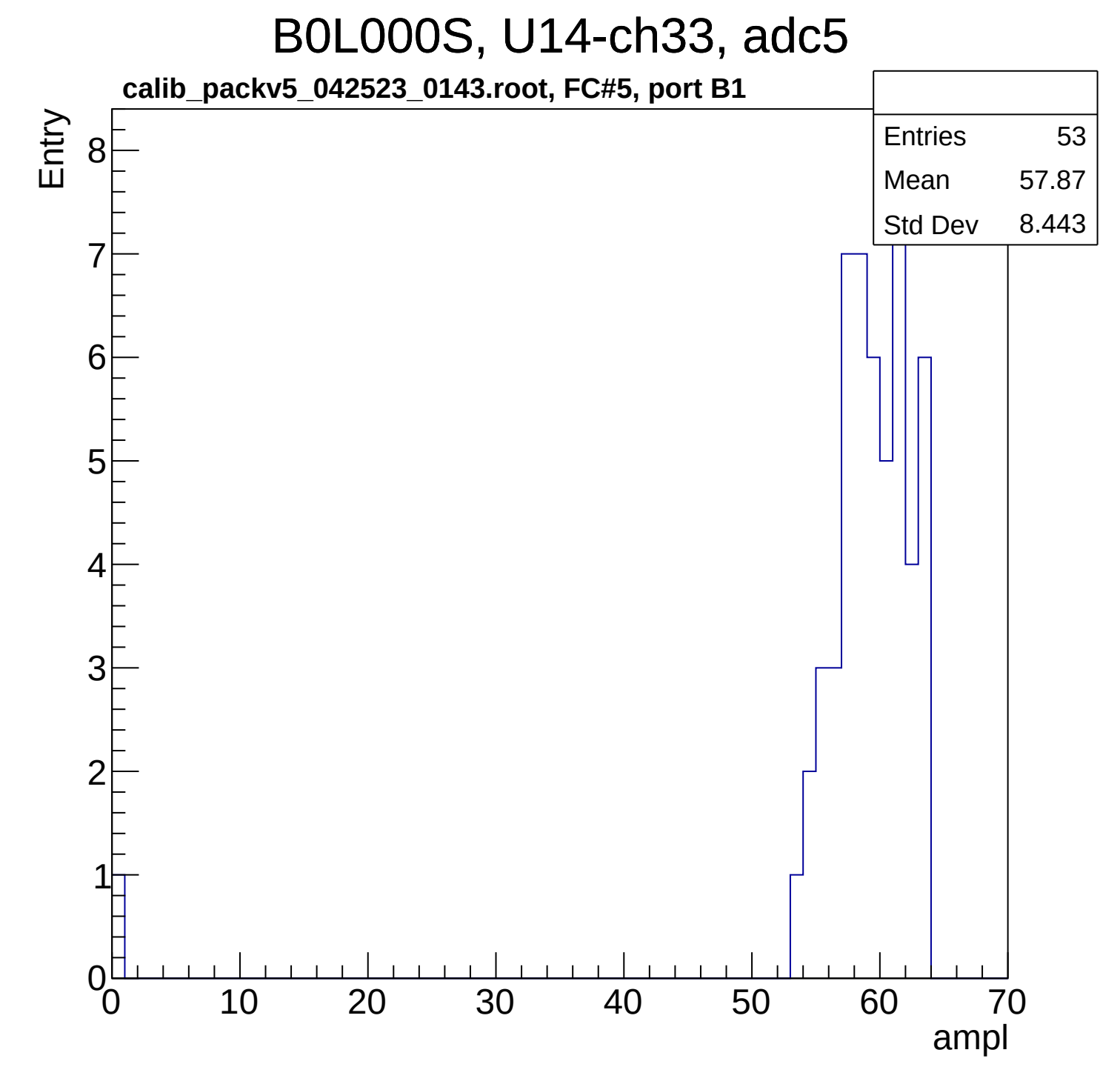
B0L000S, U14-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.87
Std Dev 8.443
Entry
ampl
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4
5
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7
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0
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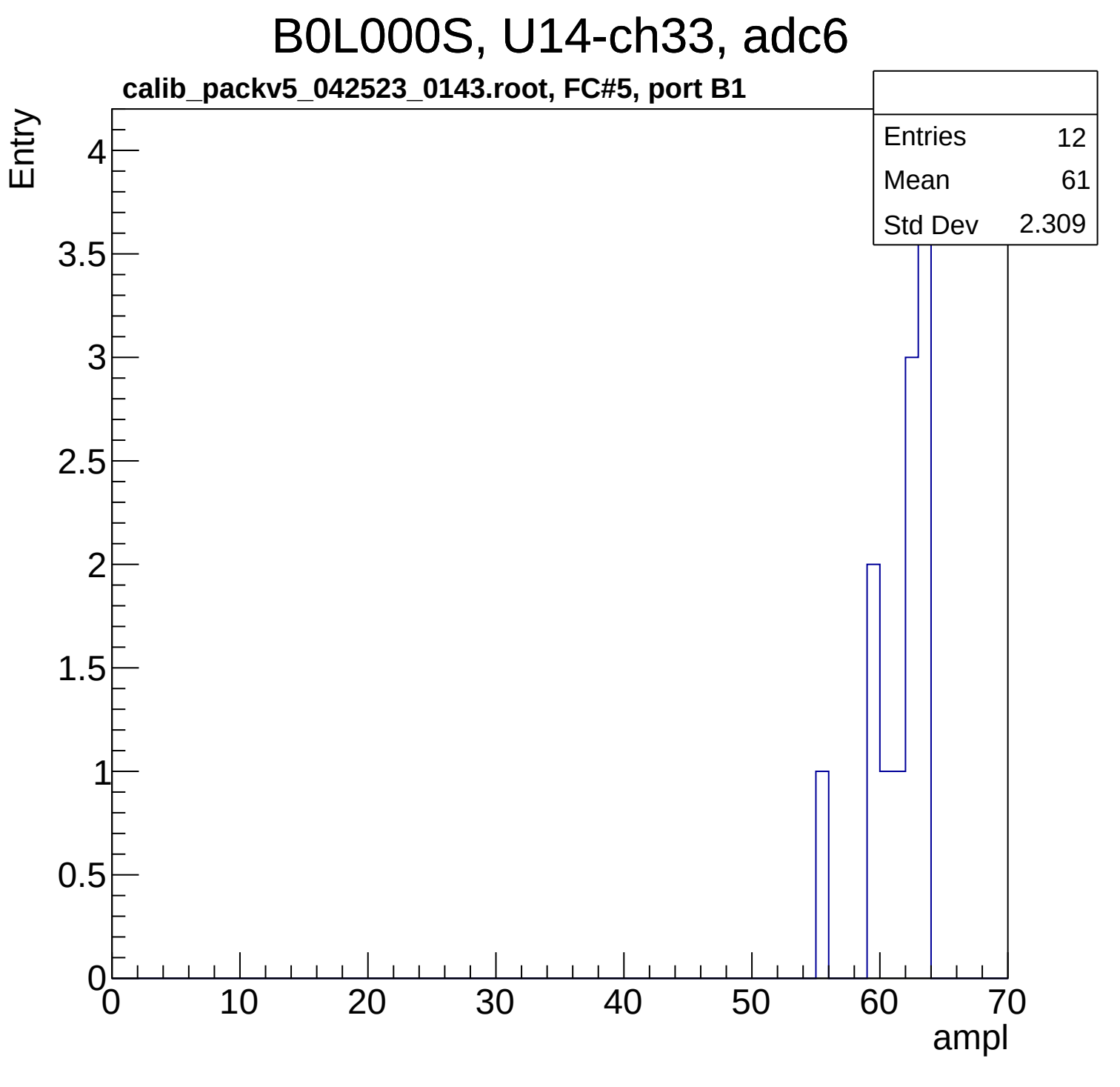
B0L000S, U14-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61
Std Dev 2.309
Entry
ampl
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1.5
2
2.5
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3.5
4
0
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60
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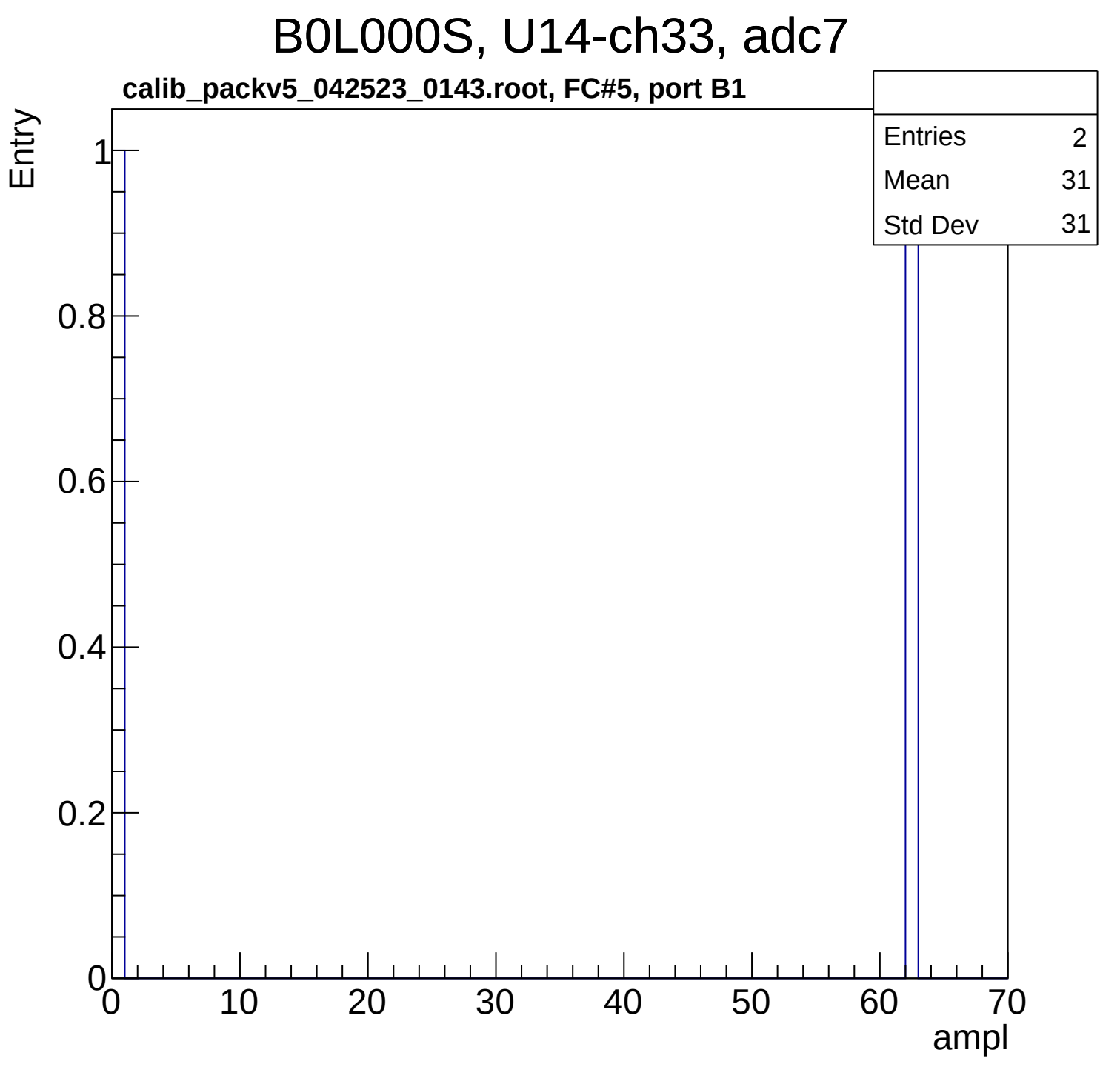
B0L000S, U14-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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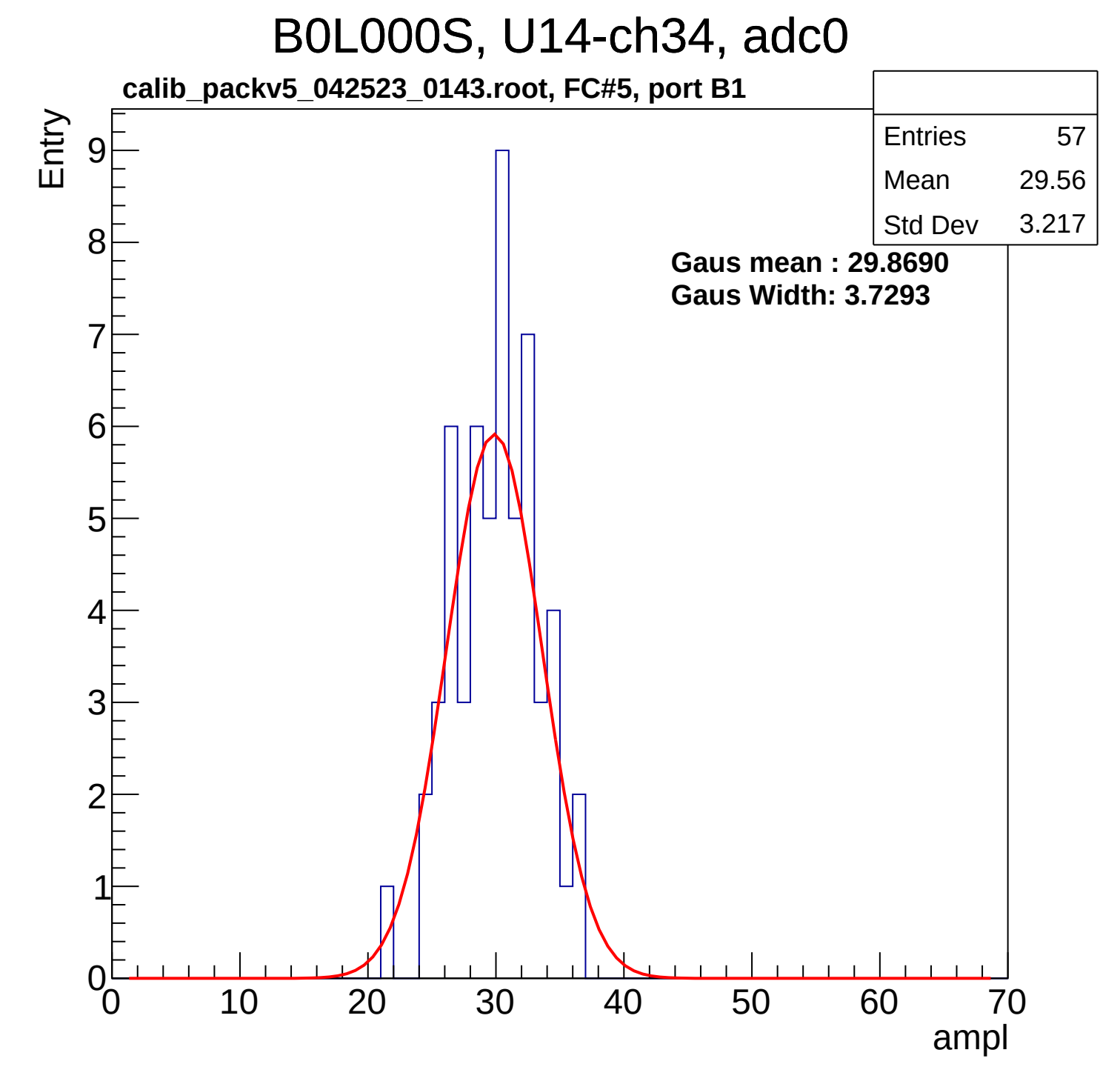
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 29.56
Std Dev 3.217
Gaus mean : 29.8690
Gaus Width: 3.7293
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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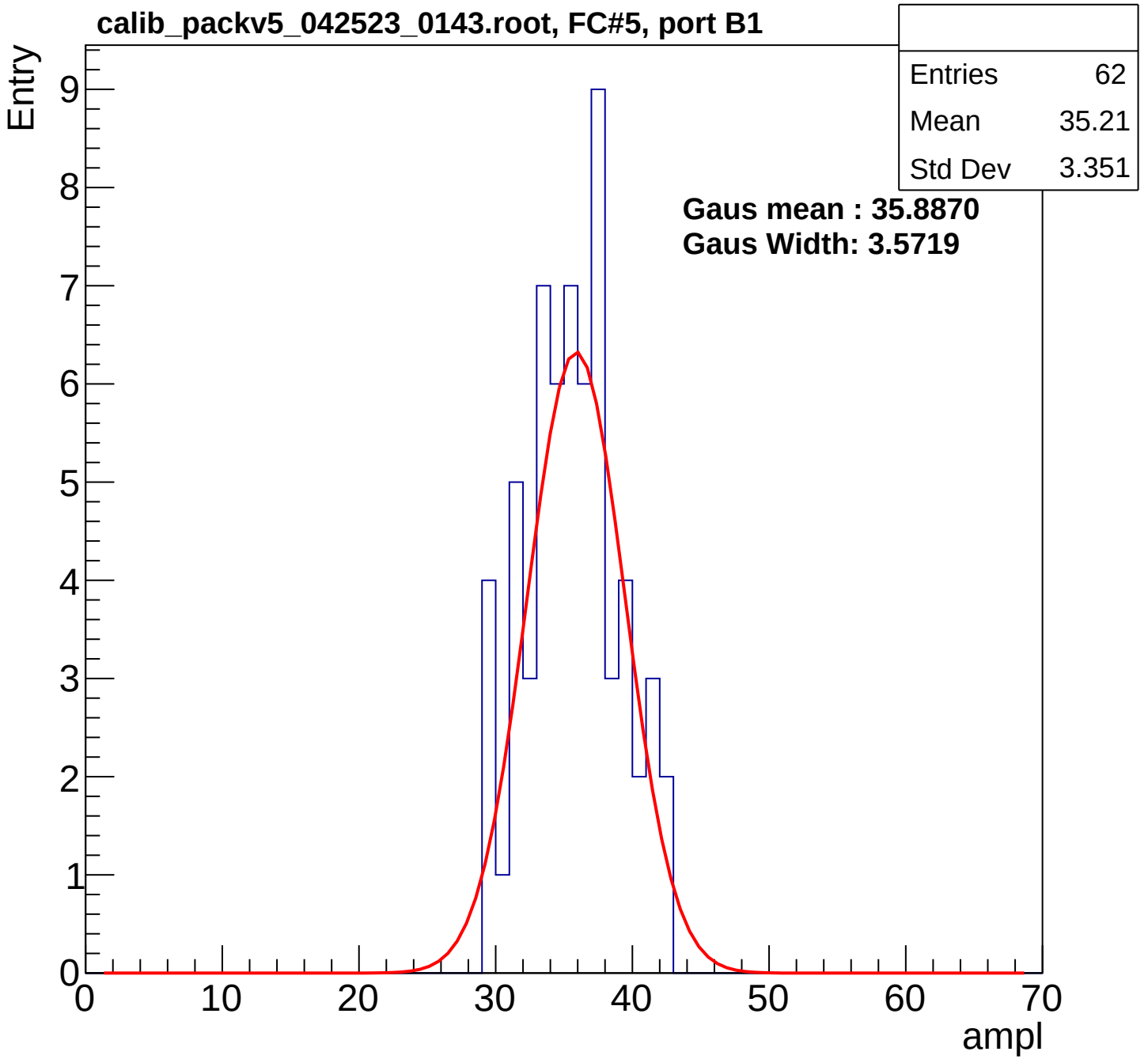
B0L000S, U14-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.21
Std Dev 3.351
Gaus mean : 35.8870
Gaus Width: 3.5719
Entry
ampl
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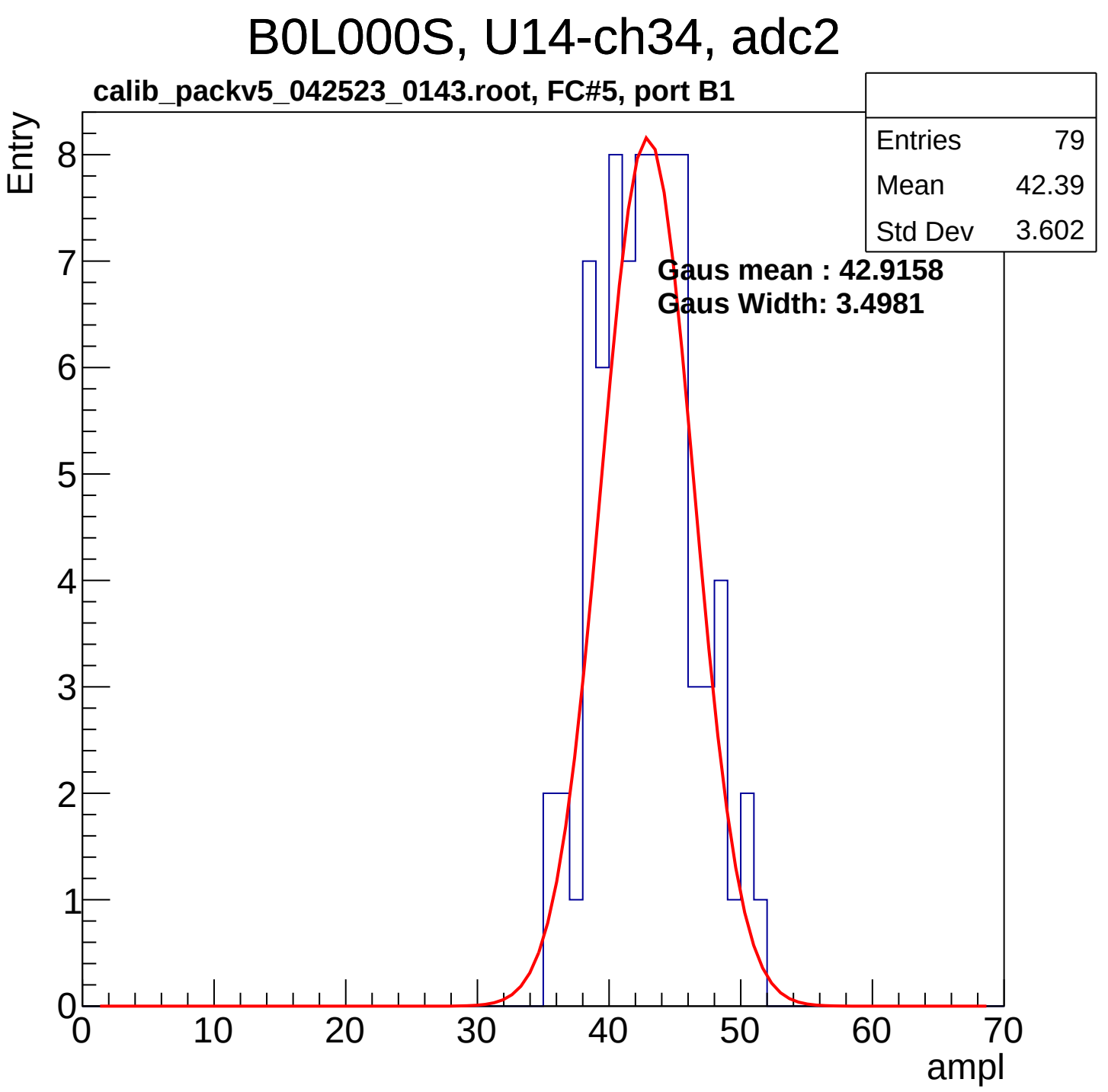
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 42.39
Std Dev 3.602
Gaus mean : 42.9158
Gaus Width: 3.4981
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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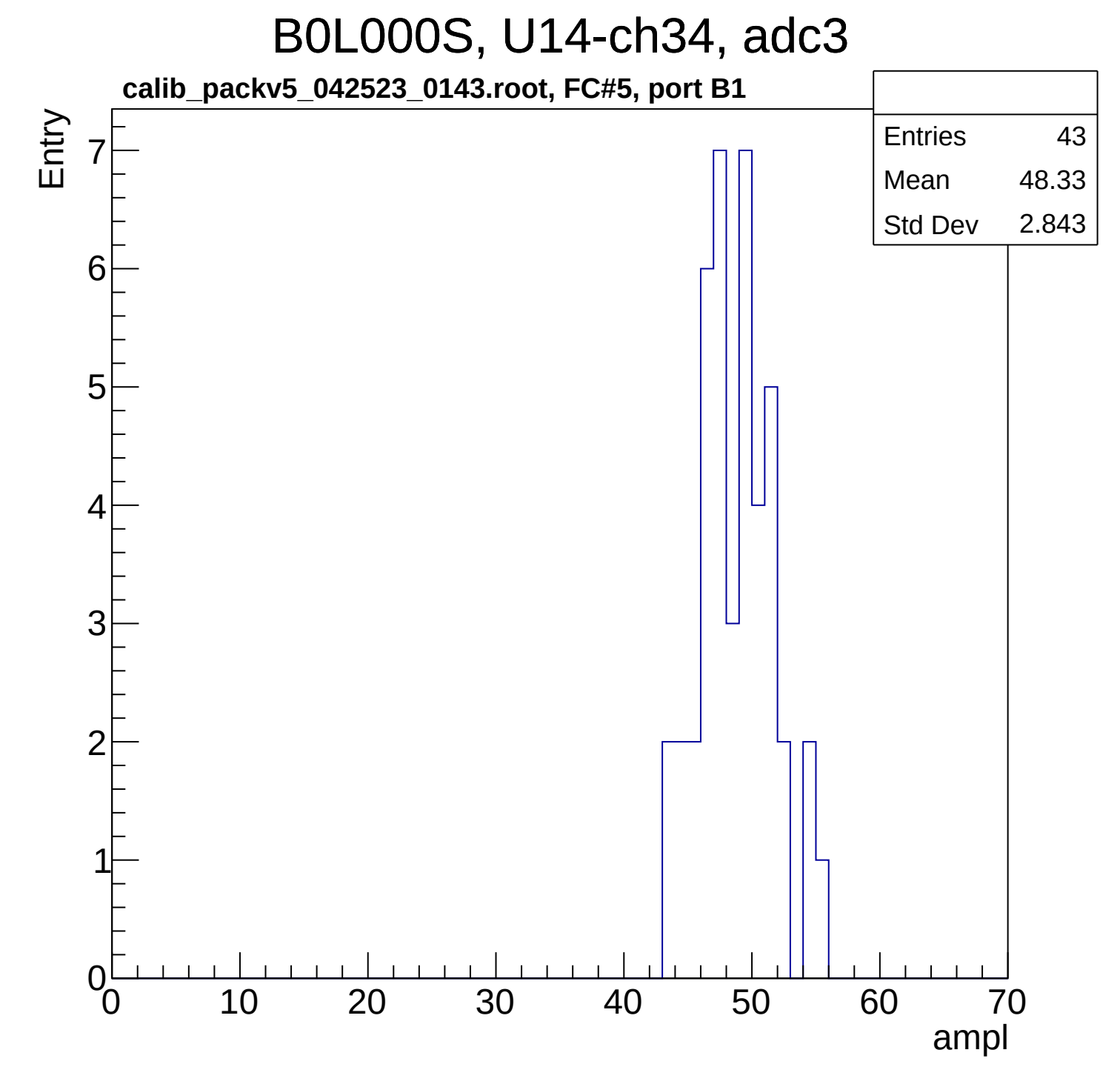
40
50
60
70


B0L000S, U14-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 48.33
Std Dev 2.843
Entry
ampl
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2
3
4
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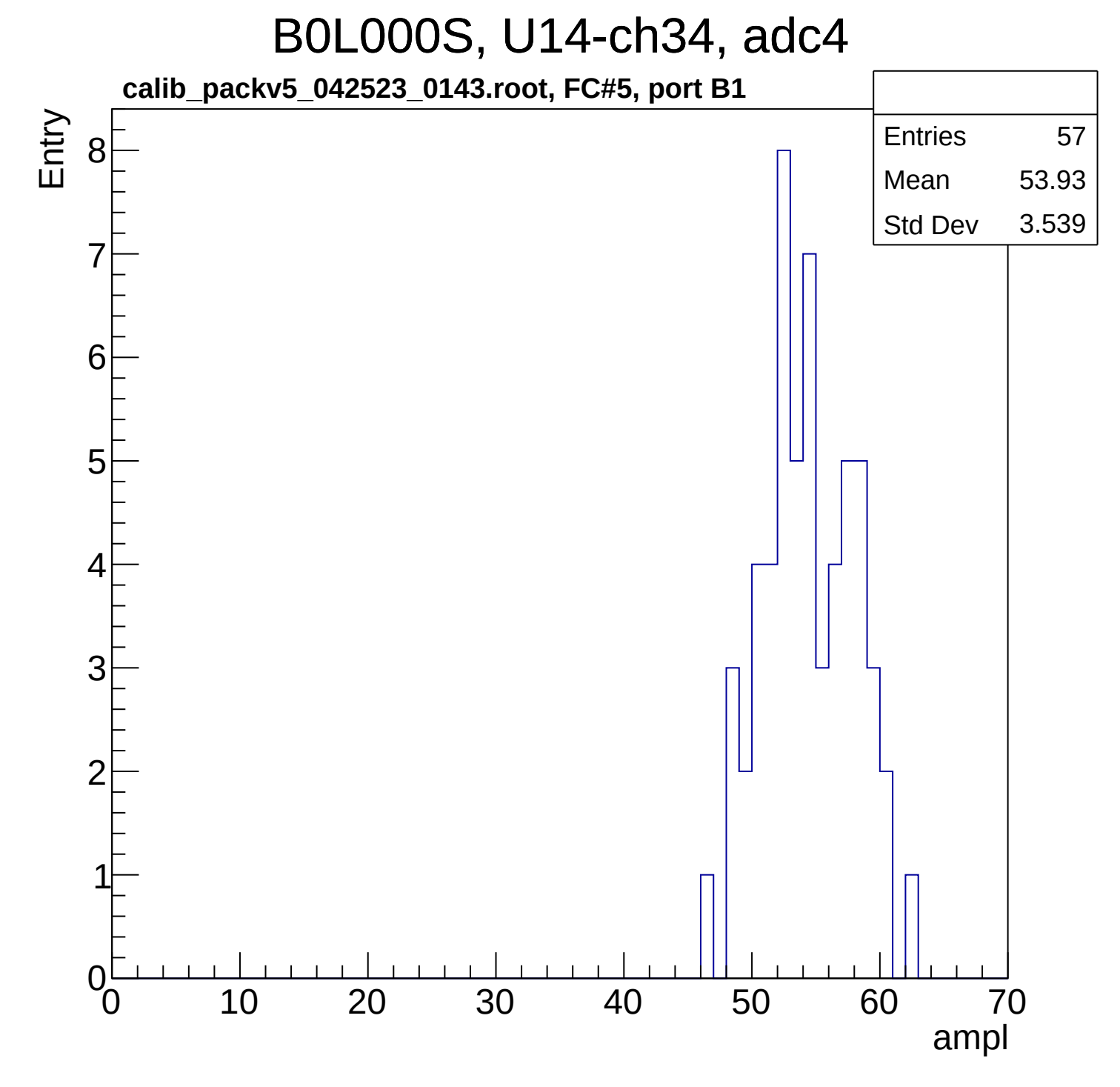
B0L000S, U14-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.93
Std Dev 3.539
Entry
ampl
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4
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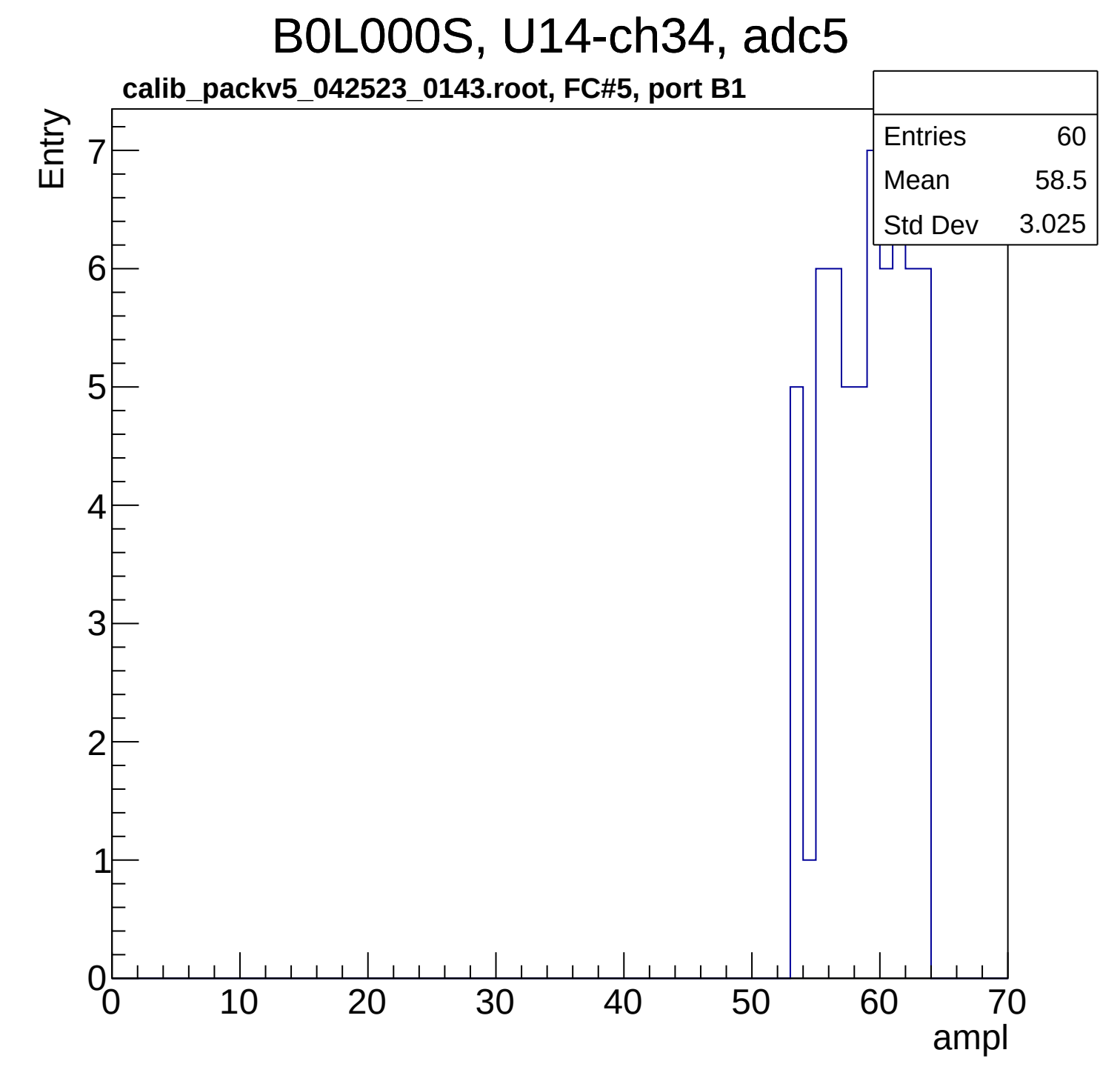
B0L000S, U14-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.5
Std Dev 3.025
Entry
ampl
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2
3
4
5
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7
0
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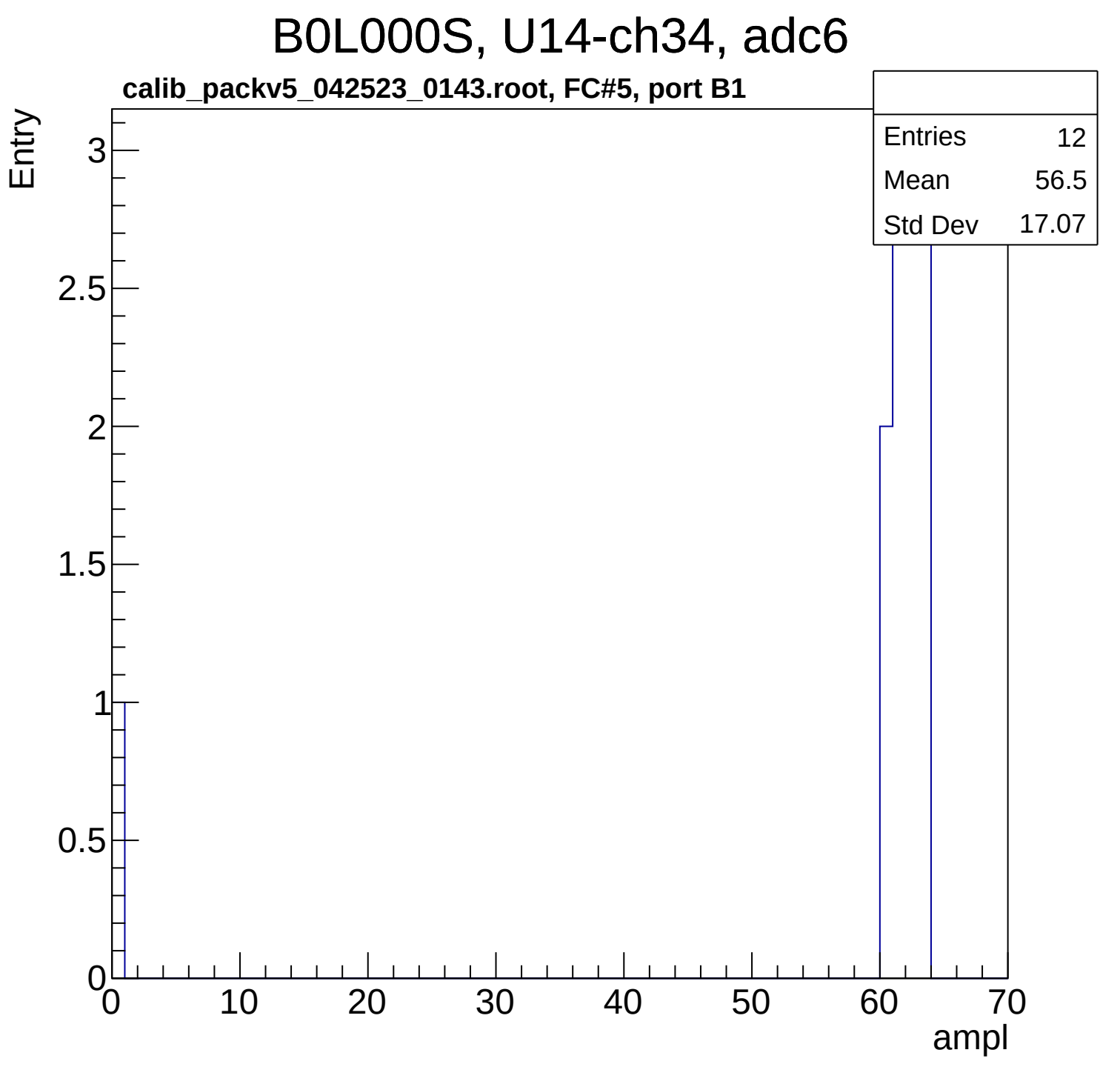
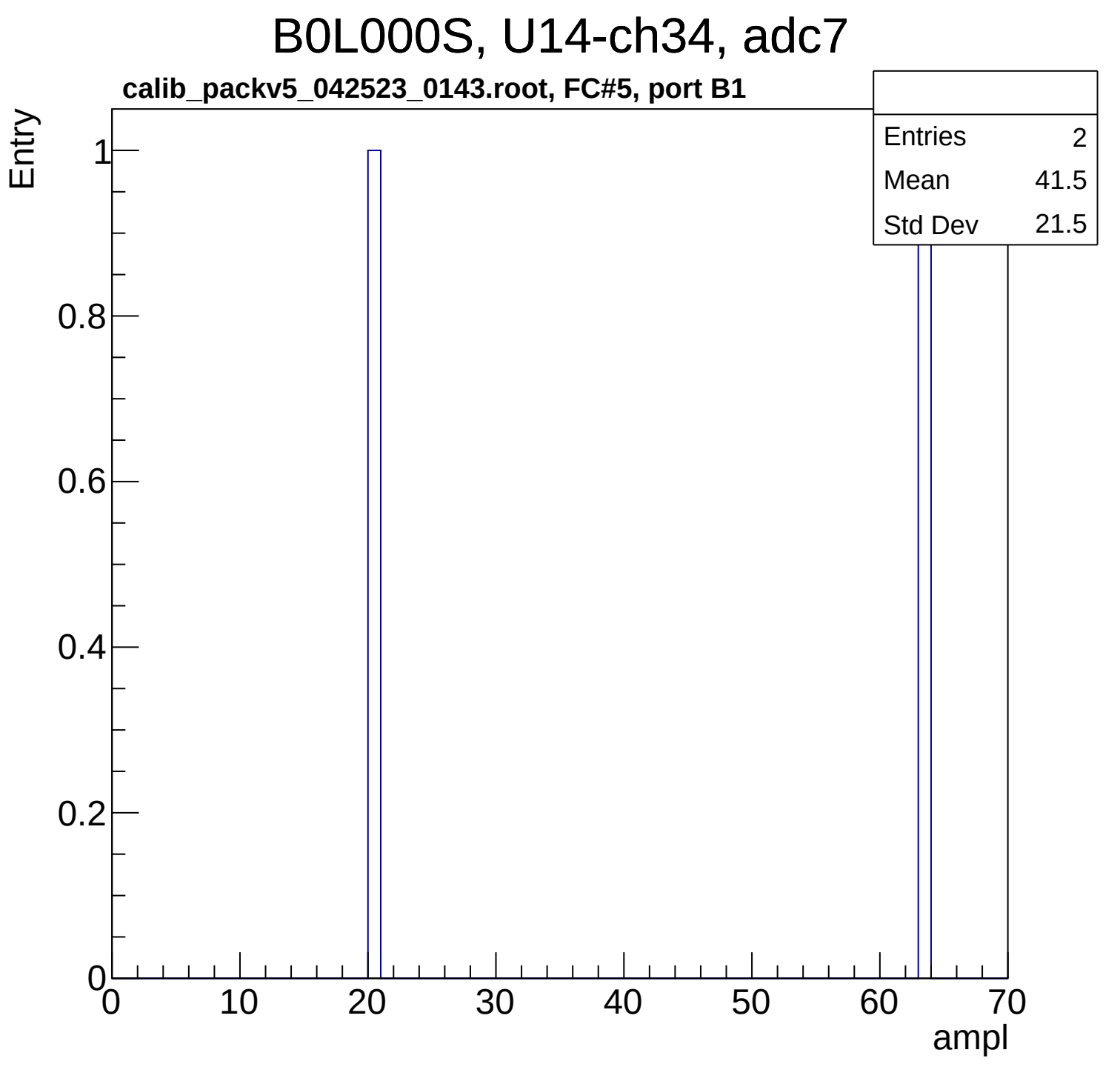

B0L000S, U14-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.5
Std Dev 17.07
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
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20
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40
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60
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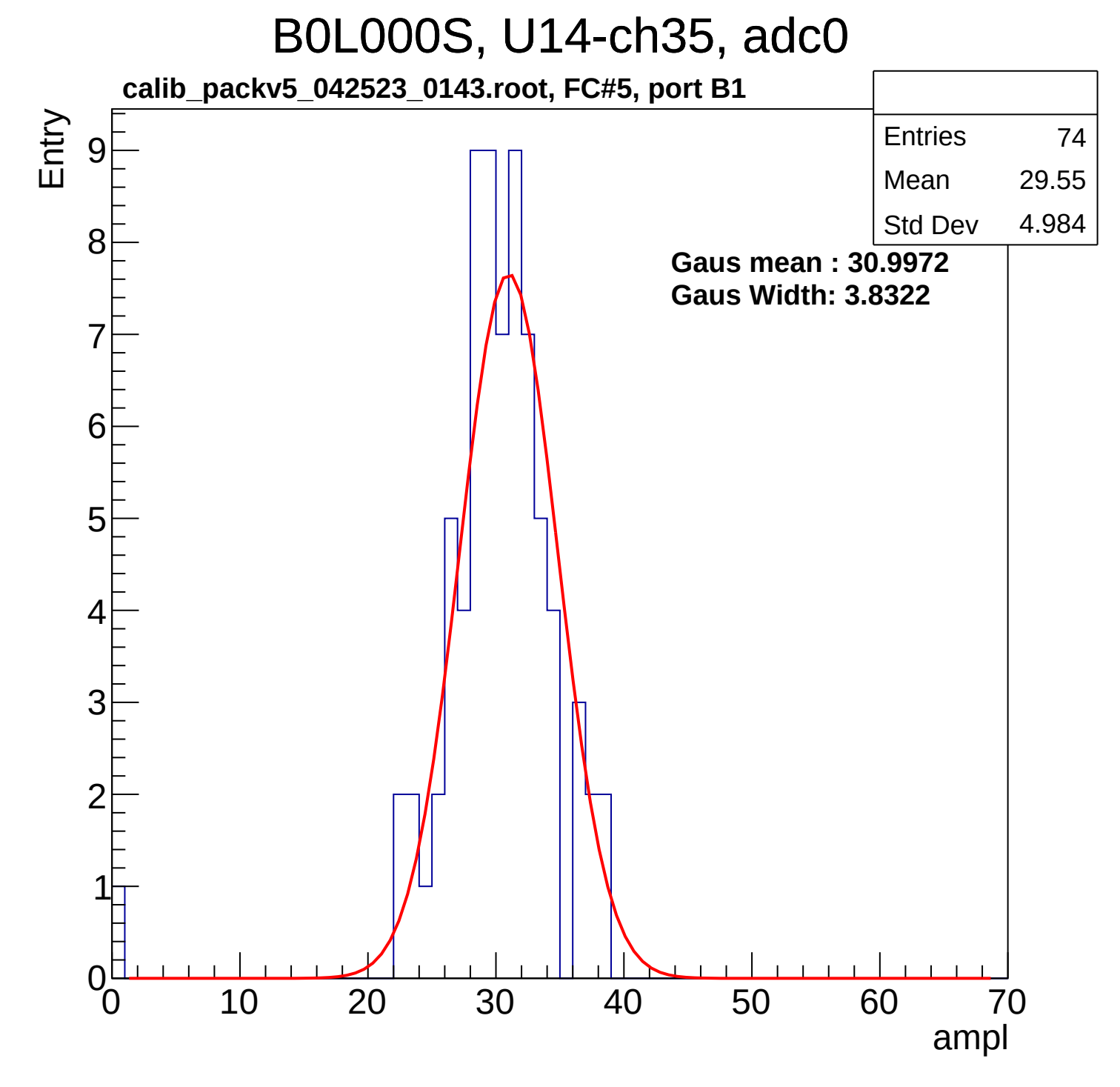
B0L000S, U14-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 29.55
Std Dev 4.984
Gaus mean : 30.9972
Gaus Width: 3.8322
Entry
ampl
0
10
20
30
40
50
60
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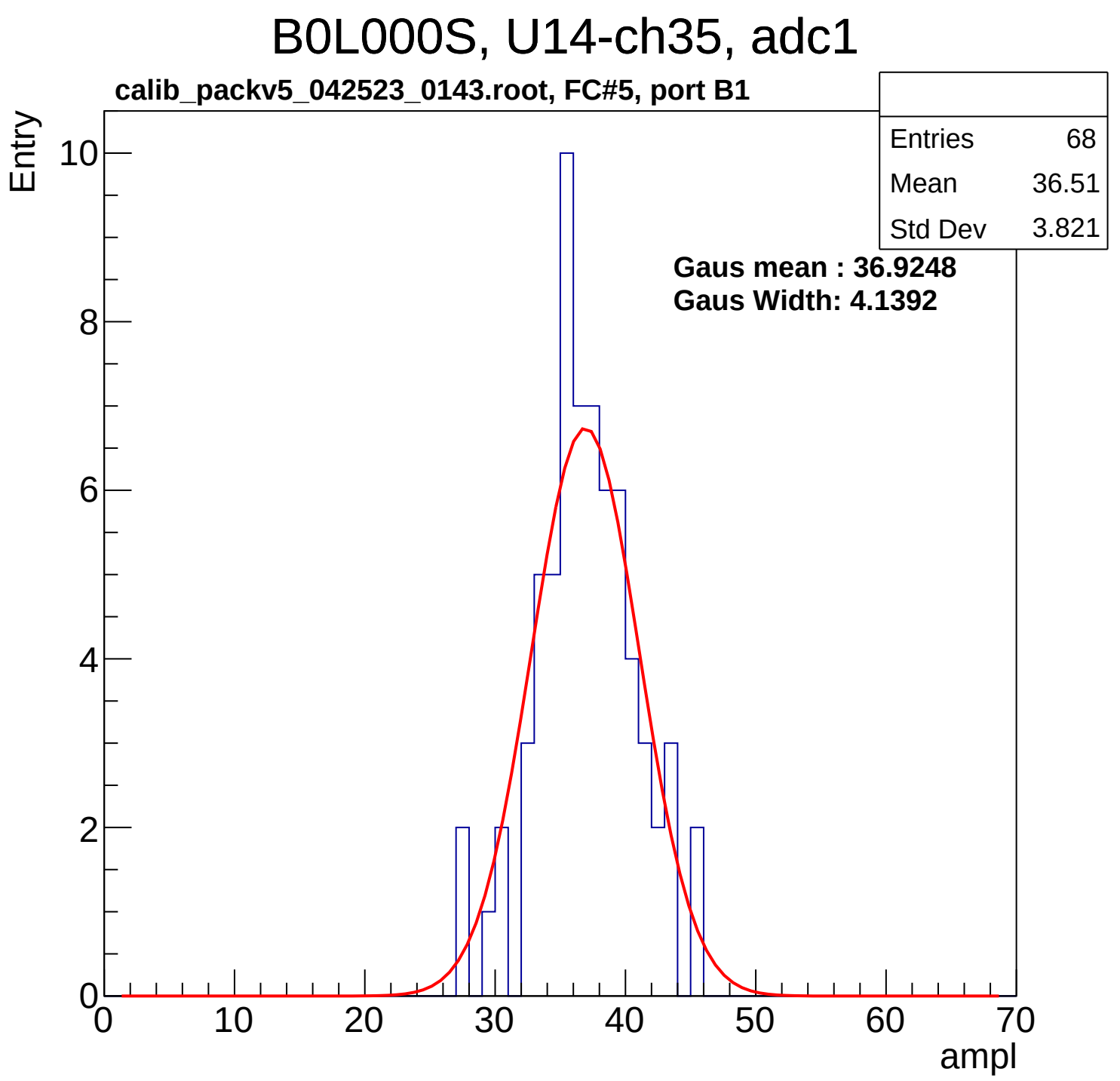
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.51
Std Dev 3.821
Gaus mean : 36.9248
Gaus Width: 4.1392
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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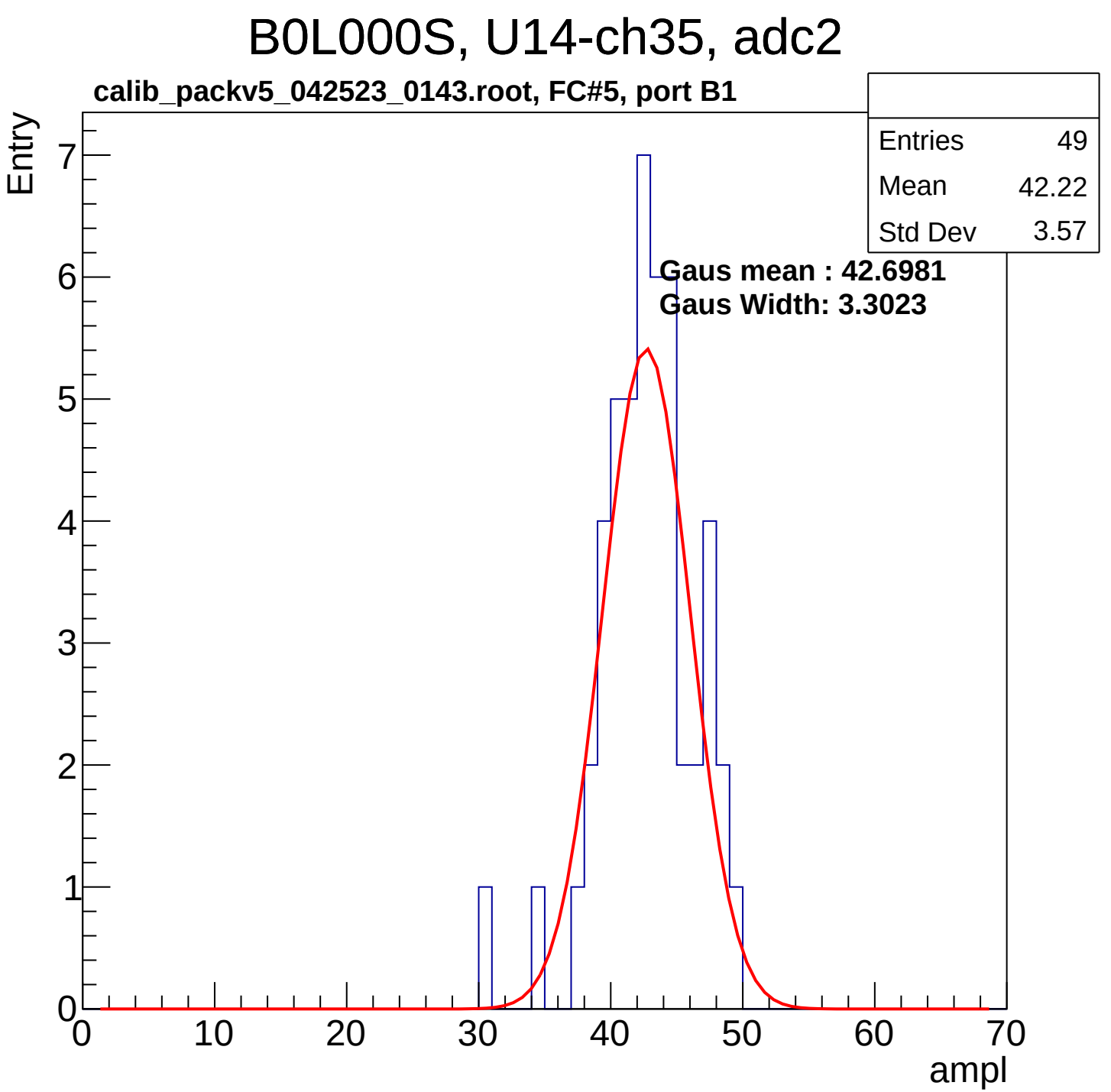
B0L000S, U14-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 42.22
Std Dev 3.57
Gaus mean : 42.6981
Gaus Width: 3.3023
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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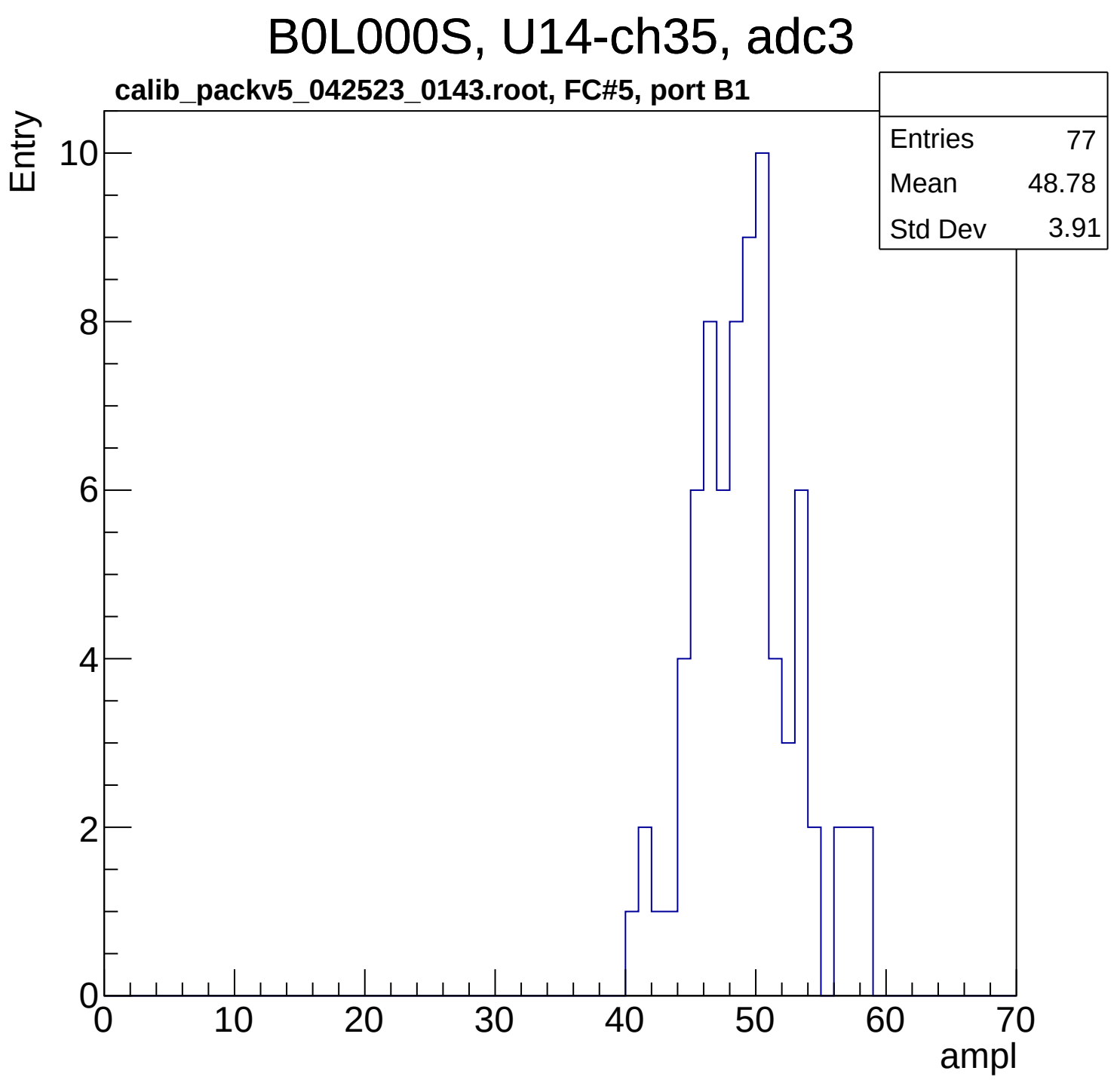
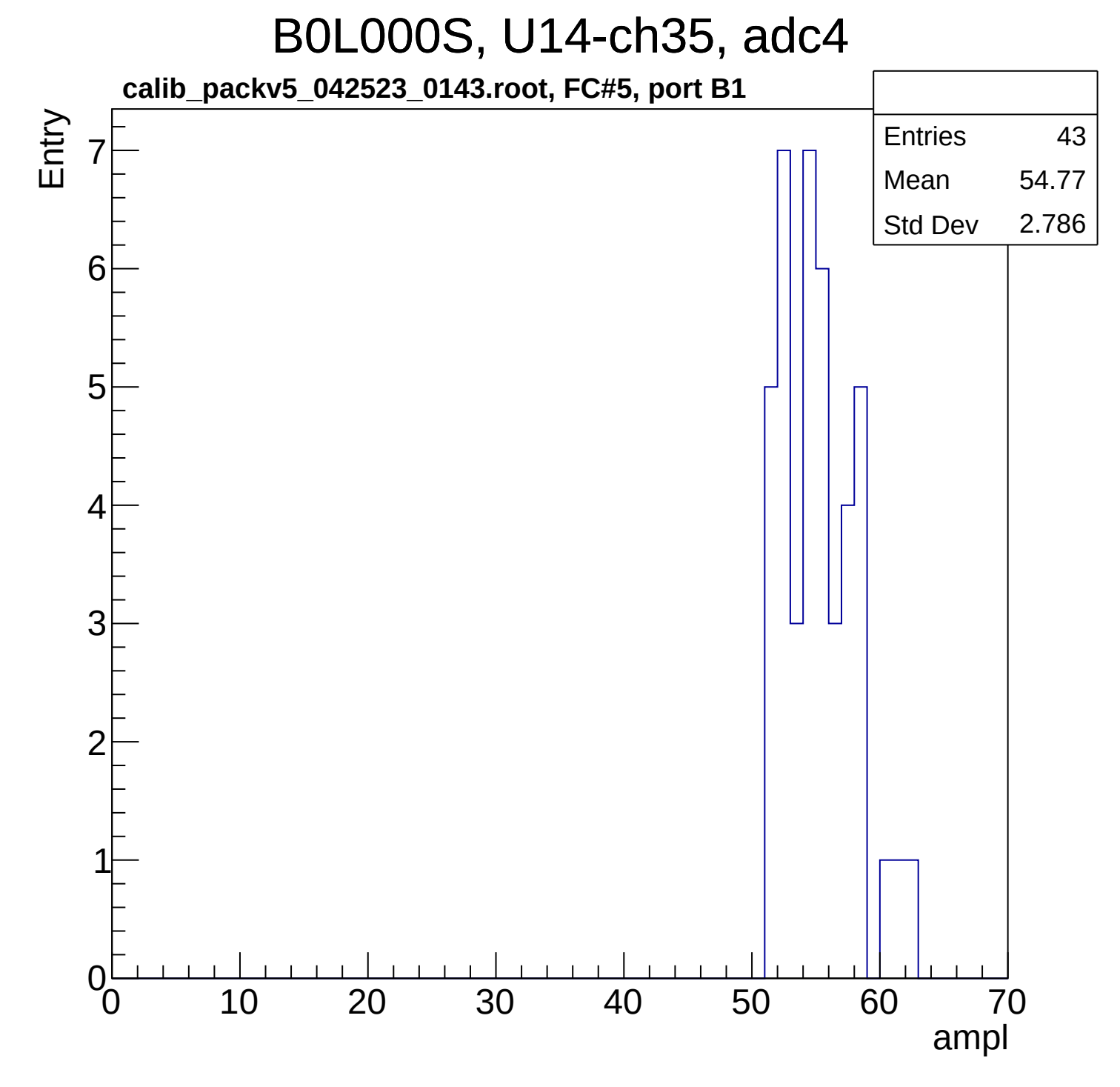

B0L000S, U14-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 48.78
Std Dev 3.91
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 54.77
Std Dev 2.786
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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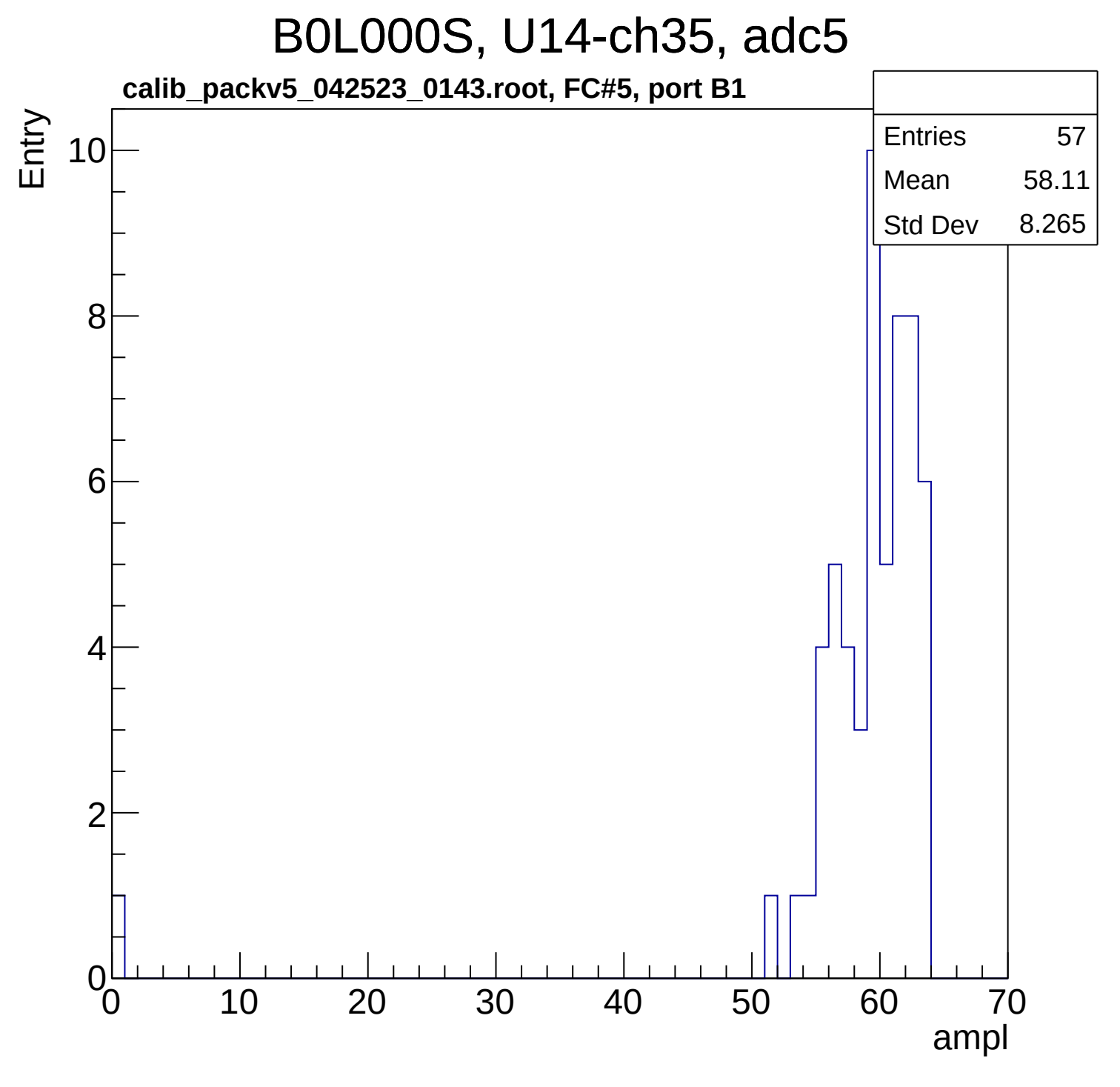
B0L000S, U14-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.11
Std Dev 8.265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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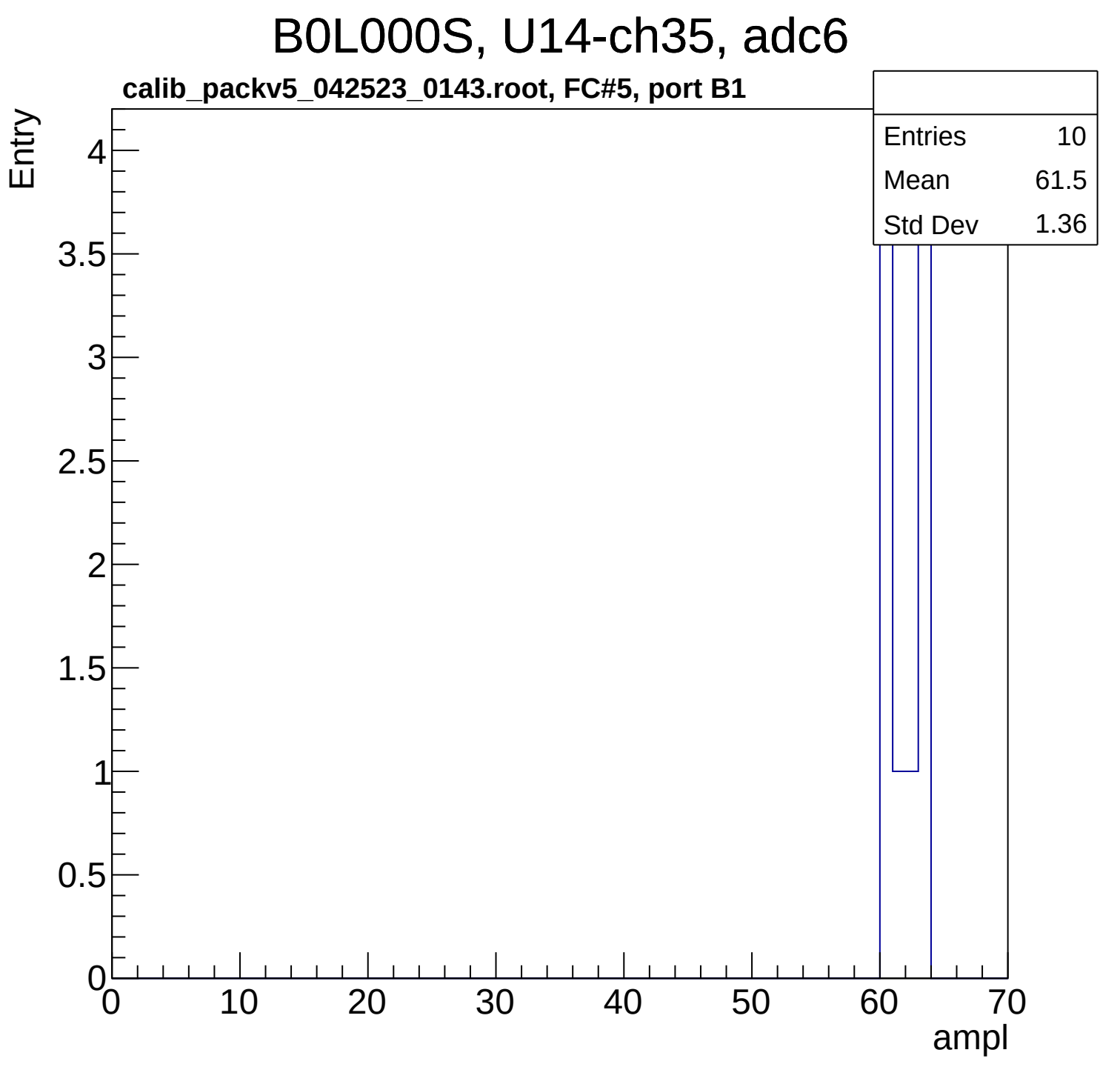
B0L000S, U14-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.5
Std Dev 1.36
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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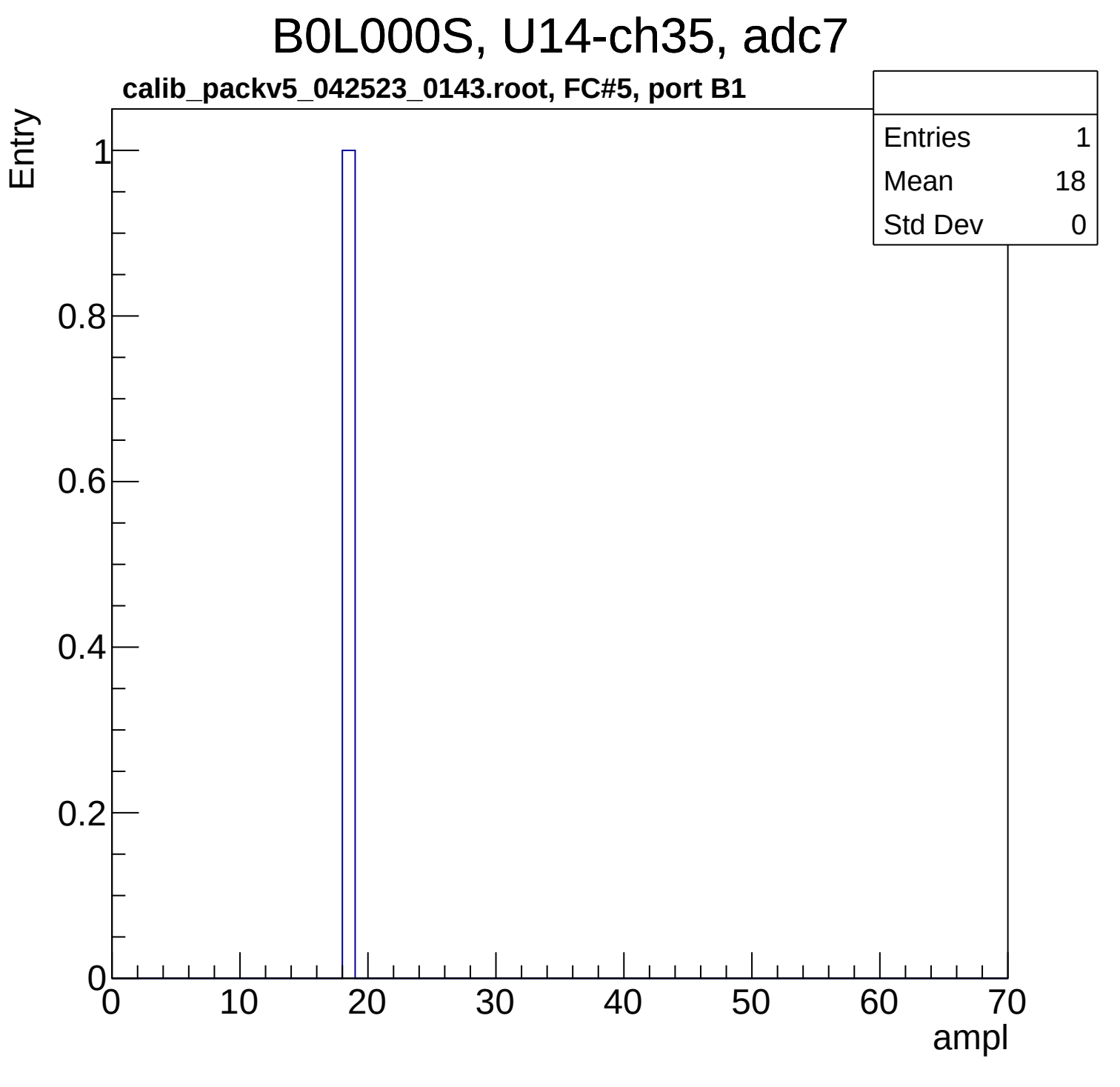
B0L000S, U14-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 18
Std Dev 0
Entry
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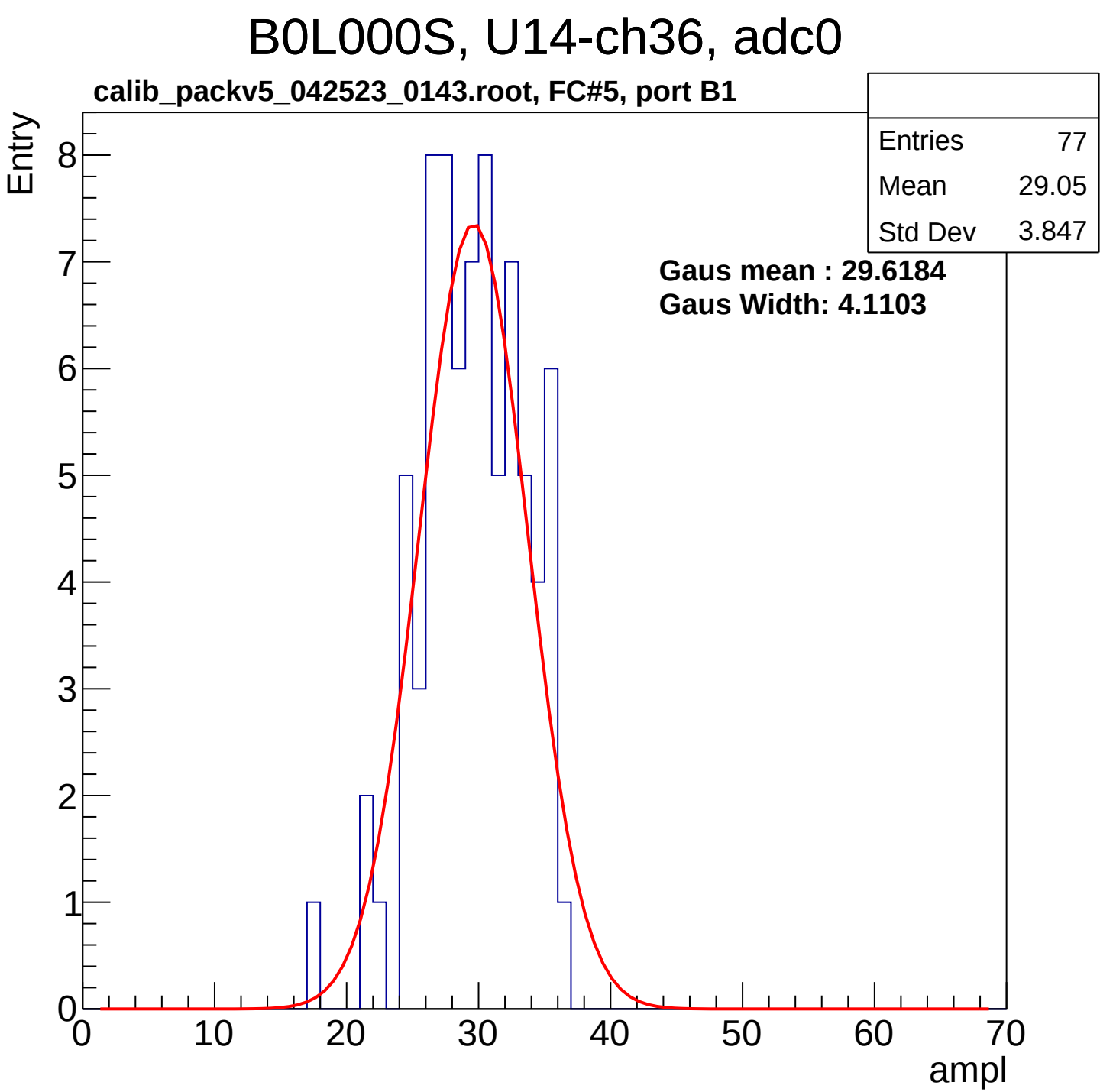
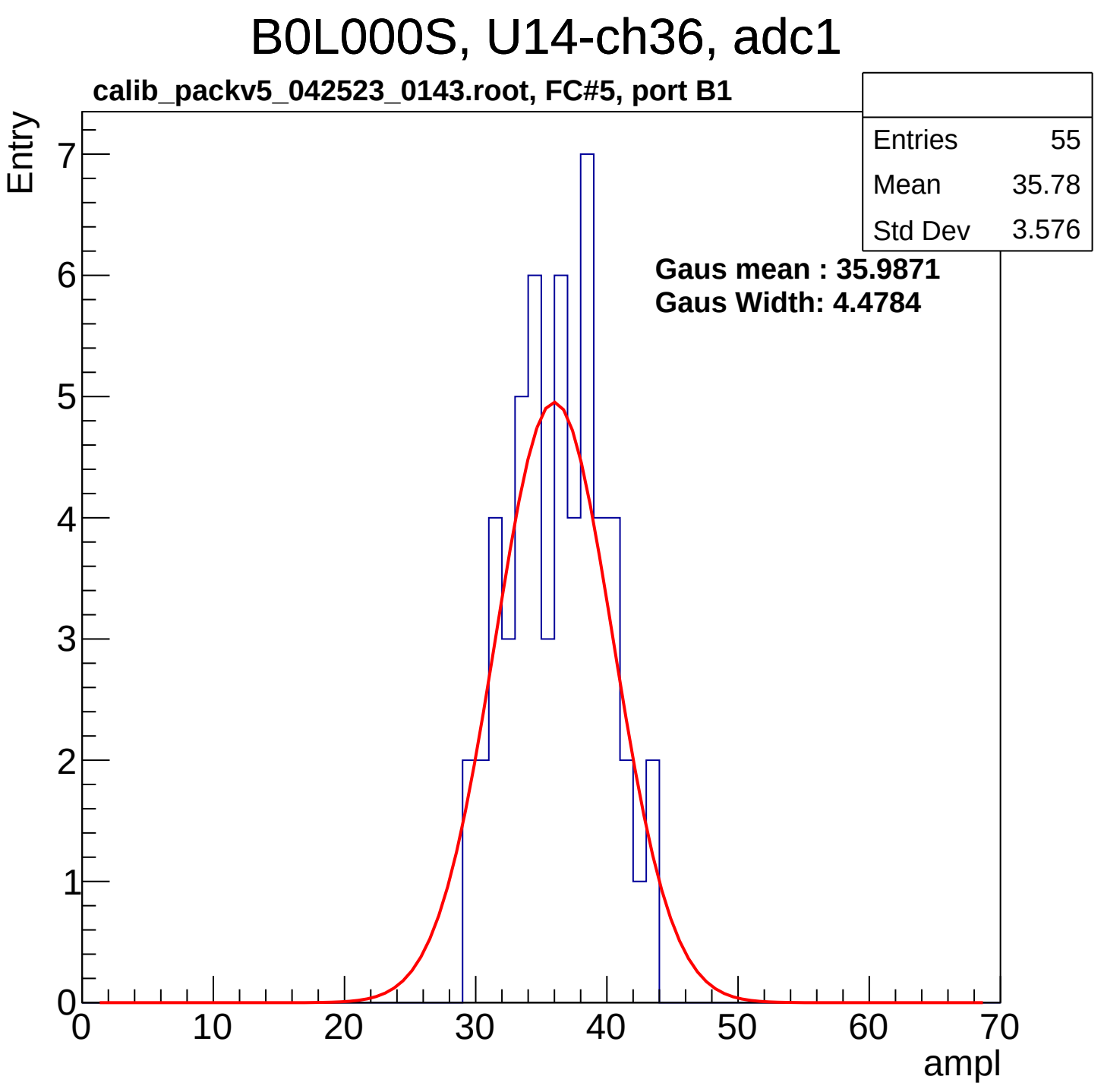
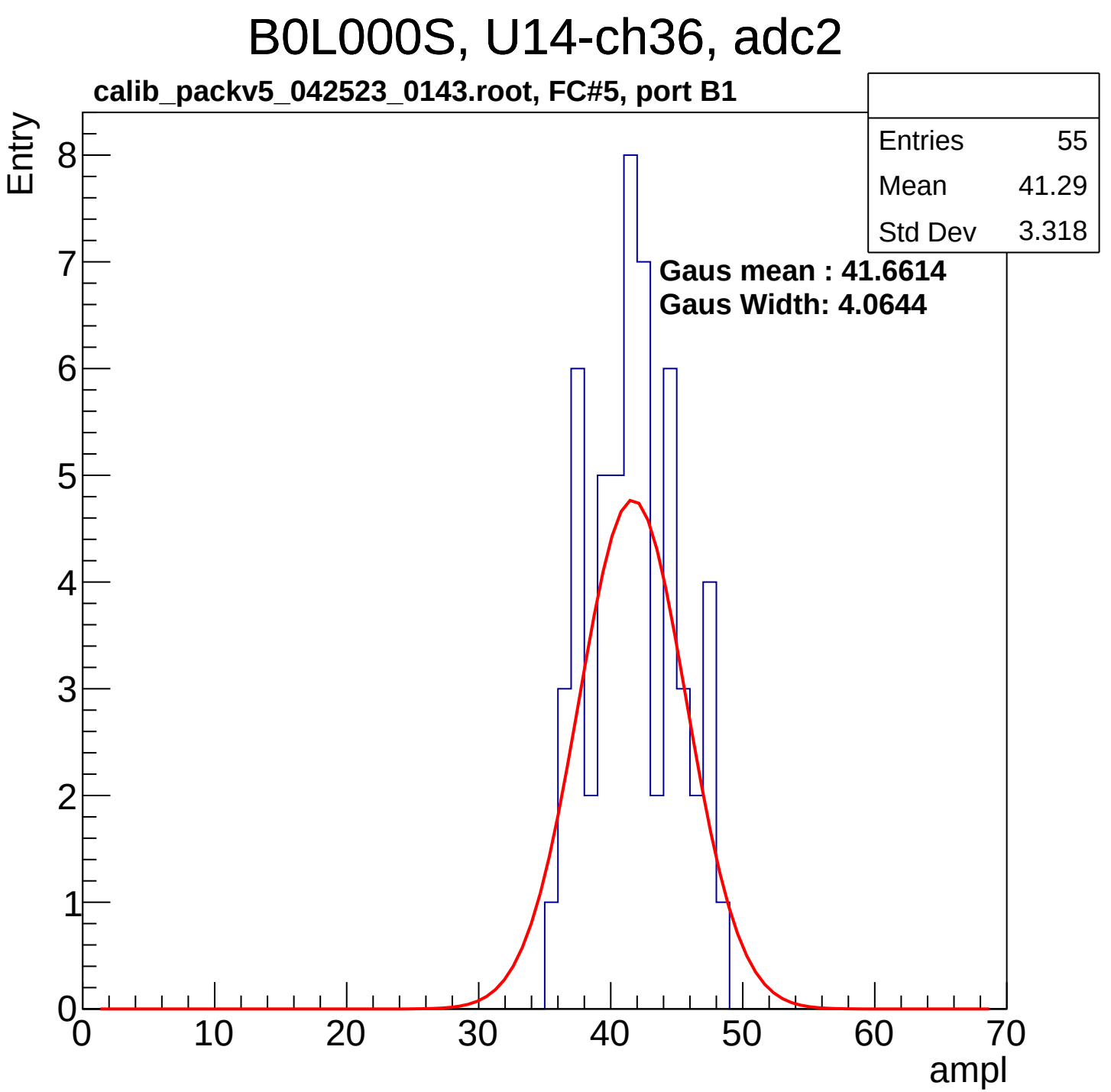
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.05
Std Dev 3.847
Gaus mean : 29.6184
Gaus Width: 4.1103
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 35.78
Std Dev 3.576
Gaus mean : 35.9871
Gaus Width: 4.4784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.29
Std Dev 3.318
Gaus mean : 41.6614
Gaus Width: 4.0644
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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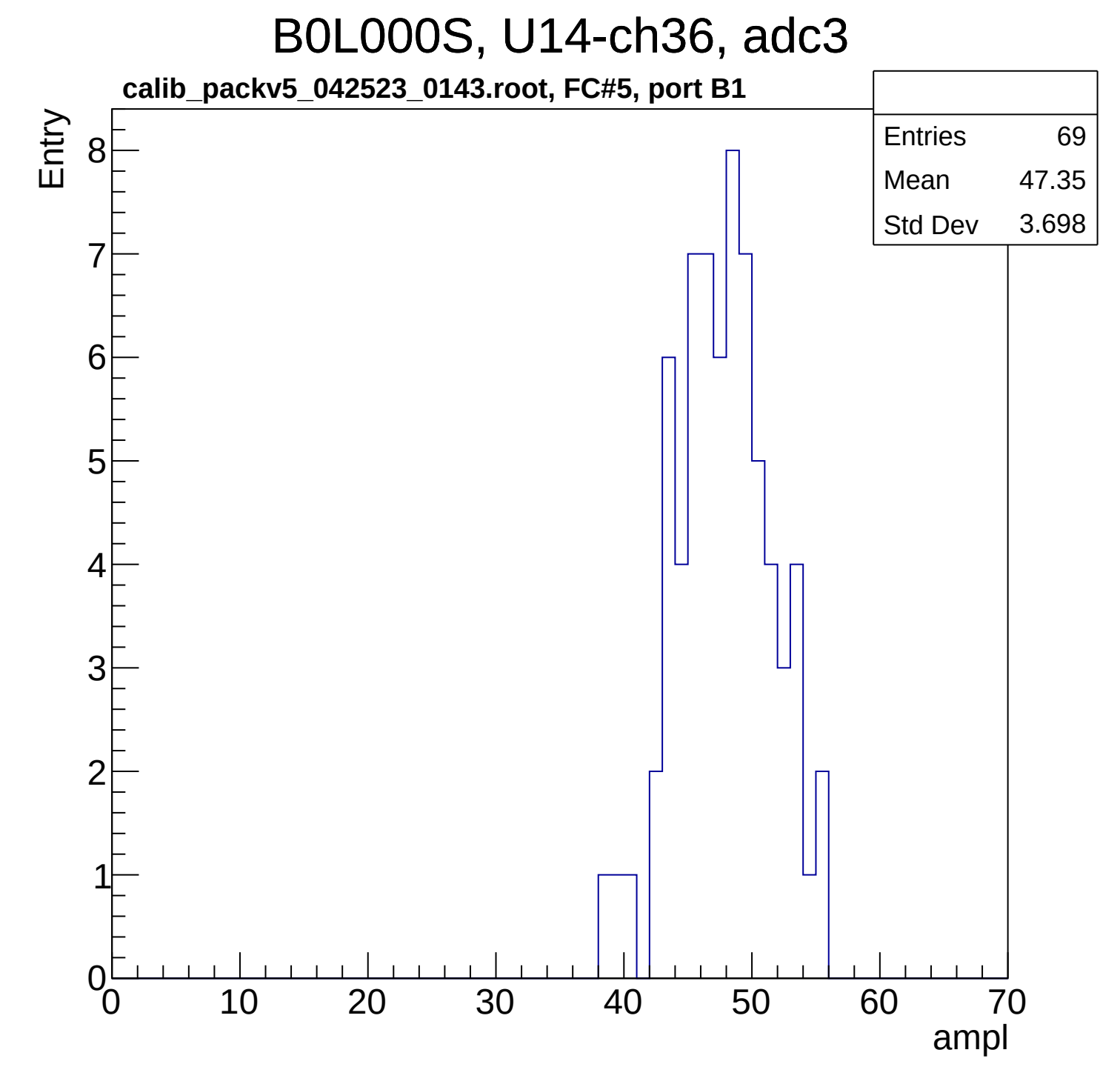
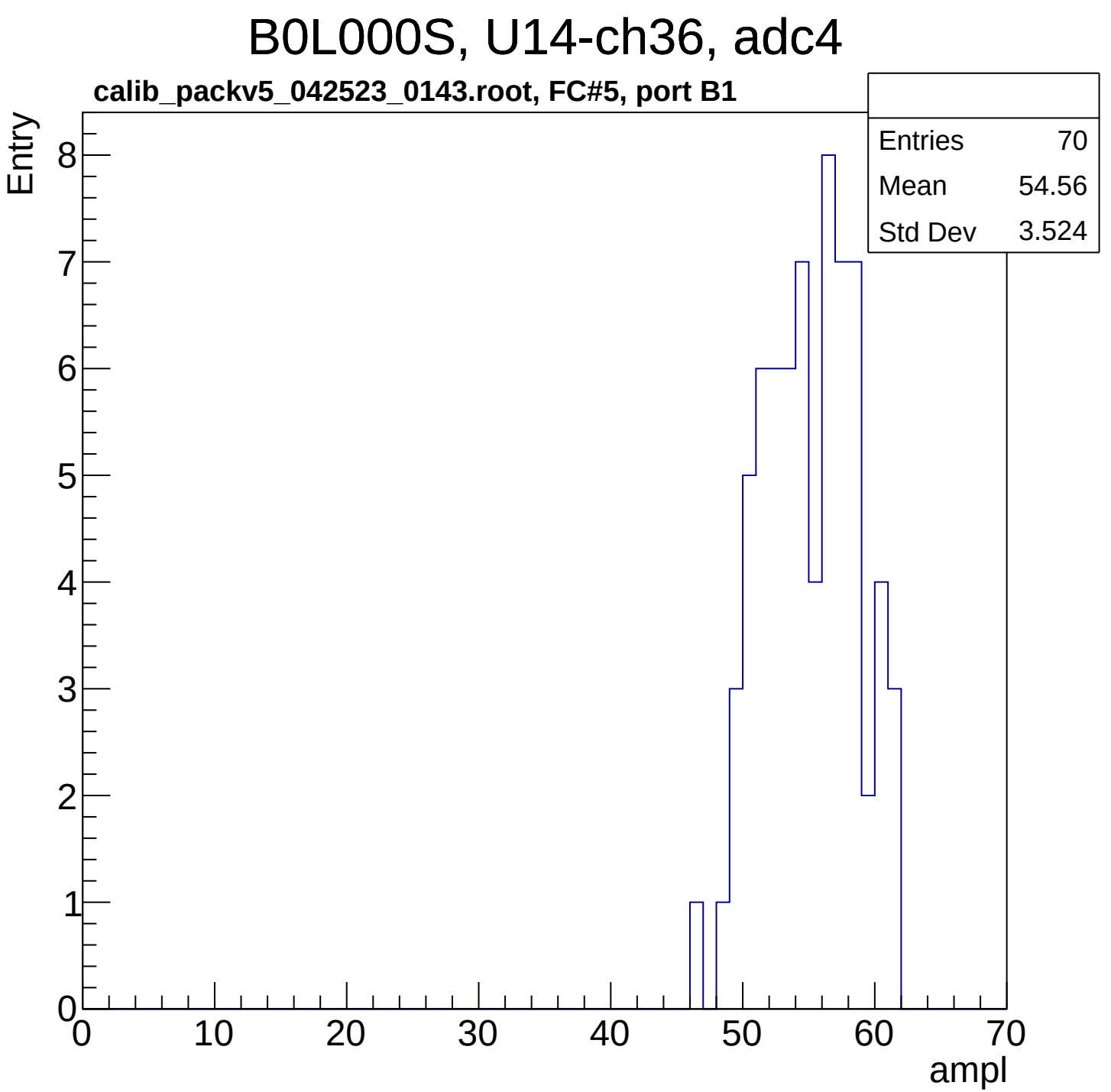

B0L000S, U14-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.35
Std Dev 3.698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 54.56
Std Dev 3.524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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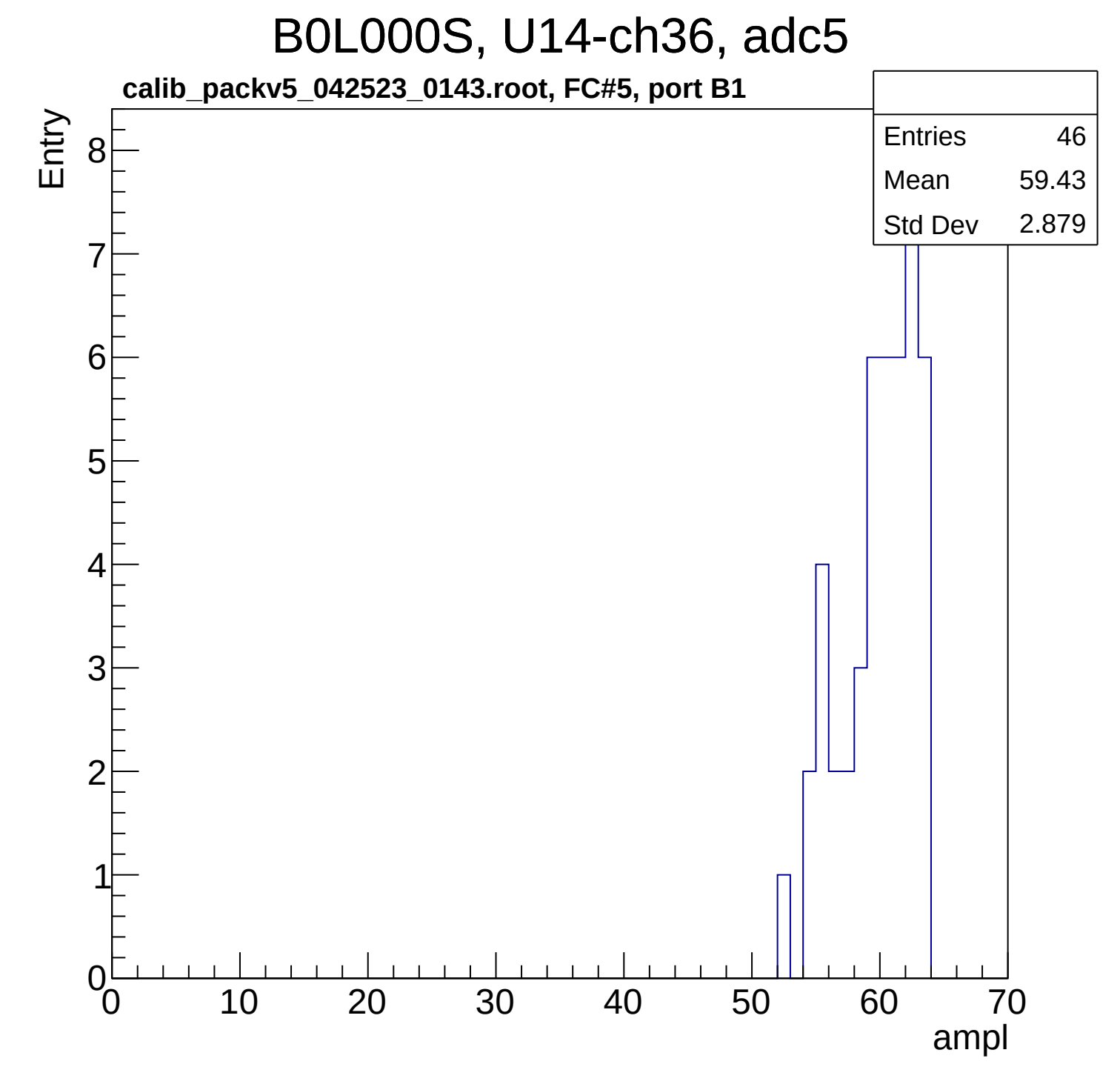
B0L000S, U14-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.43
Std Dev 2.879
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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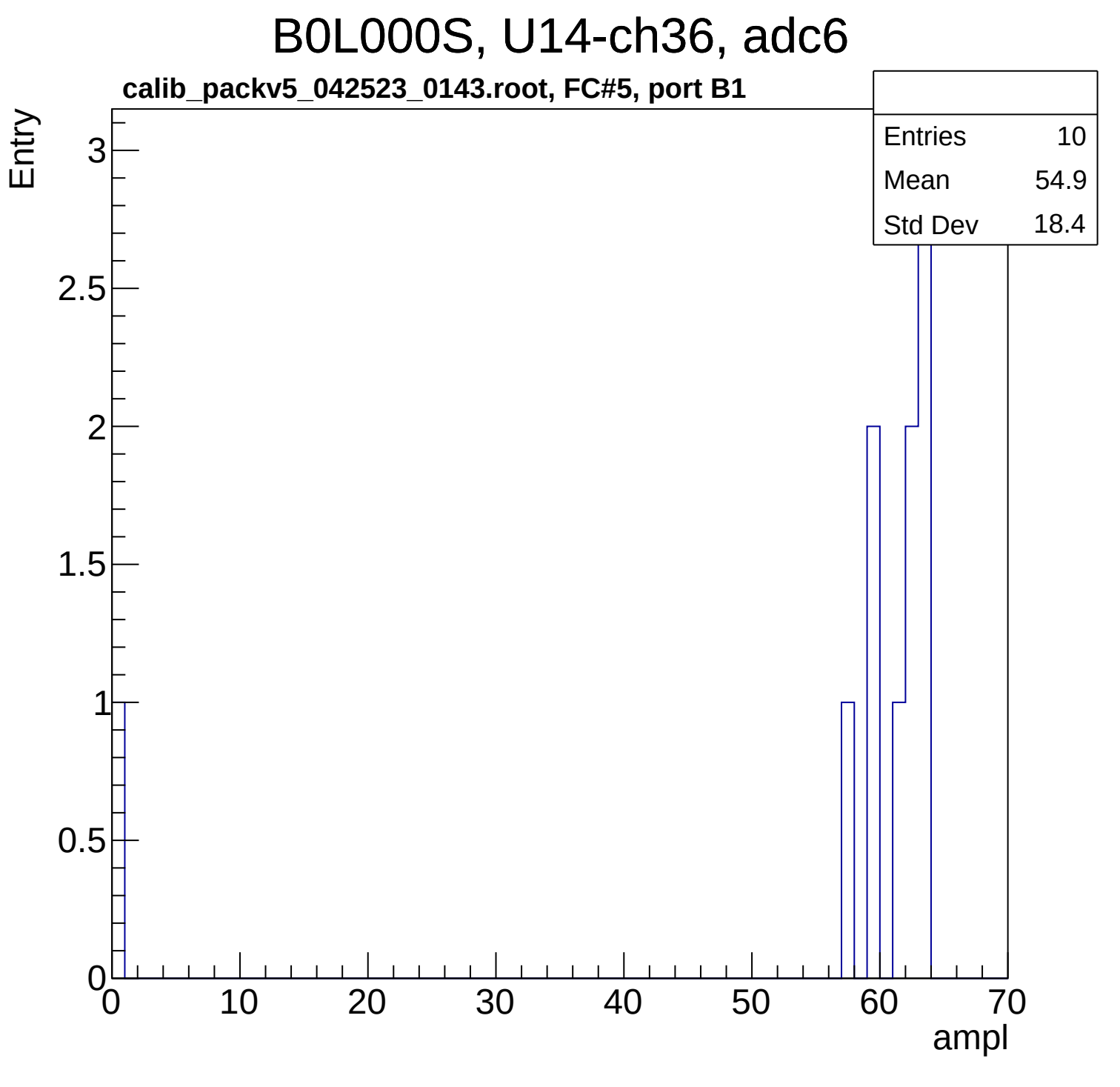
B0L000S, U14-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 54.9
Std Dev 18.4
Entry
ampl
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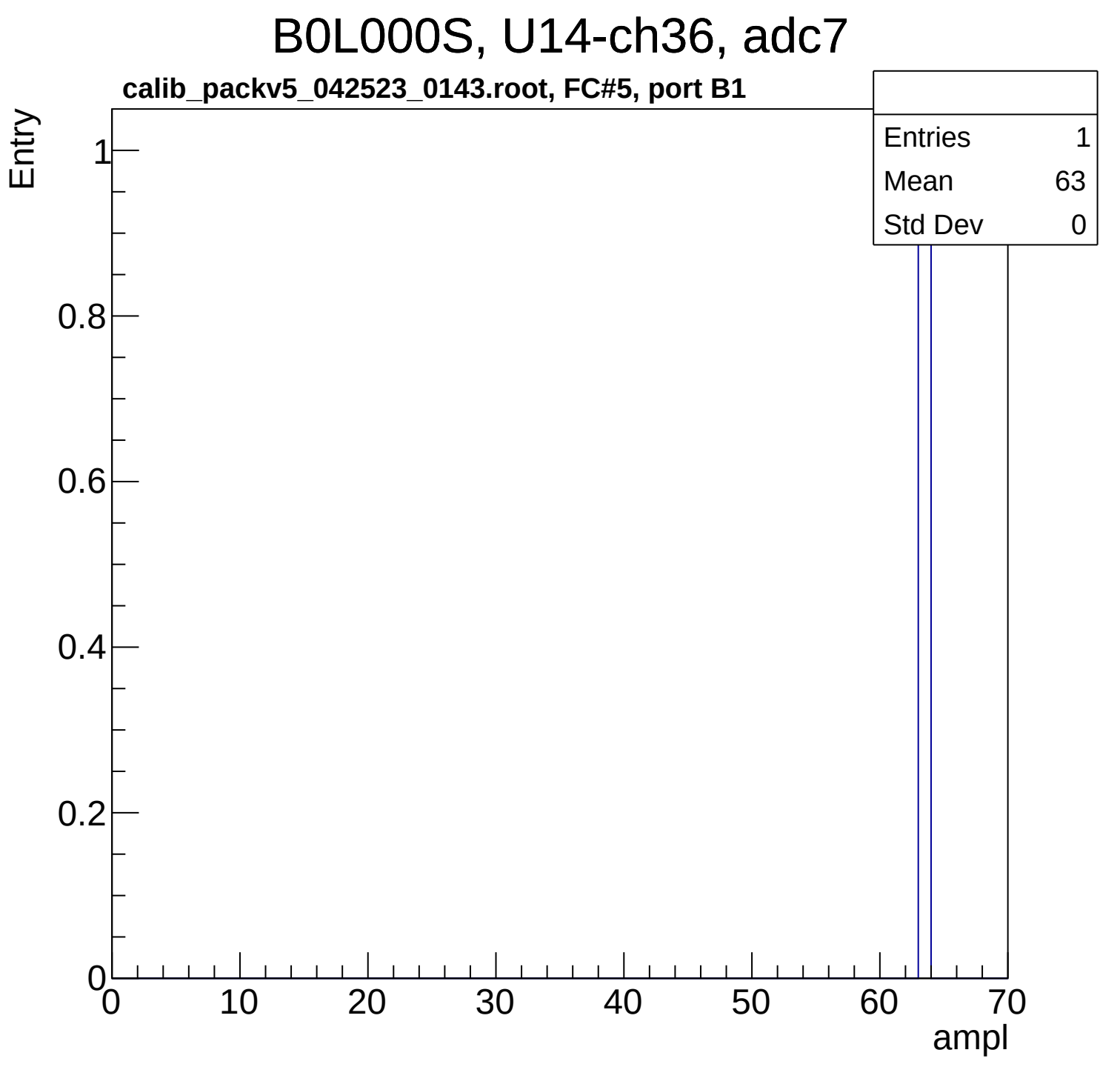
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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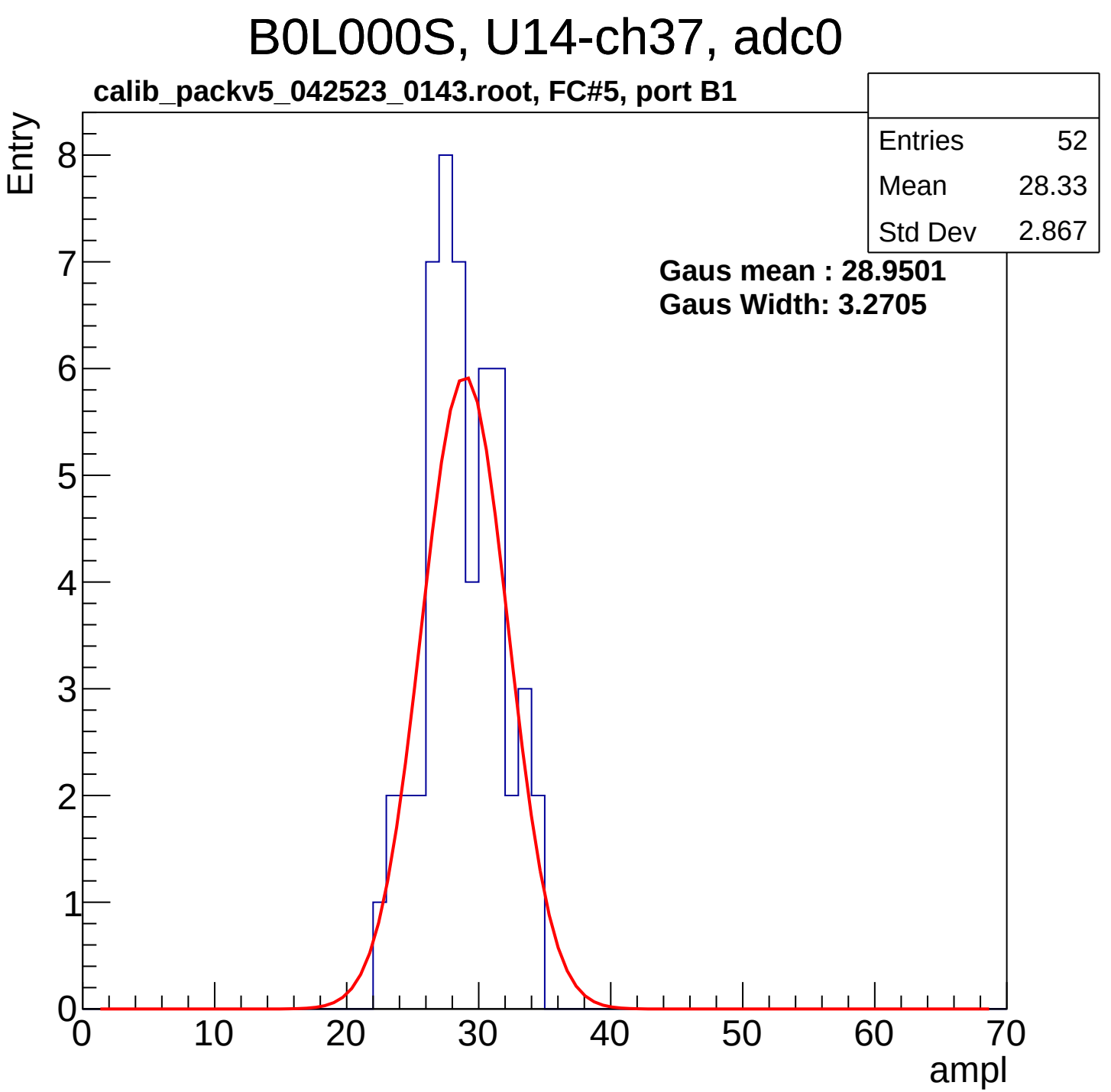
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 28.33
Std Dev 2.867
Gaus mean : 28.9501
Gaus Width: 3.2705
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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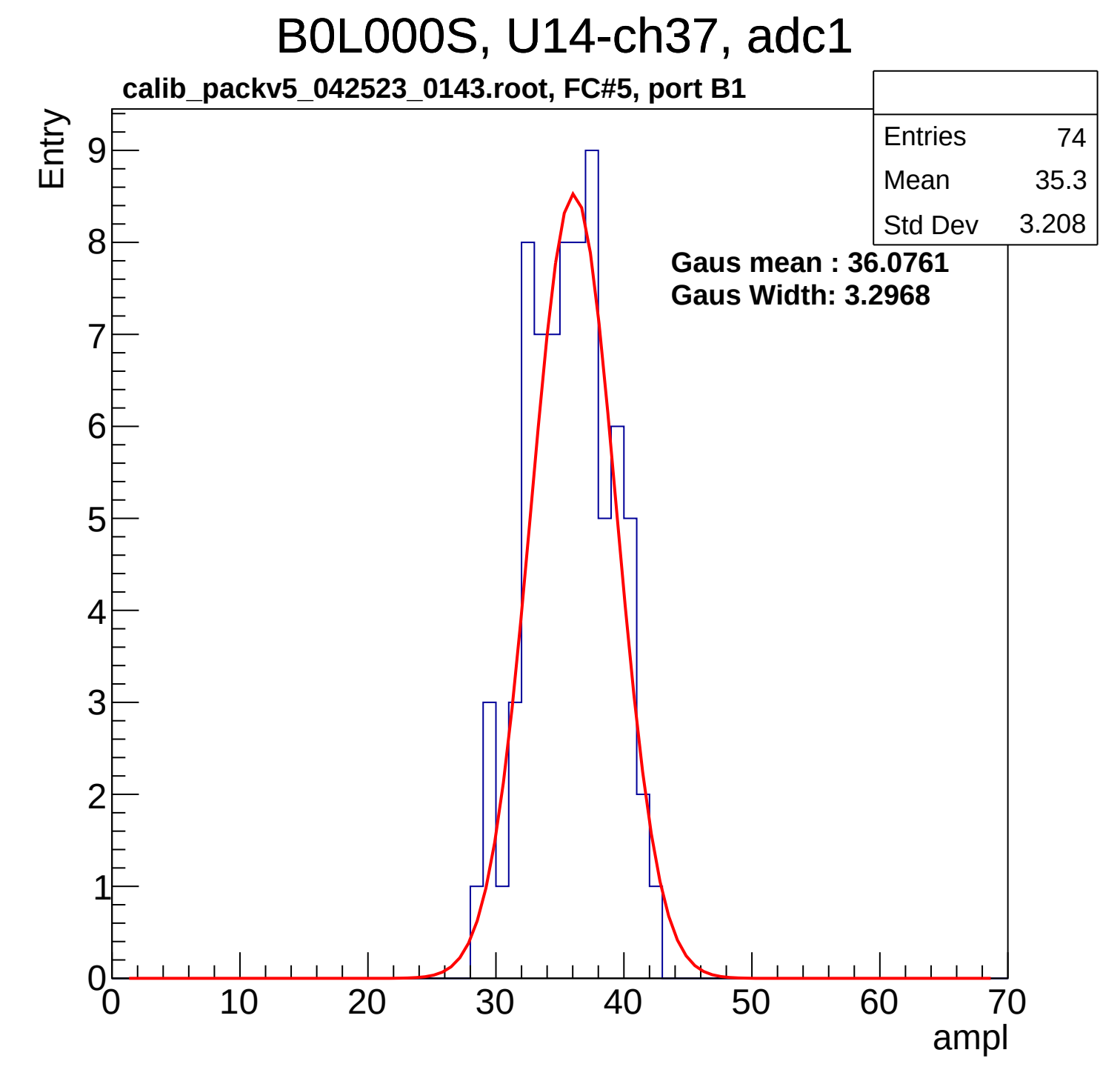
B0L000S, U14-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.3
Std Dev 3.208
Gaus mean : 36.0761
Gaus Width: 3.2968
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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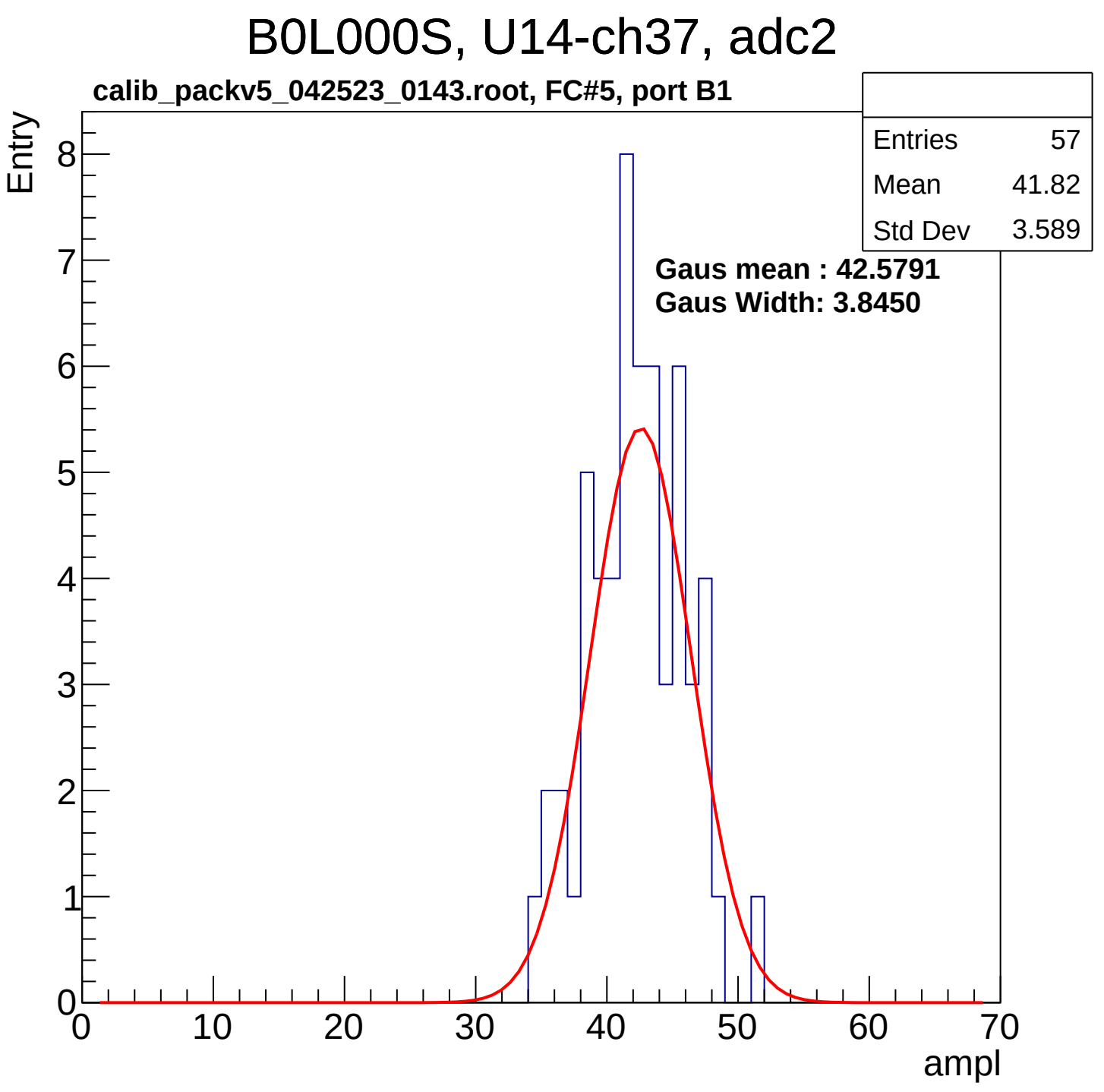
B0L000S, U14-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 41.82
Std Dev 3.589
Gaus mean : 42.5791
Gaus Width: 3.8450
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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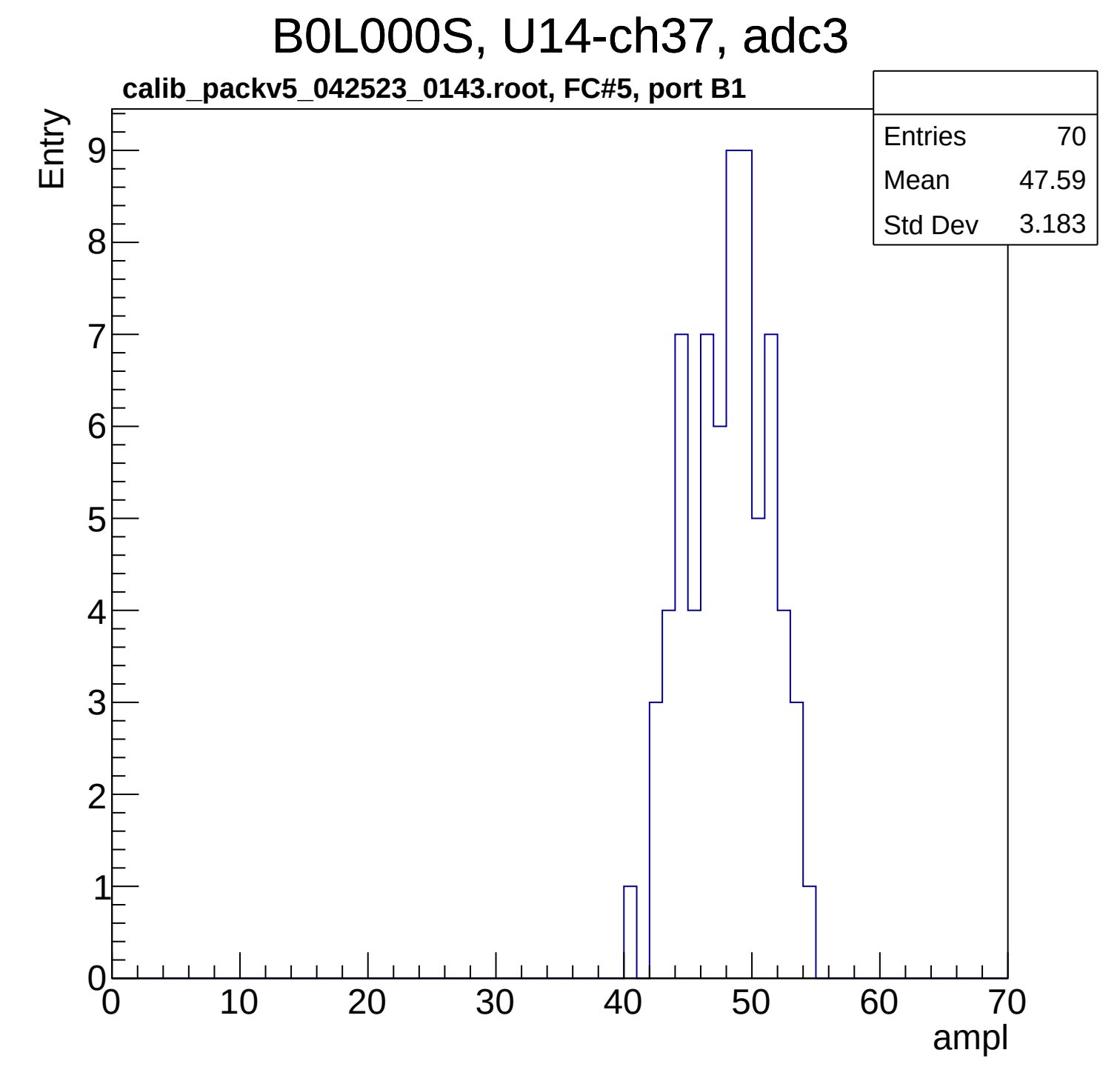
60
70


B0L000S, U14-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 47.59
Std Dev 3.183
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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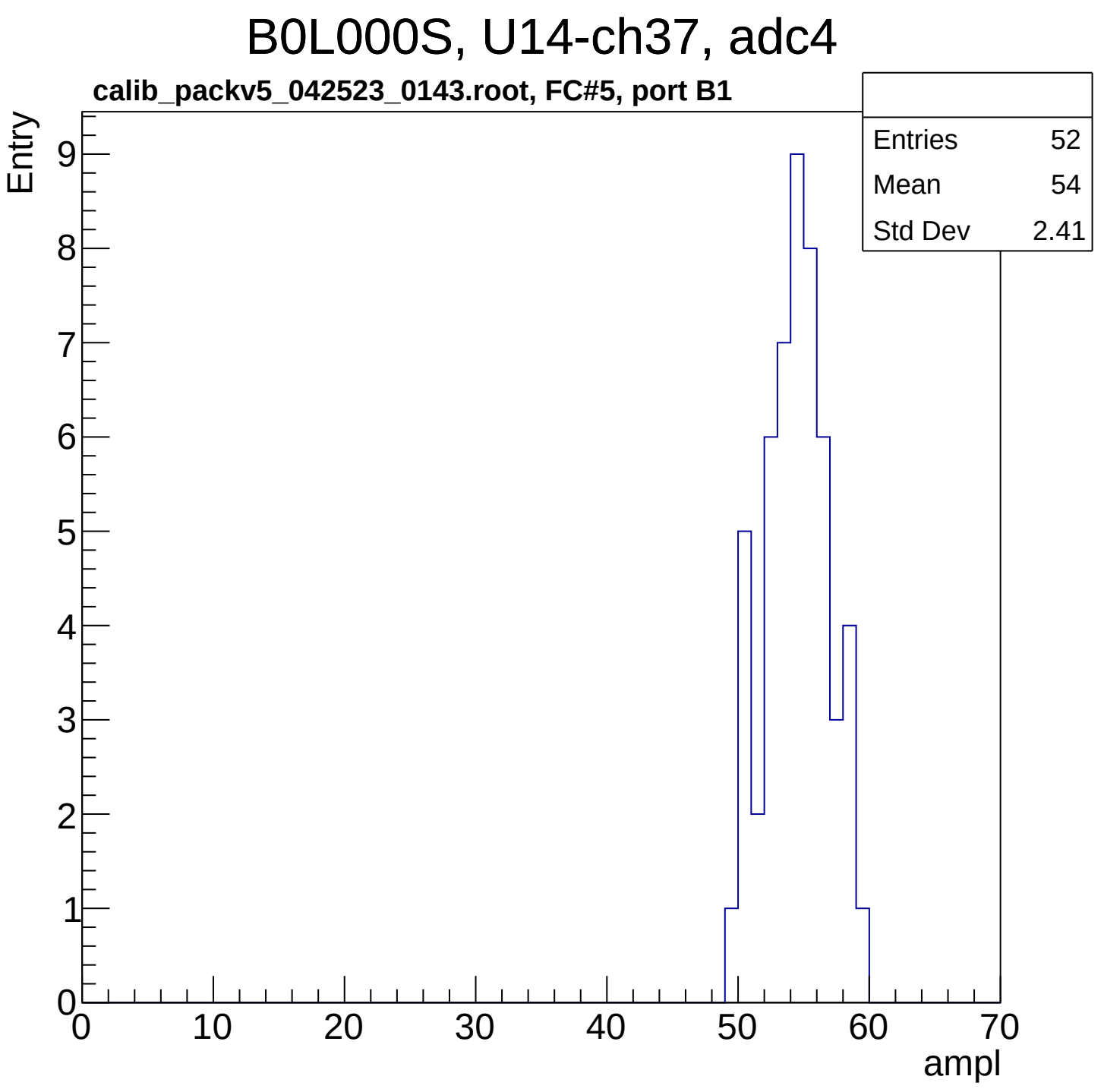
B0L000S, U14-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54
Std Dev 2.41
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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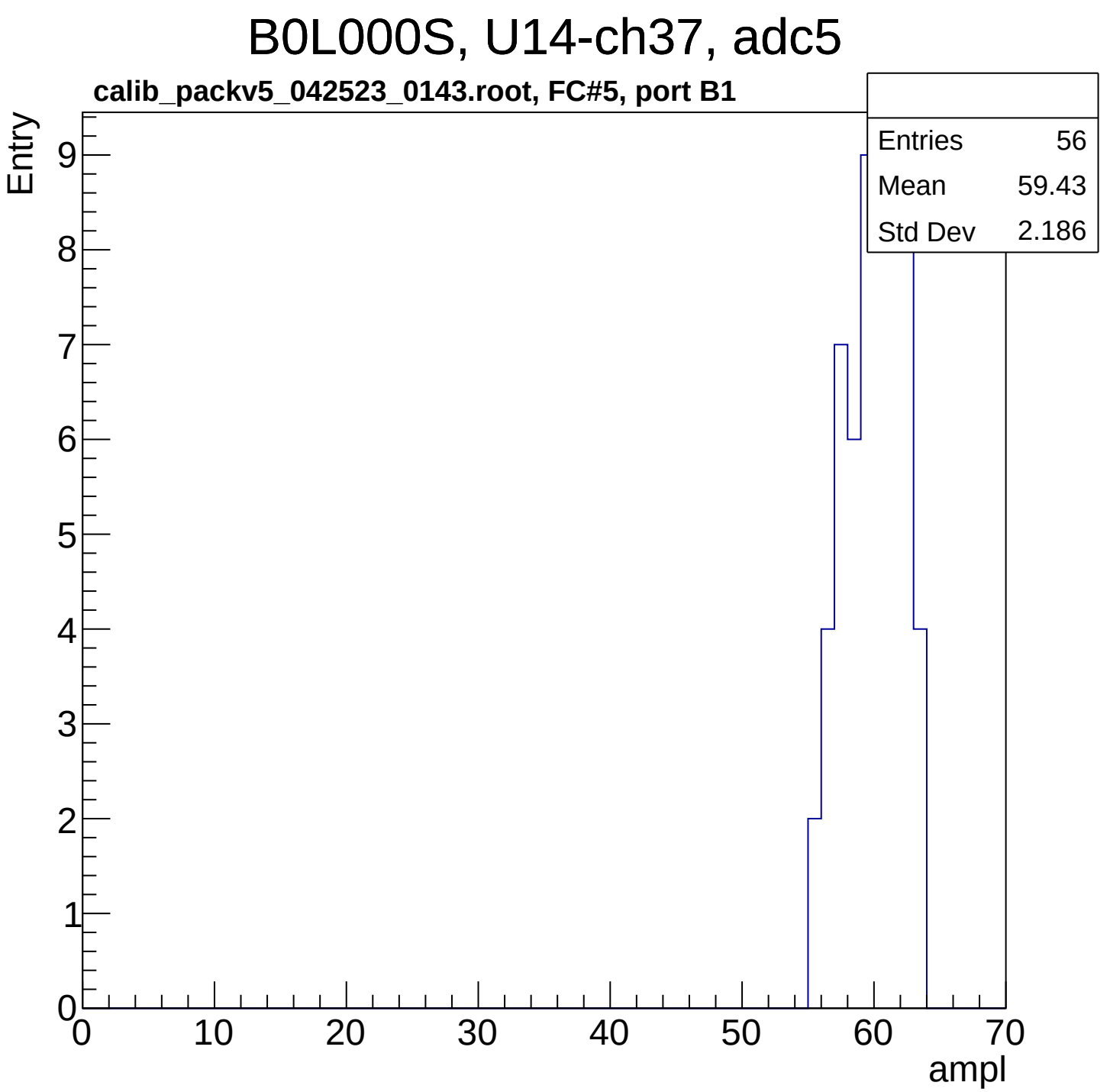
B0L000S, U14-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.43
Std Dev 2.186
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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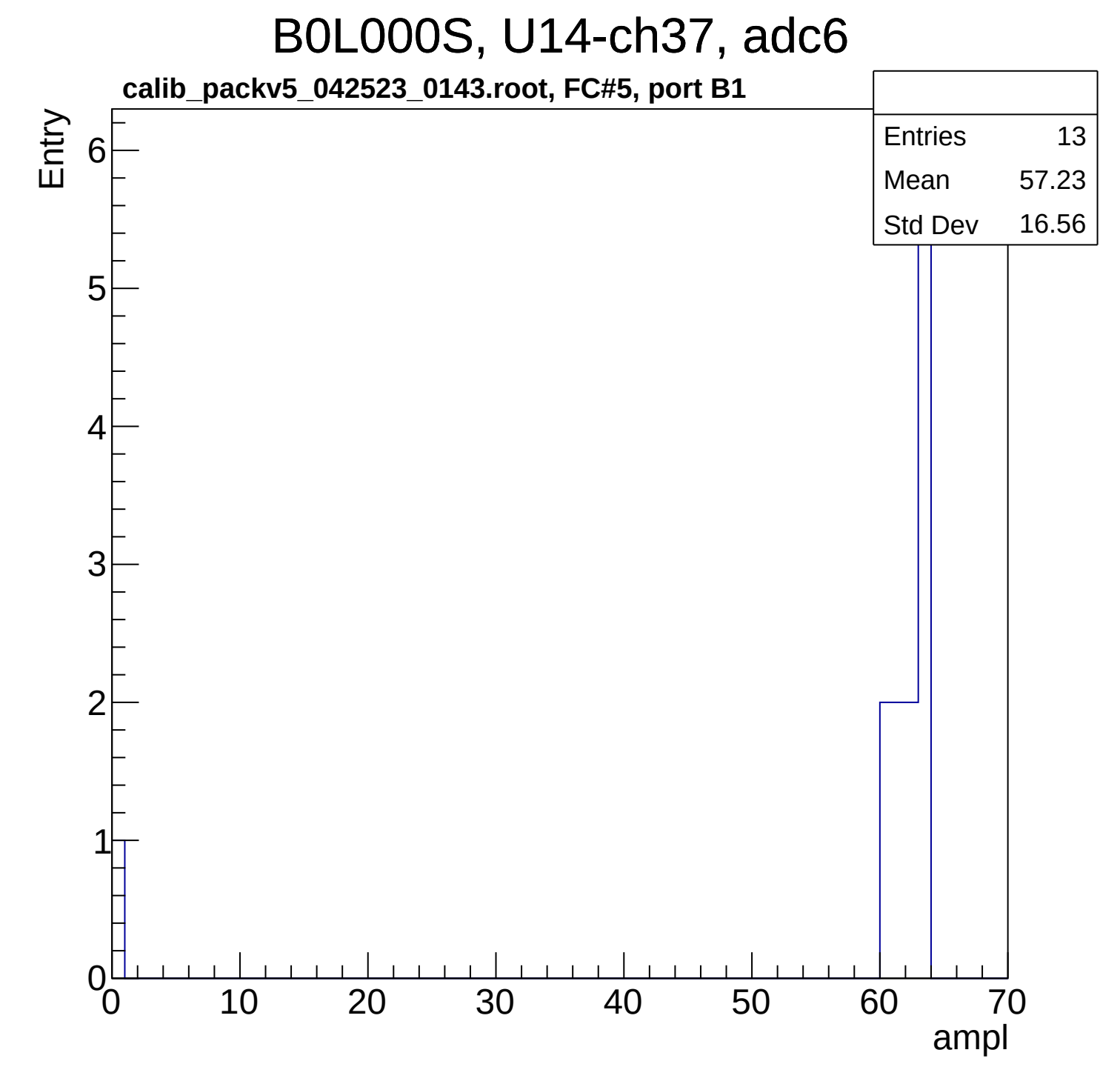
B0L000S, U14-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.23
Std Dev 16.56
Entry
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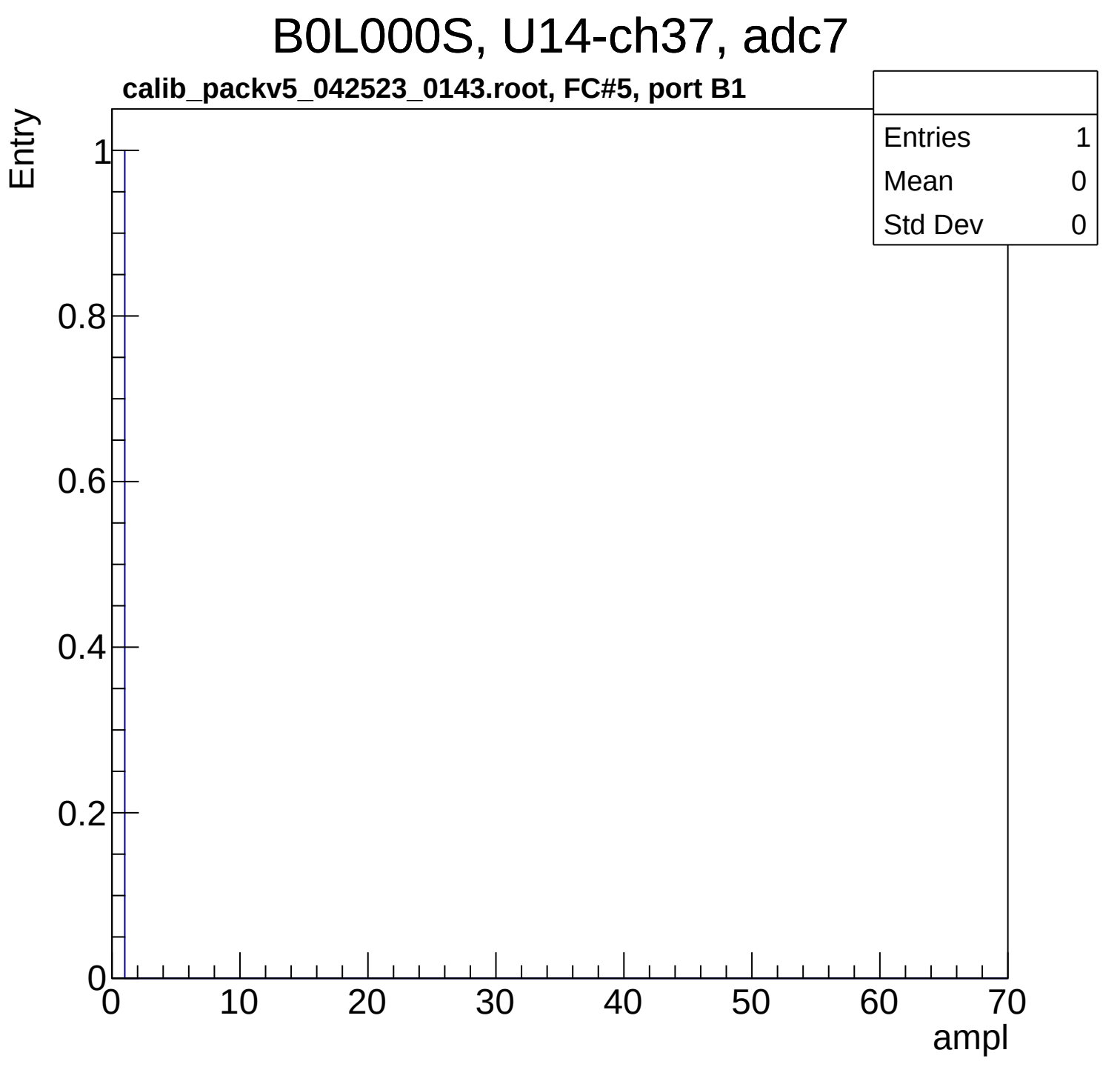
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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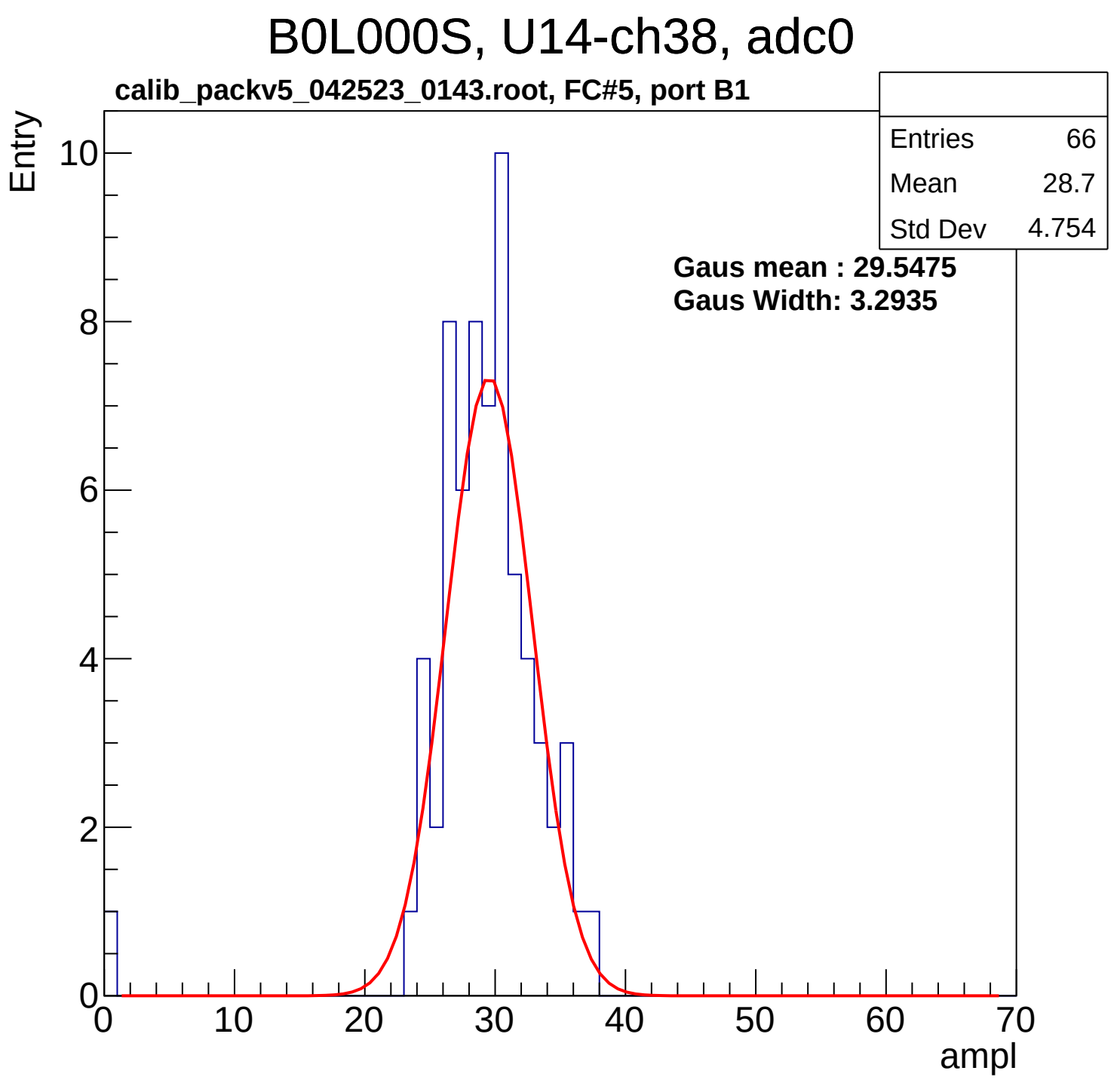
50
60
70
ampl


B0L000S, U14-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.7
Std Dev 4.754
Gaus mean : 29.5475
Gaus Width: 3.2935
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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60
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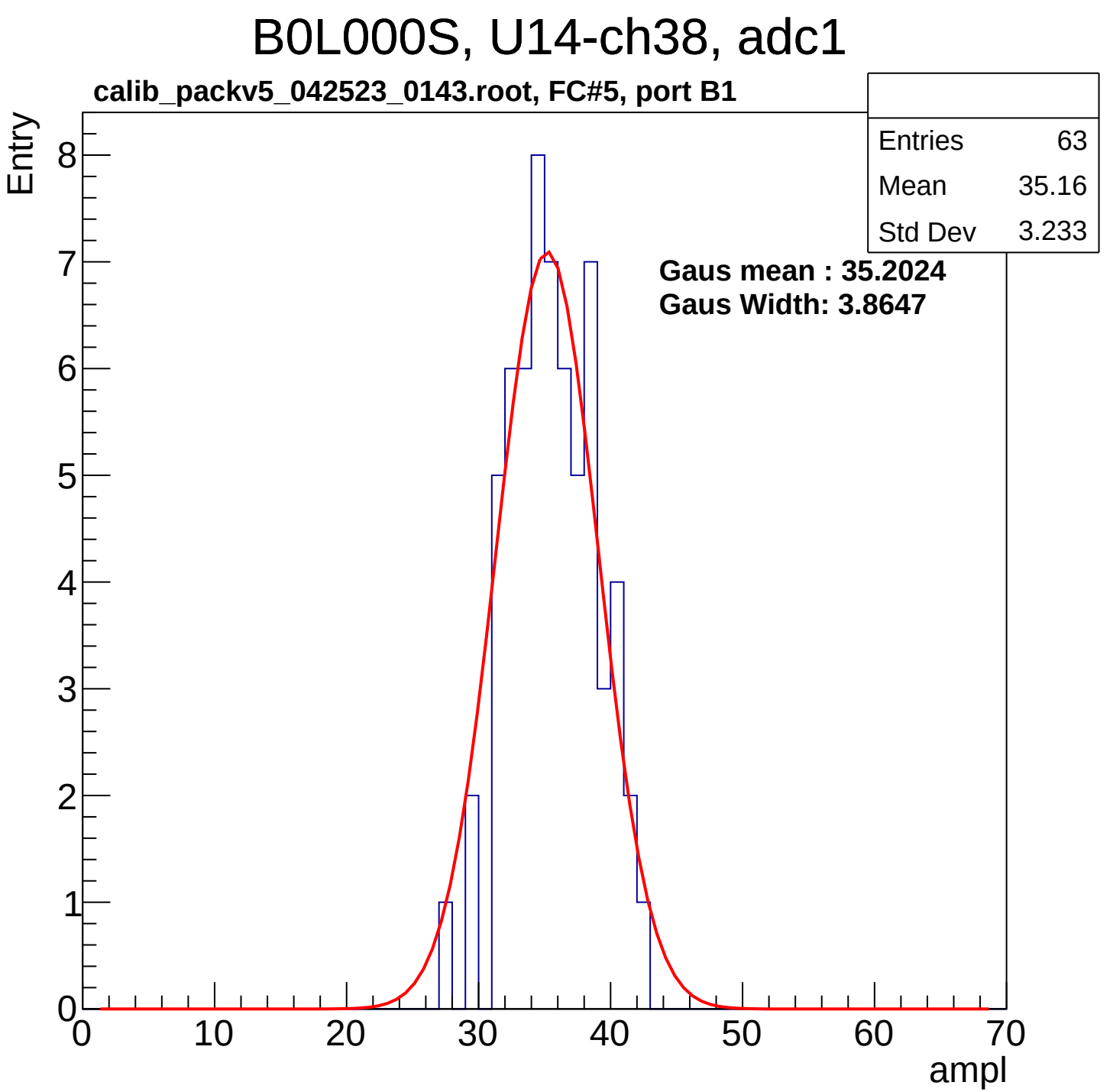
B0L000S, U14-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.16
Std Dev 3.233
Gaus mean : 35.2024
Gaus Width: 3.8647
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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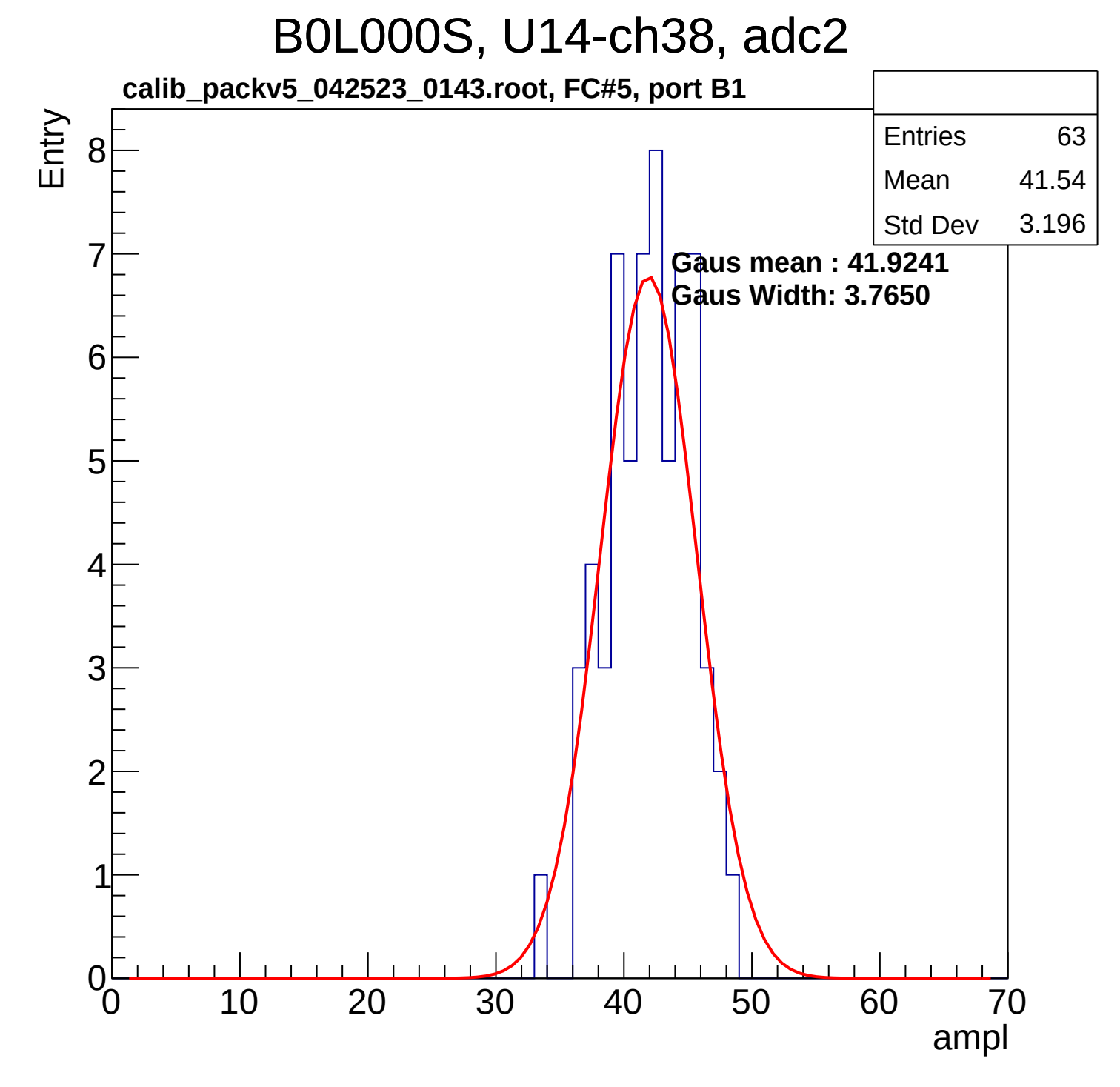
B0L000S, U14-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.54
Std Dev 3.196
Gaus mean : 41.9241
Gaus Width: 3.7650
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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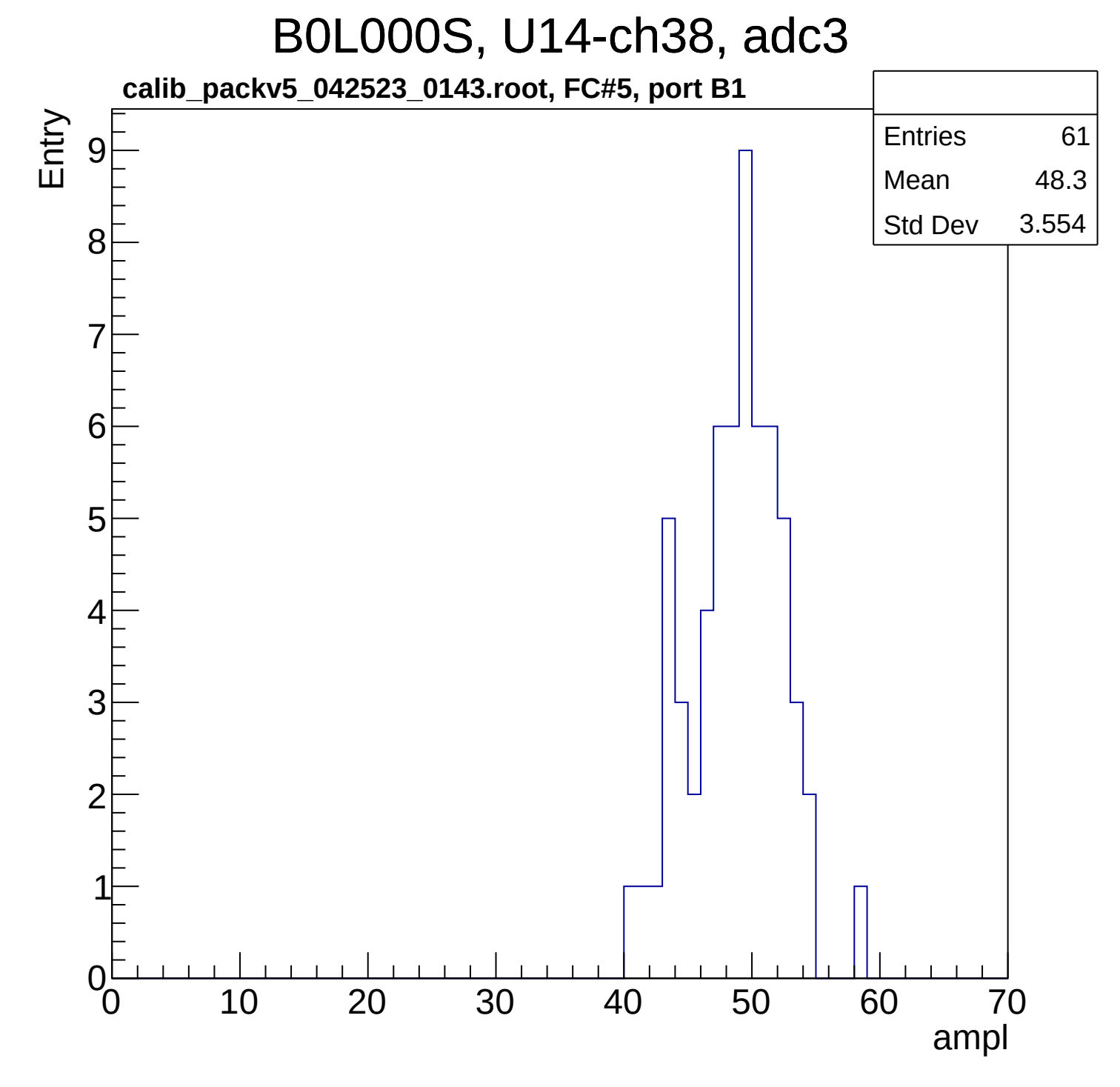
B0L000S, U14-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.3
Std Dev 3.554
Entry
ampl
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3
4
5
6
7
8
9
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10
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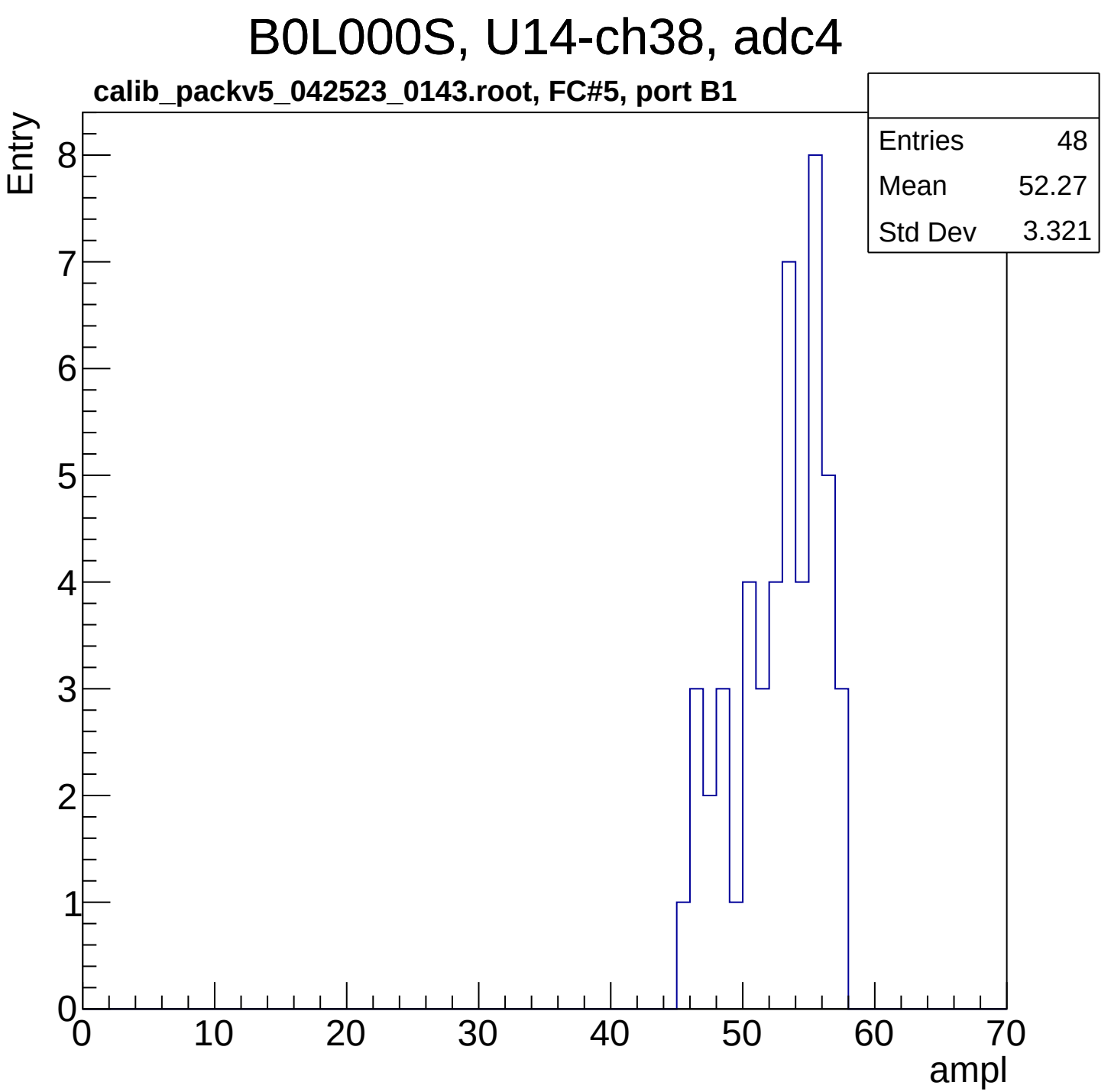
B0L000S, U14-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 52.27
Std Dev 3.321
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
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60
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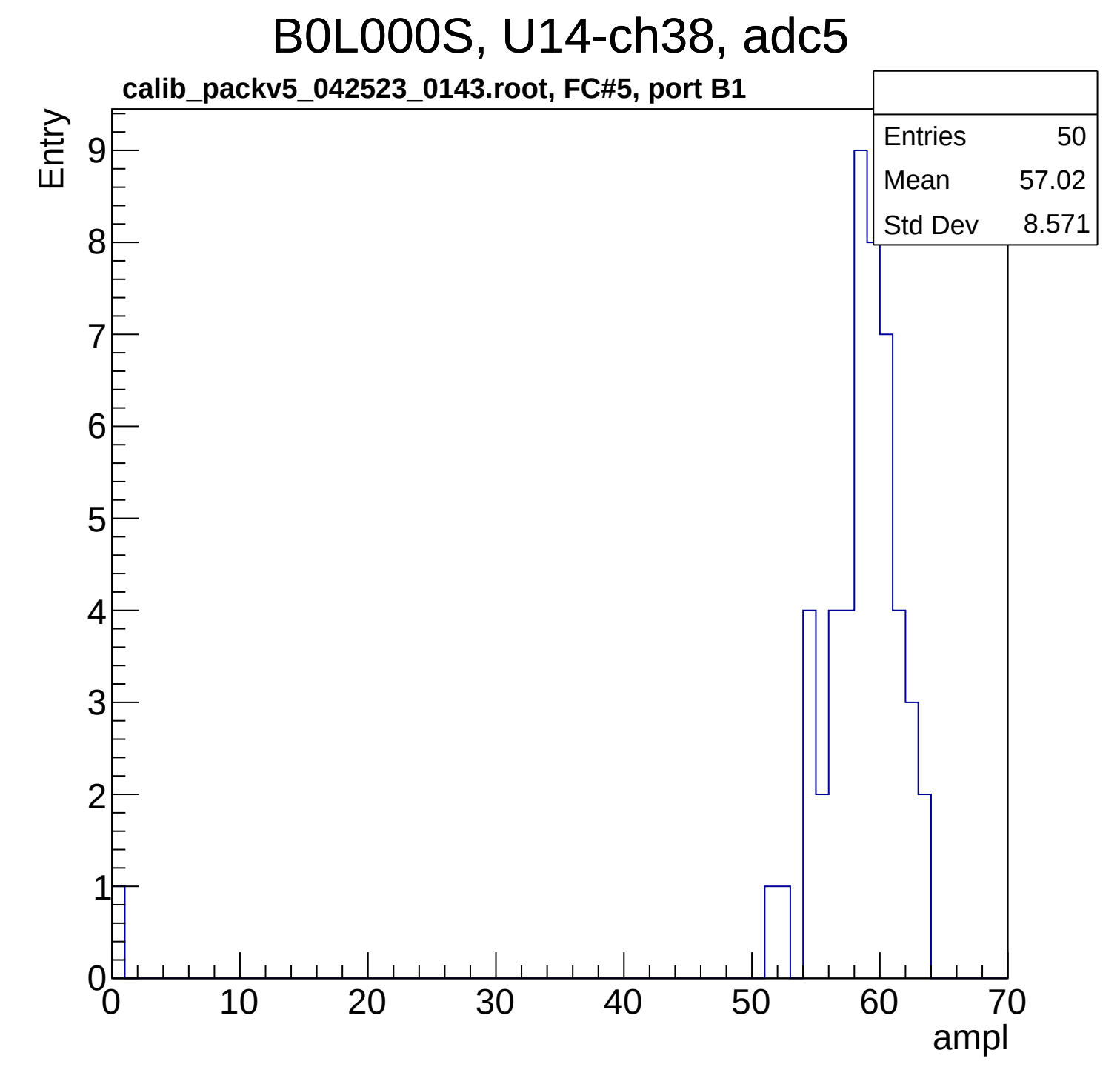
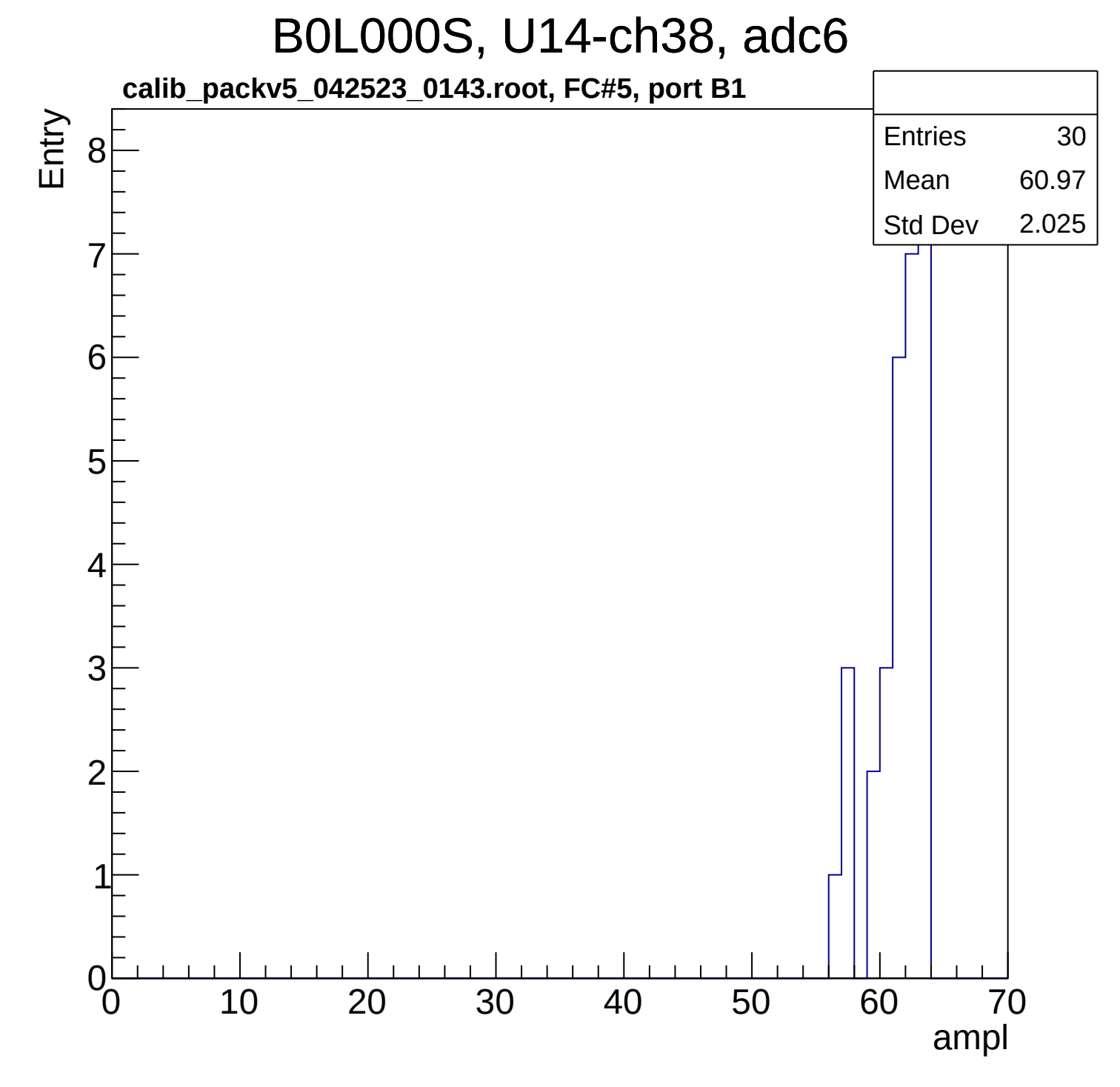

B0L000S, U14-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.02
Std Dev 8.571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.97
Std Dev 2.025
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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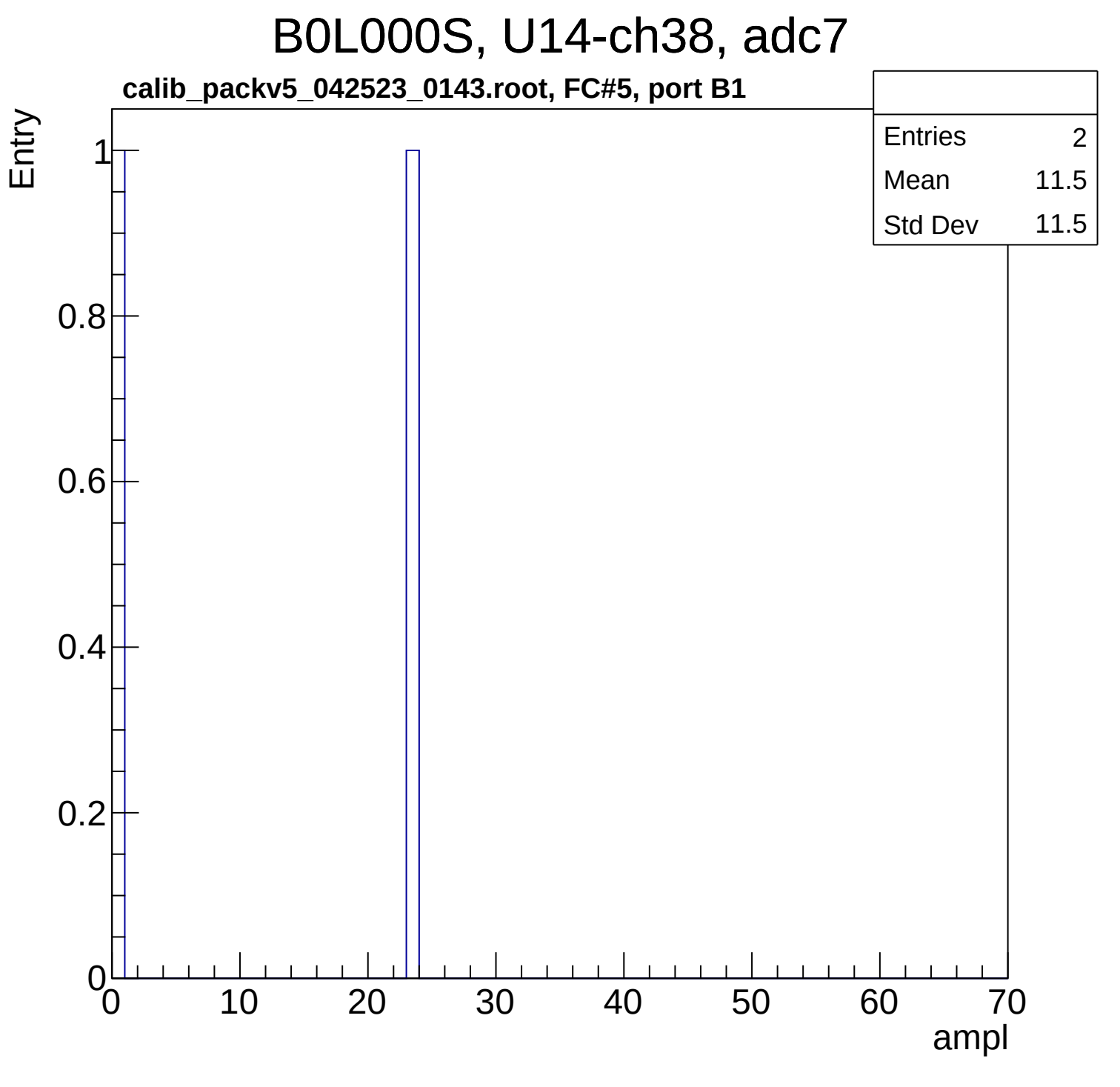
B0L000S, U14-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.8
0.6
0.4
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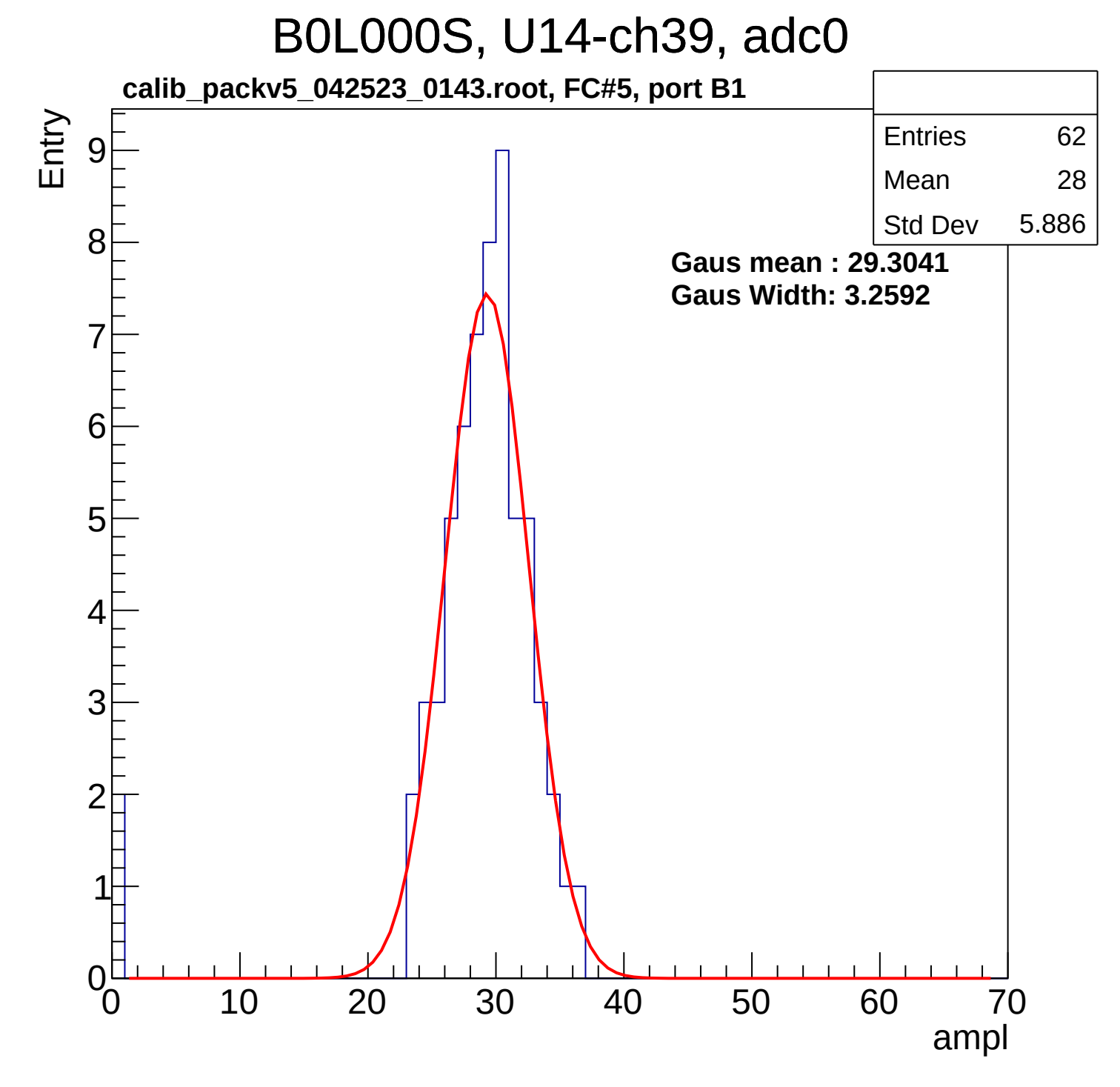
70
ampl


B0L000S, U14-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 28
Std Dev 5.886
Gaus mean : 29.3041
Gaus Width: 3.2592
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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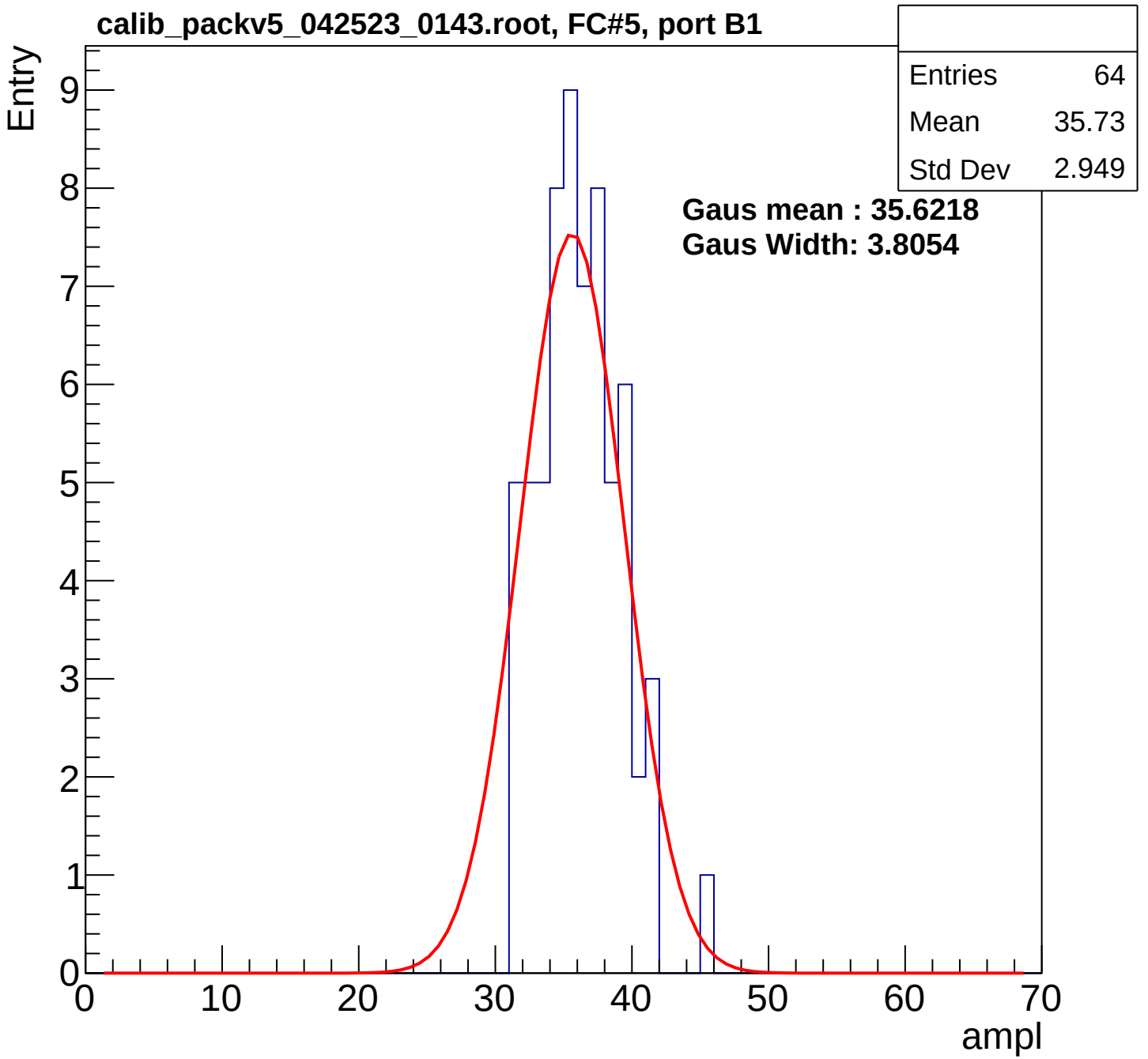
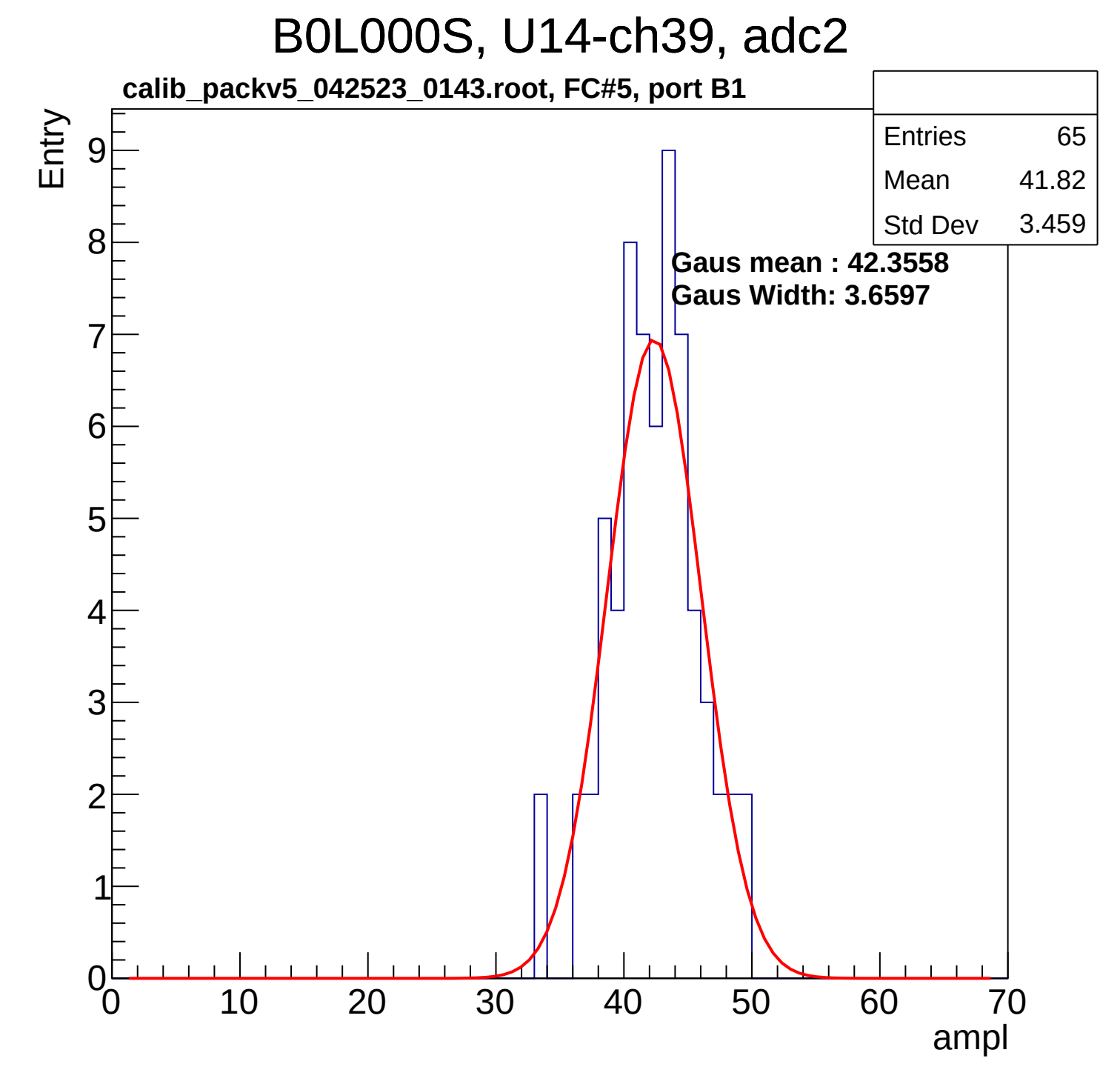

B0L000S, U14-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.73
Std Dev 2.949
Gaus mean : 35.6218
Gaus Width: 3.8054
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.82
Std Dev 3.459
Gaus mean : 42.3558
Gaus Width: 3.6597
Entry
ampl
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3
4
5
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7
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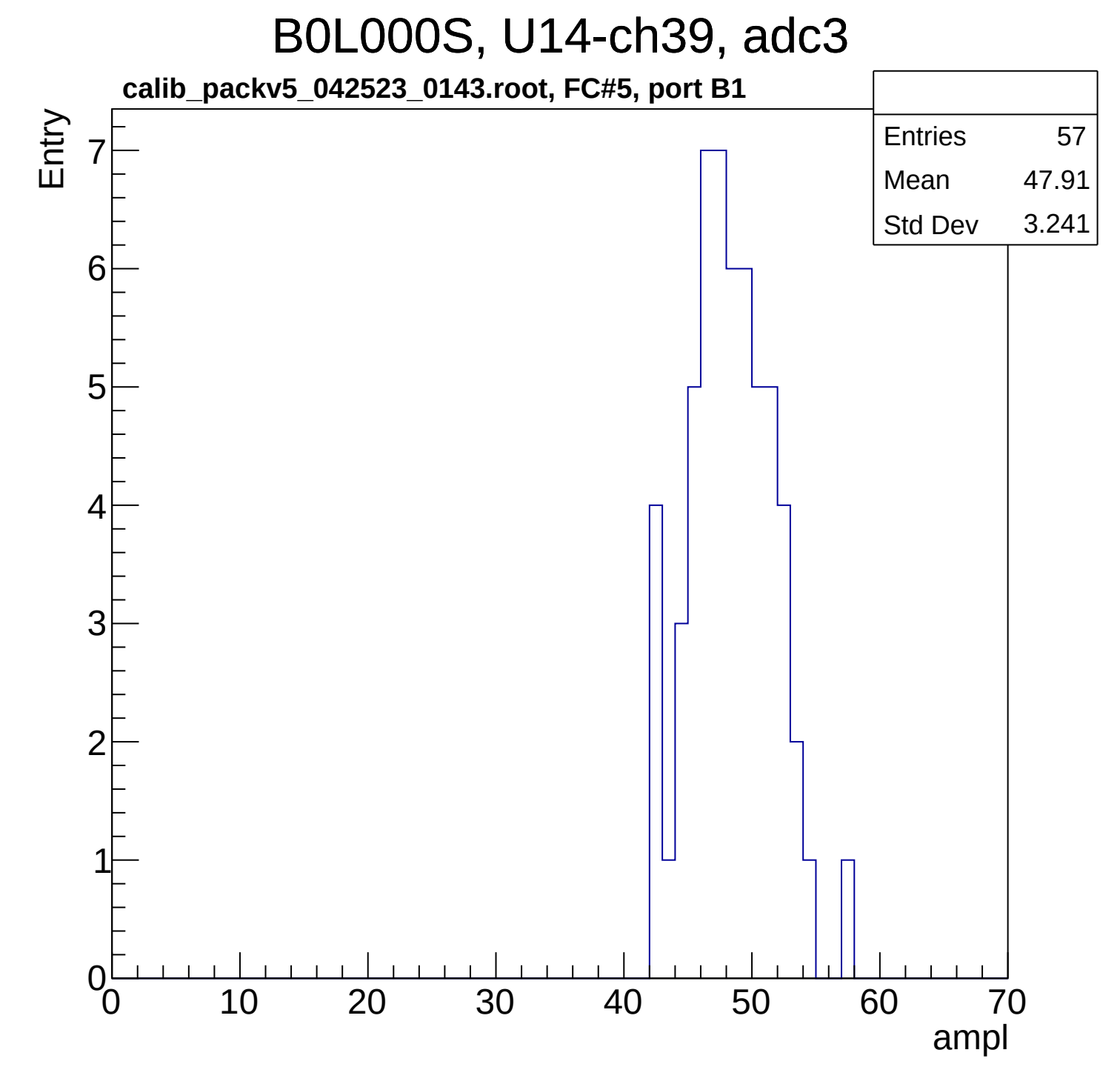
B0L000S, U14-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 47.91
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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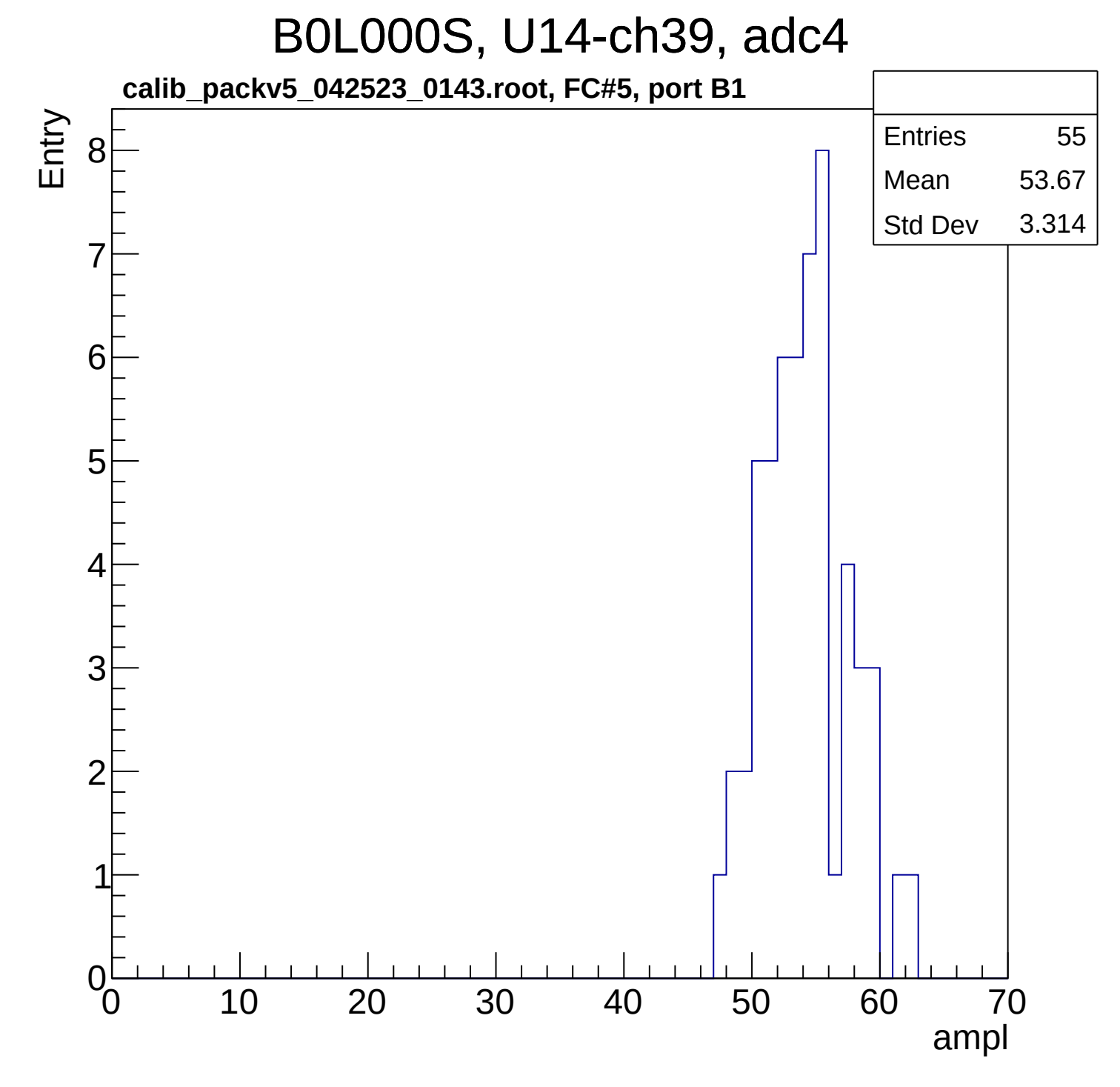
B0L000S, U14-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.67
Std Dev 3.314
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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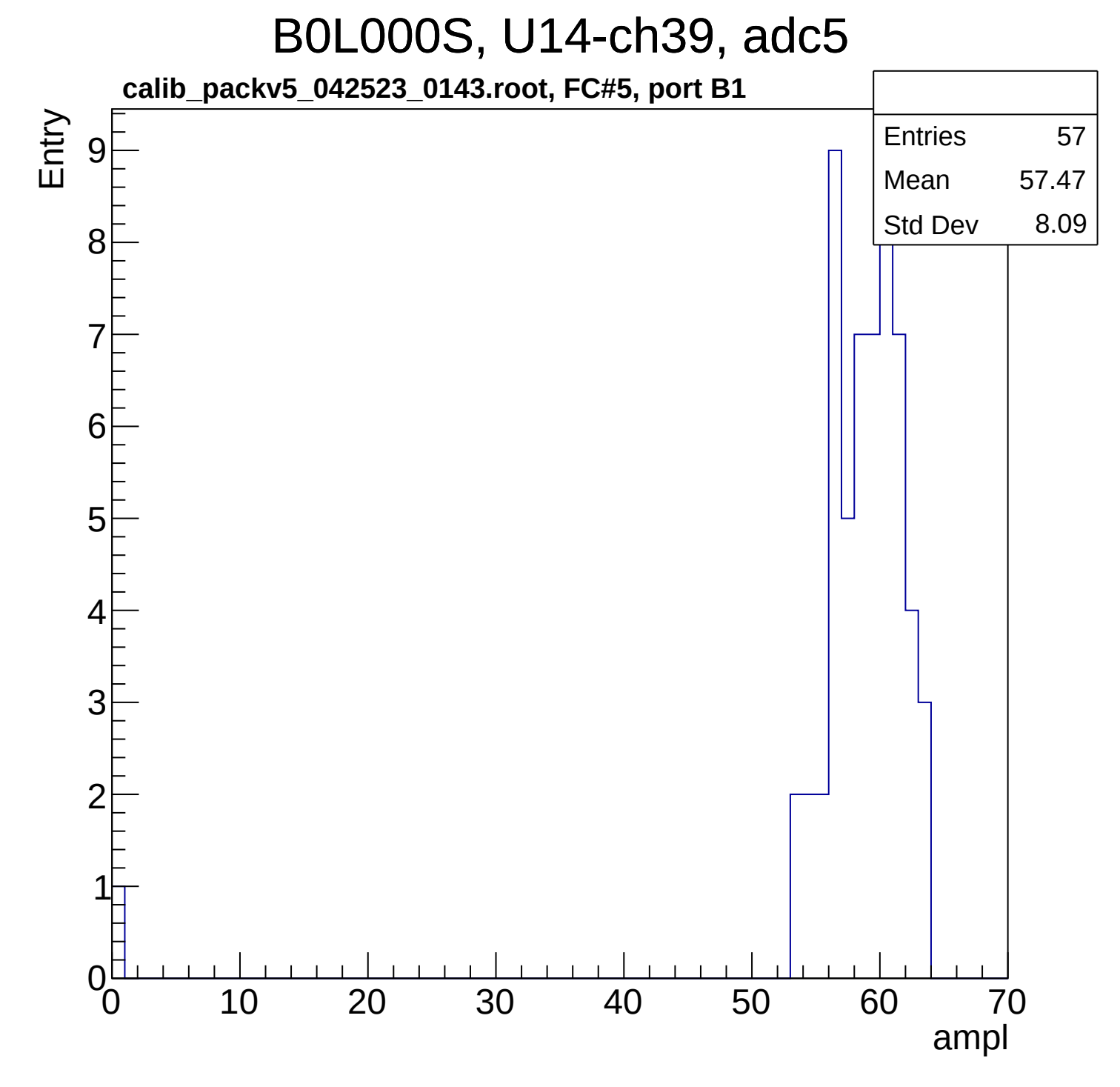
B0L000S, U14-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.47
Std Dev 8.09
Entry
ampl
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2
3
4
5
6
7
8
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0
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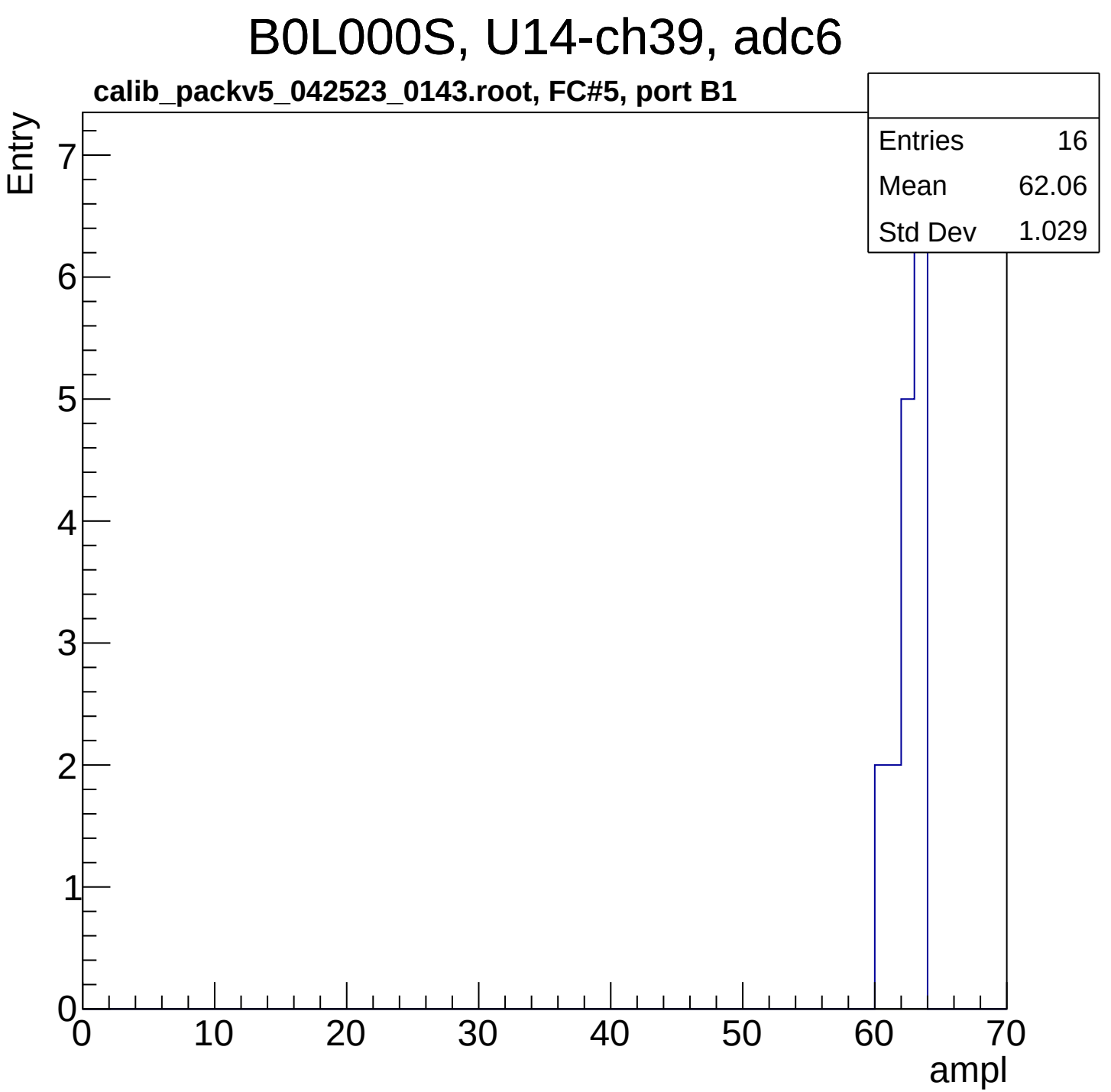
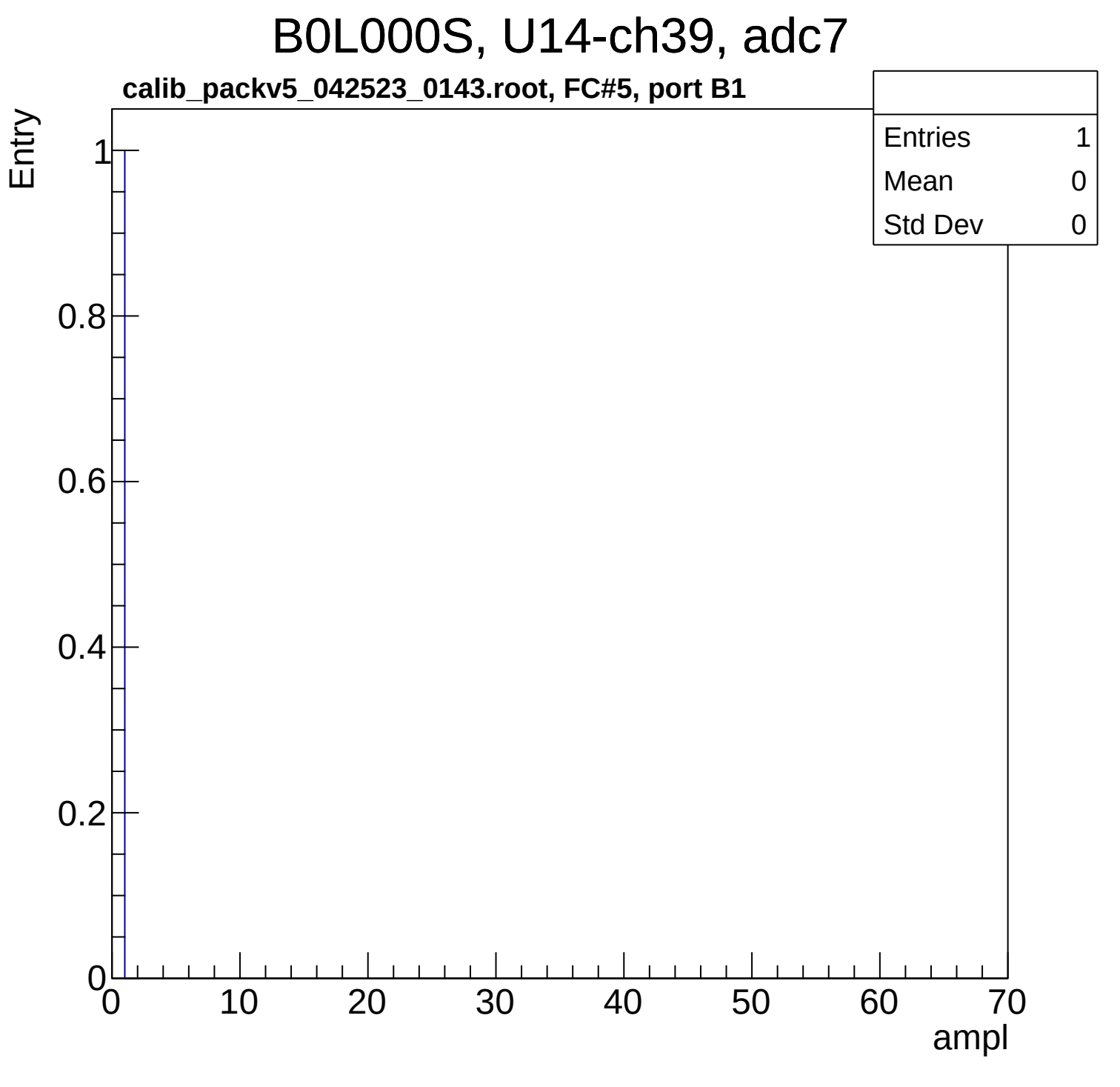
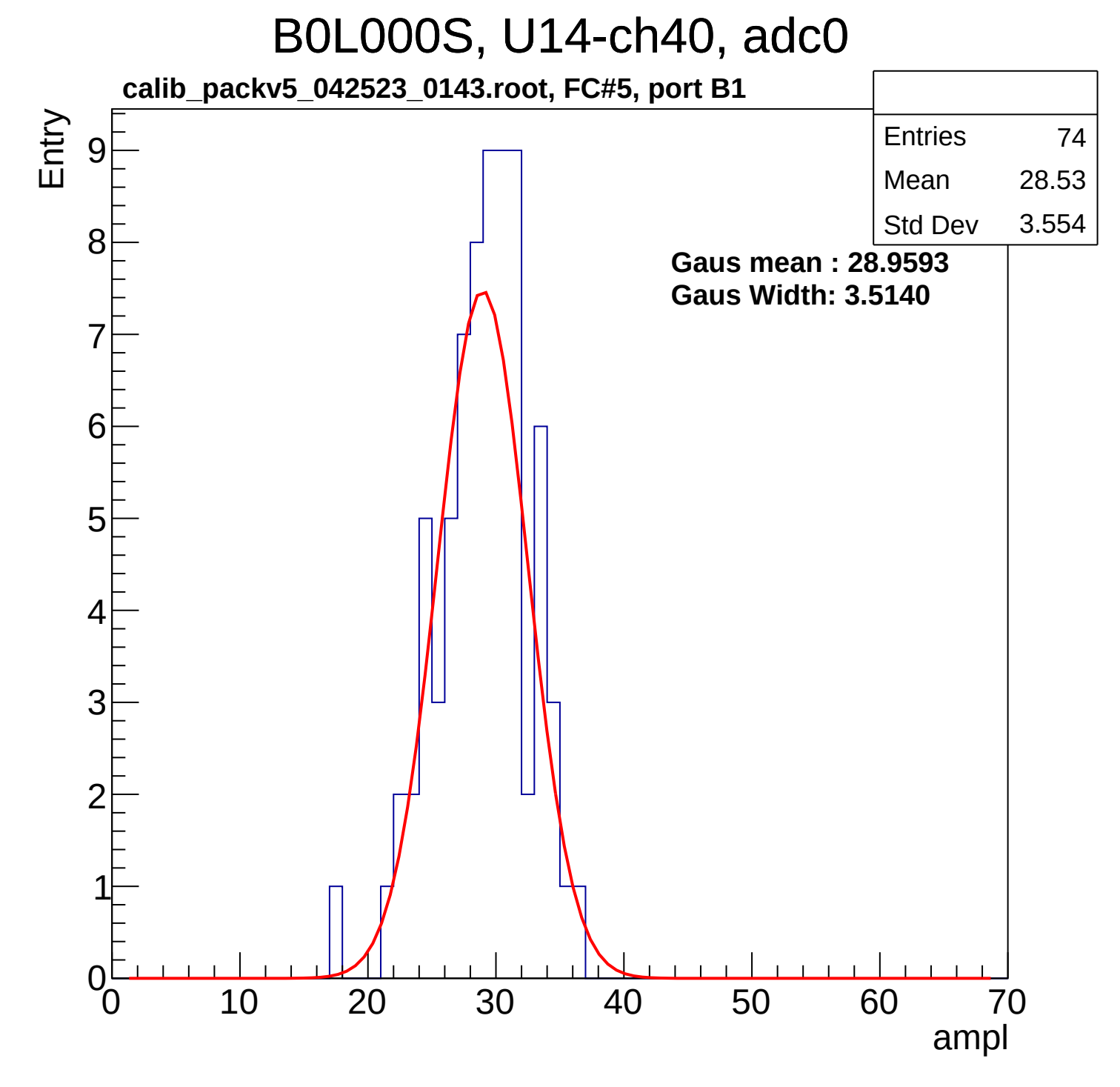

B0L000S, U14-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 62.06
Std Dev 1.029
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.53
Std Dev 3.554
Gaus mean : 28.9593
Gaus Width: 3.5140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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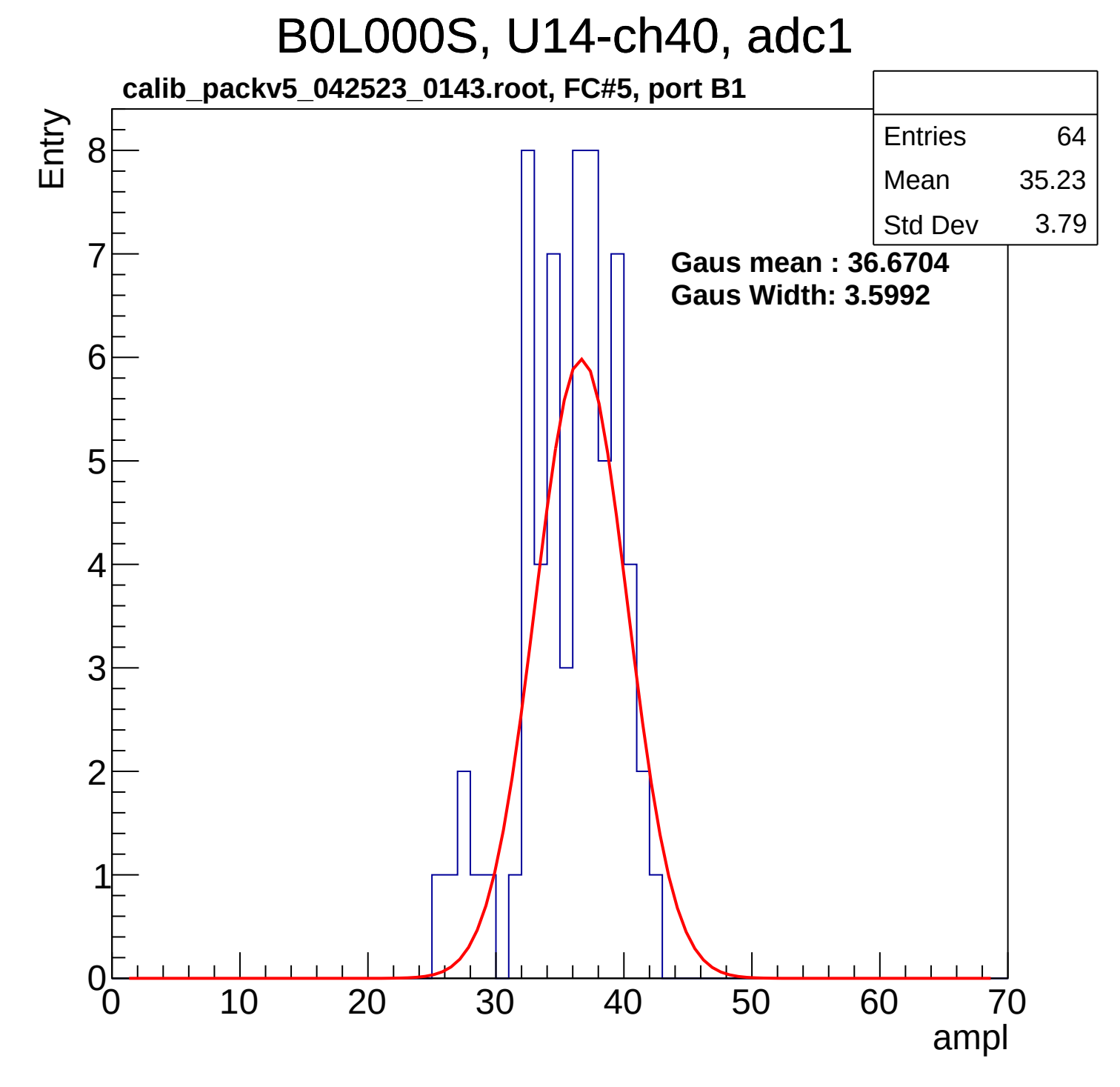
B0L000S, U14-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.23
Std Dev 3.79
Gaus mean : 36.6704
Gaus Width: 3.5992
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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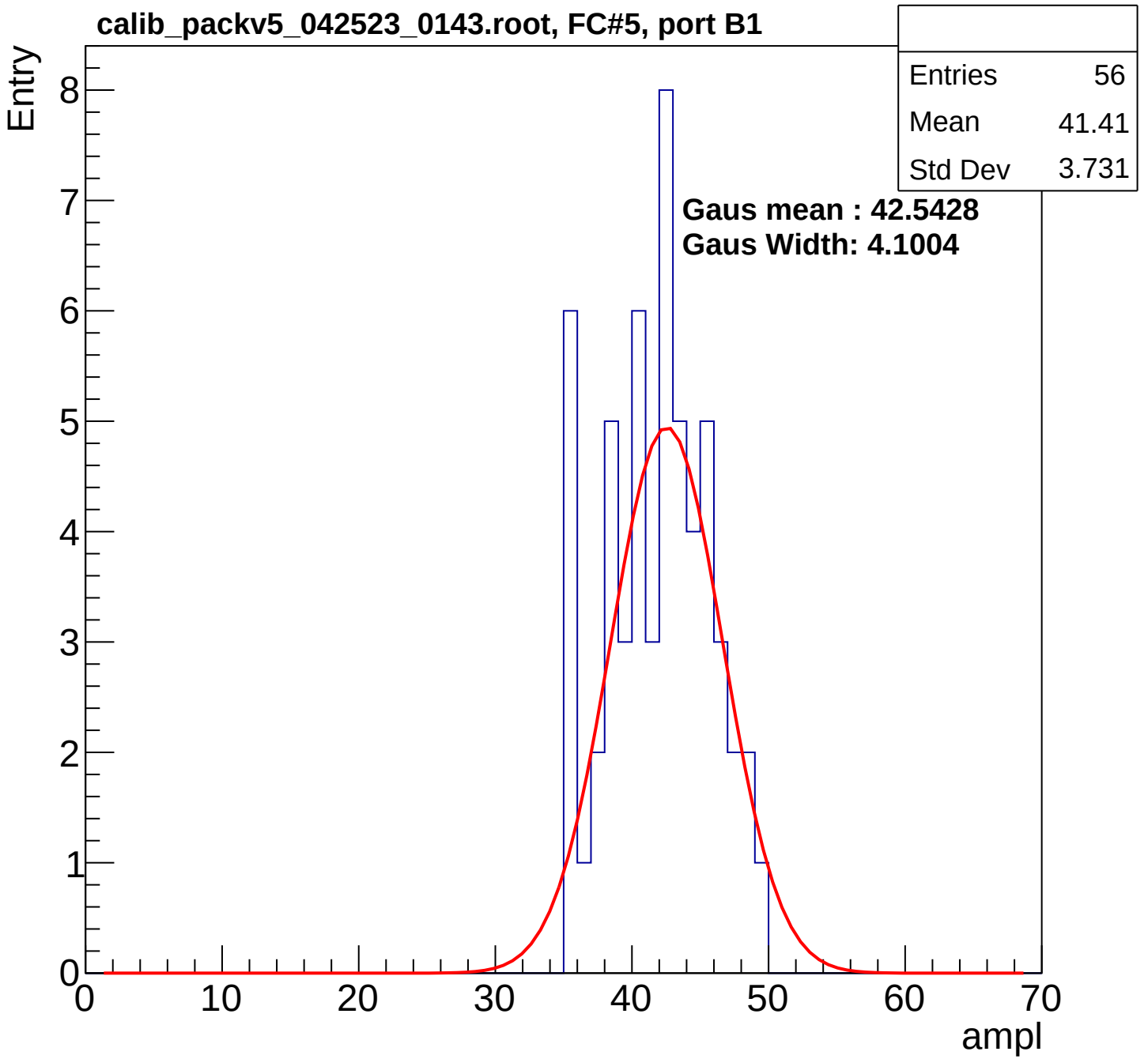
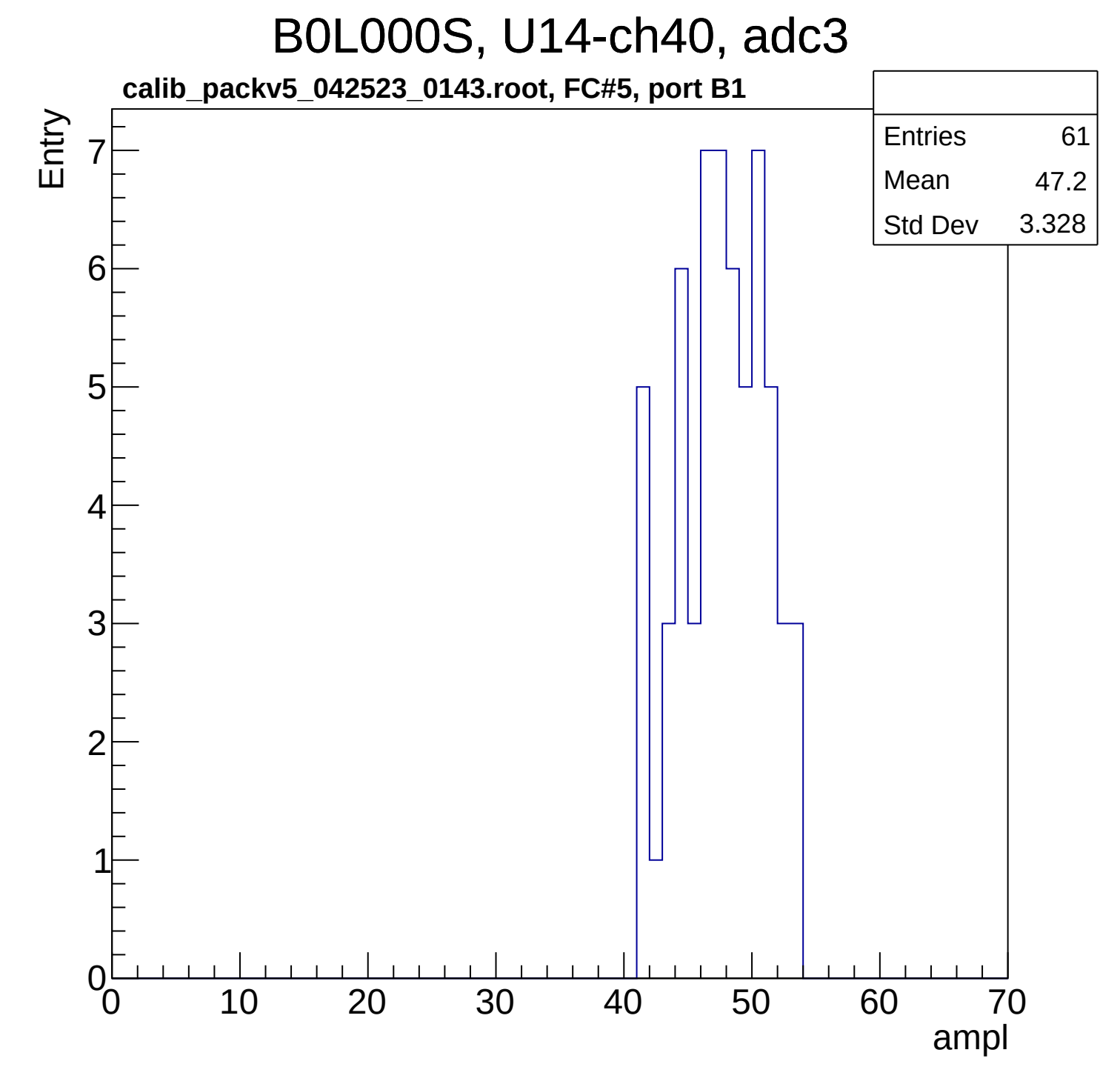
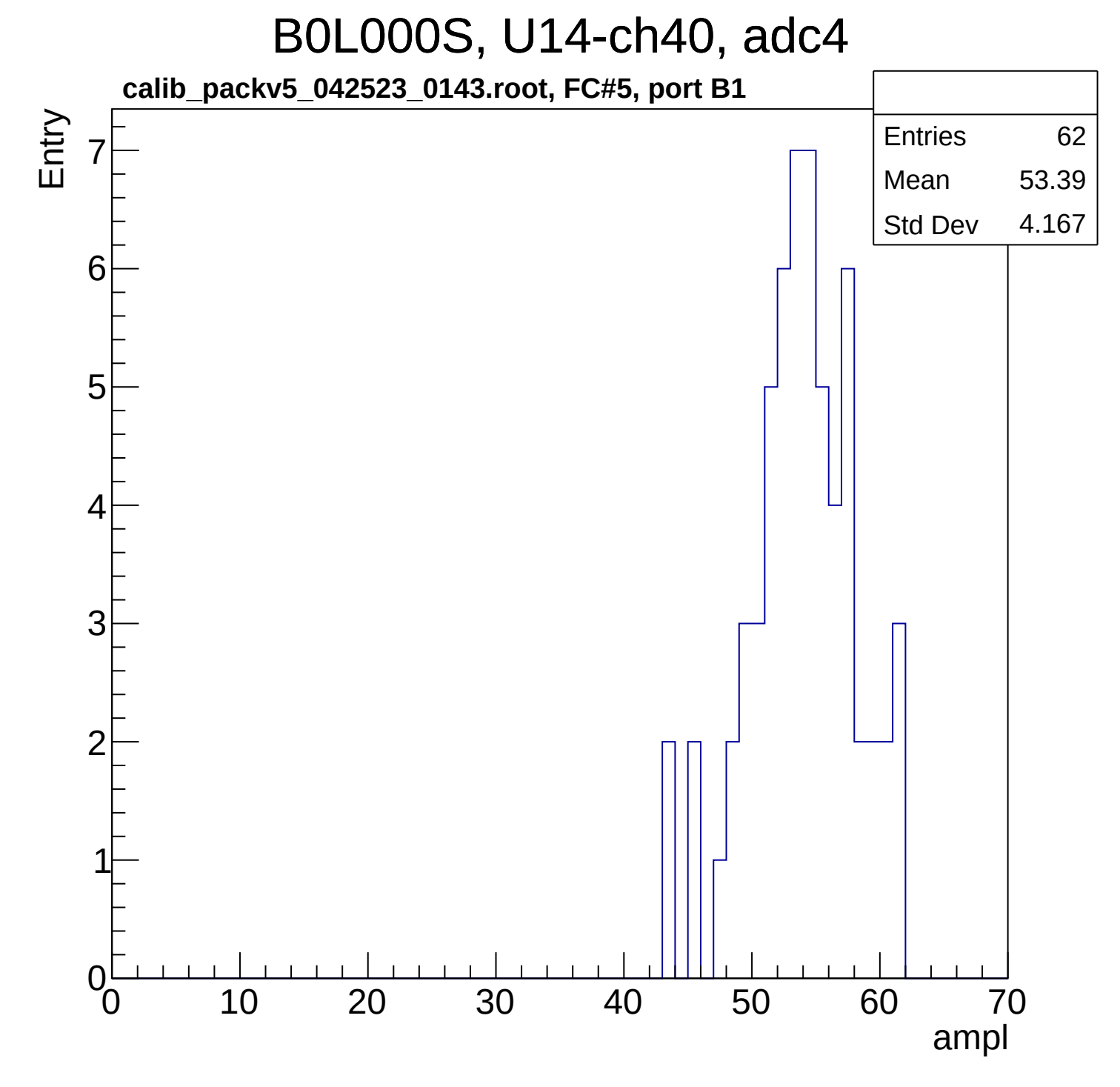
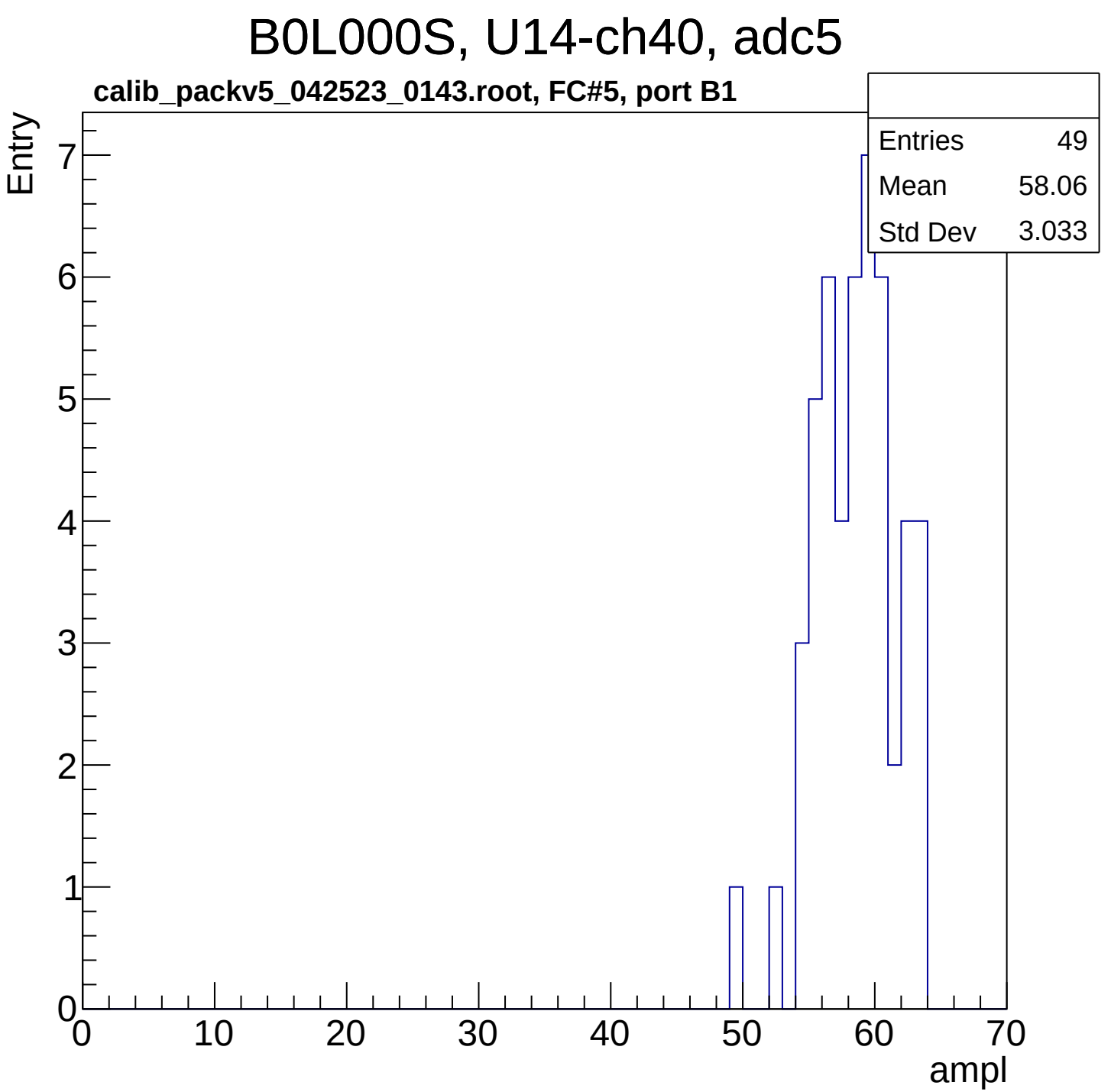

B0L000S, U14-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 41.41
Std Dev 3.731
Gaus mean : 42.5428
Gaus Width: 4.1004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.2
Std Dev 3.328
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 53.39
Std Dev 4.167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.06
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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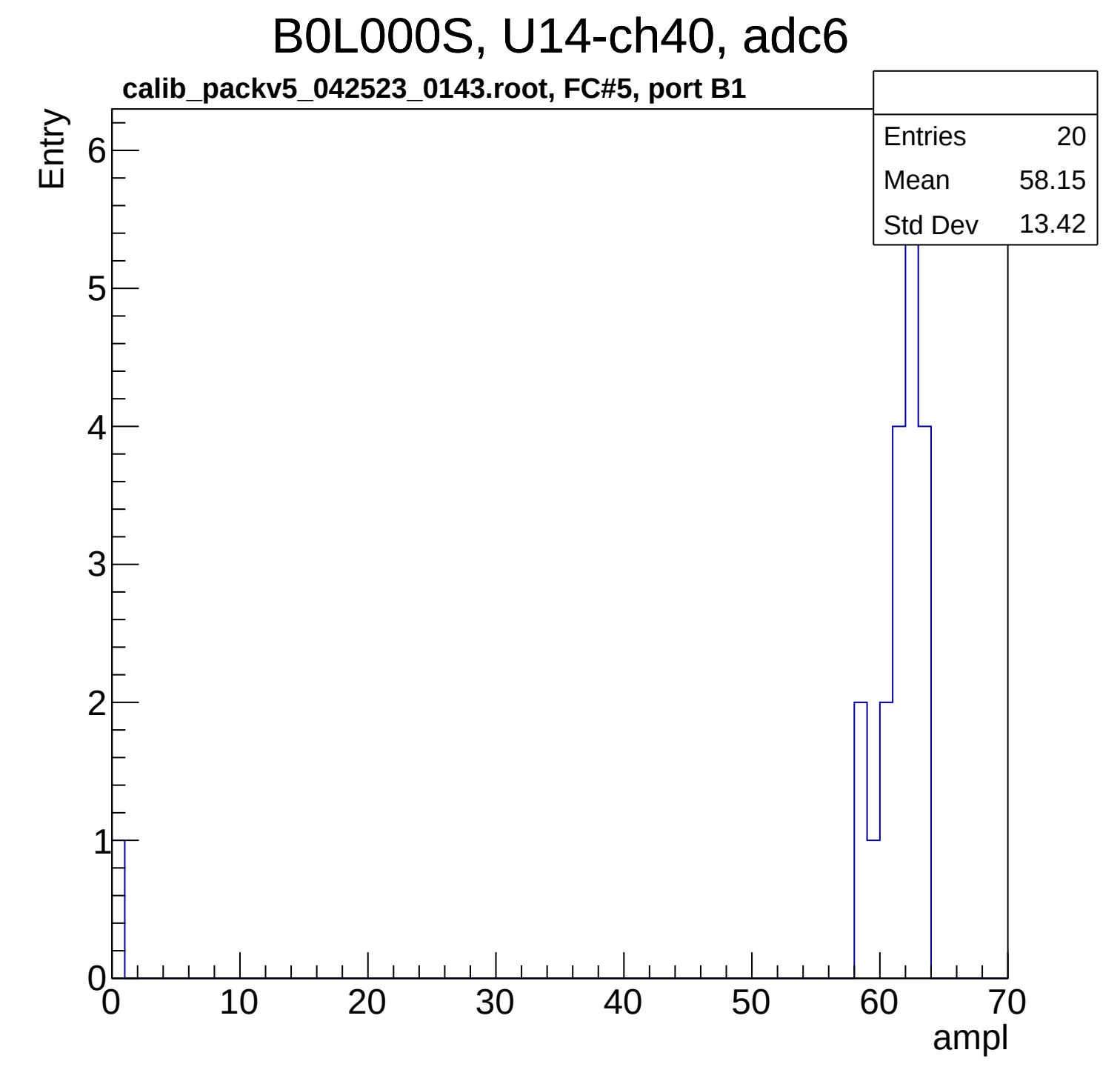
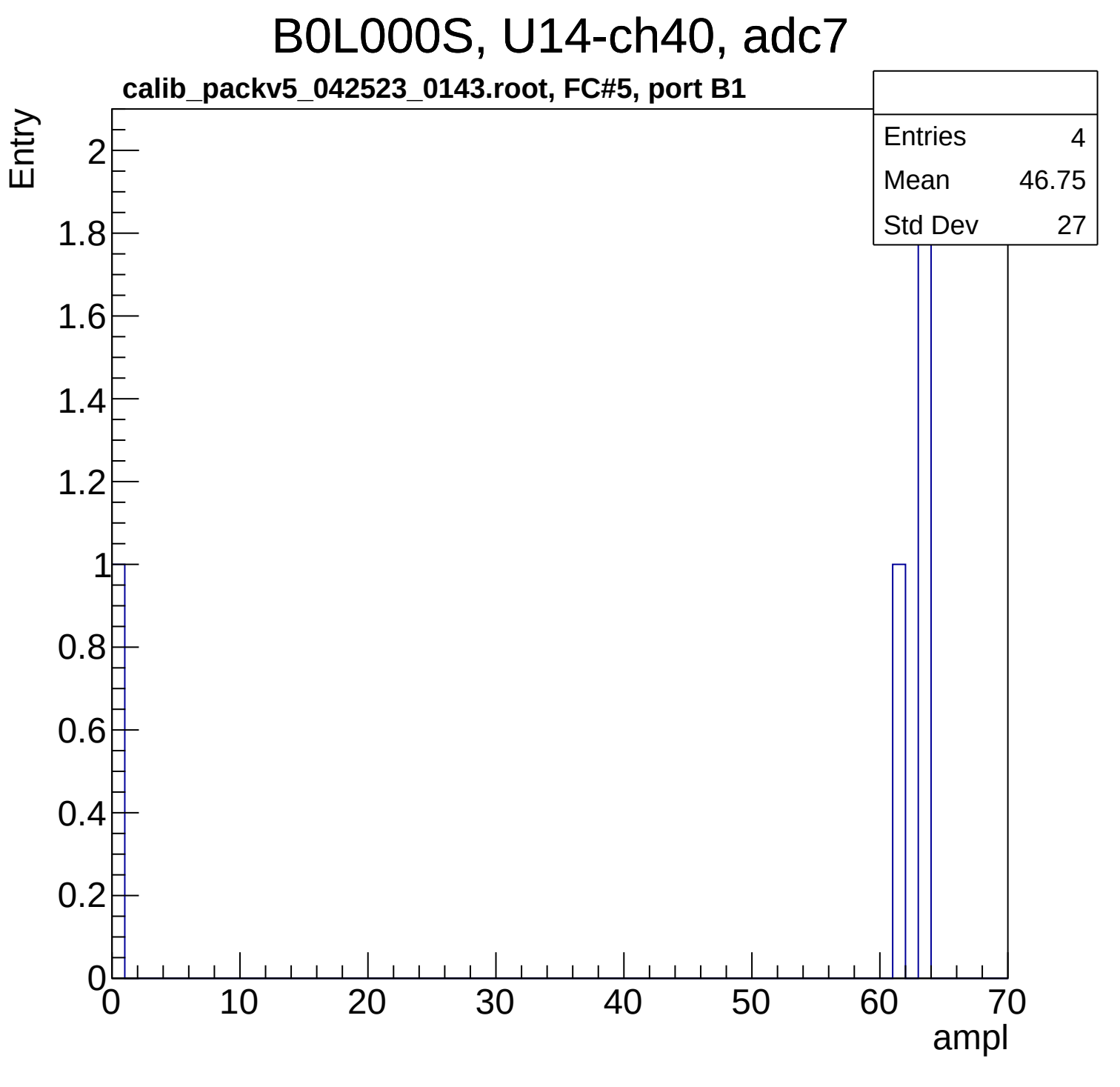

B0L000S, U14-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.15
Std Dev 13.42
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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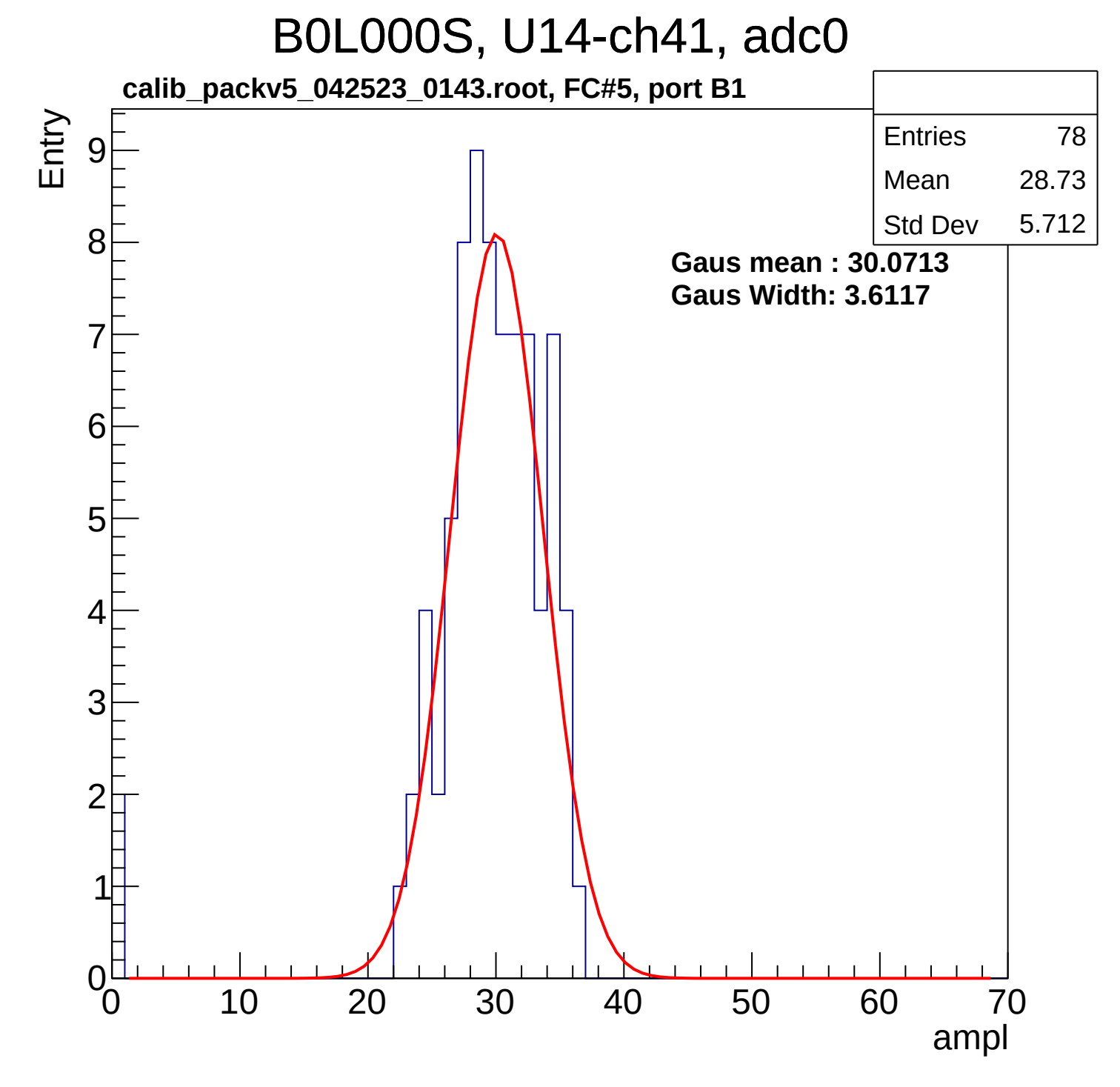
B0L000S, U14-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 28.73
Std Dev 5.712
Gaus mean : 30.0713
Gaus Width: 3.6117
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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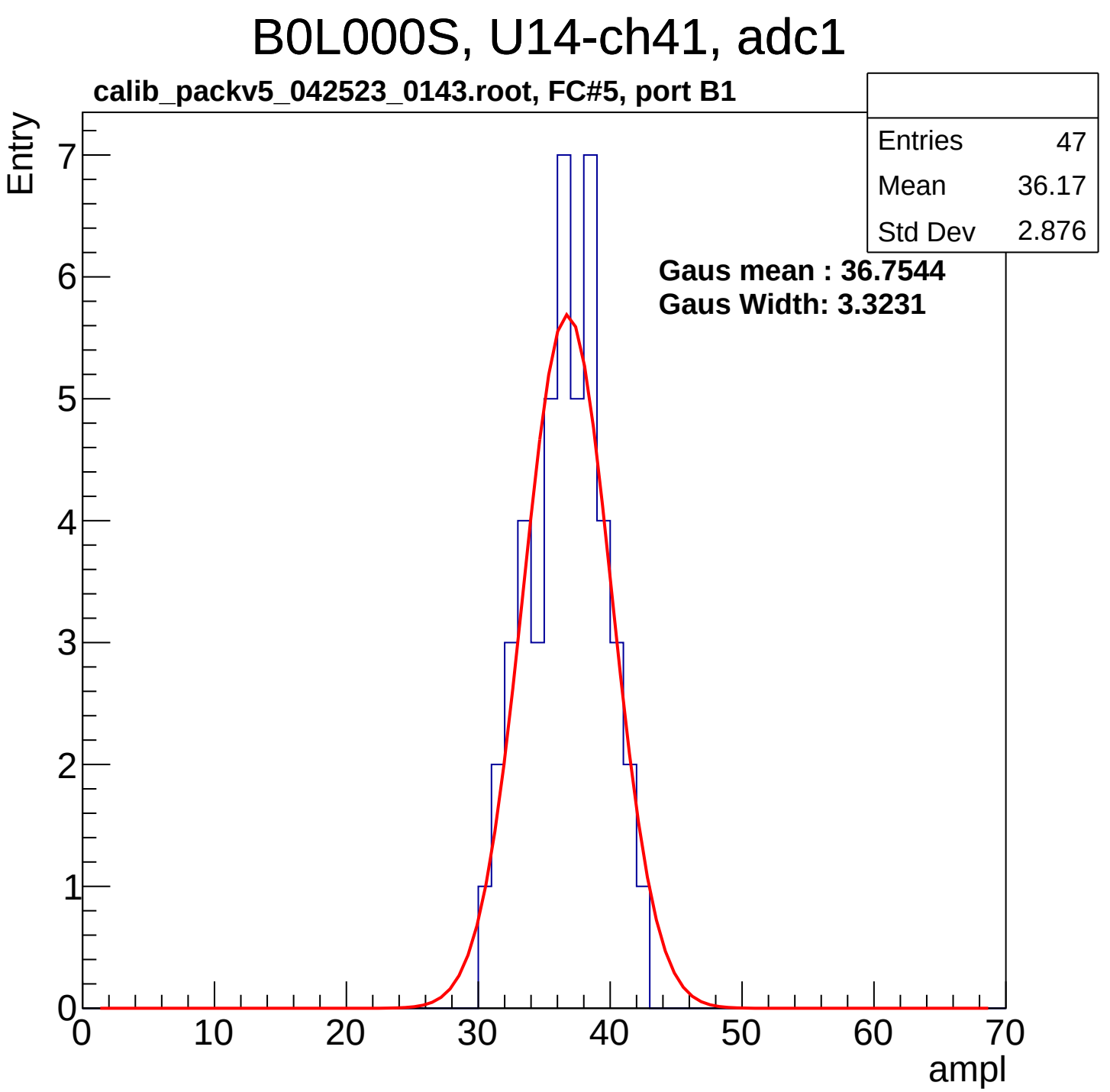
B0L000S, U14-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 36.17
Std Dev 2.876
Gaus mean : 36.7544
Gaus Width: 3.3231
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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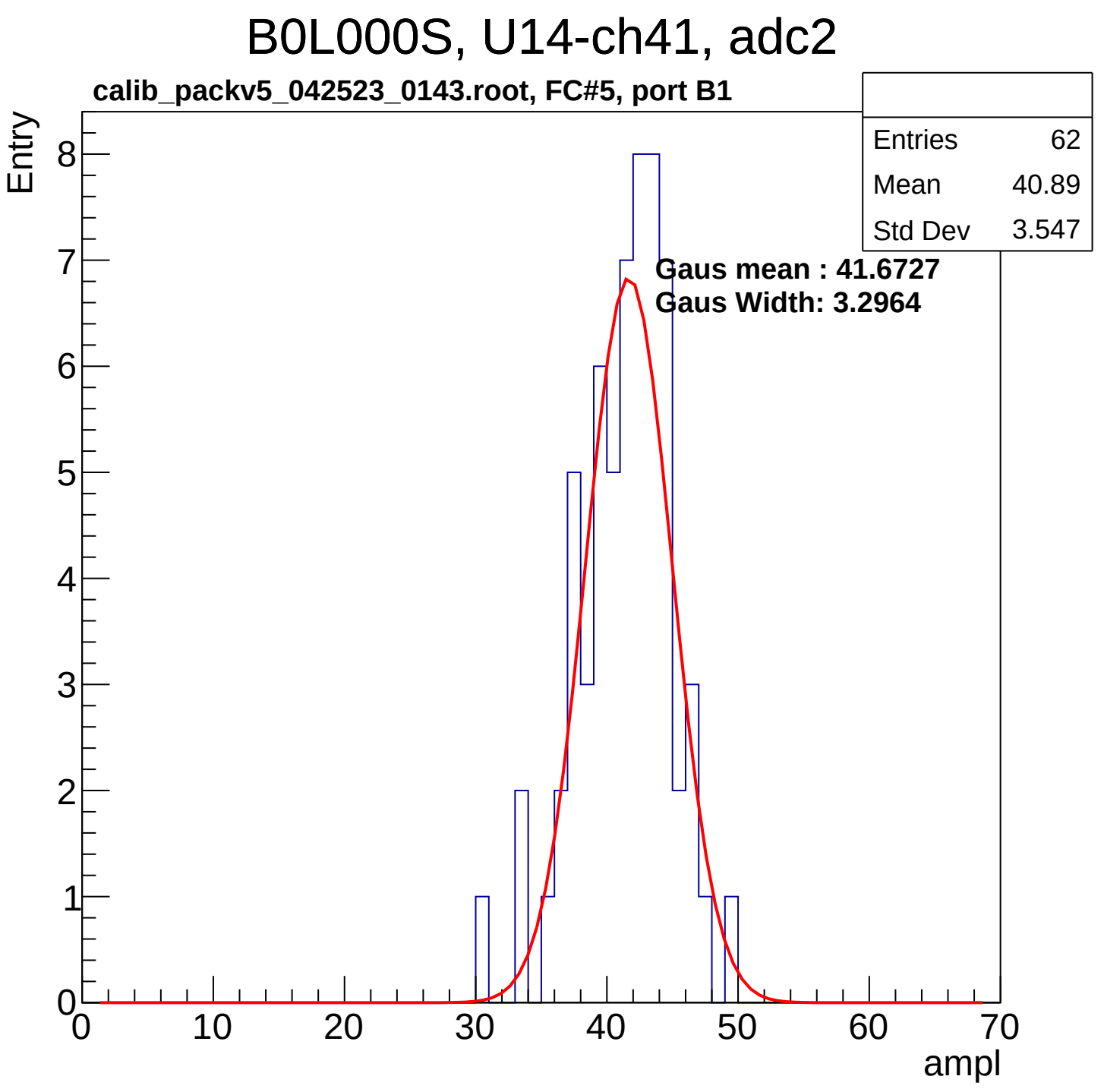
B0L000S, U14-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 40.89
Std Dev 3.547
Gaus mean : 41.6727
Gaus Width: 3.2964
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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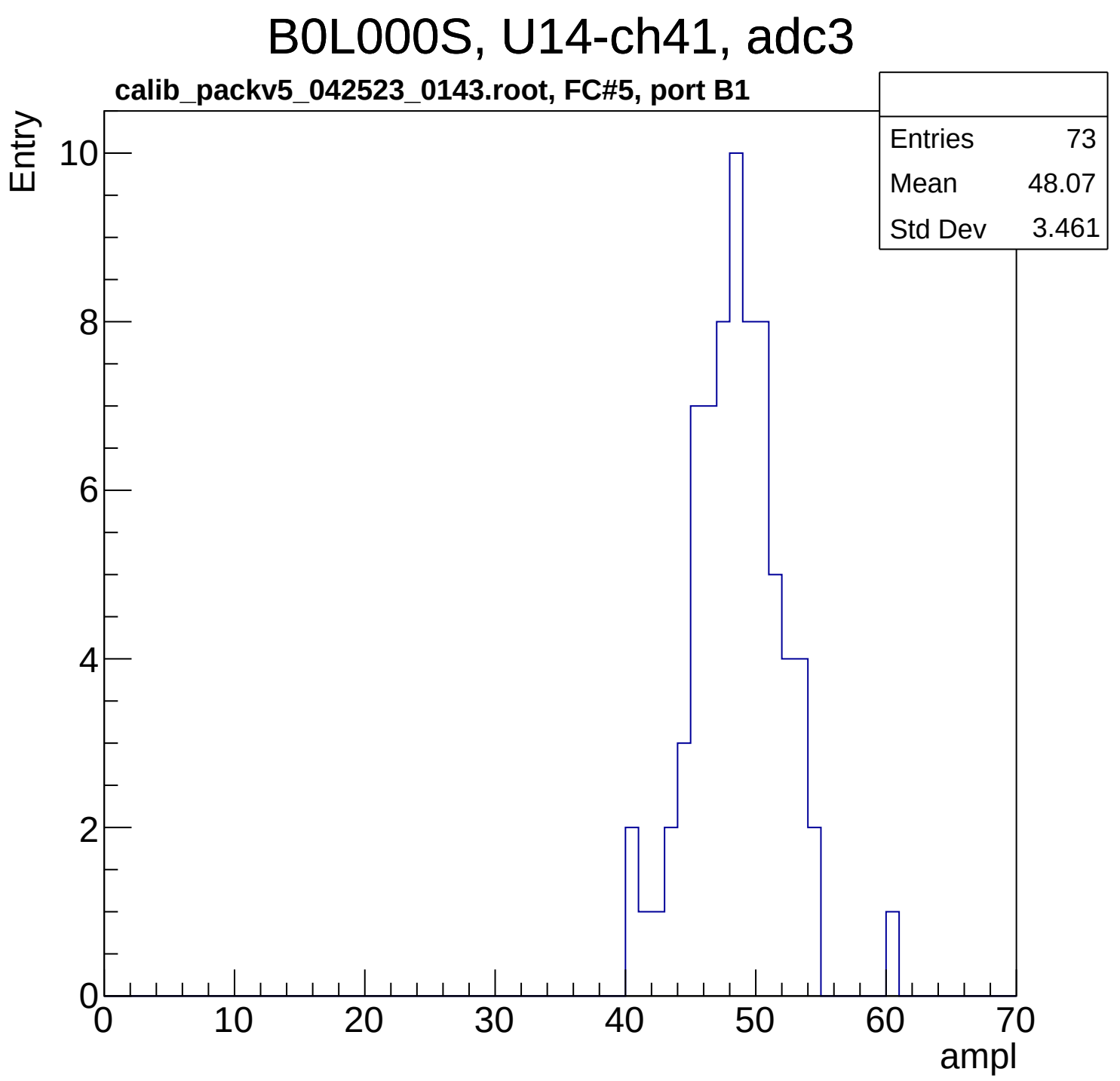
B0L000S, U14-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.07
Std Dev 3.461
Entry
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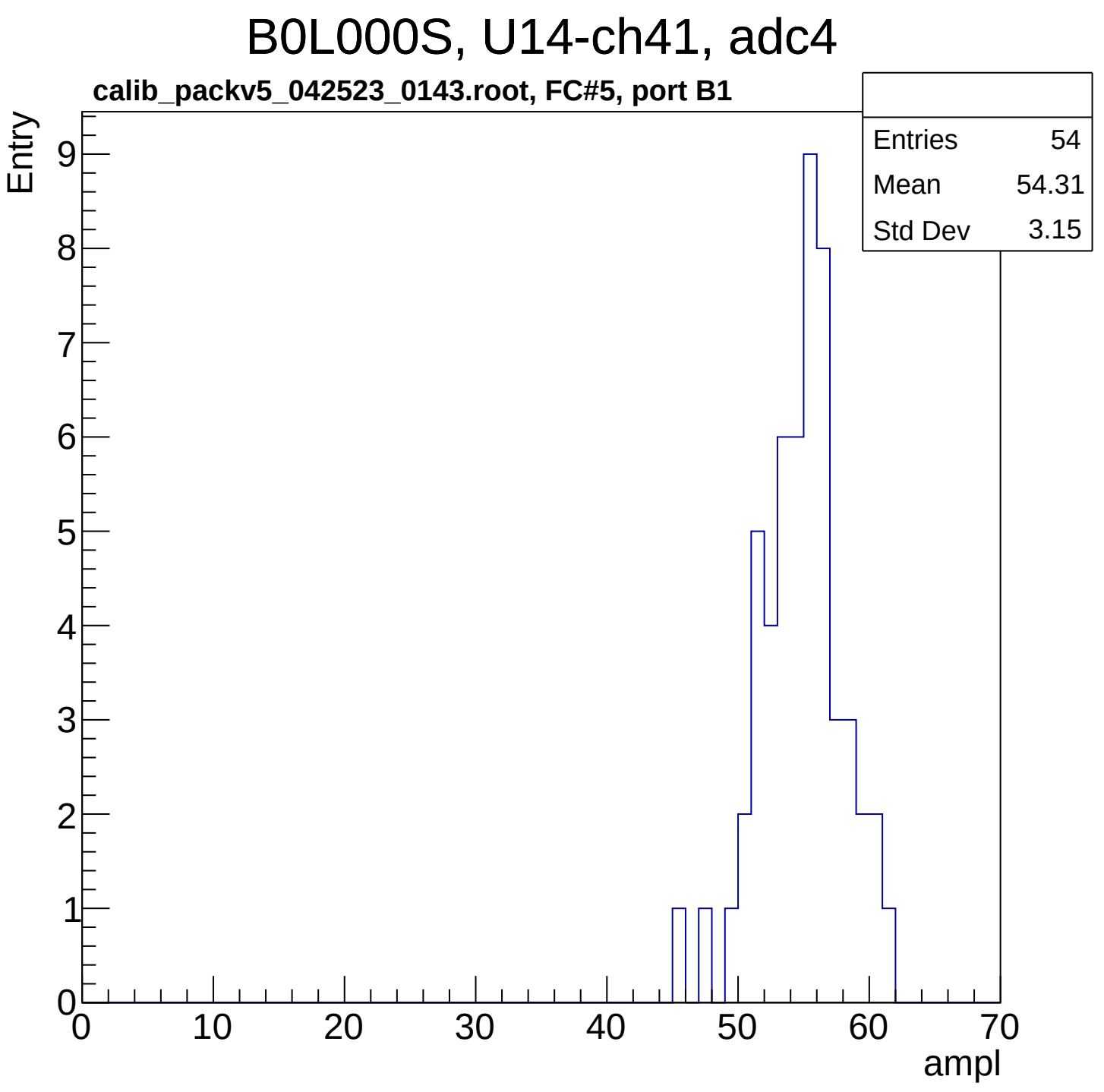
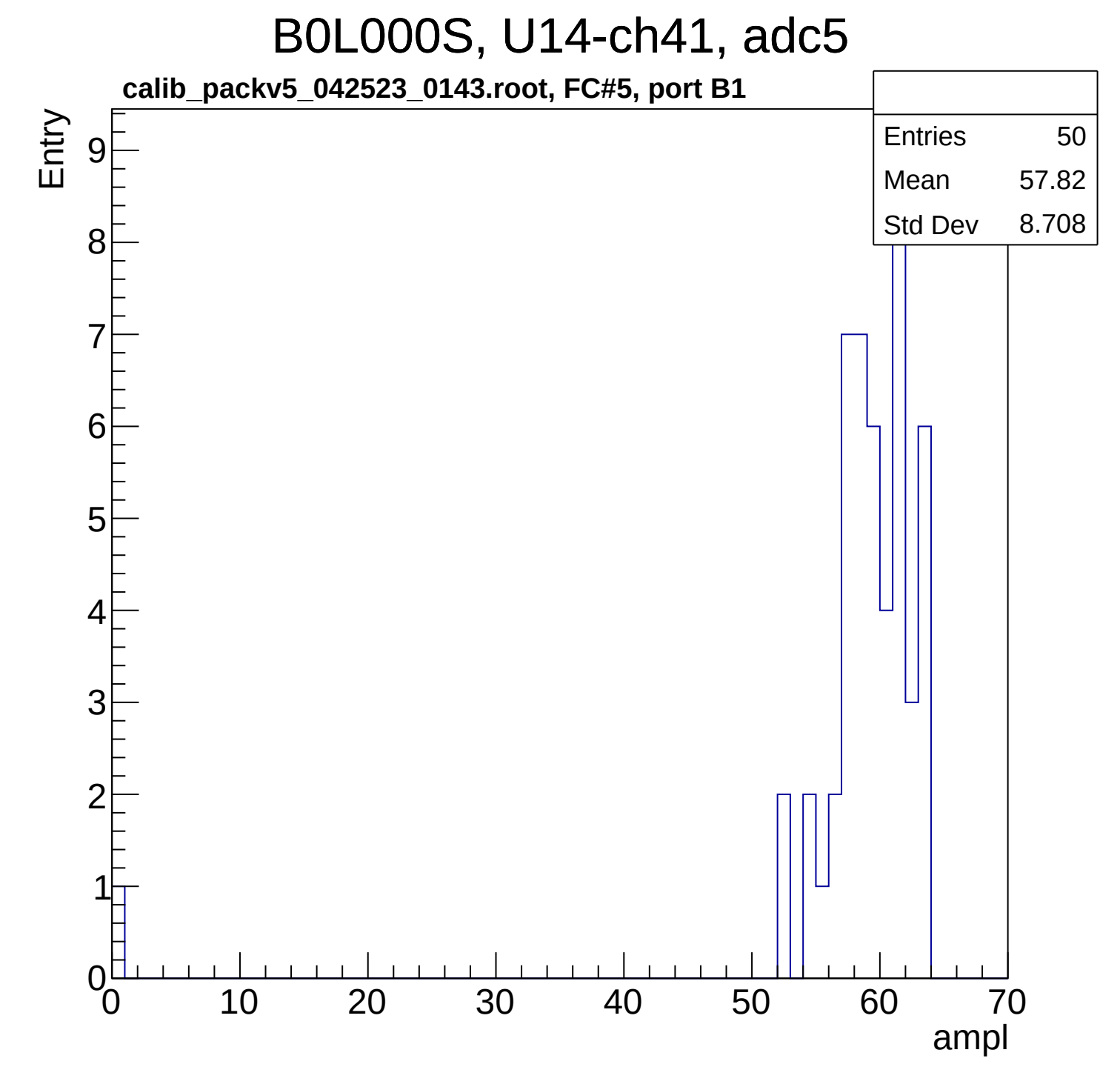
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.31
Std Dev 3.15
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.82
Std Dev 8.708
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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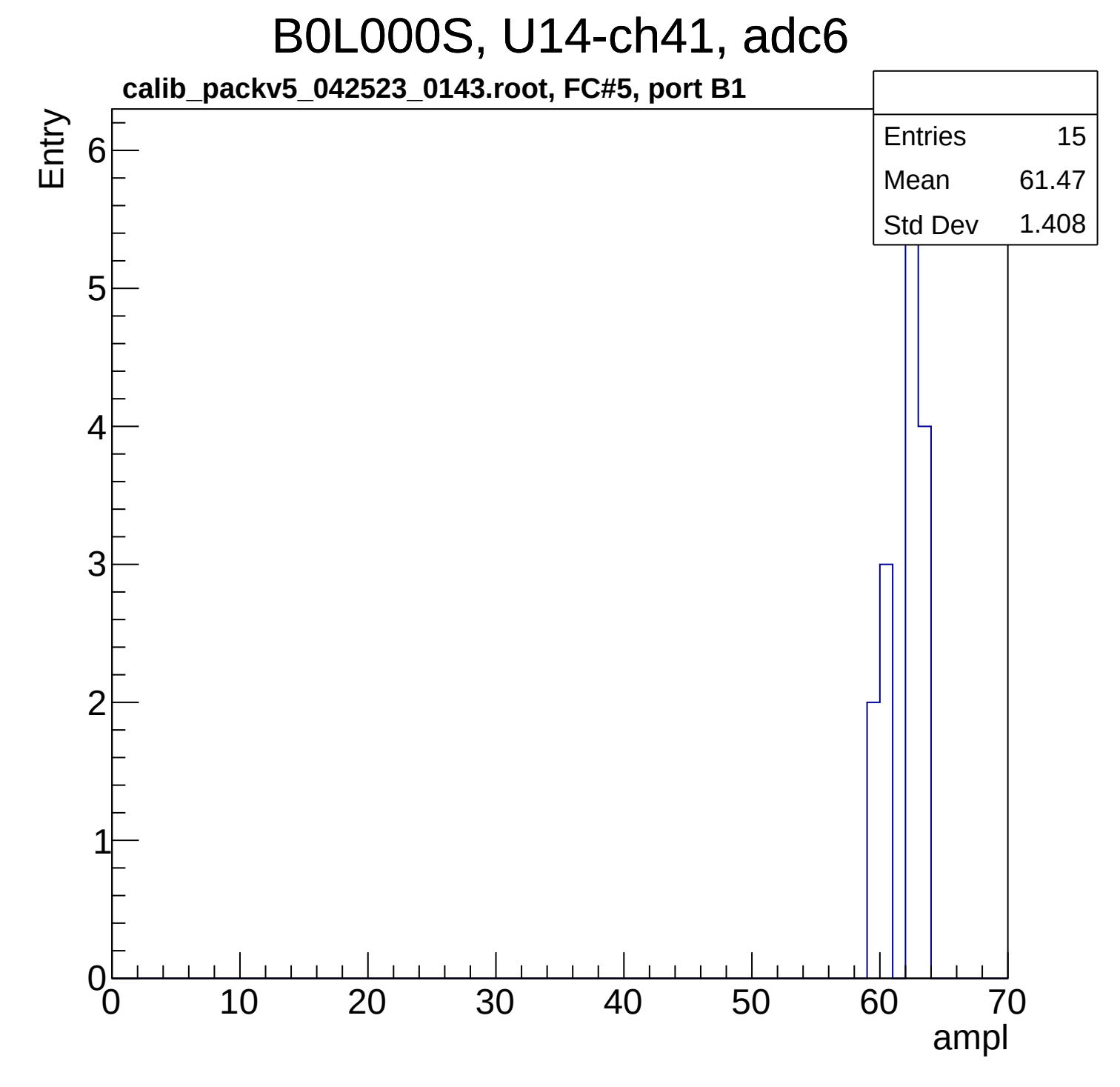
B0L000S, U14-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.47
Std Dev 1.408
Entry
0
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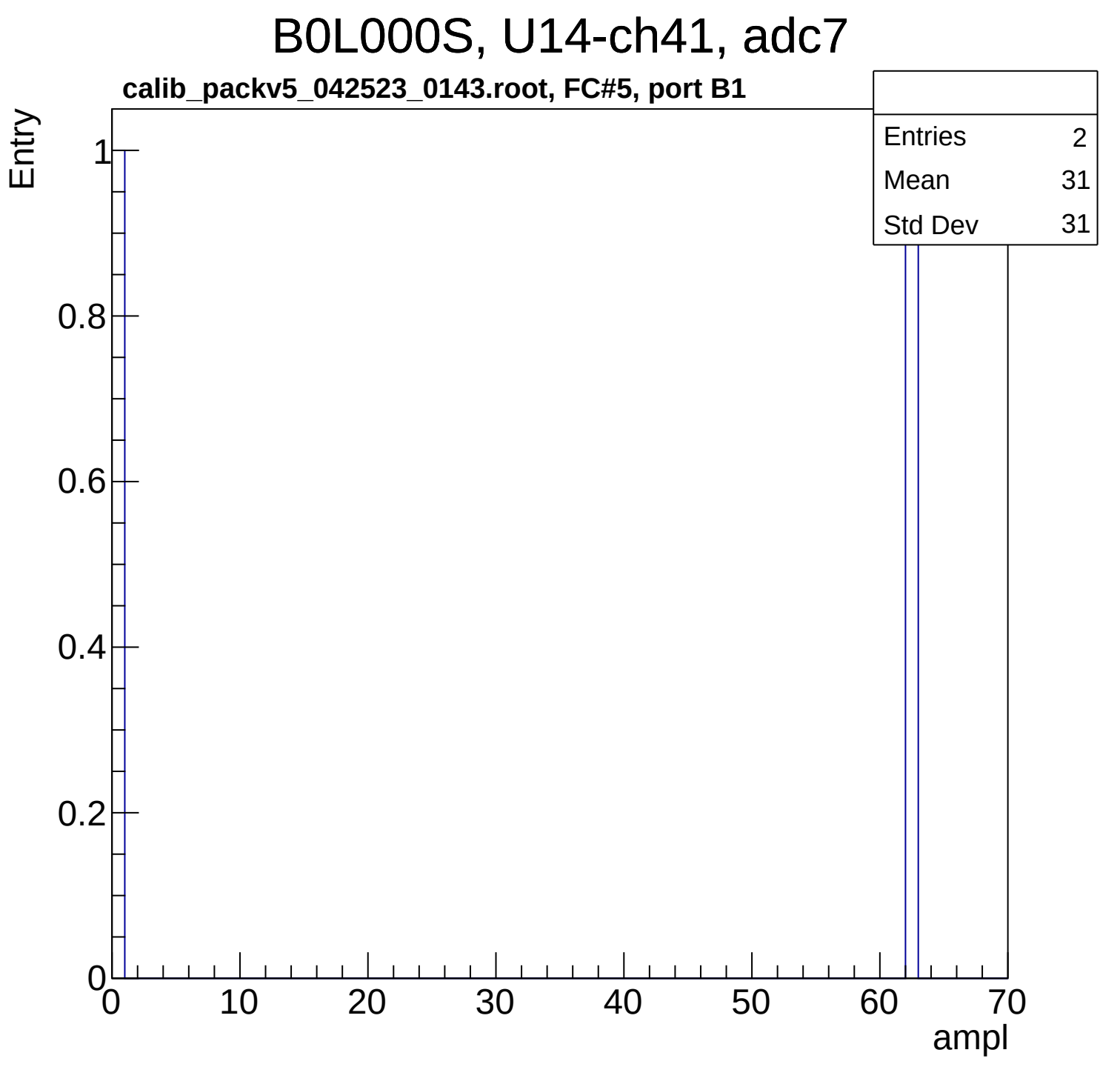
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
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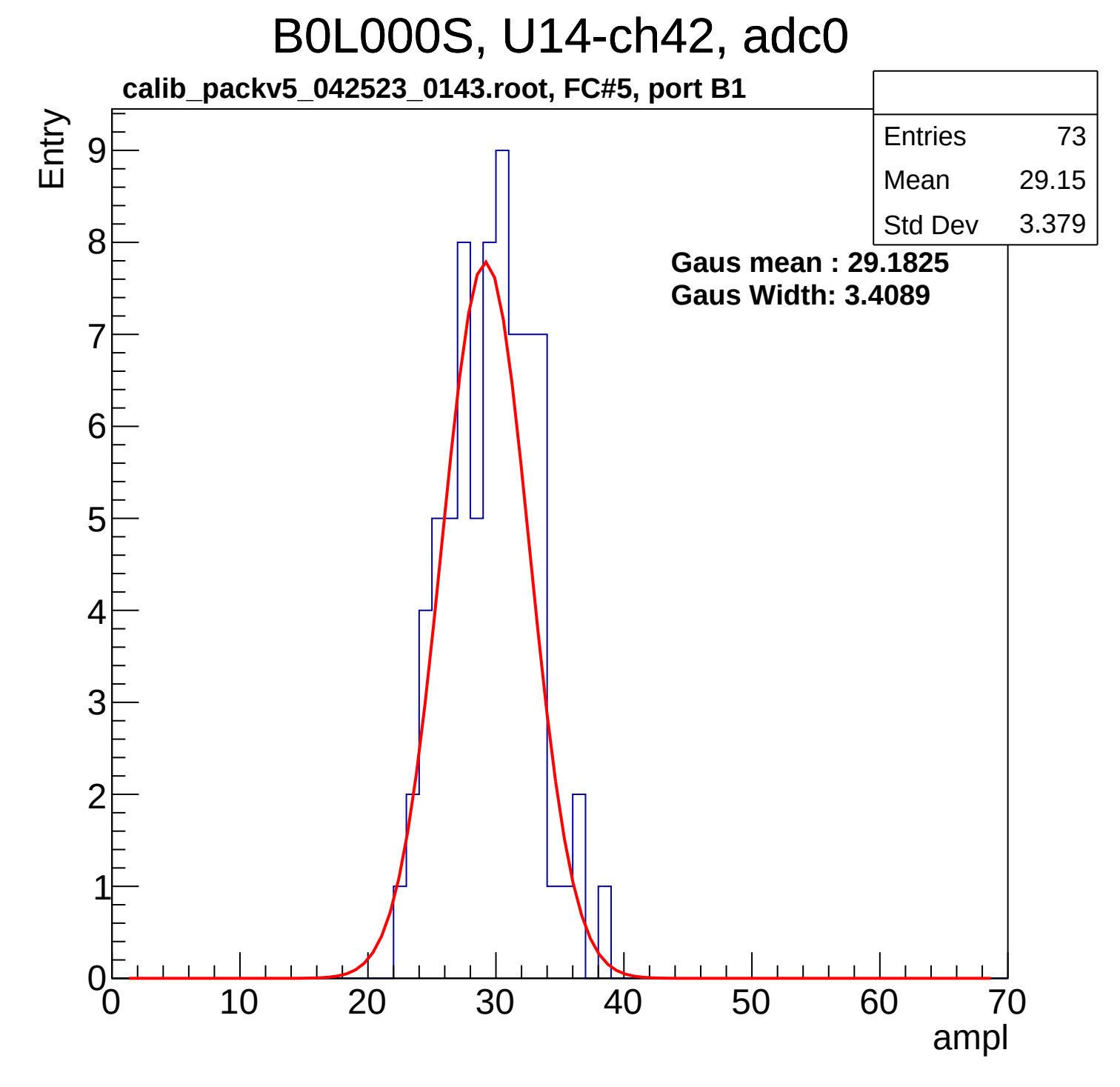
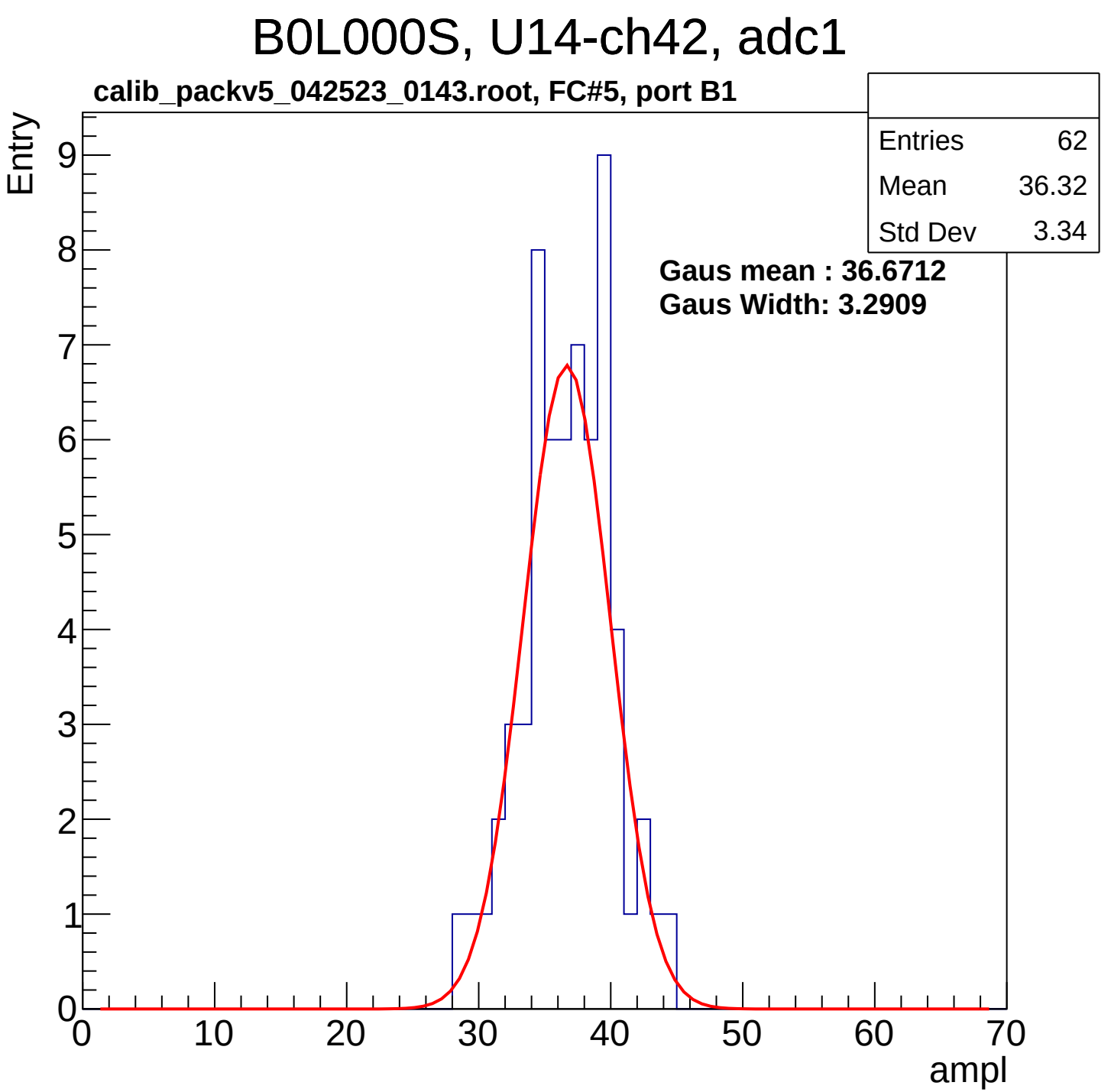
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.15
Std Dev 3.379
Gaus mean : 29.1825
Gaus Width: 3.4089
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.32
Std Dev 3.34
Gaus mean : 36.6712
Gaus Width: 3.2909
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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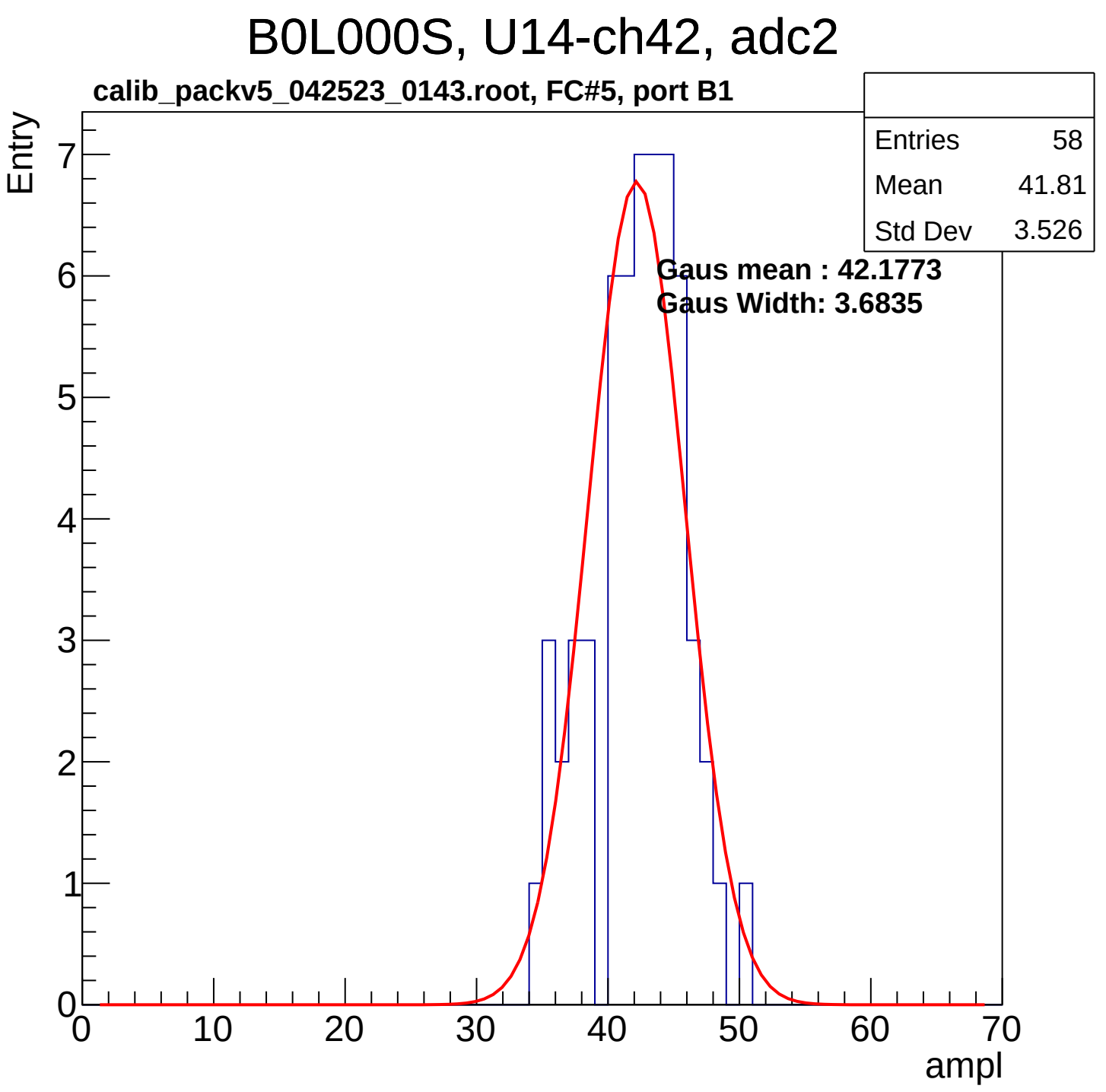
B0L000S, U14-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 41.81
Std Dev 3.526
Gaus mean : 42.1773
Gaus Width: 3.6835
Entry
ampl
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1
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3
4
5
6
7
0
10
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40
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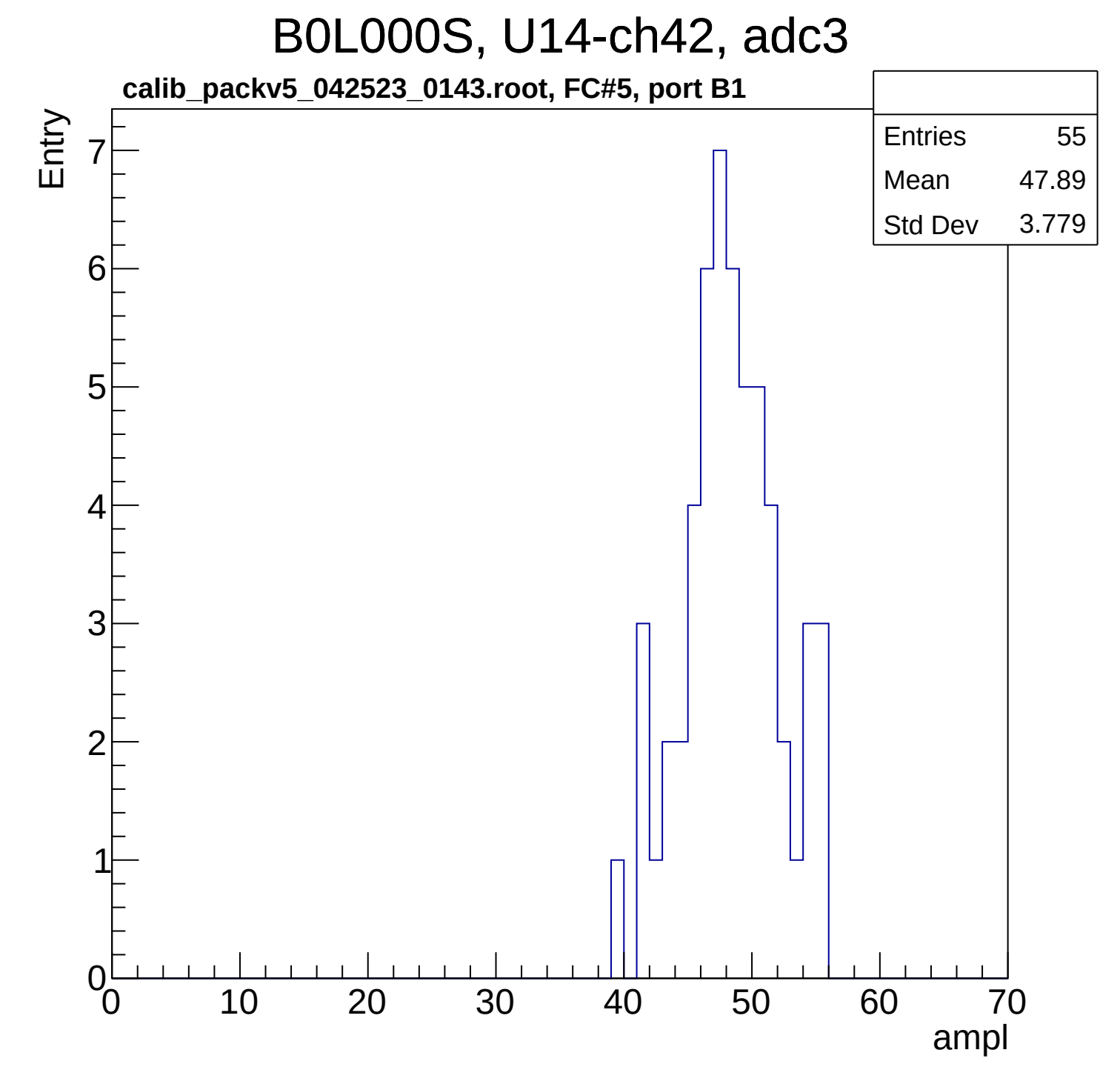
B0L000S, U14-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.89
Std Dev 3.779
Entry
ampl
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3
4
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7
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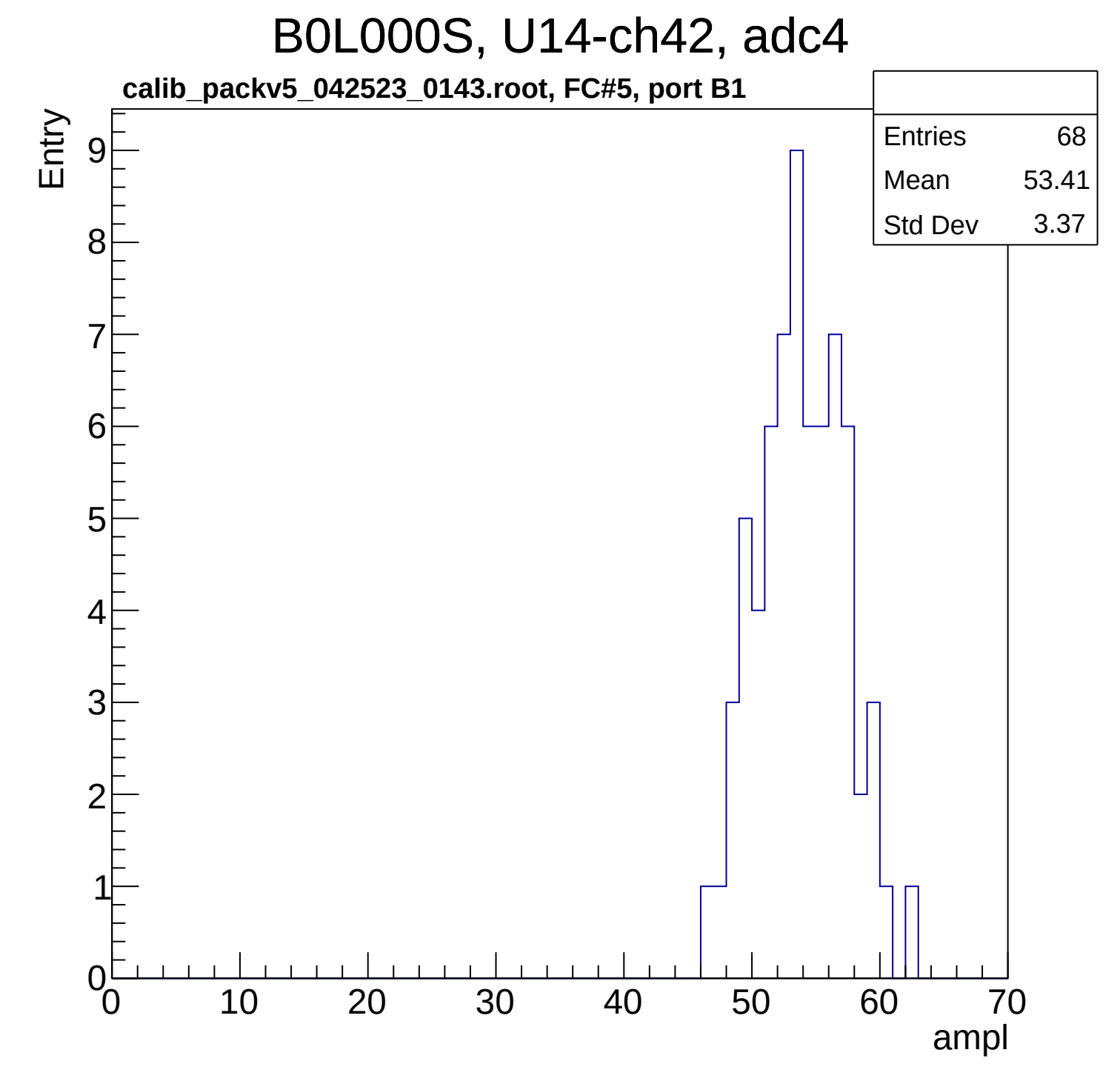
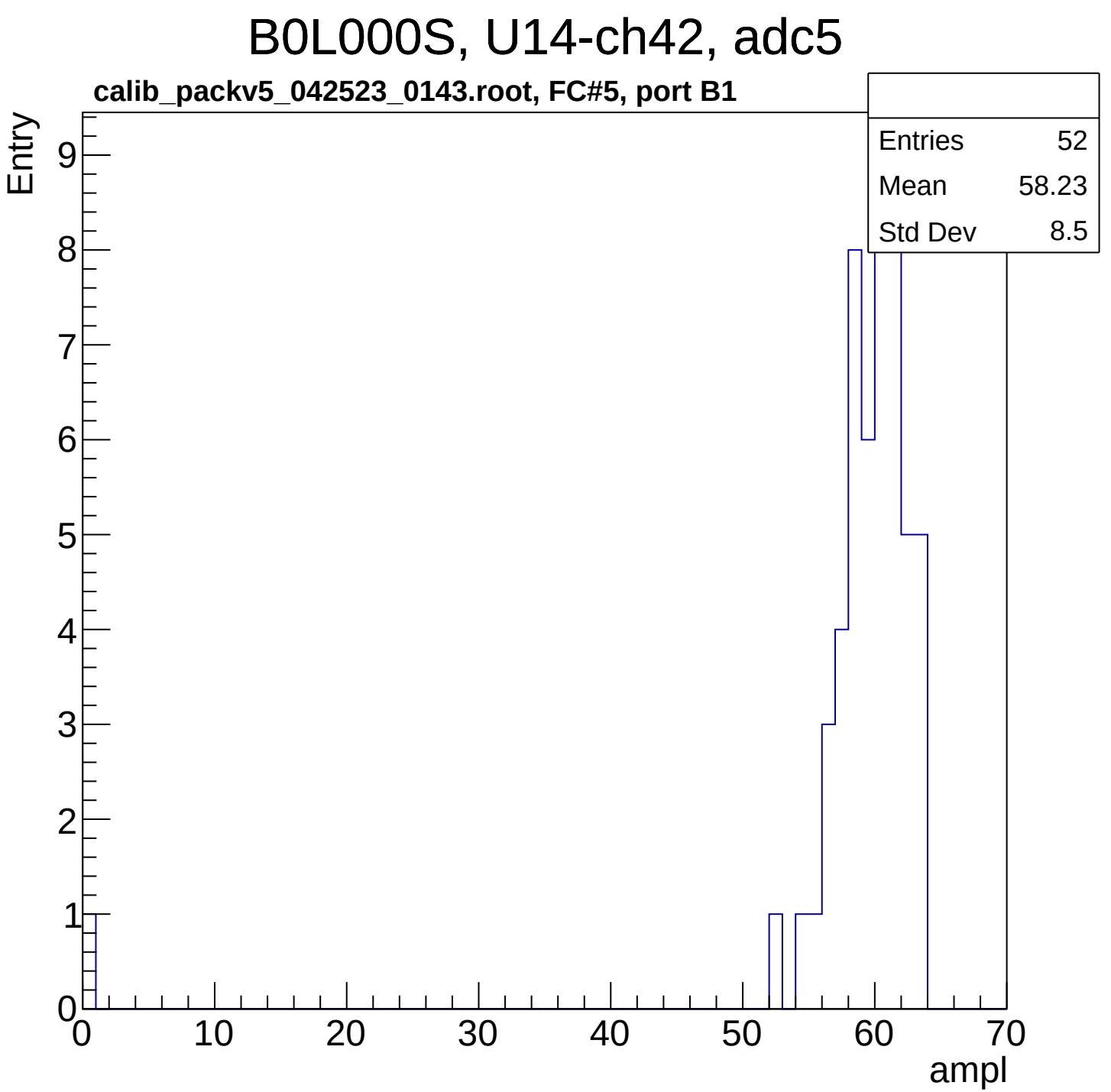

B0L000S, U14-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 53.41
Std Dev 3.37
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.23
Std Dev 8.5
Entry
ampl
0
1
2
3
4
5
6
7
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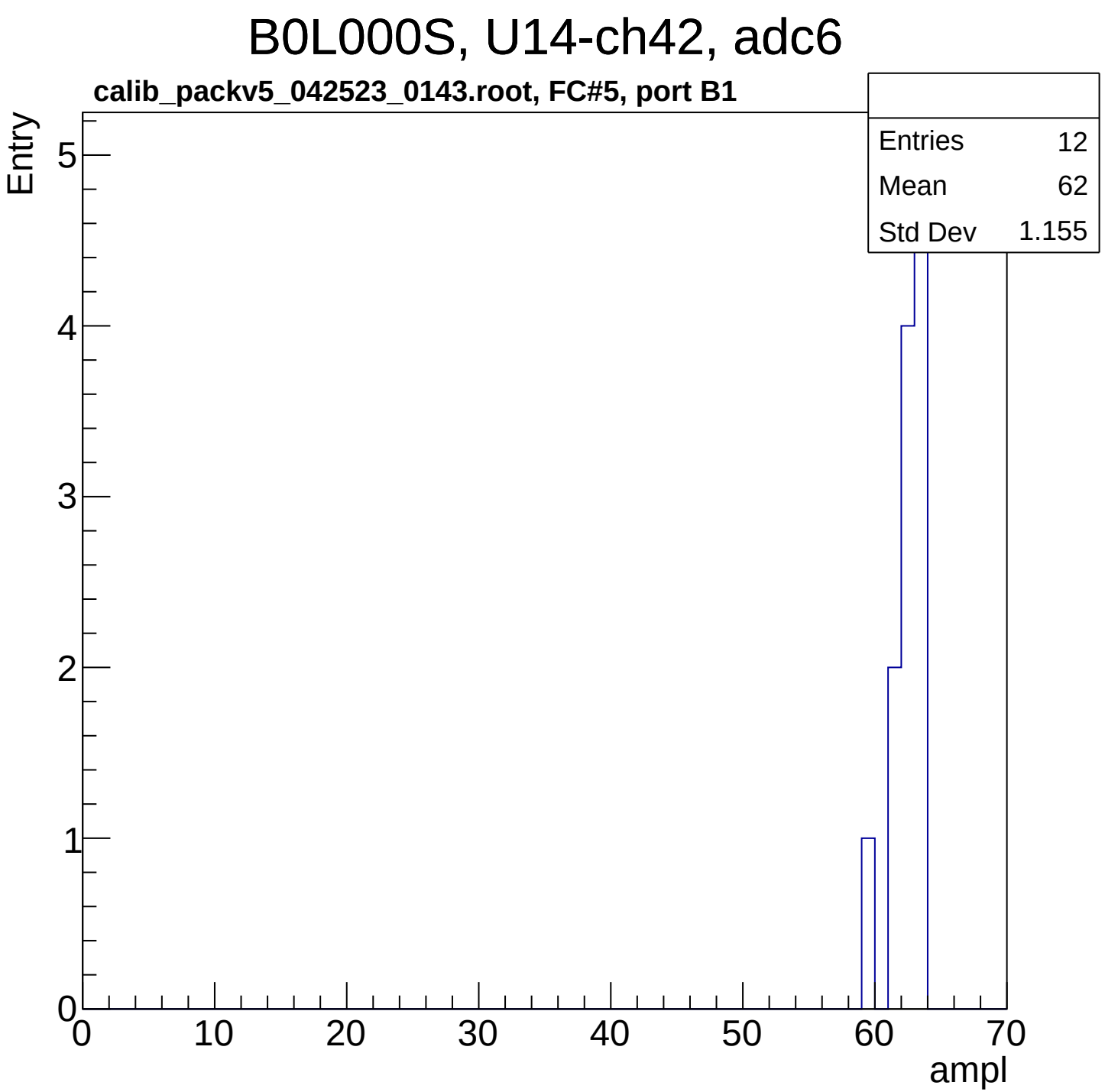
B0L000S, U14-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 62
Std Dev 1.155
Entry
0
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4
5
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10
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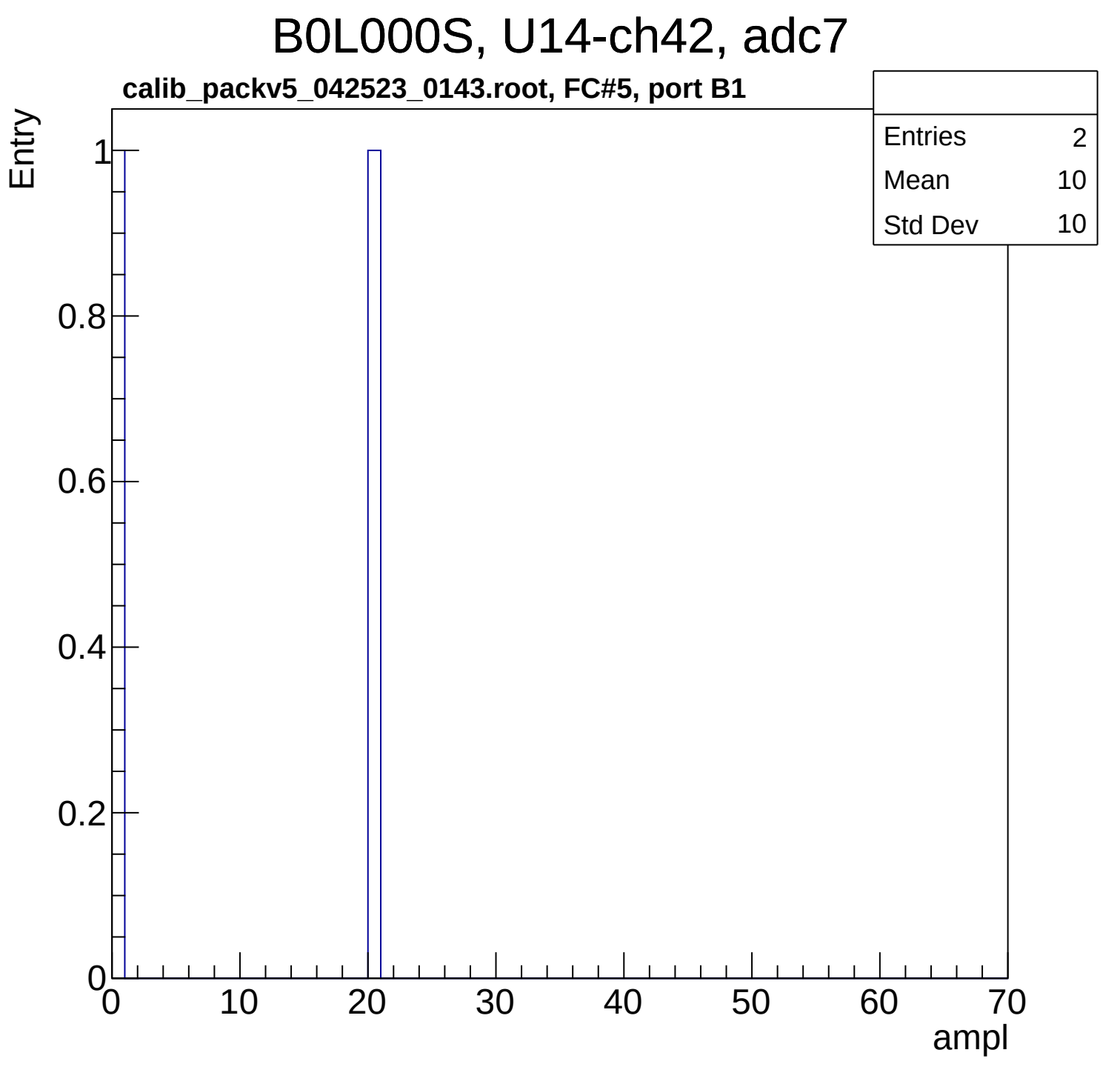
50
60
70
ampl


B0L000S, U14-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
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0.6
0.4
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0
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50
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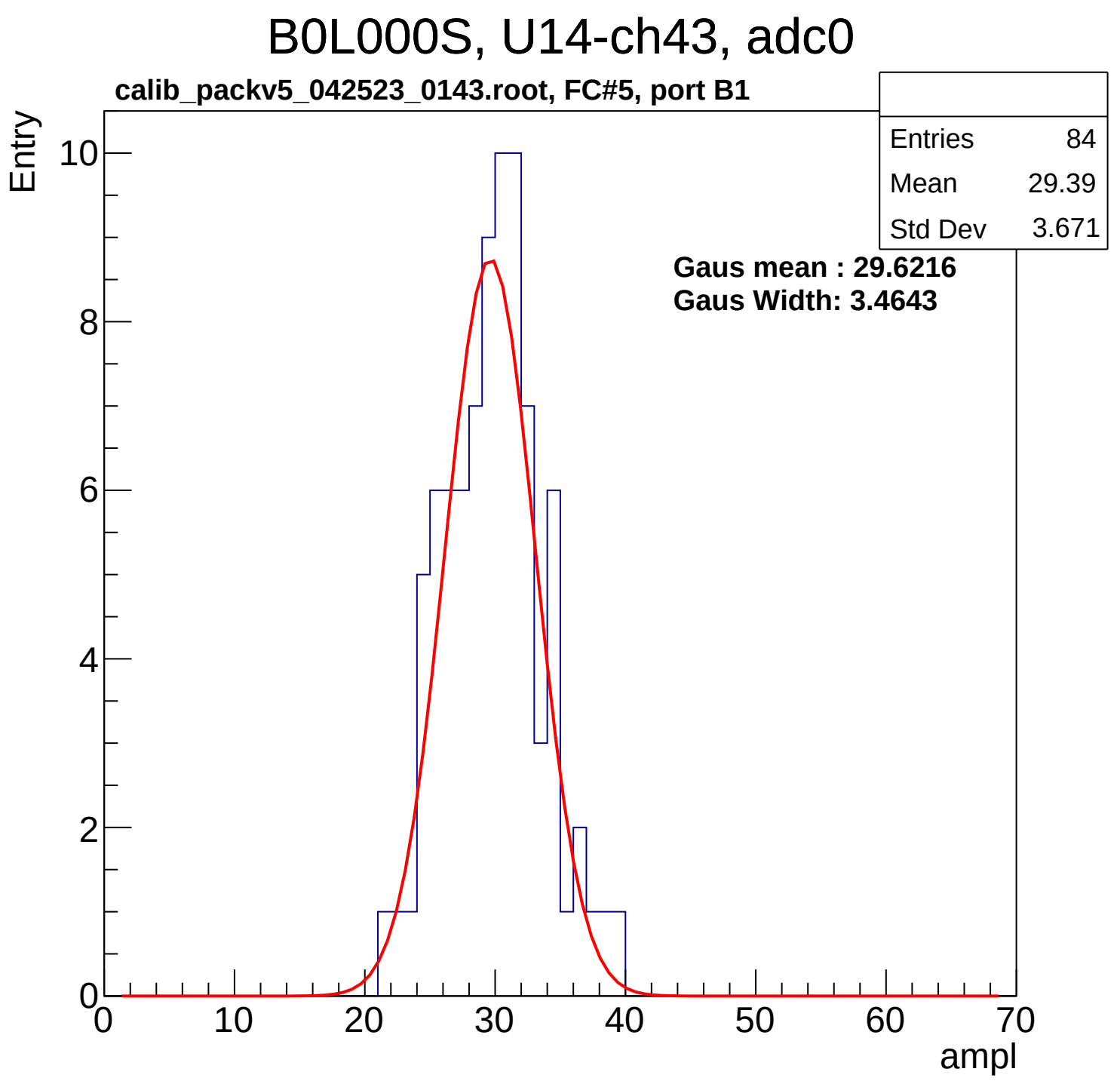
70
ampl


B0L000S, U14-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 29.39
Std Dev 3.671
Gaus mean : 29.6216
Gaus Width: 3.4643
Entry
ampl
0
2
4
6
8
10
0
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60
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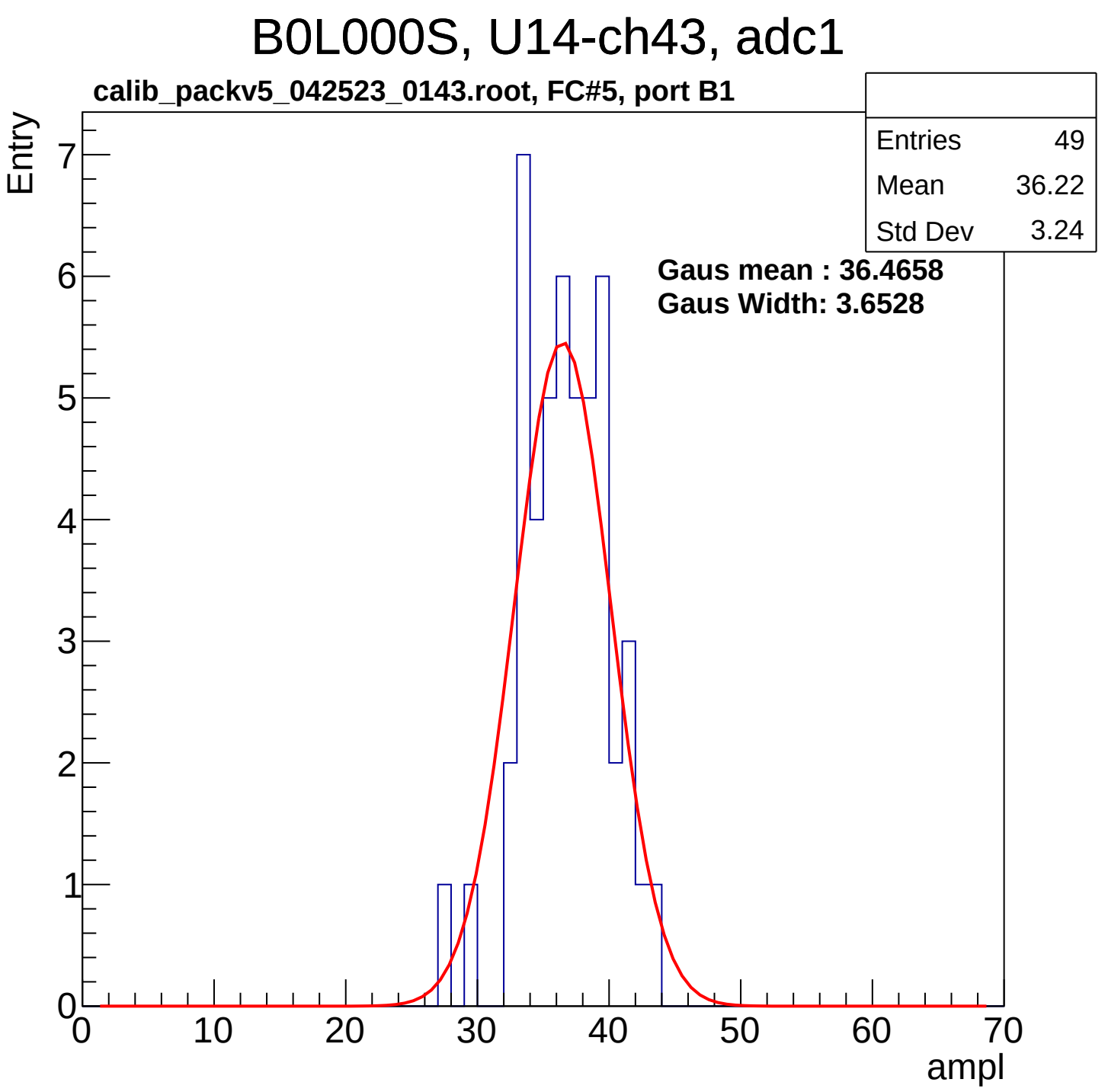
B0L000S, U14-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 36.22
Std Dev 3.24
Gaus mean : 36.4658
Gaus Width: 3.6528
Entry
ampl
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3
4
5
6
7
0
10
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40
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60
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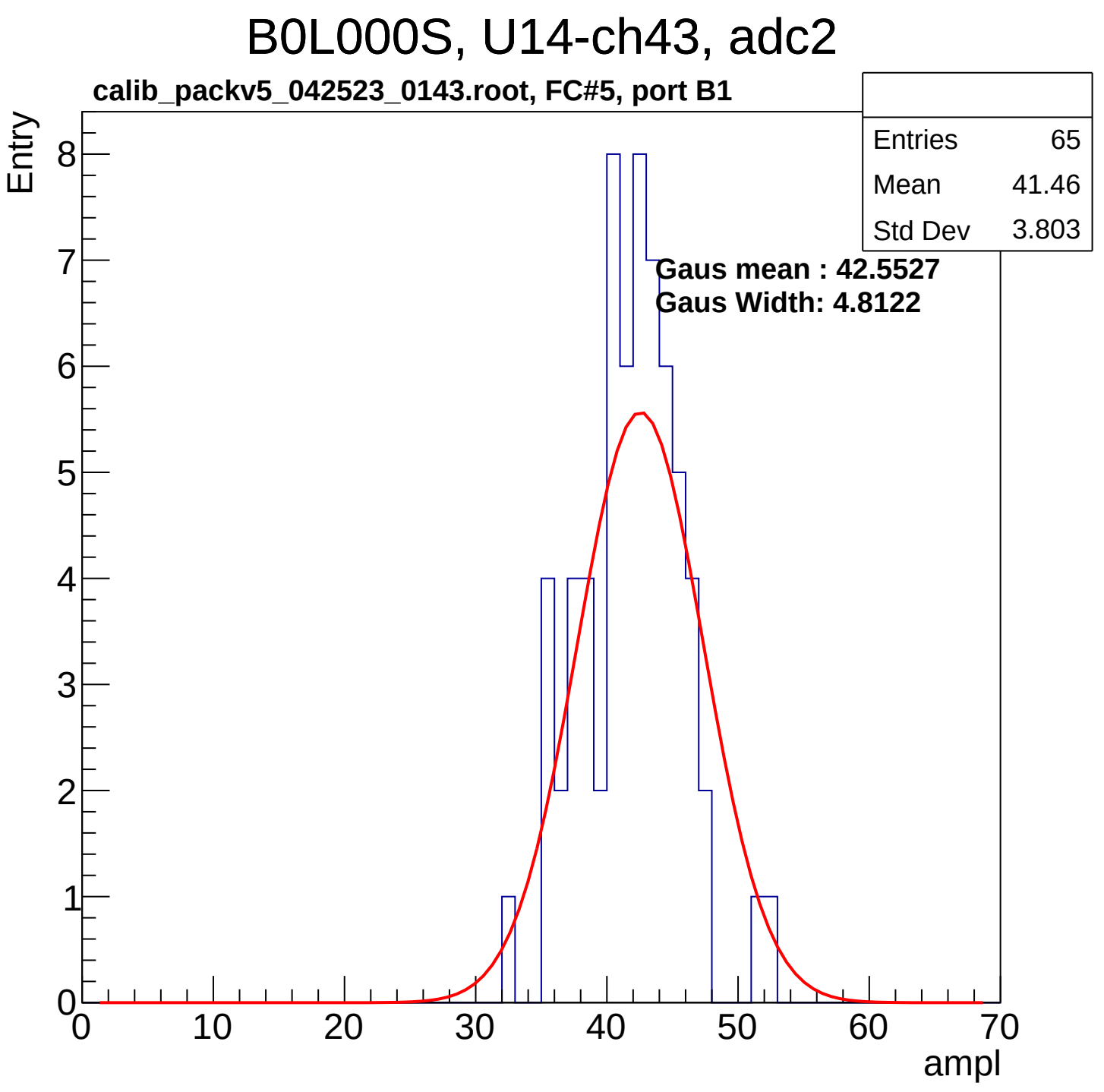
B0L000S, U14-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.46
Std Dev 3.803
Gaus mean : 42.5527
Gaus Width: 4.8122
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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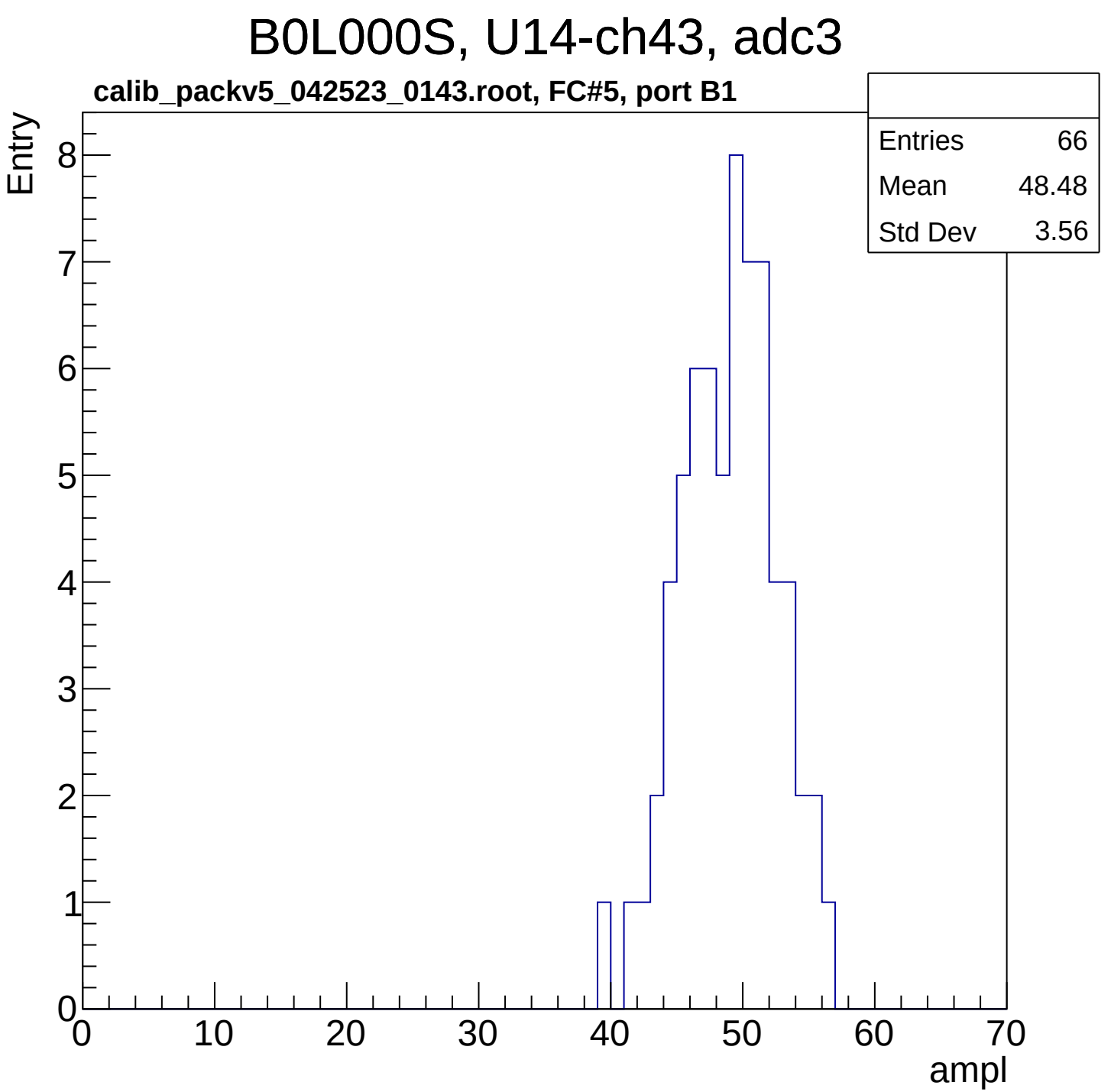
B0L000S, U14-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 48.48
Std Dev 3.56
Entry
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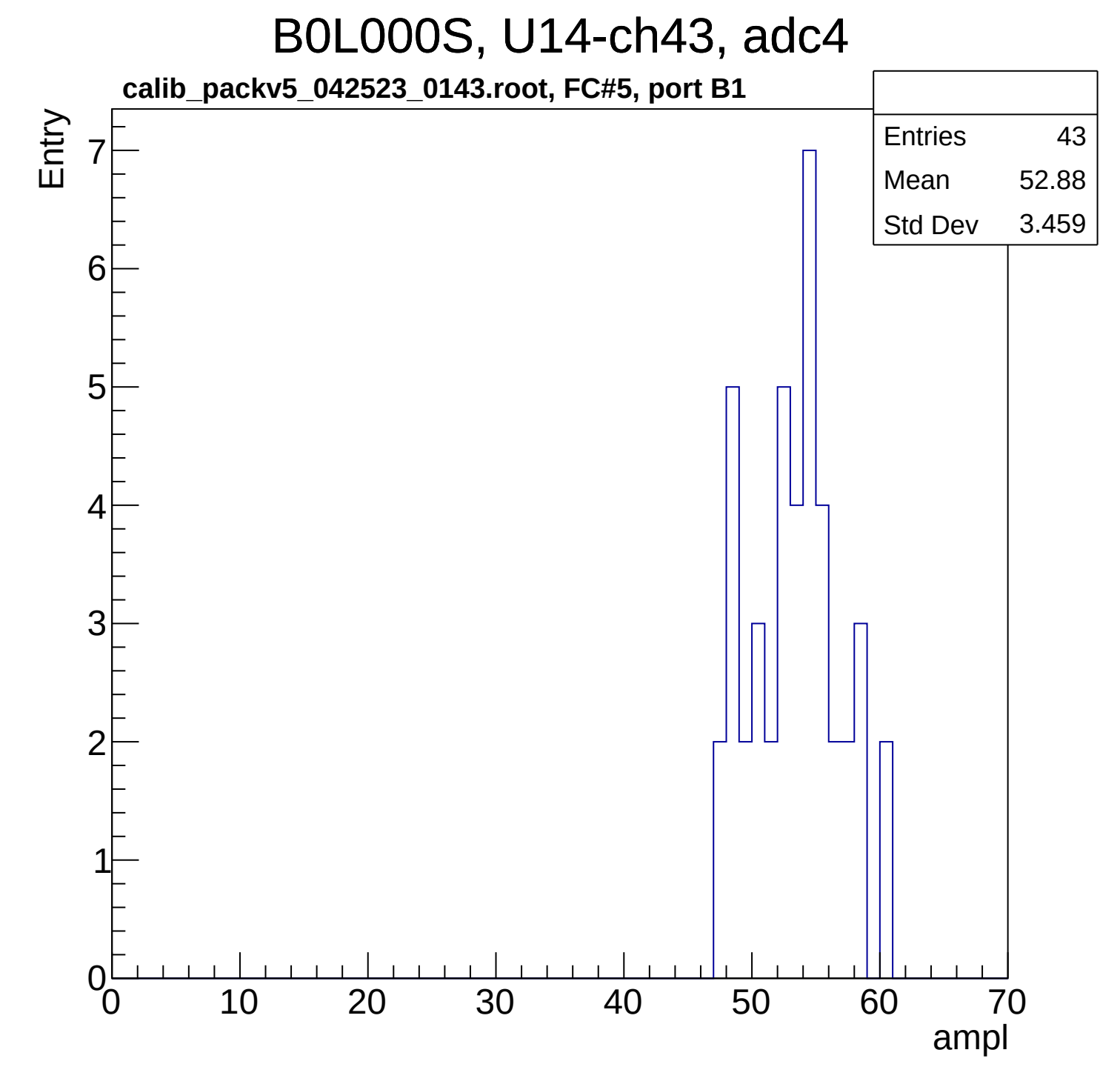
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 52.88
Std Dev 3.459
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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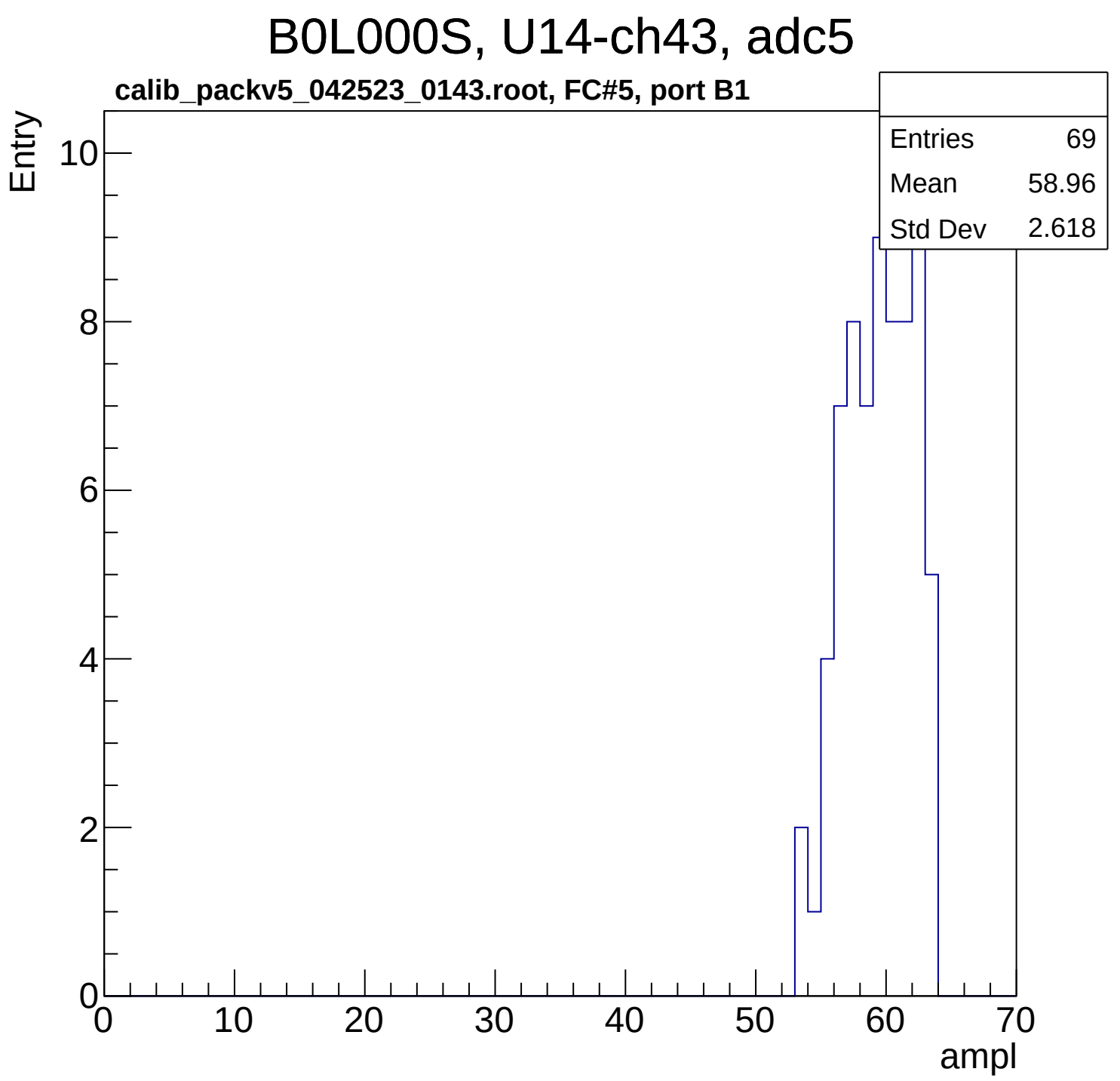
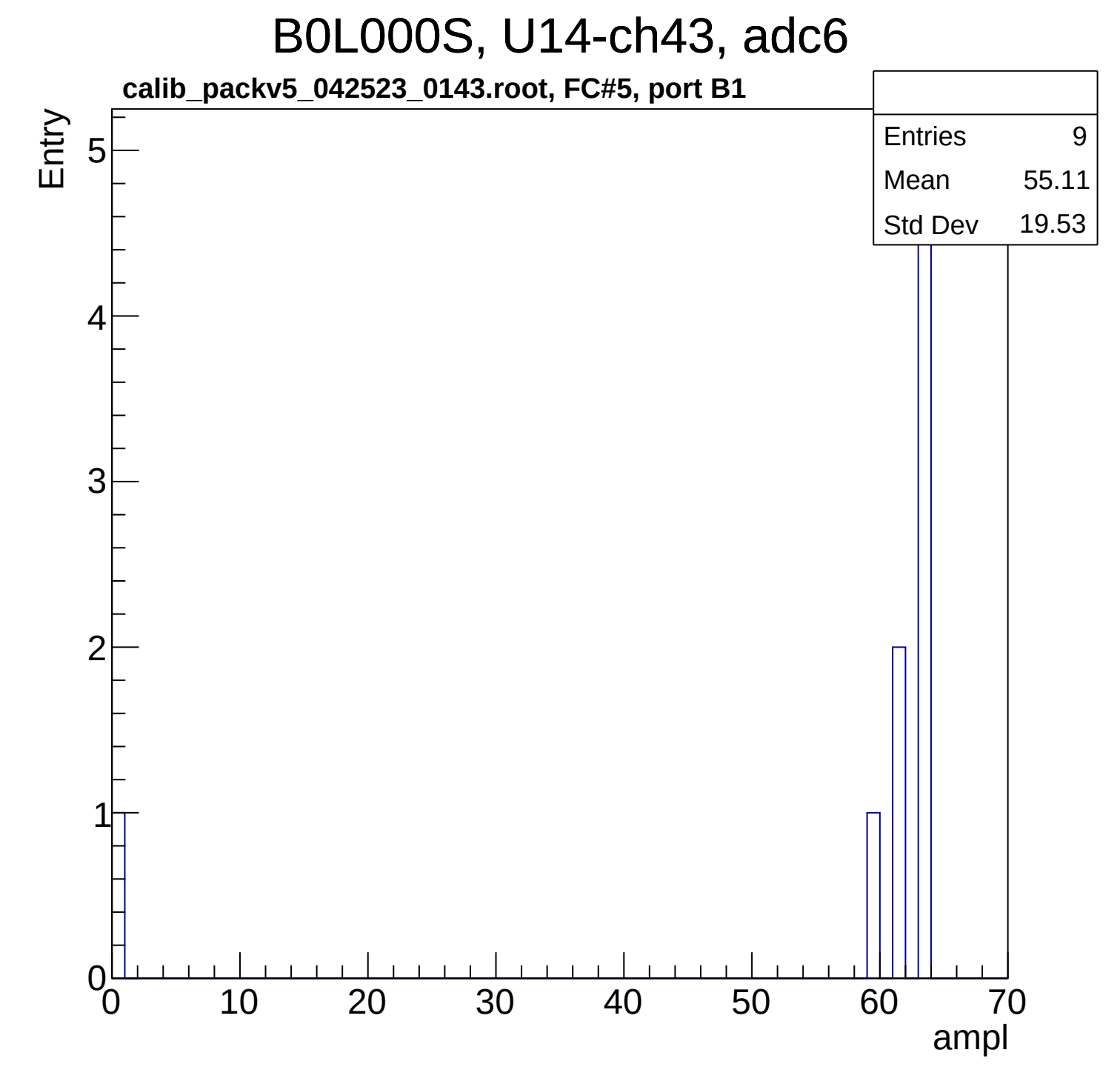
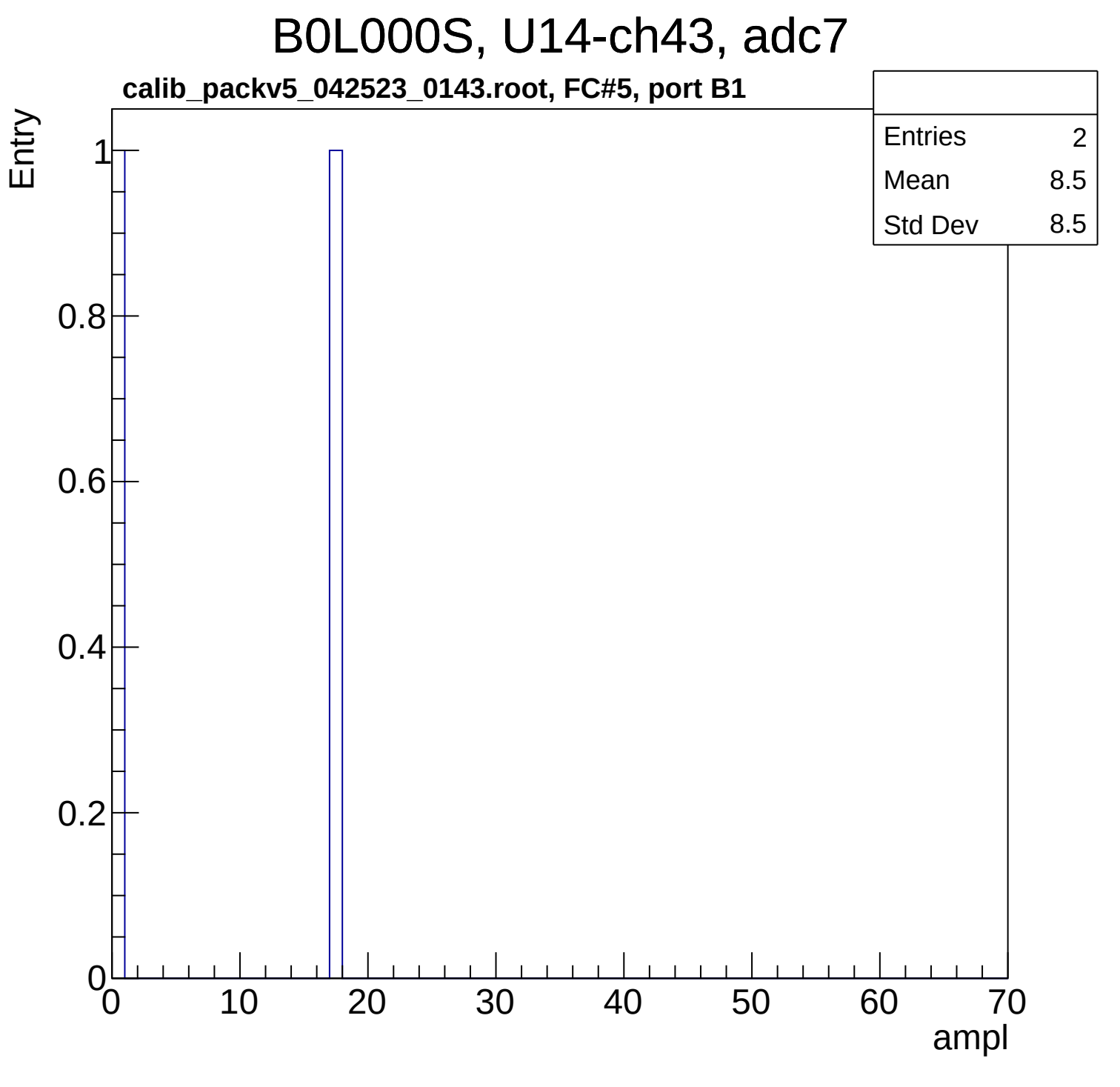

B0L000S, U14-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 58.96
Std Dev 2.618
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 55.11
Std Dev 19.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 8.5
Std Dev 8.5
Entry
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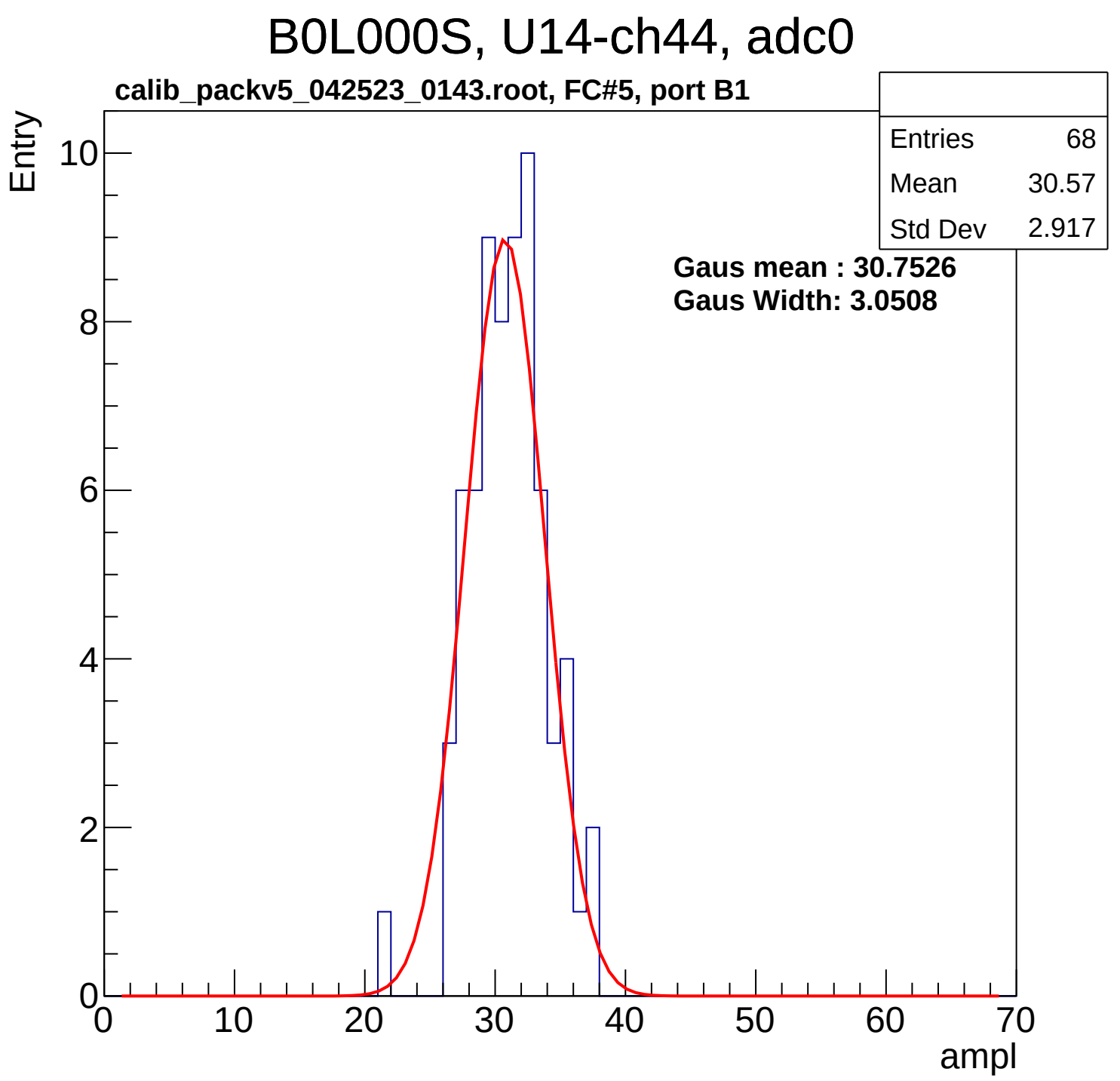
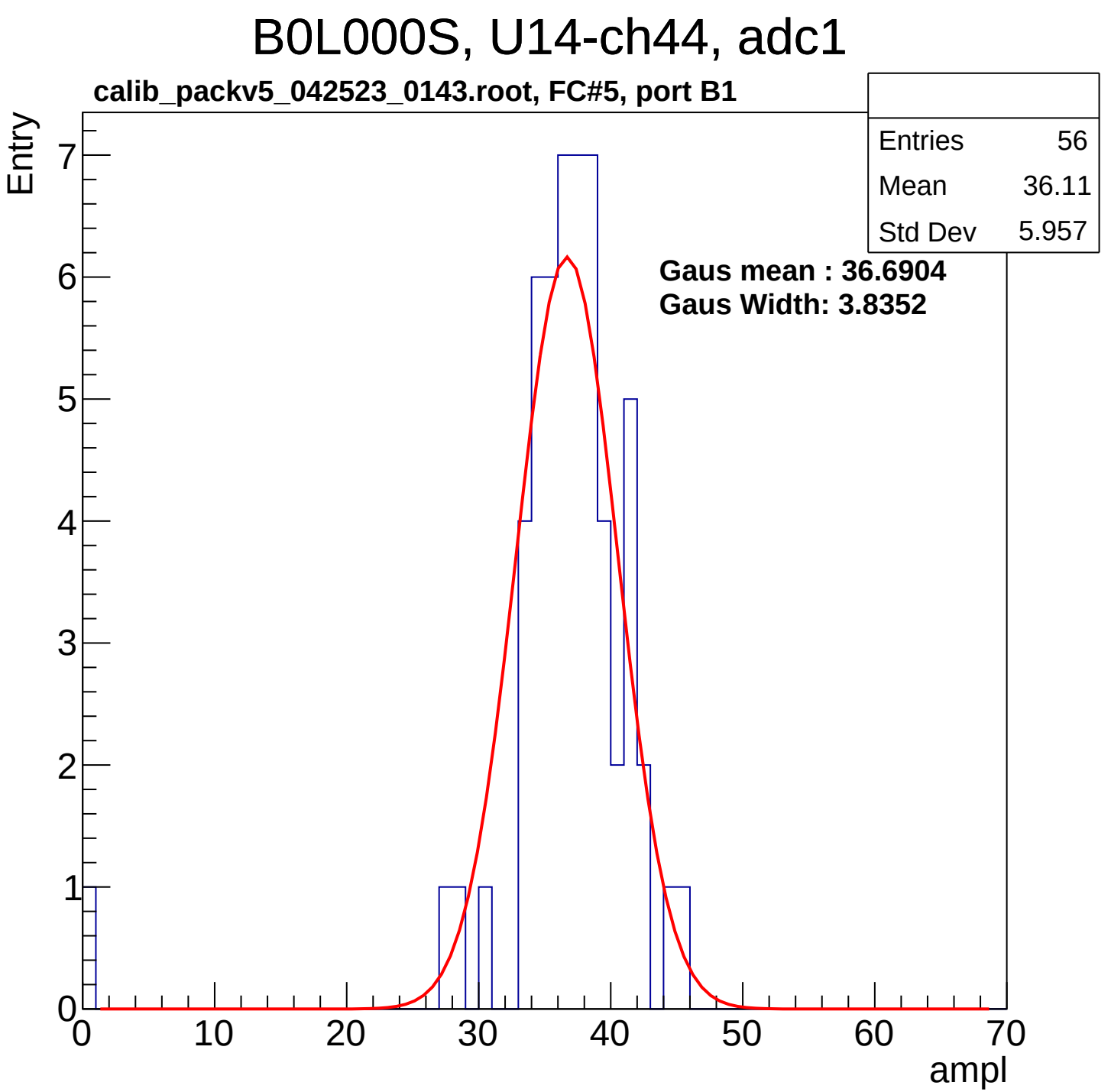
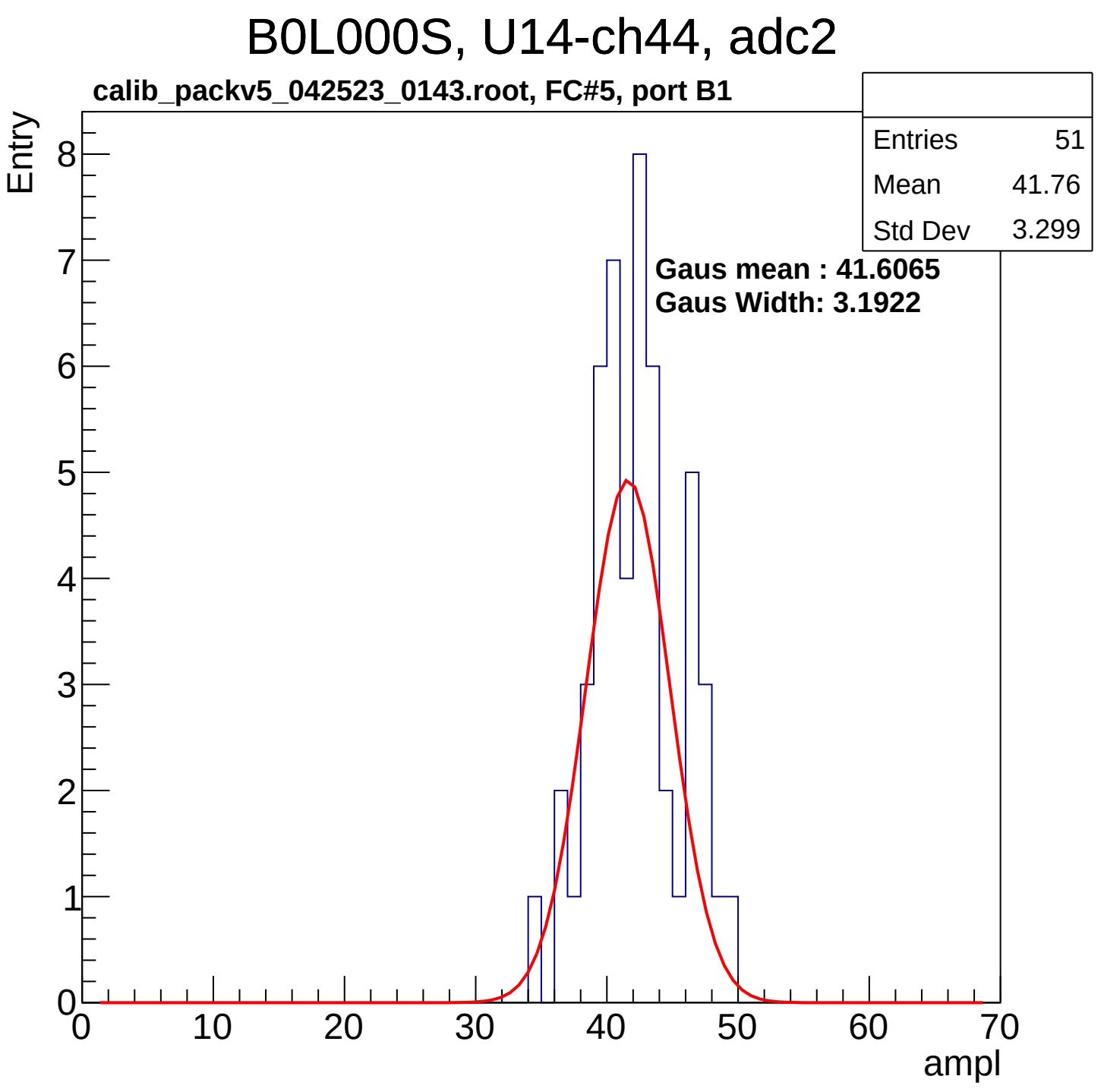
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.57
Std Dev 2.917
Gaus mean : 30.7526
Gaus Width: 3.0508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.11
Std Dev 5.957
Gaus mean : 36.6904
Gaus Width: 3.8352
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 41.76
Std Dev 3.299
Gaus mean : 41.6065
Gaus Width: 3.1922
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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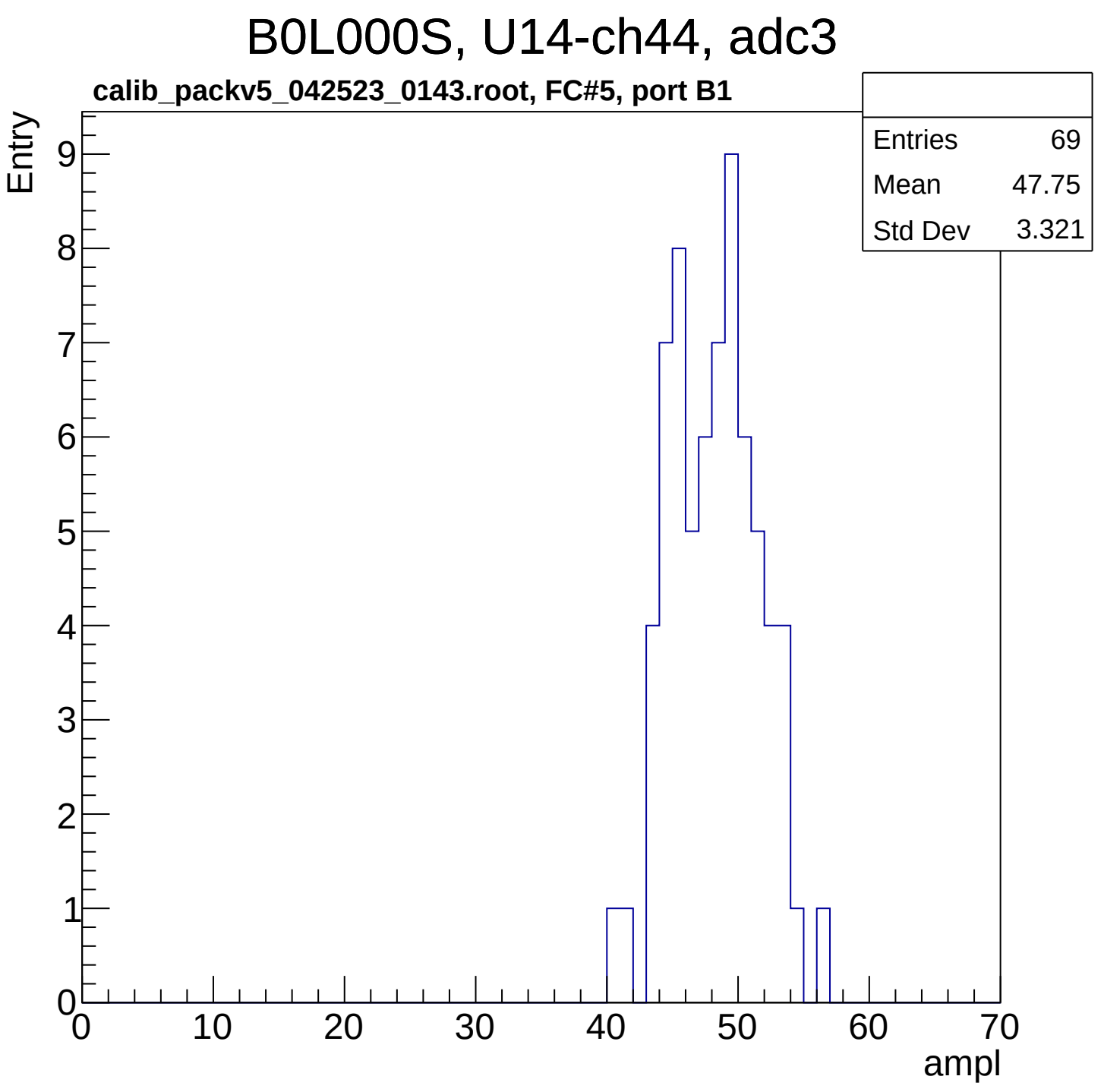
40
50
60
70


B0L000S, U14-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.75
Std Dev 3.321
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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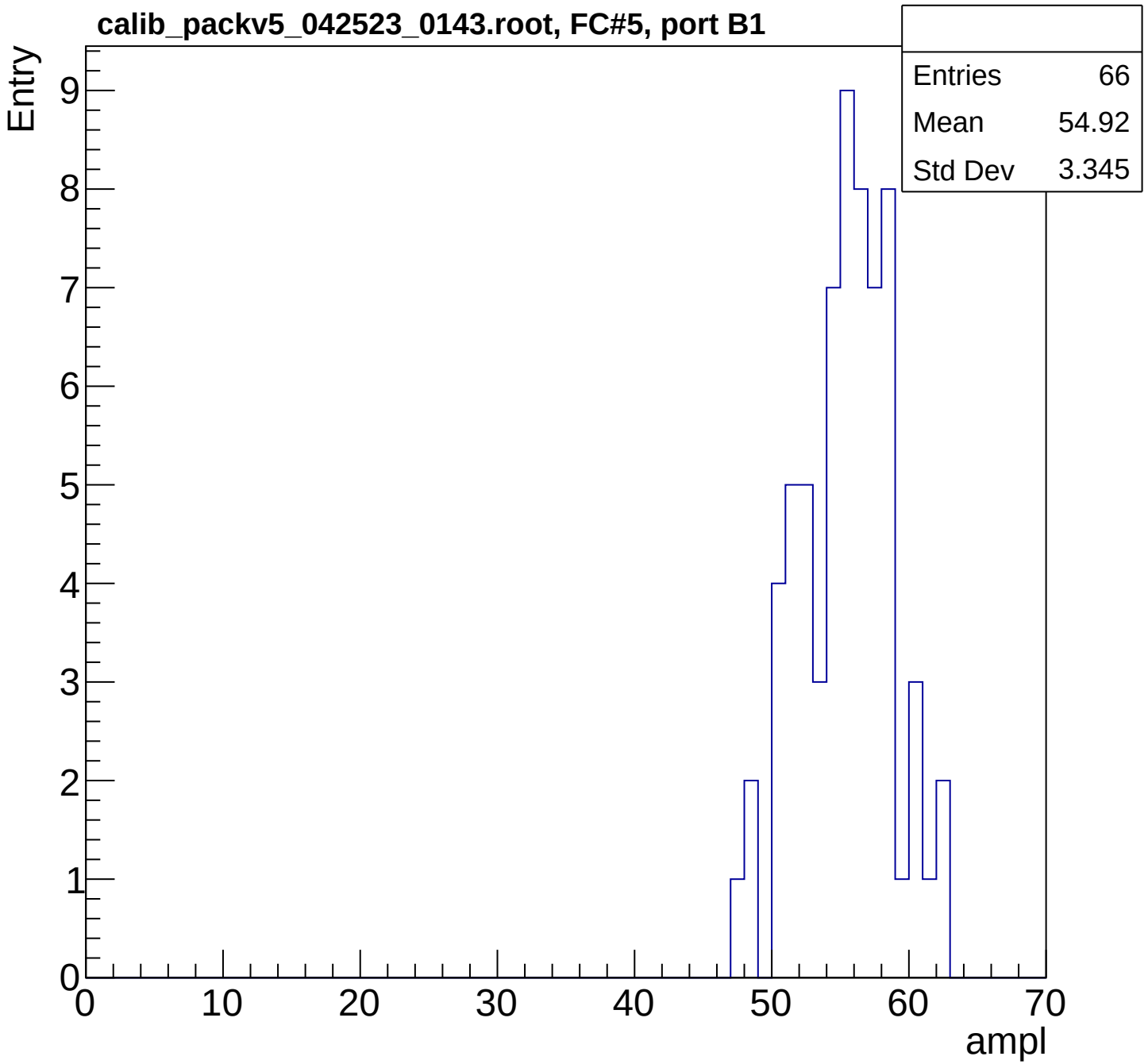
B0L000S, U14-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.92
Std Dev 3.345
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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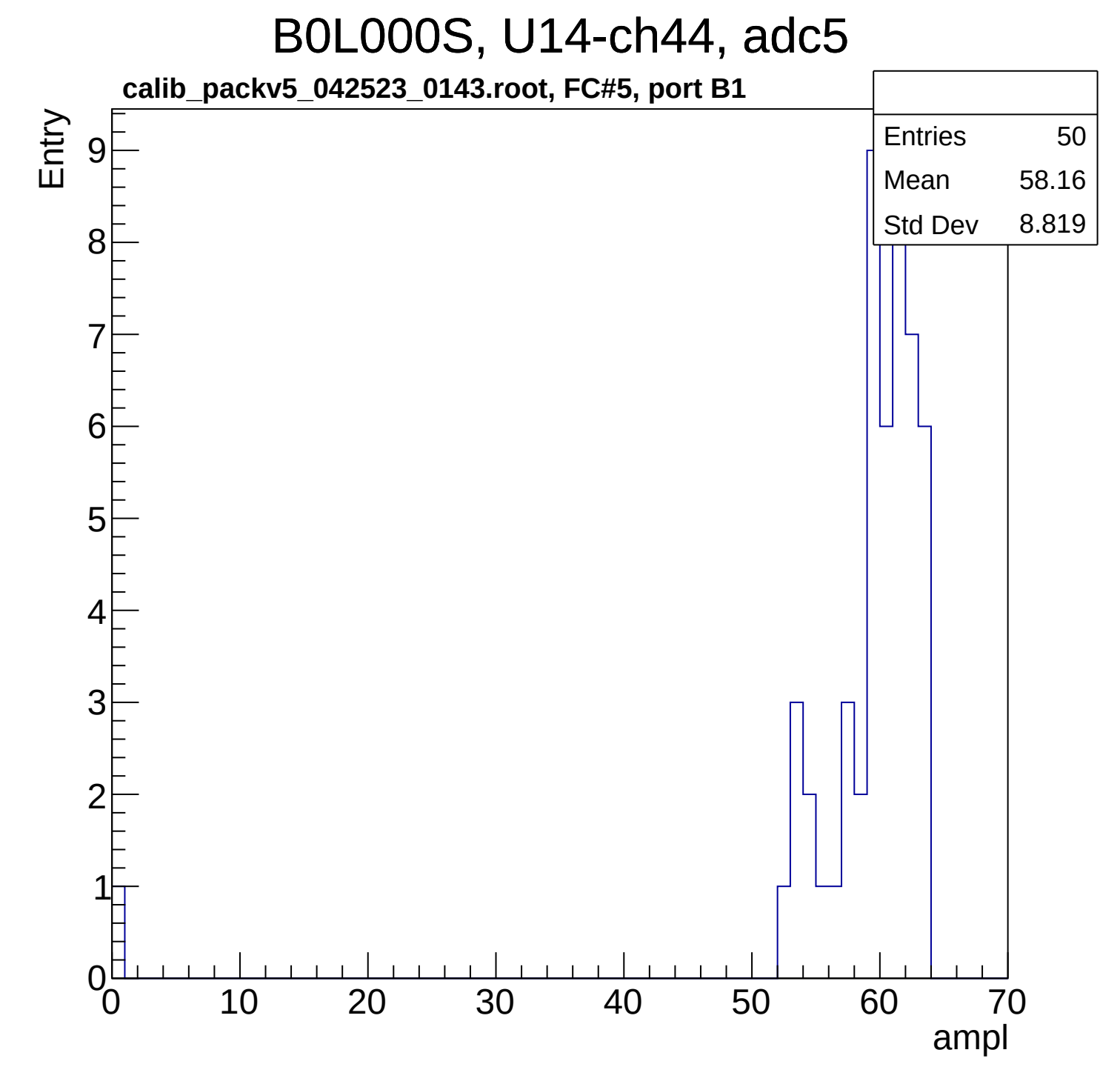
B0L000S, U14-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.16
Std Dev 8.819
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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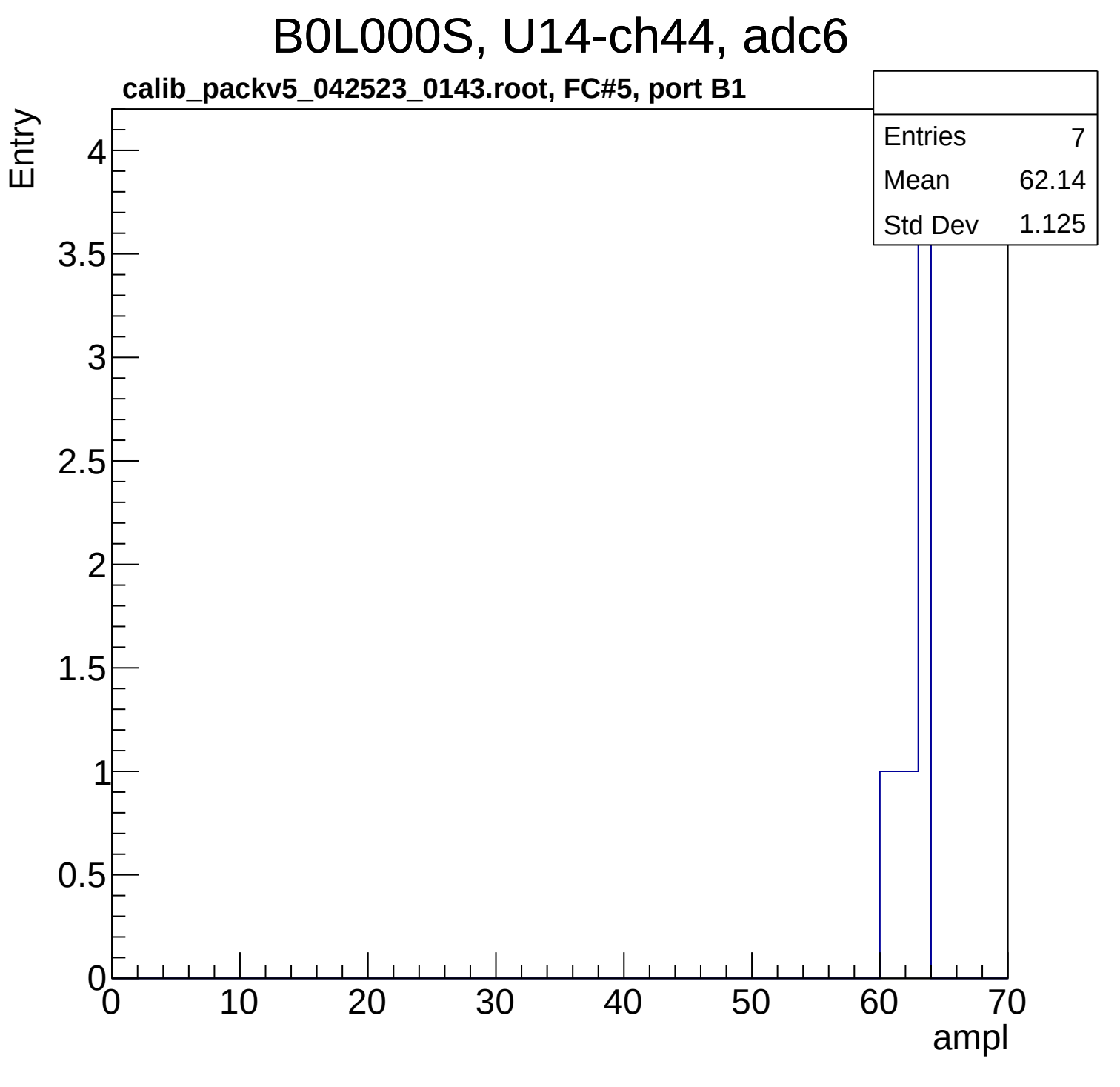
B0L000S, U14-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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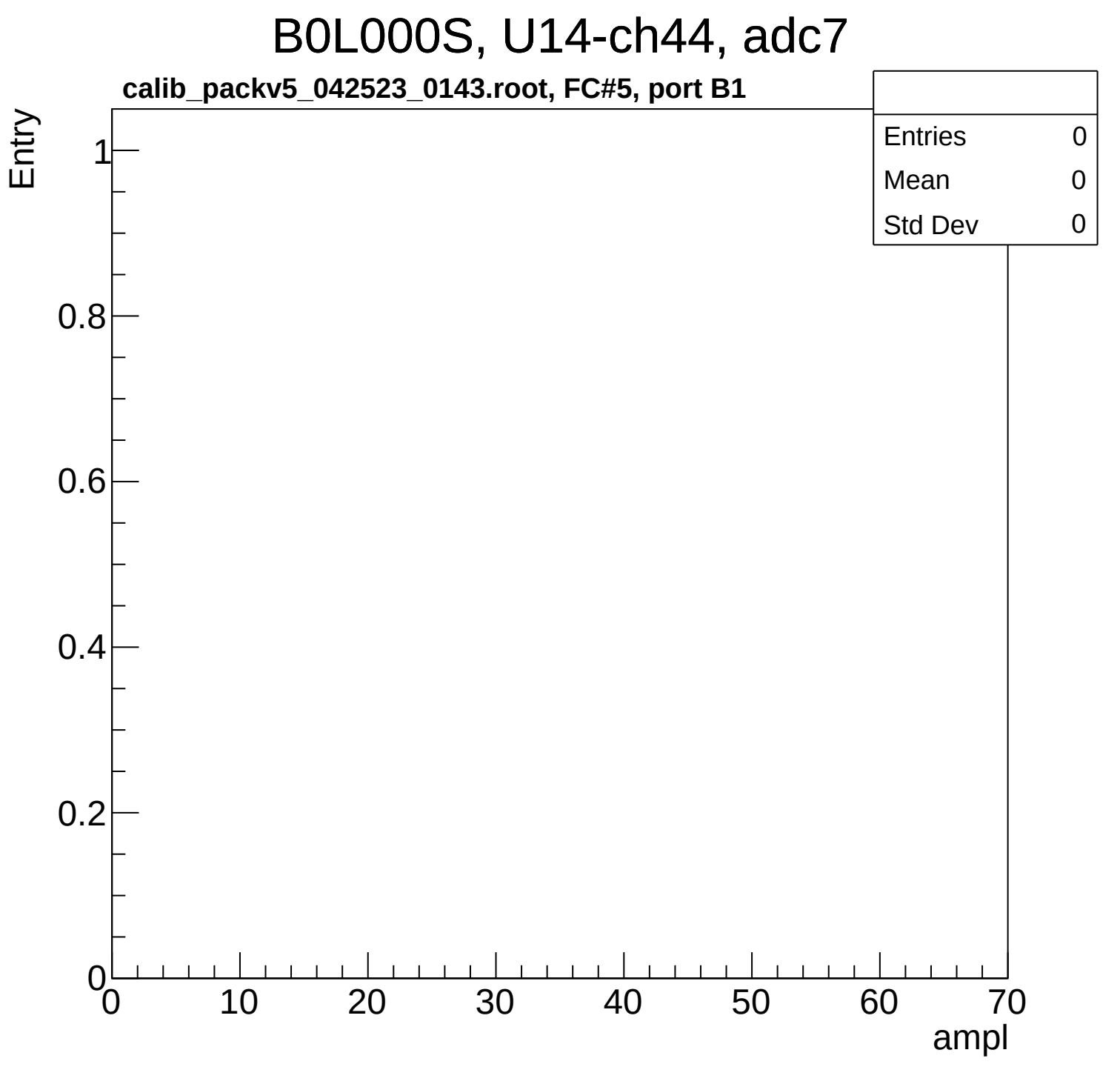
B0L000S, U14-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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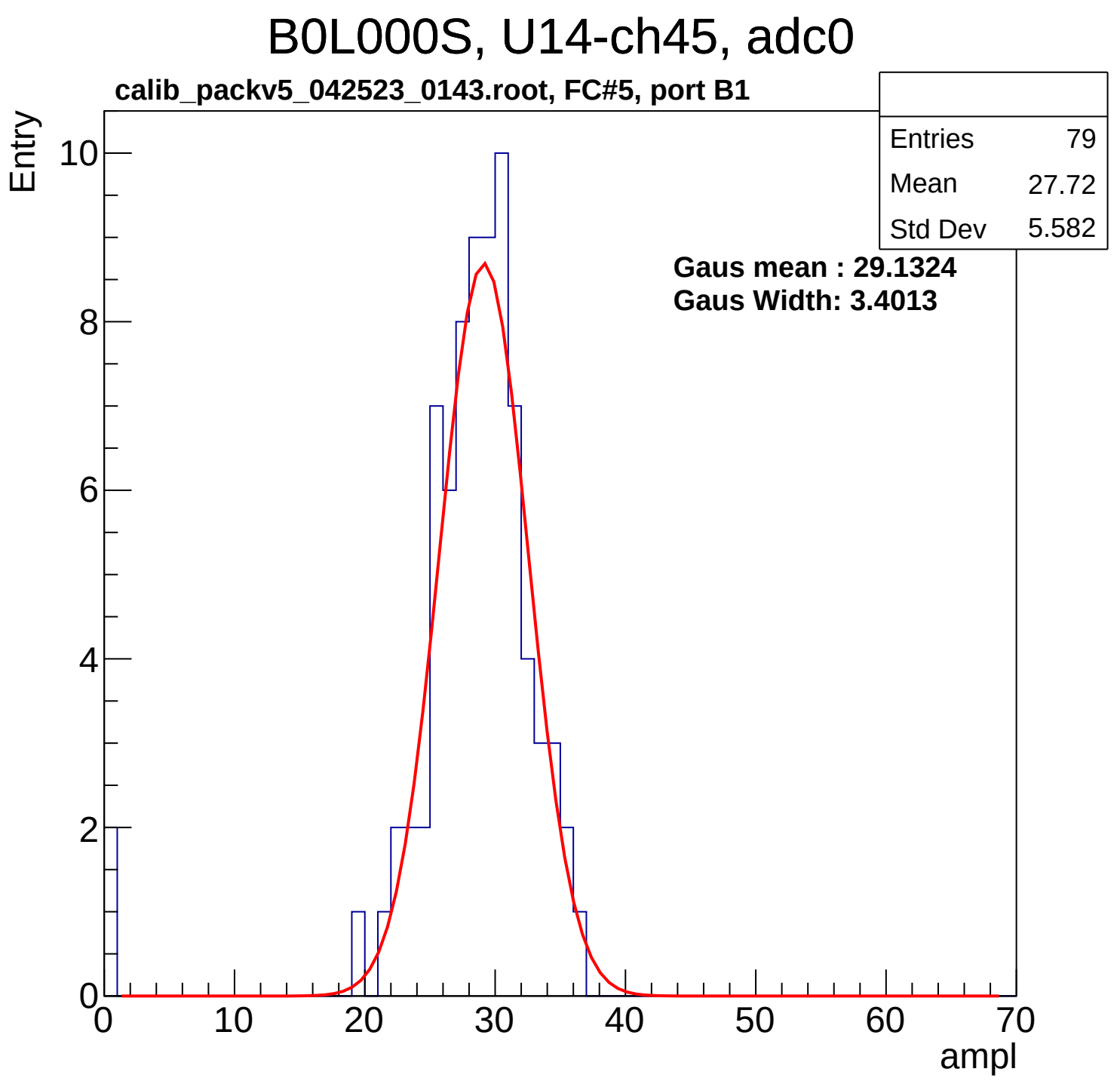
50
60
70
ampl


B0L000S, U14-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 27.72
Std Dev 5.582
Gaus mean : 29.1324
Gaus Width: 3.4013
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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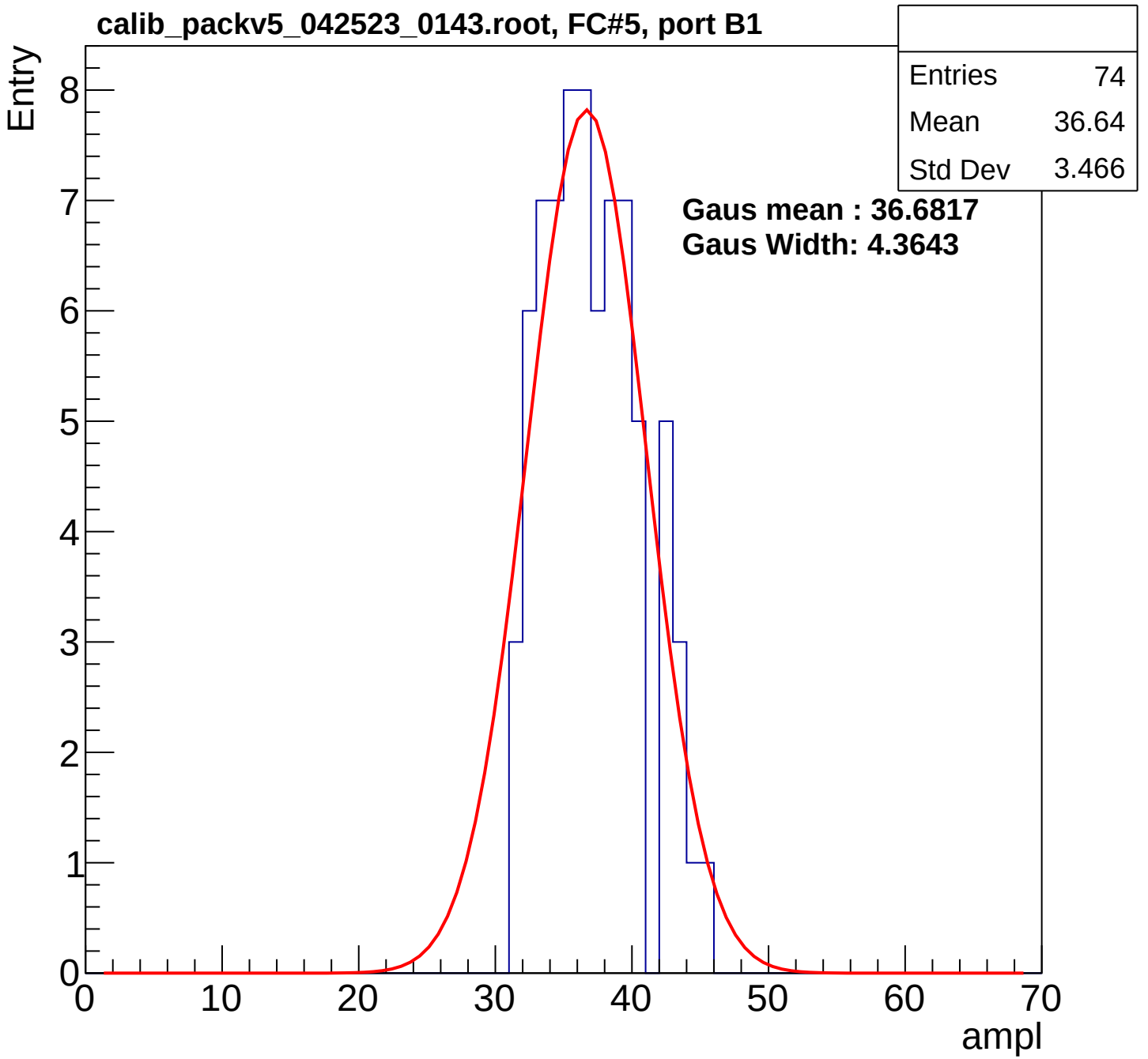
B0L000S, U14-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 36.64
Std Dev 3.466
Gaus mean : 36.6817
Gaus Width: 4.3643
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
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60
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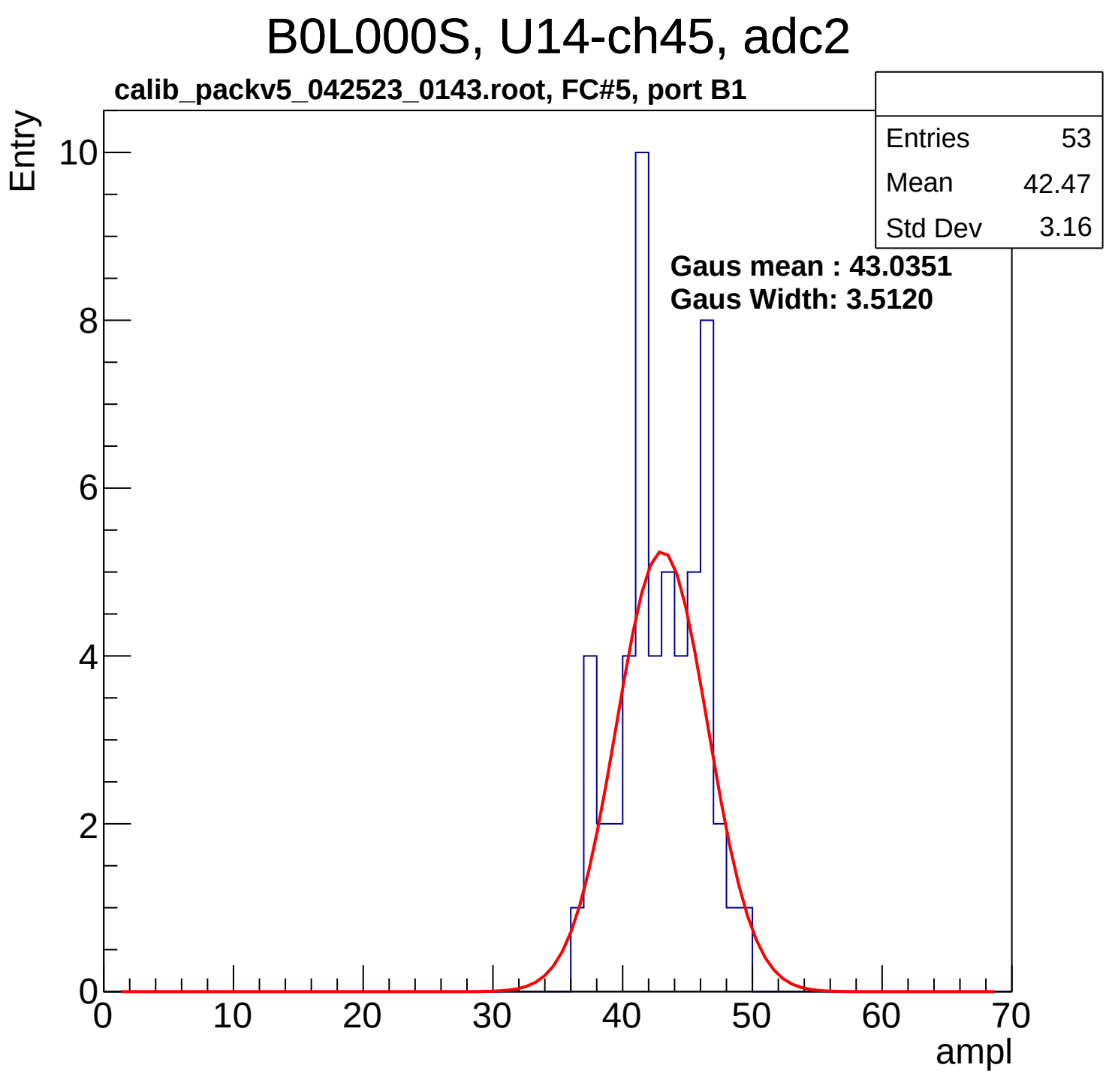
B0L000S, U14-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.47
Std Dev 3.16
Gaus mean : 43.0351
Gaus Width: 3.5120
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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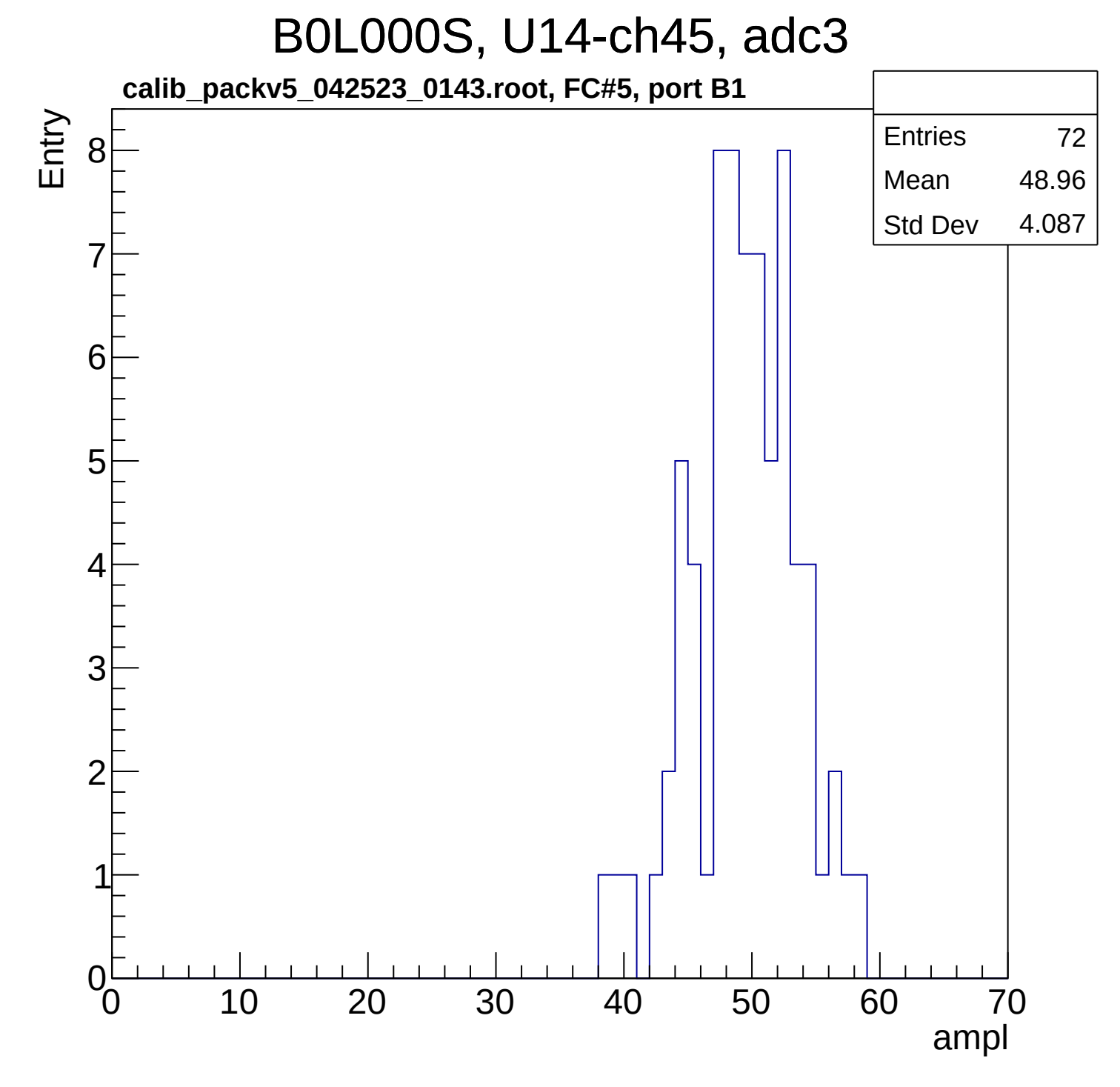
B0L000S, U14-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 48.96
Std Dev 4.087
Entry
ampl
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2
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4
5
6
7
8
0
10
20
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40
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60
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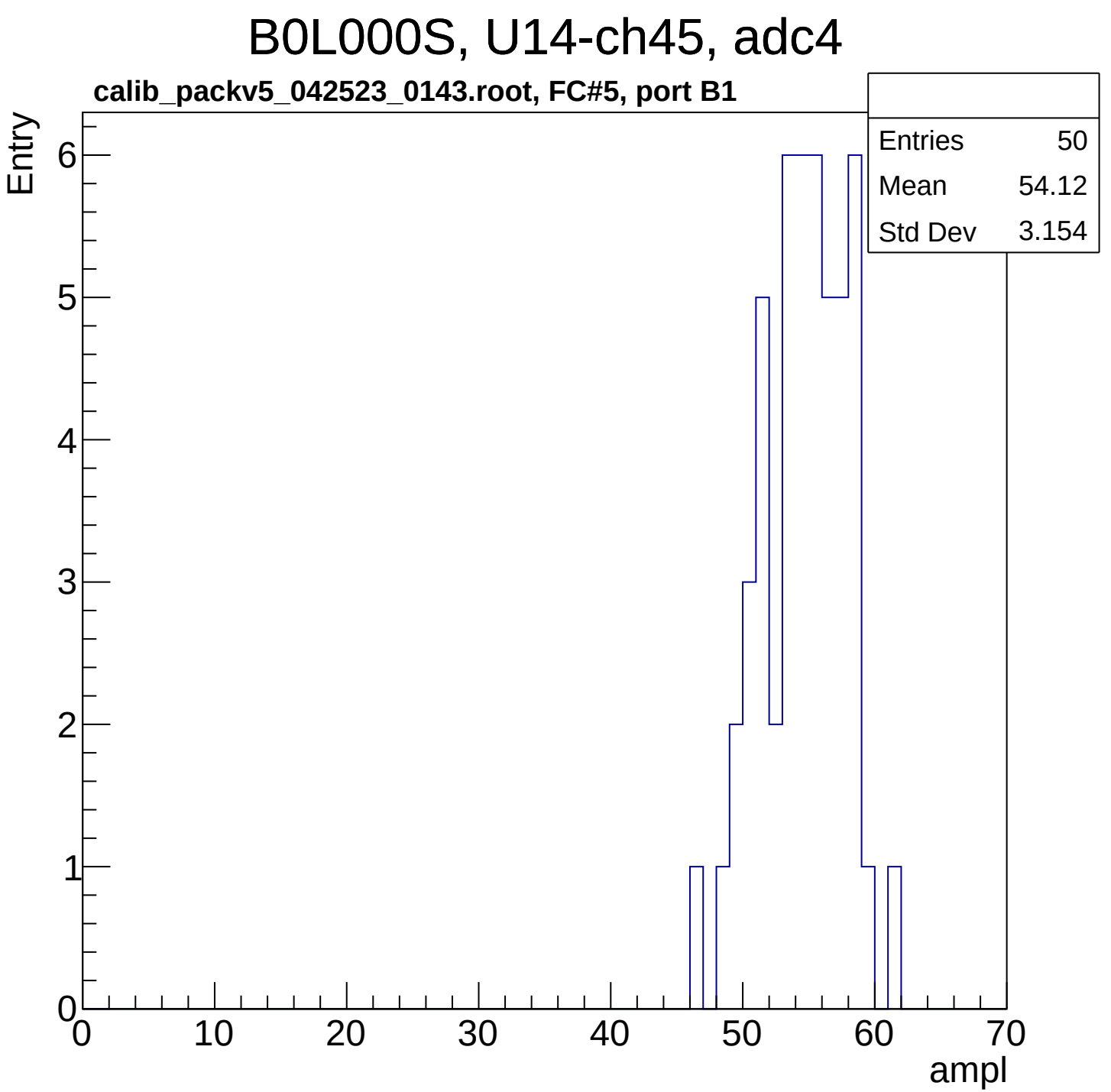
B0L000S, U14-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.12
Std Dev 3.154
Entry
ampl
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0
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60
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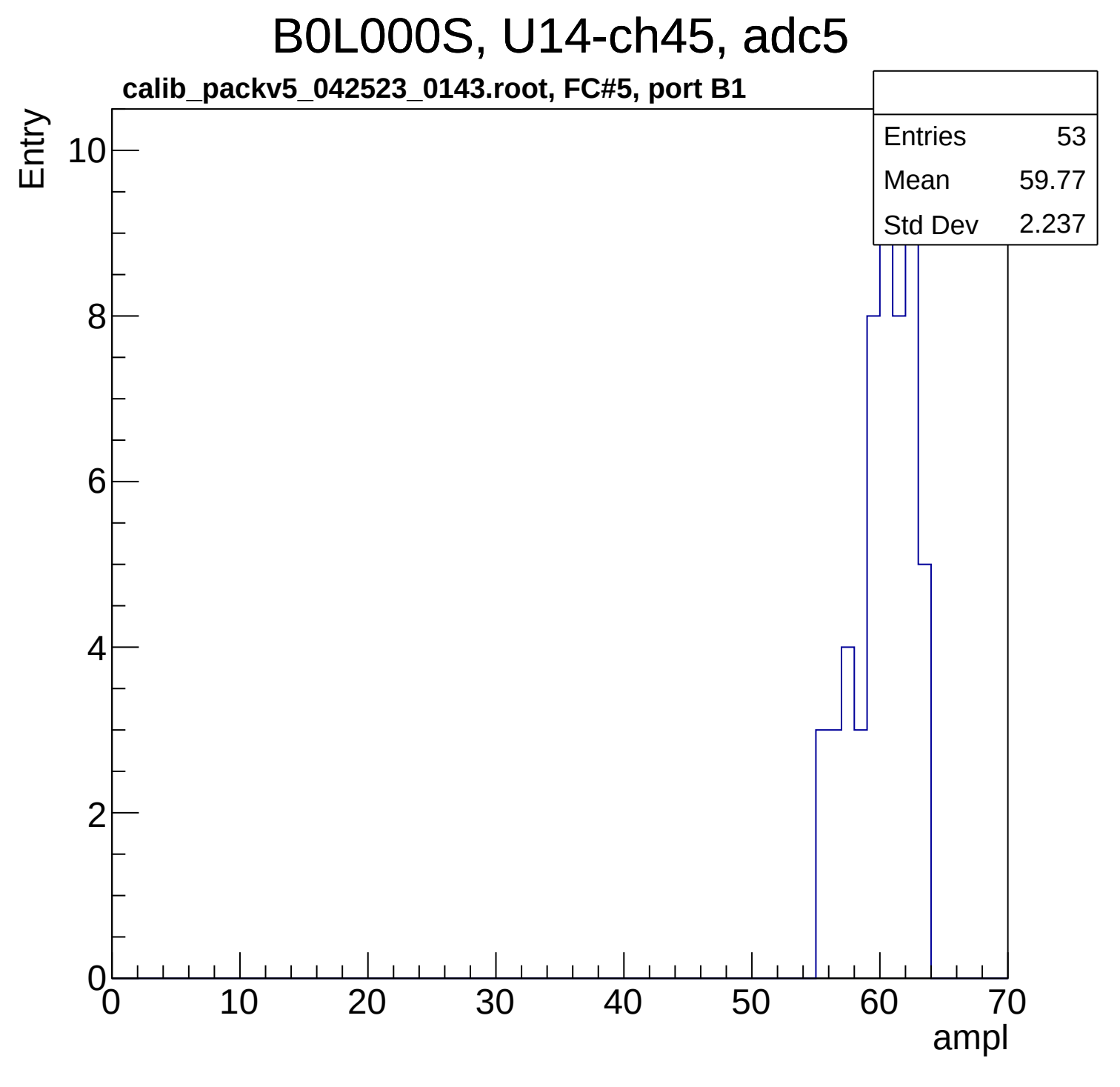
B0L000S, U14-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 59.77
Std Dev 2.237
Entry
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4
6
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10
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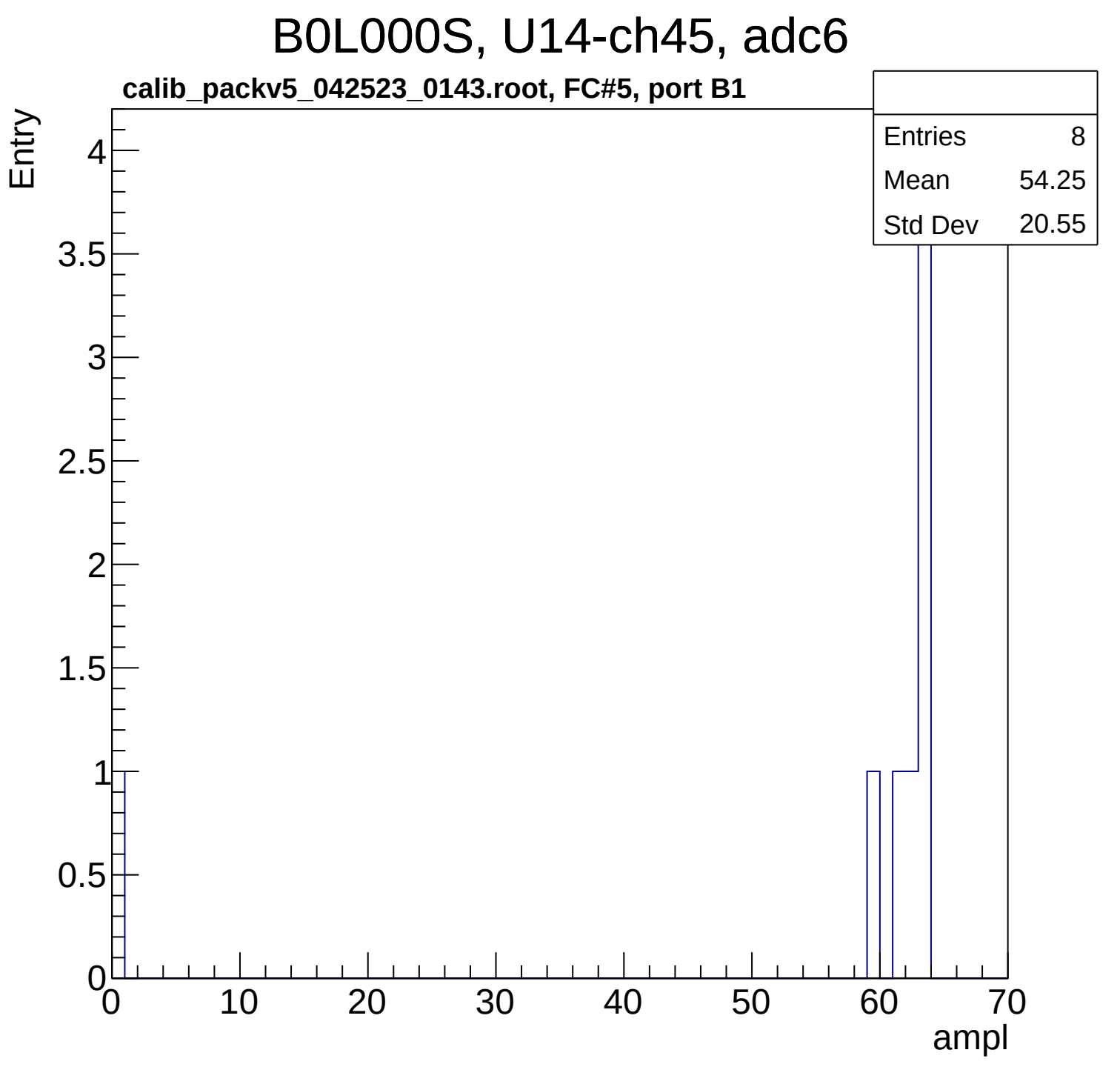
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.25
Std Dev 20.55
Entry
ampl
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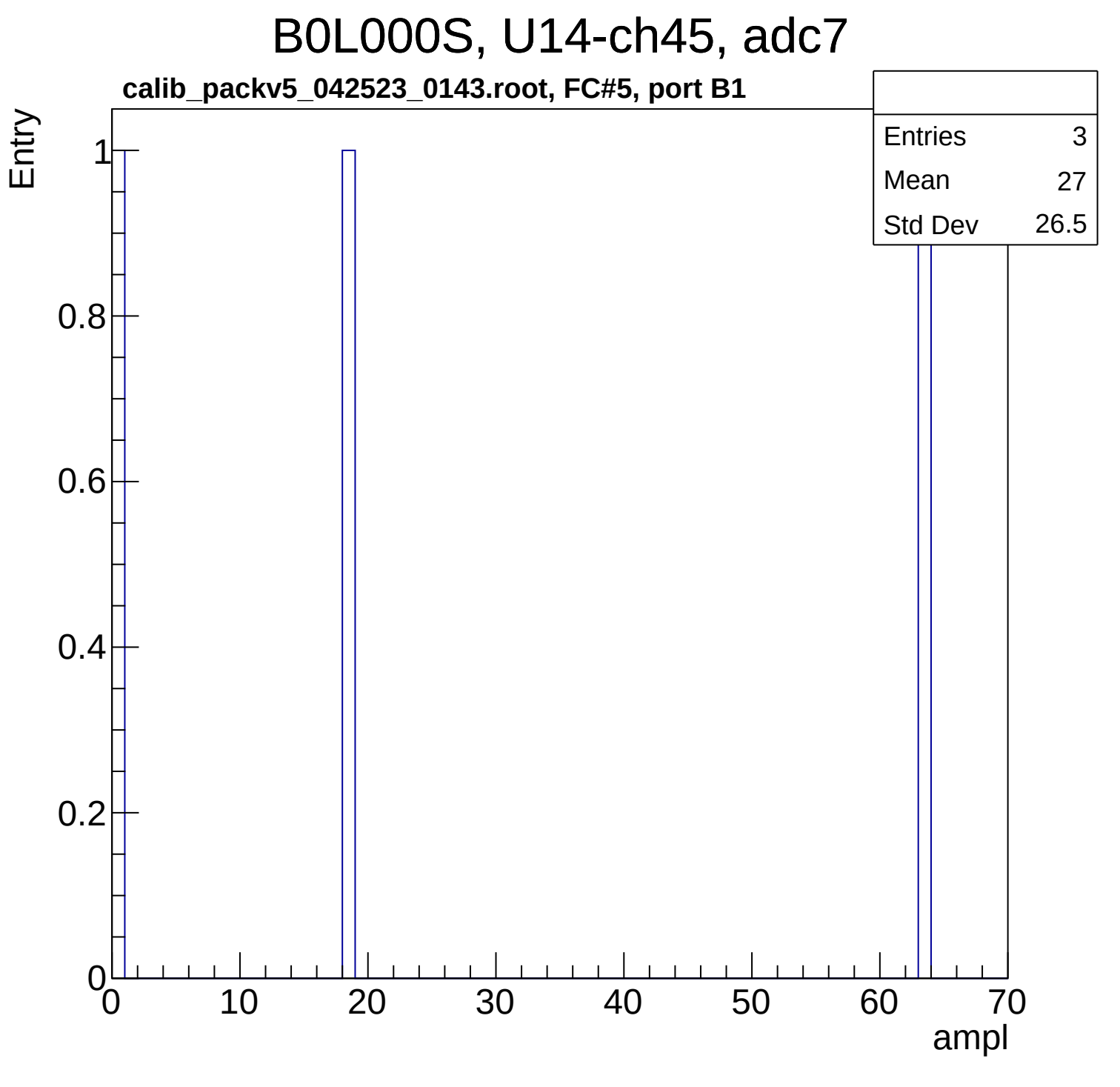
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27
Std Dev 26.5
Entry
ampl
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0.6
0.8
1
0
10
20
30
40
50
60
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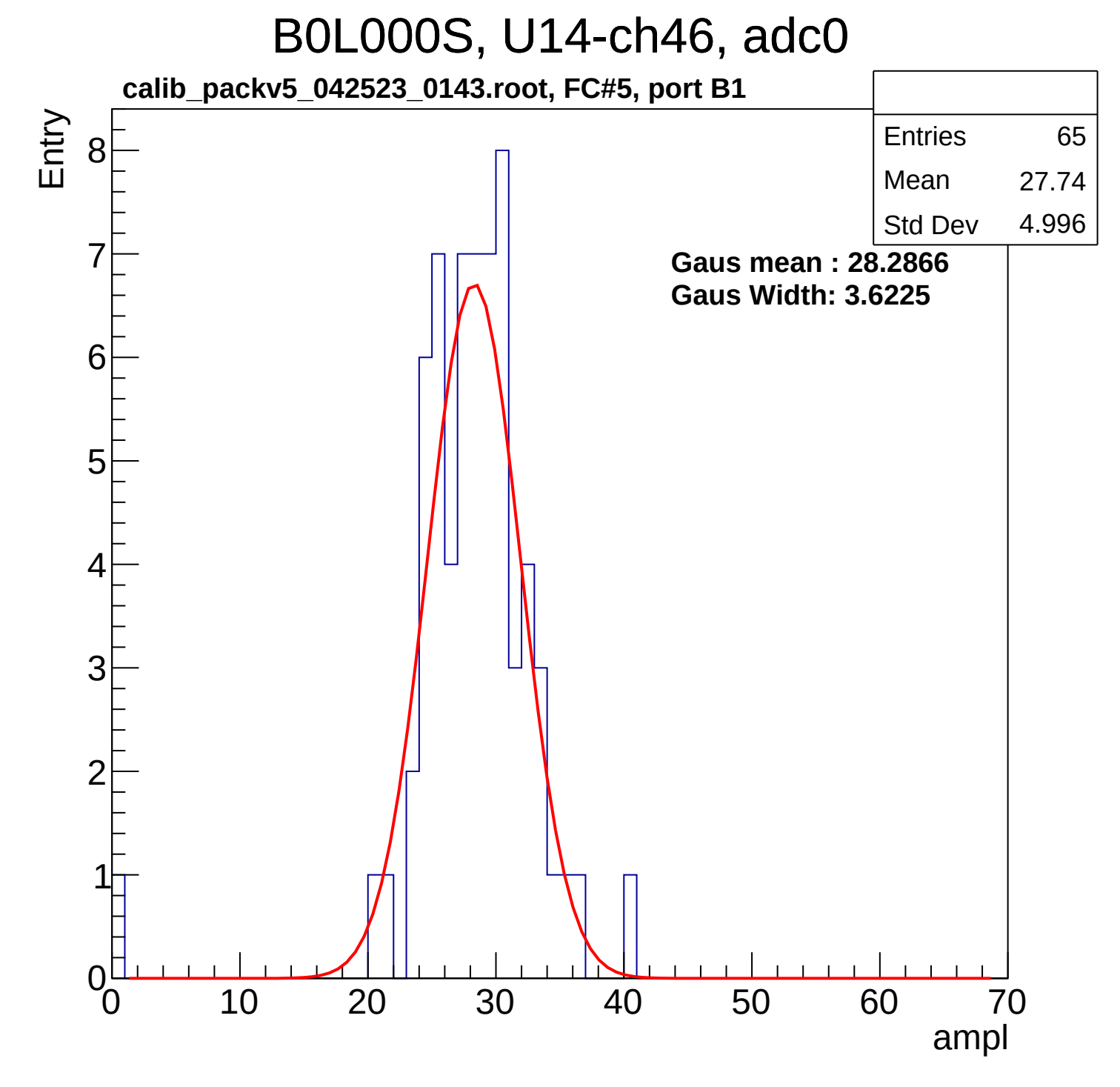
B0L000S, U14-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 27.74
Std Dev 4.996
Gaus mean : 28.2866
Gaus Width: 3.6225
Entry
ampl
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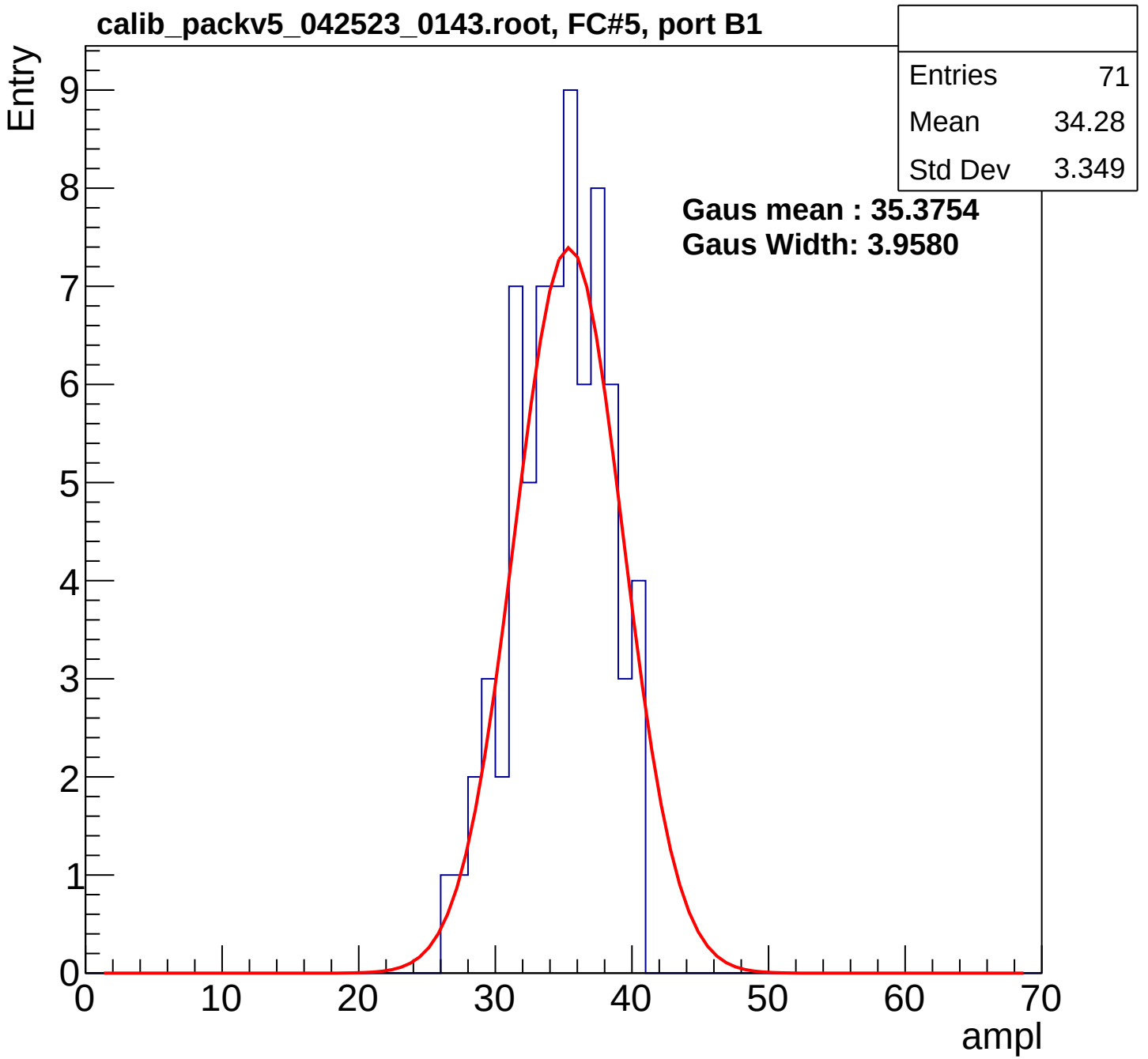
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 34.28
Std Dev 3.349
Gaus mean : 35.3754
Gaus Width: 3.9580
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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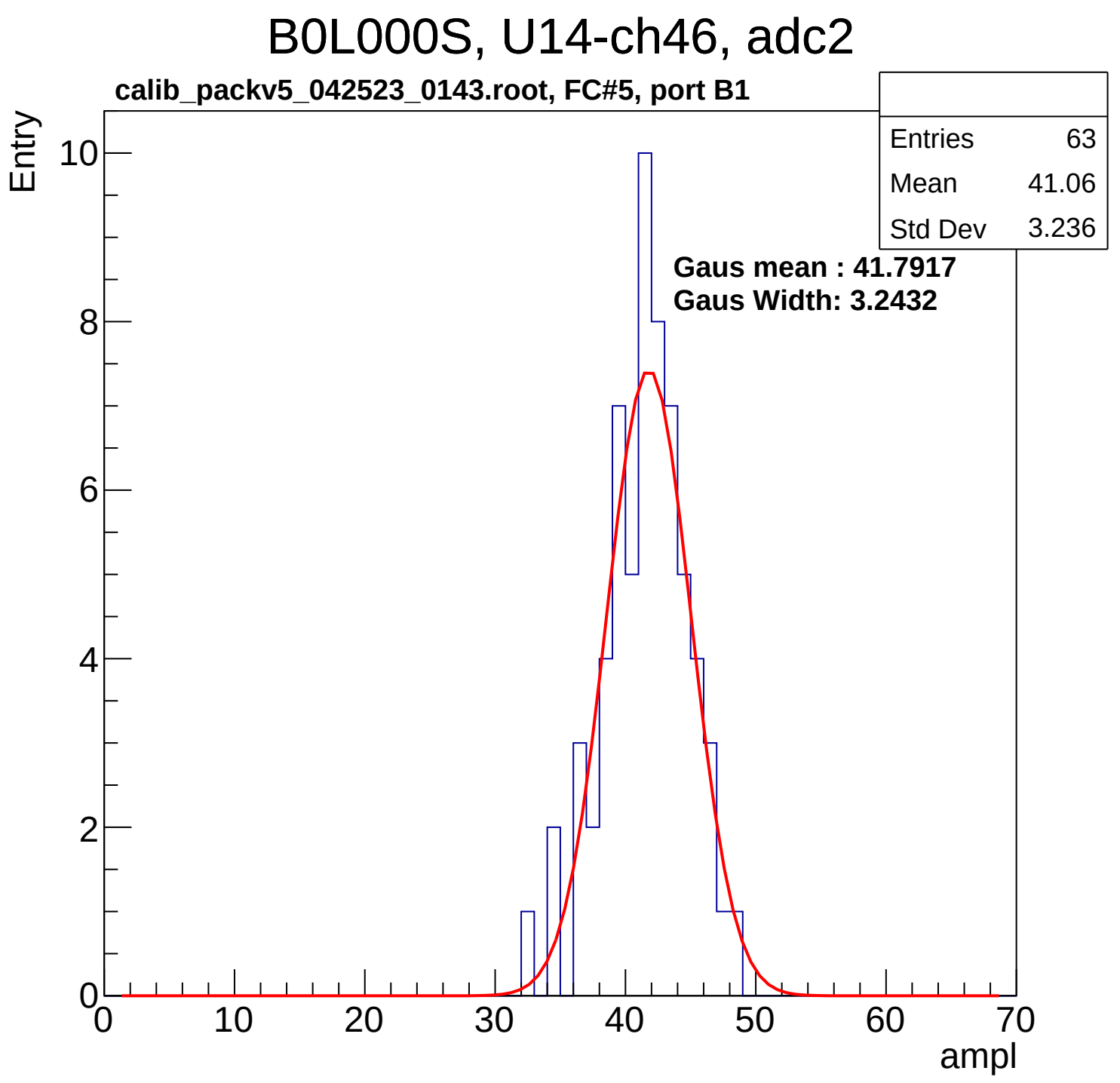
B0L000S, U14-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.06
Std Dev 3.236
Gaus mean : 41.7917
Gaus Width: 3.2432
Entry
ampl
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6
8
10
0
10
20
30
40
50
60
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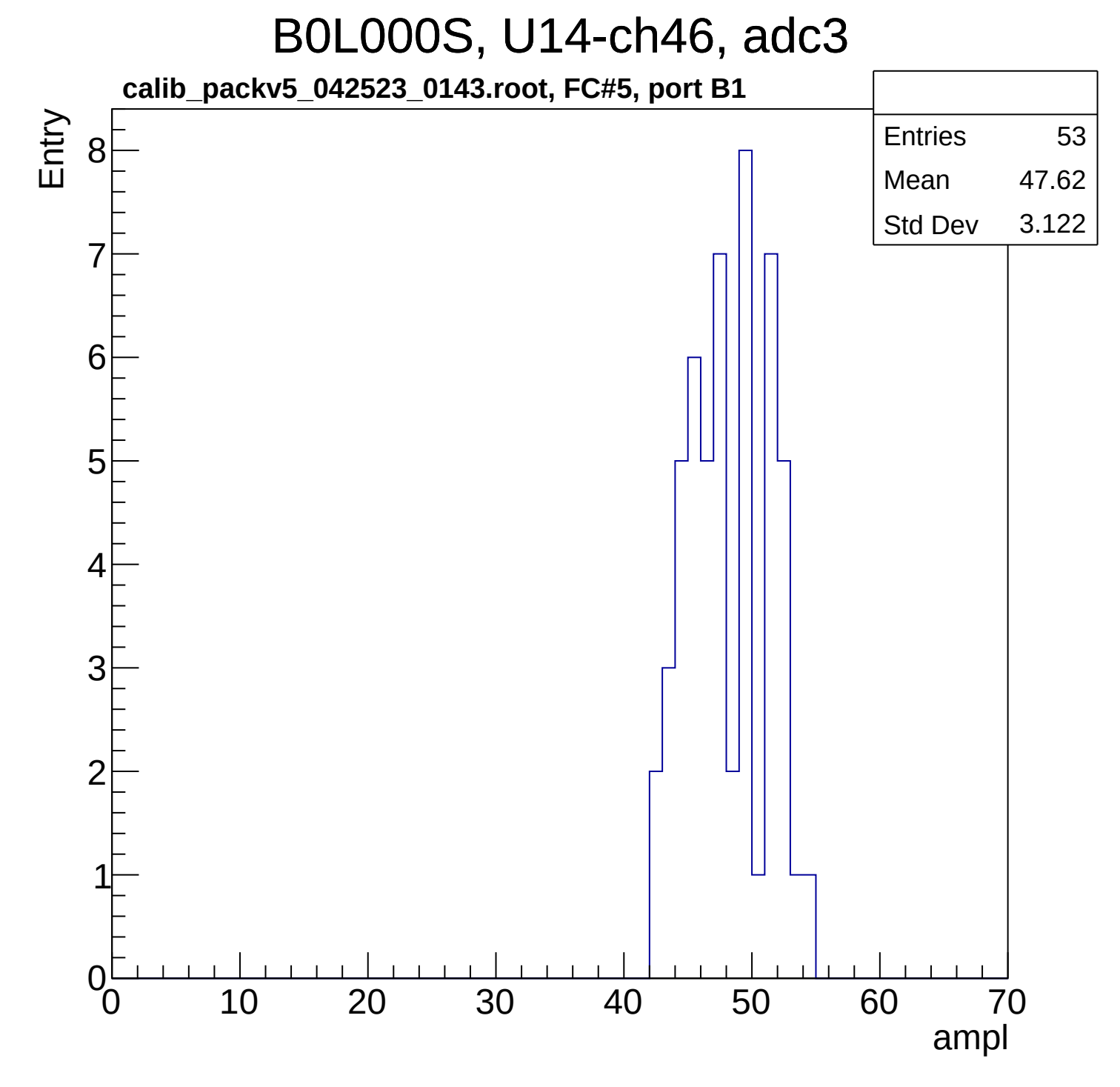
B0L000S, U14-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 47.62
Std Dev 3.122
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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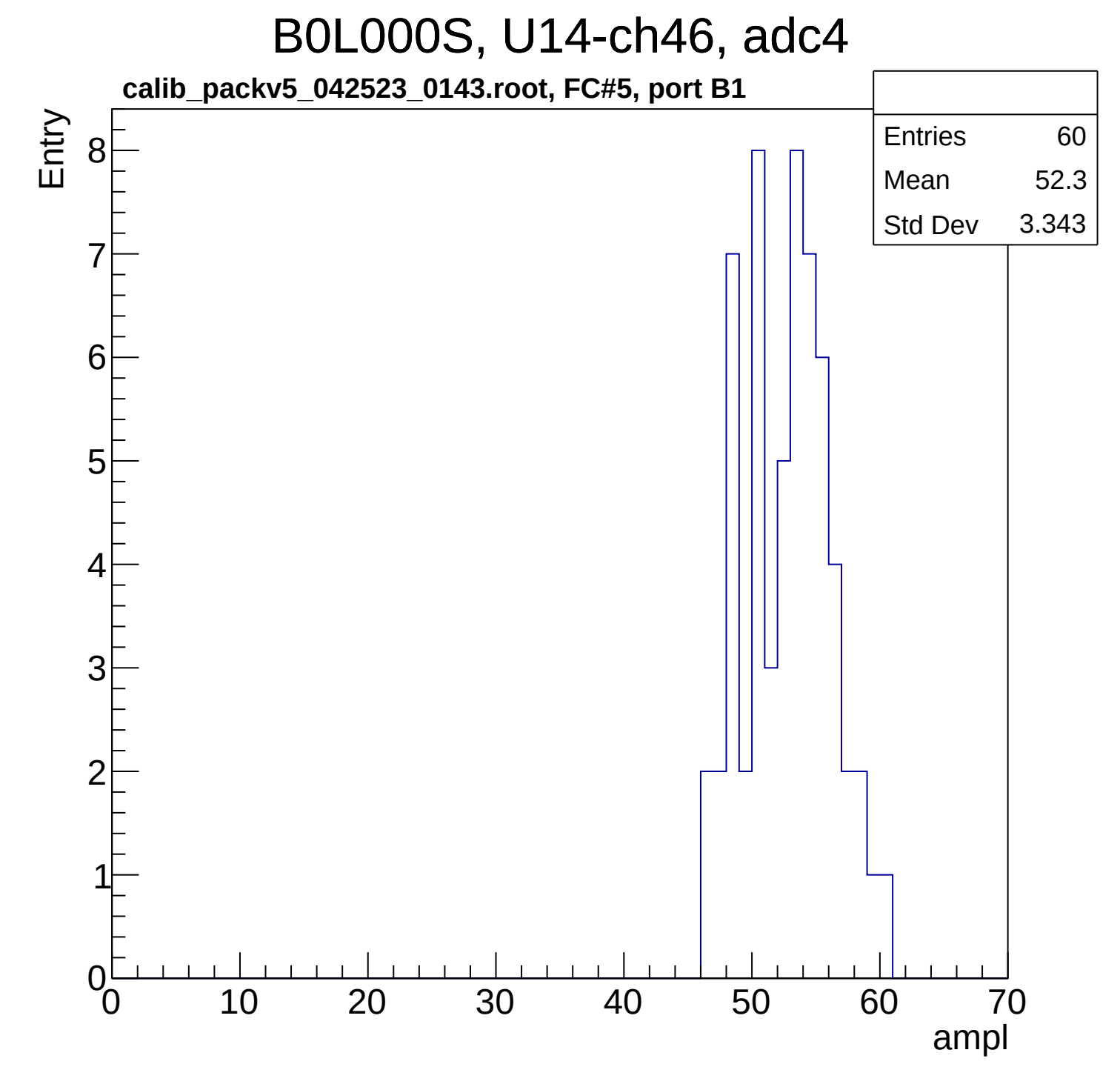
B0L000S, U14-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 52.3
Std Dev 3.343
Entry
ampl
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2
3
4
5
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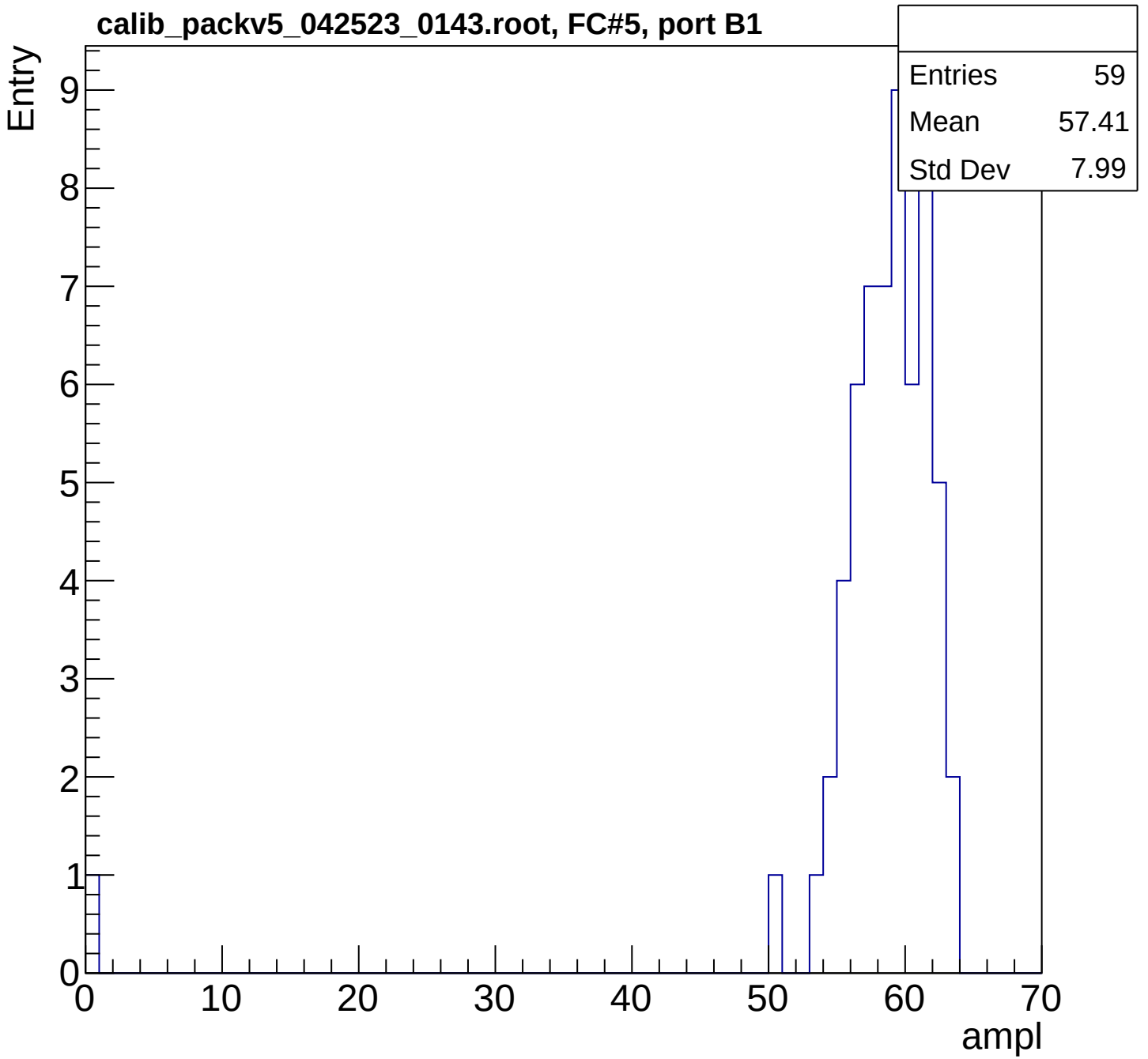
B0L000S, U14-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.41
Std Dev 7.99
Entry
ampl
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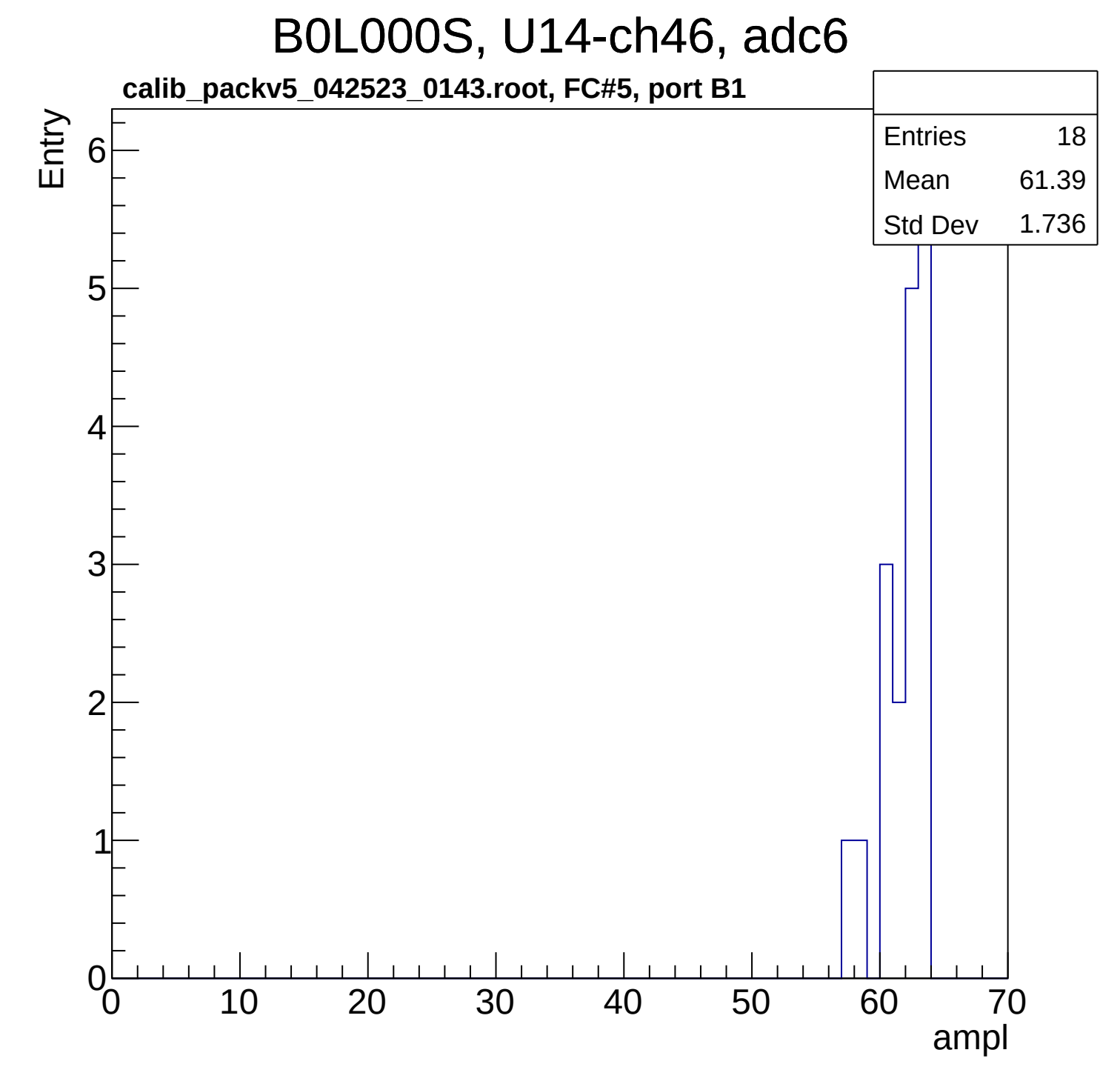
B0L000S, U14-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.39
Std Dev 1.736
Entry
ampl
0
1
2
3
4
5
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0
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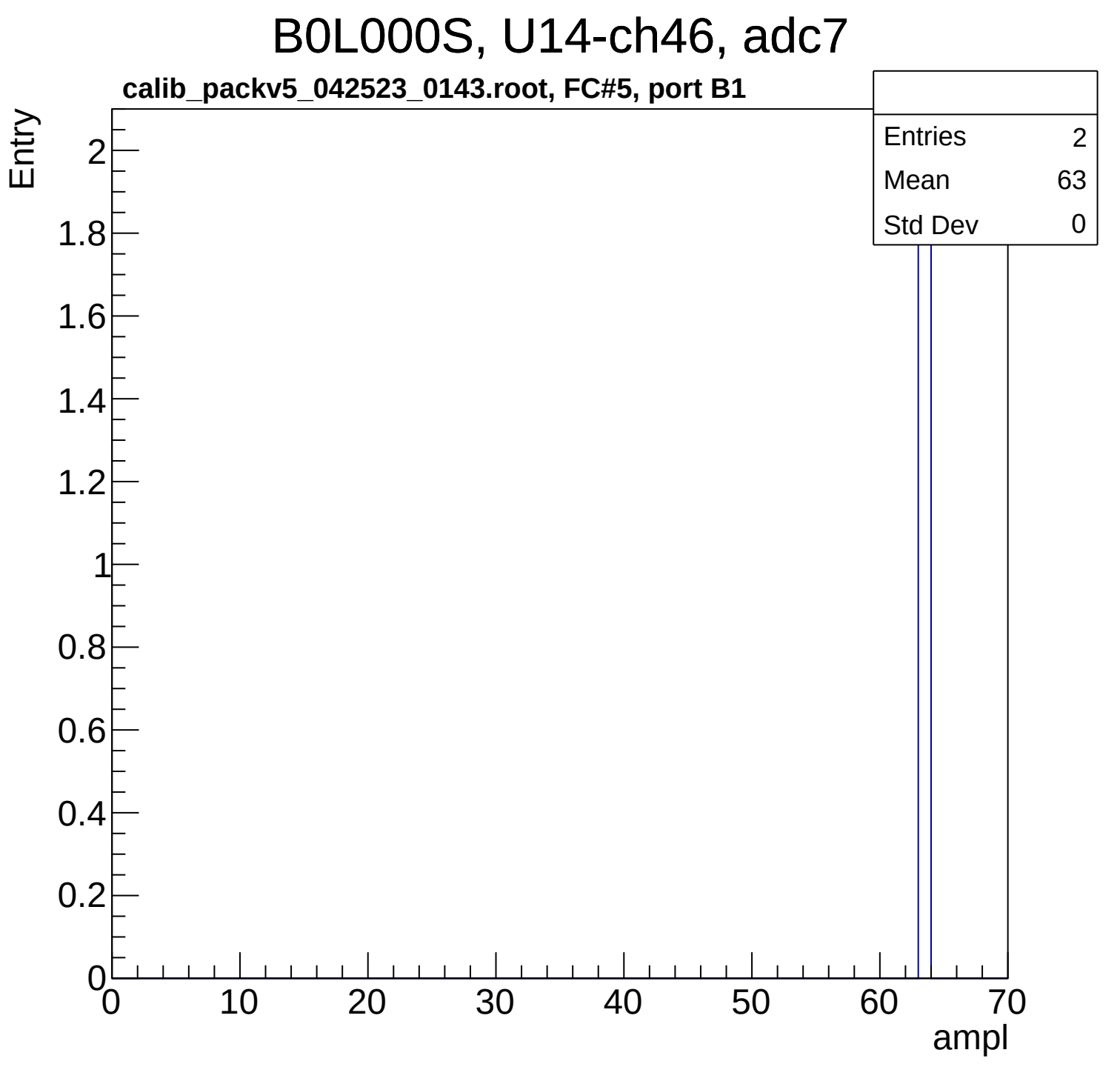
B0L000S, U14-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
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60
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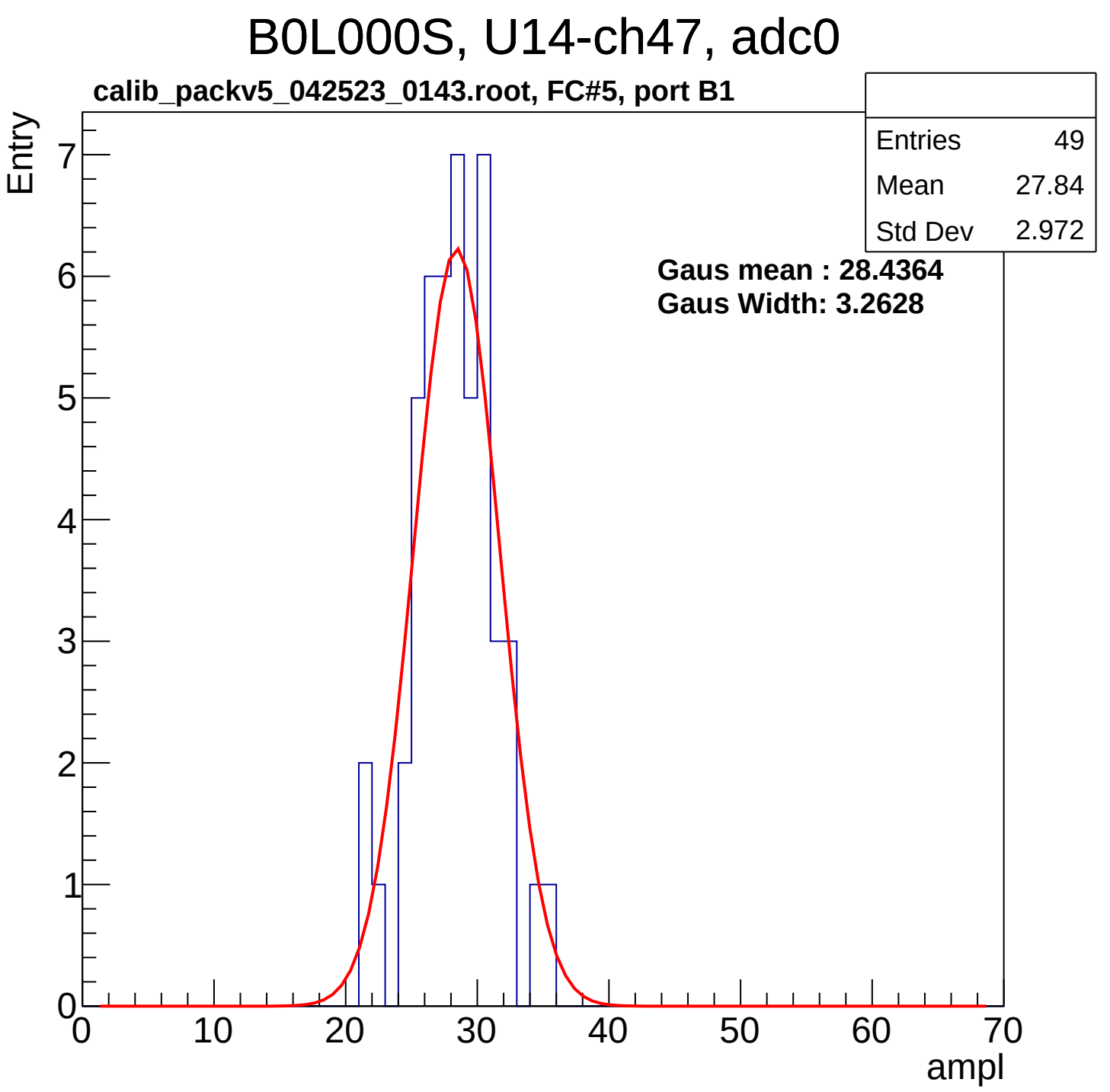
B0L000S, U14-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 27.84
Std Dev 2.972
Gaus mean : 28.4364
Gaus Width: 3.2628
Entry
ampl
0
1
2
3
4
5
6
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0
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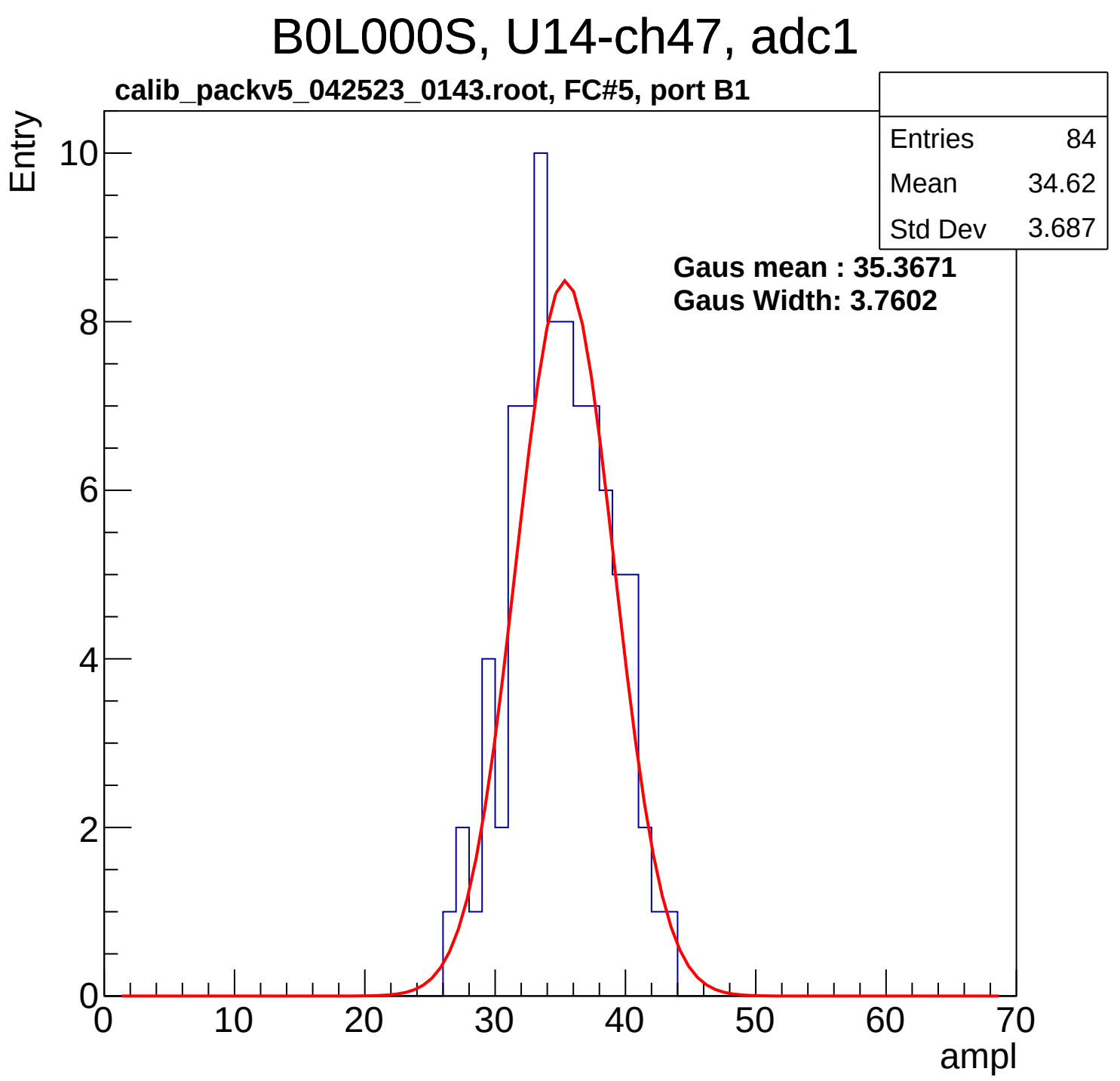
B0L000S, U14-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 34.62
Std Dev 3.687
Gaus mean : 35.3671
Gaus Width: 3.7602
Entry
ampl
0
2
4
6
8
10
0
10
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40
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60
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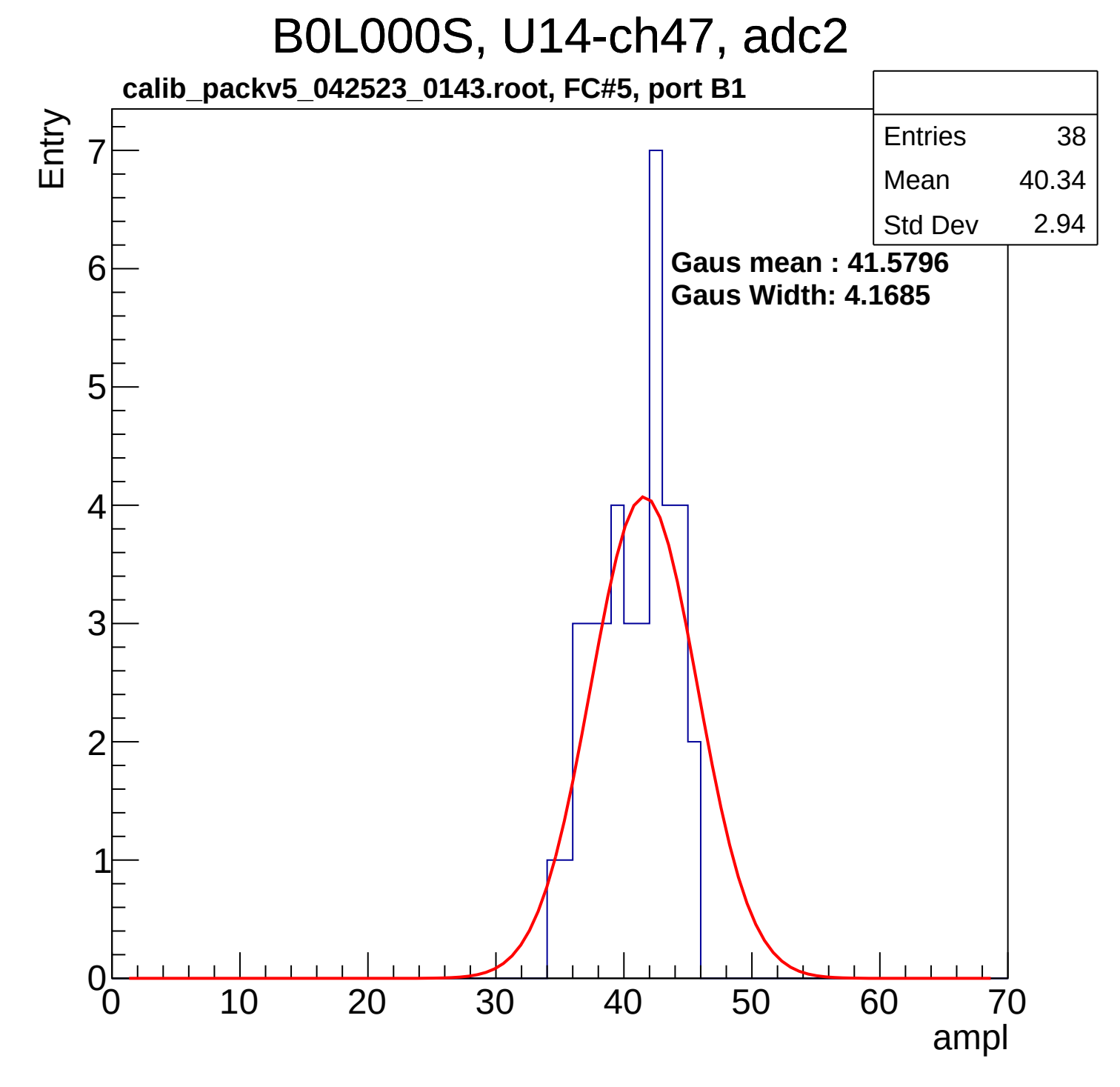
B0L000S, U14-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 40.34
Std Dev 2.94
Gaus mean : 41.5796
Gaus Width: 4.1685
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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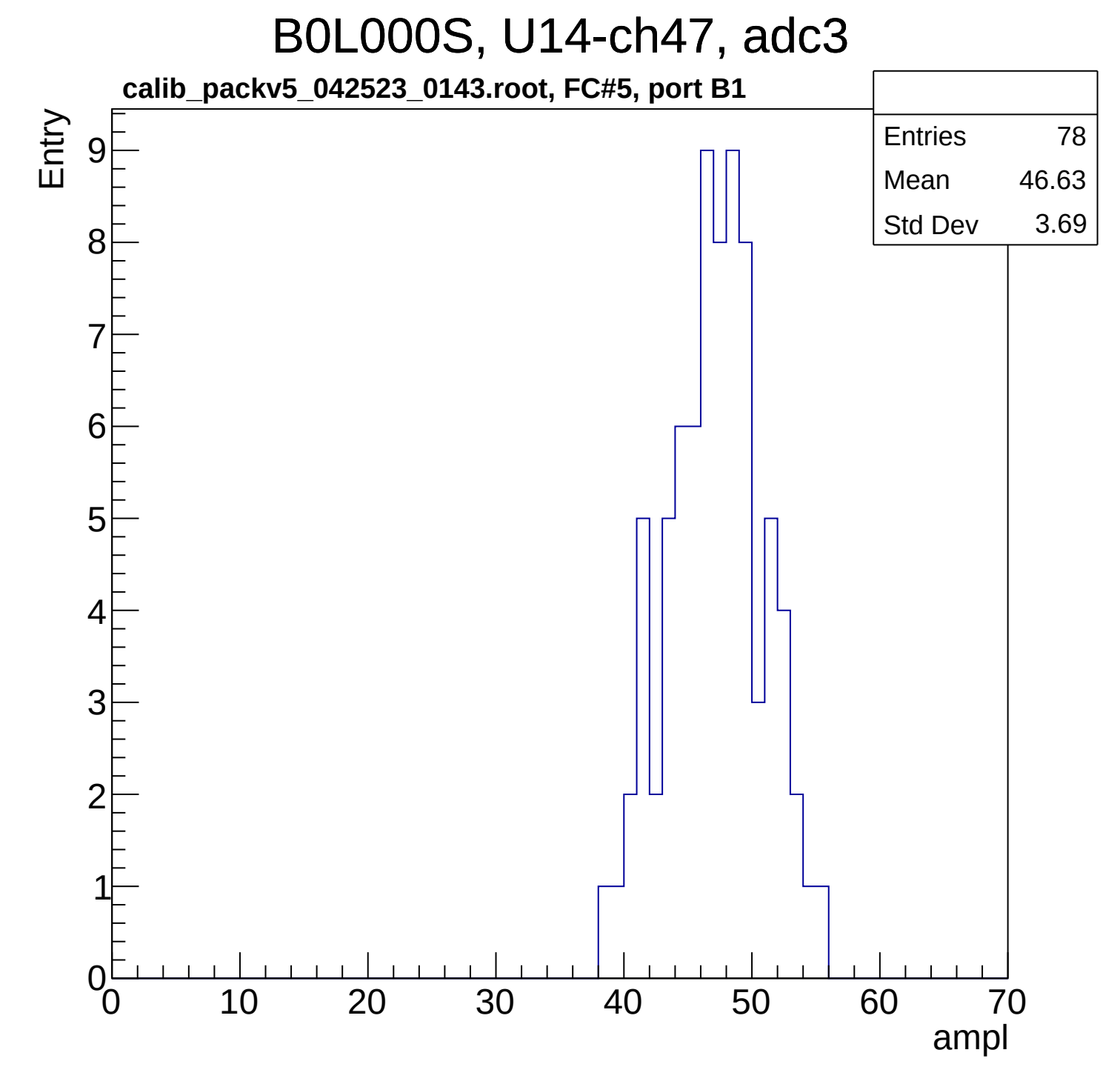
B0L000S, U14-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 46.63
Std Dev 3.69
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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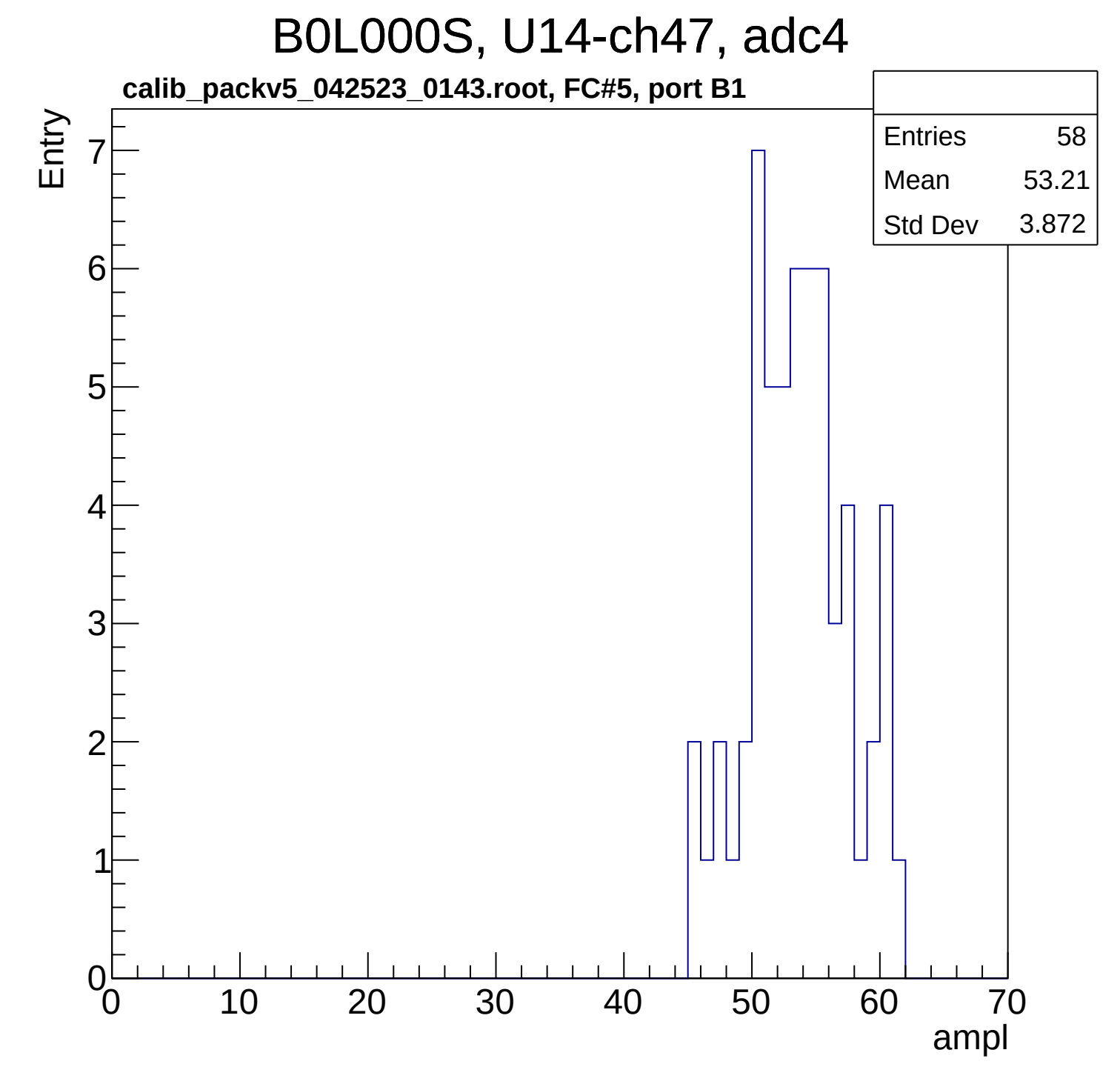
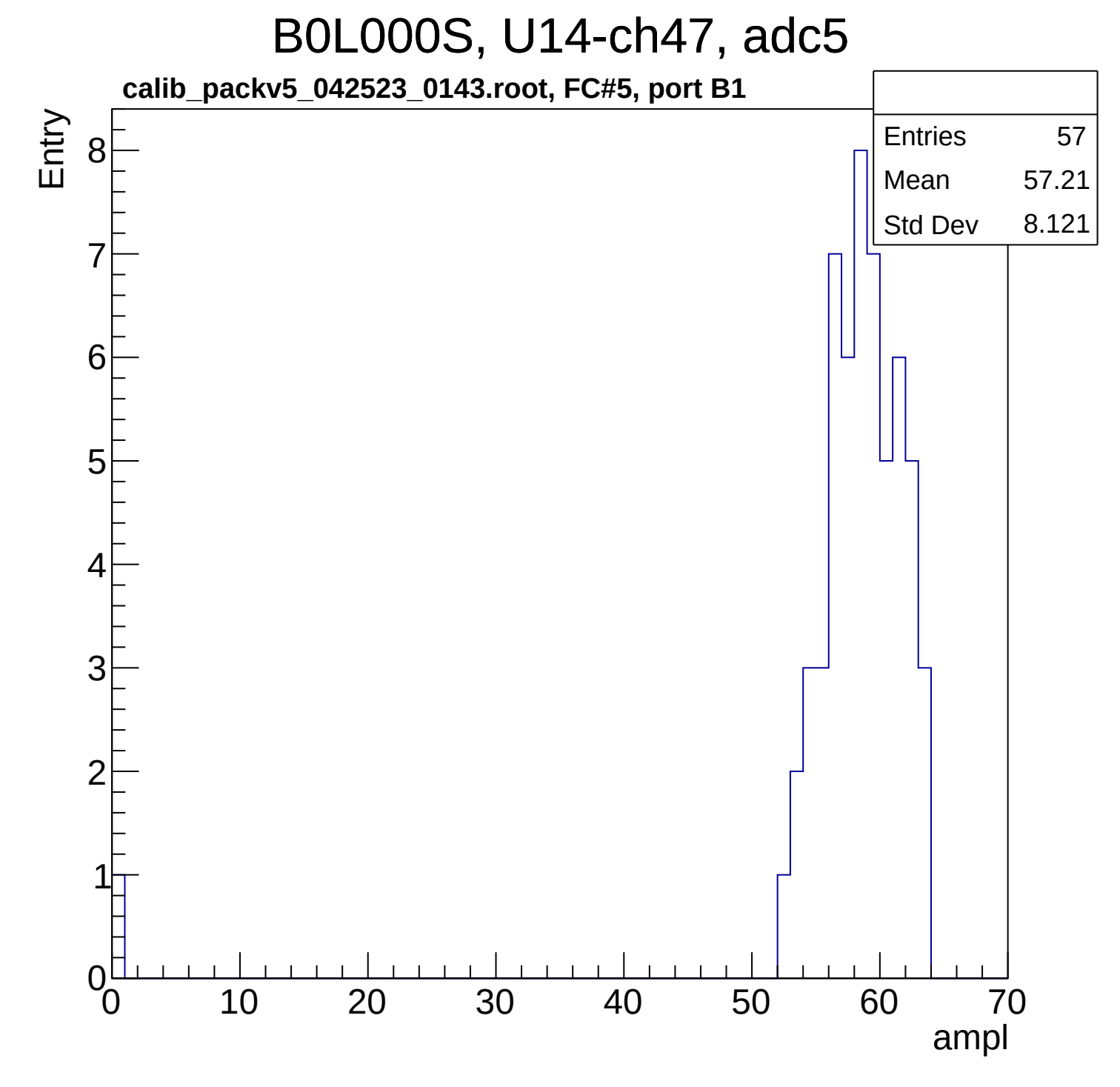

B0L000S, U14-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.21
Std Dev 3.872
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.21
Std Dev 8.121
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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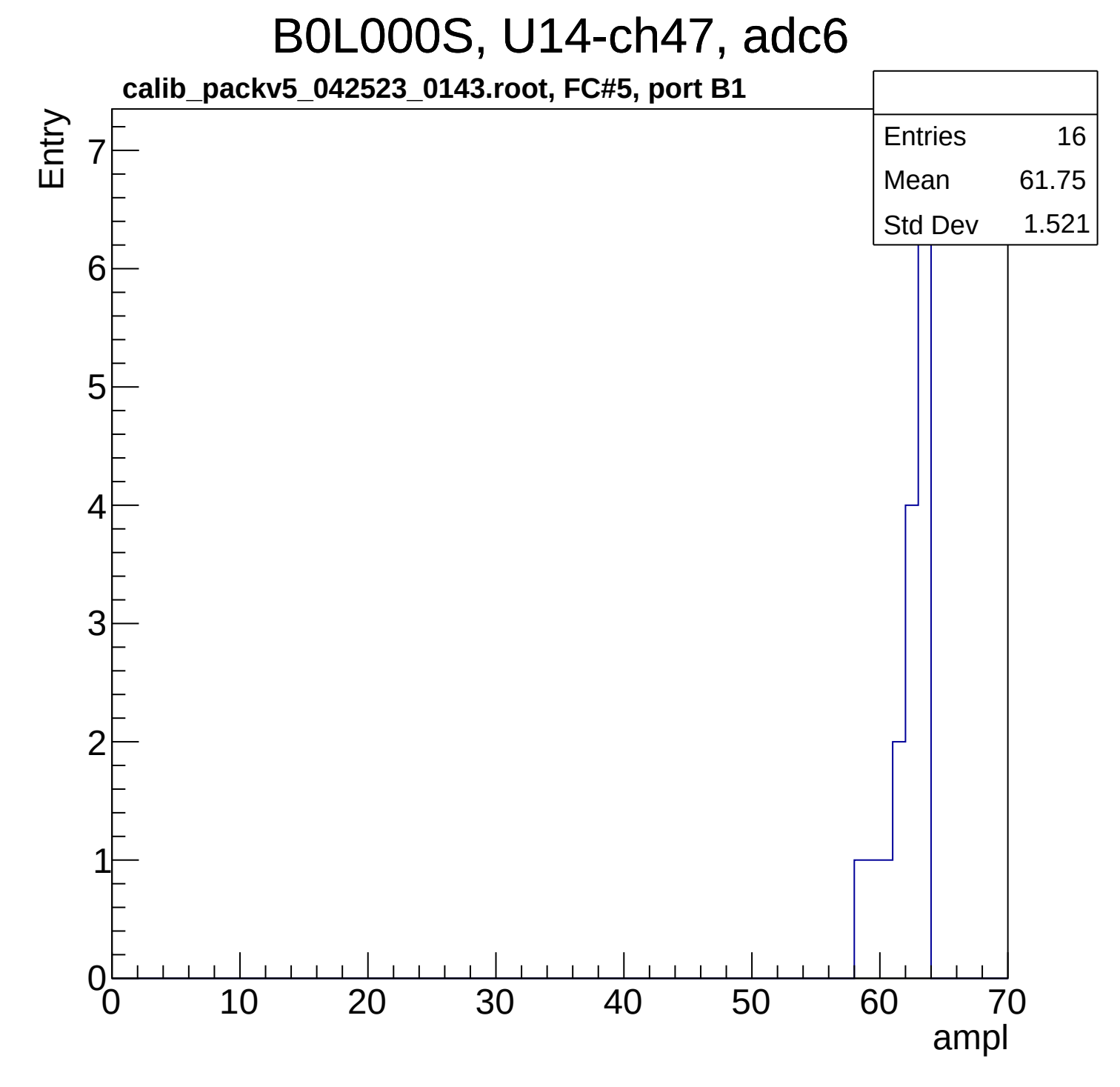
B0L000S, U14-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.75
Std Dev 1.521
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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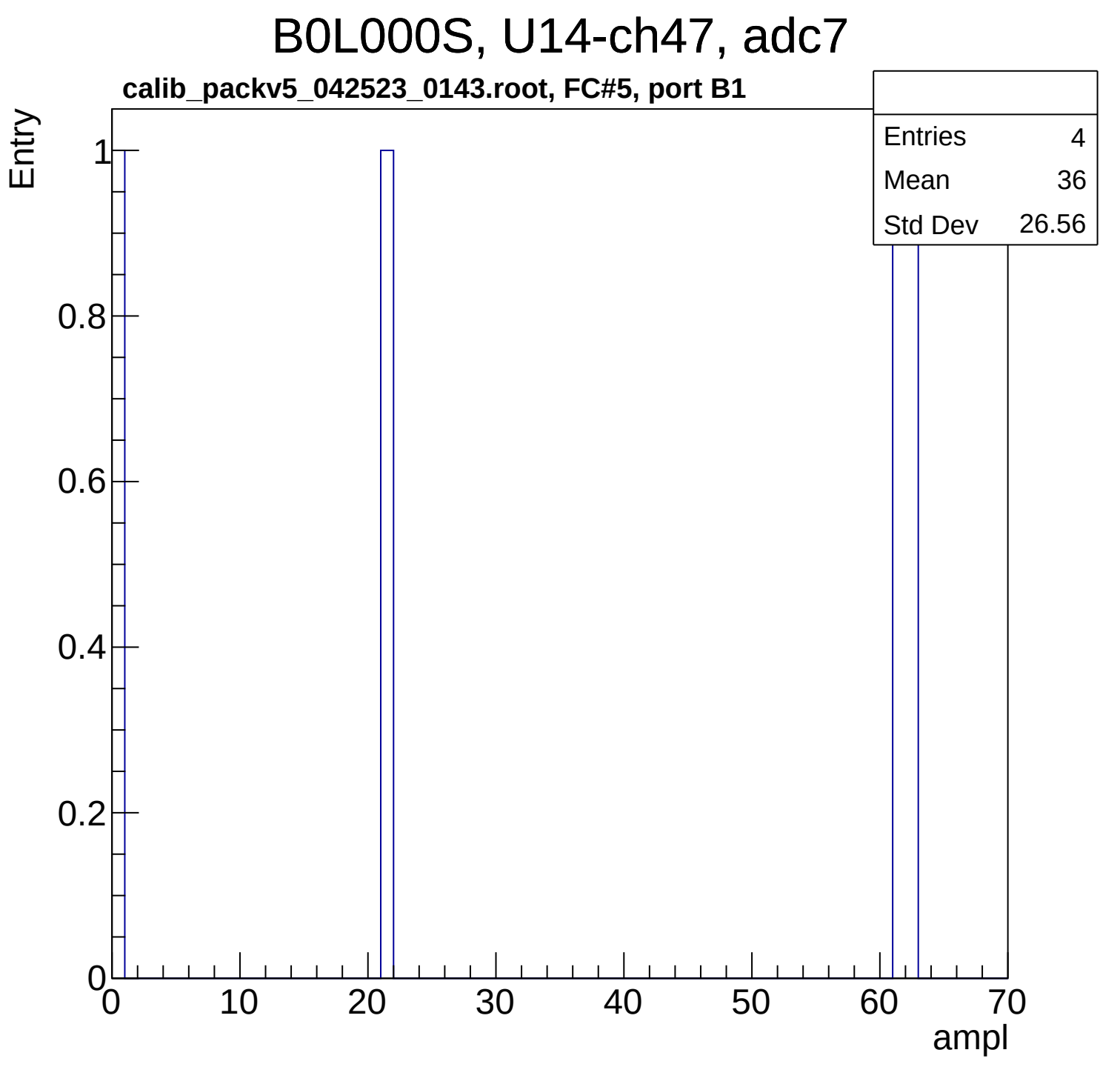
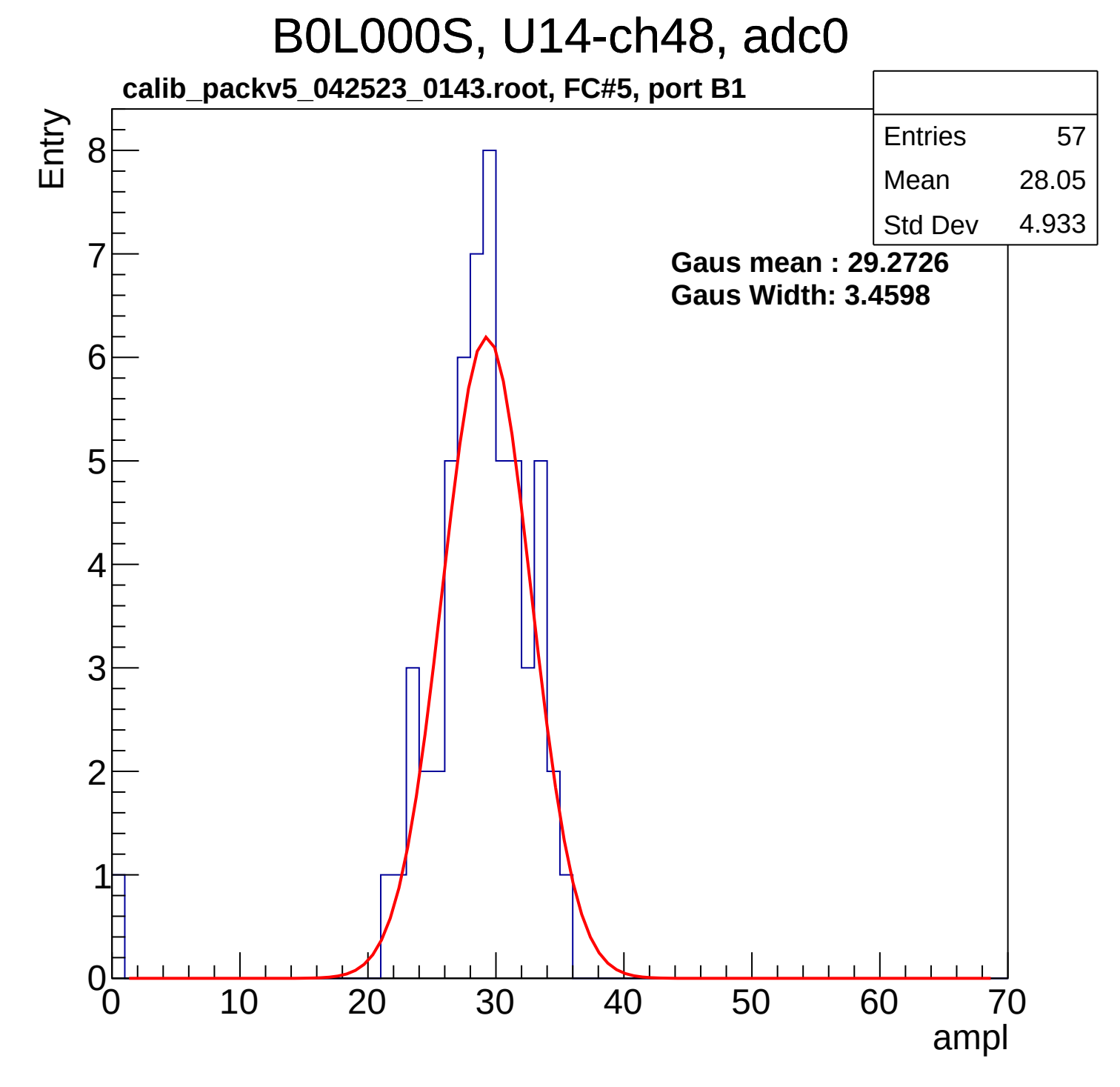
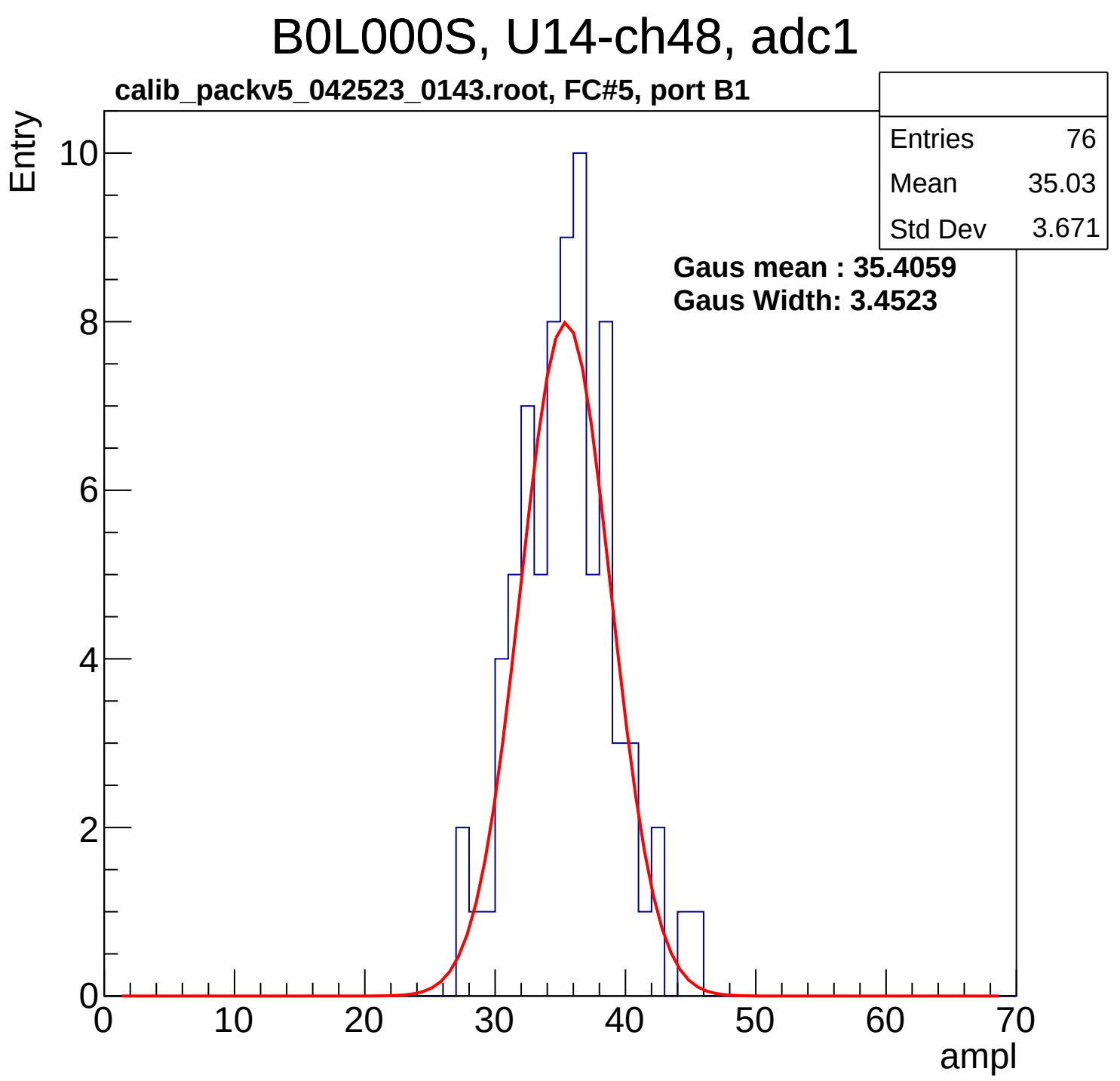

B0L000S, U14-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 36
Std Dev 26.56
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U14-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 28.05
Std Dev 4.933
Gaus mean : 29.2726
Gaus Width: 3.4598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 35.03
Std Dev 3.671
Gaus mean : 35.4059
Gaus Width: 3.4523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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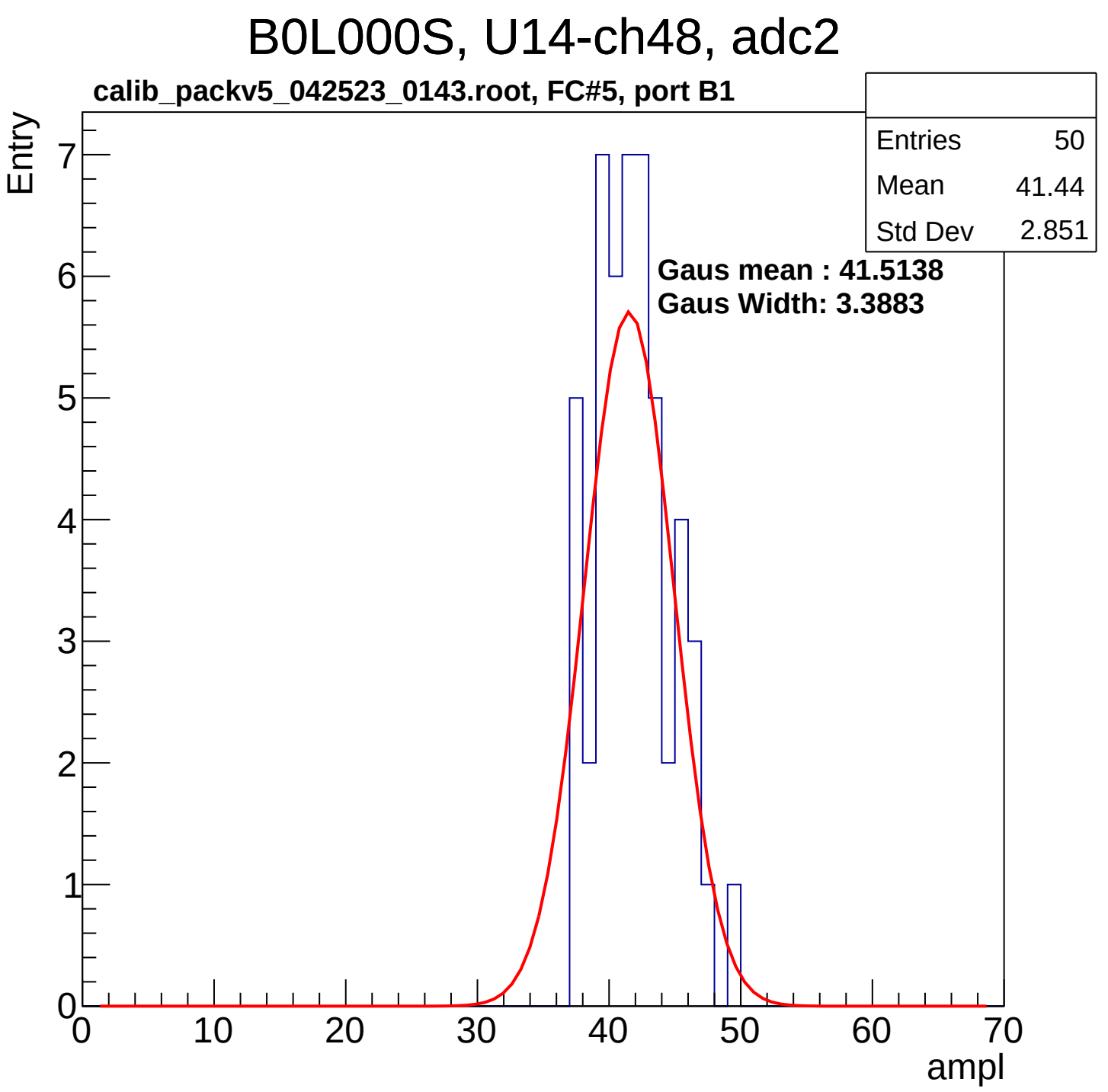
B0L000S, U14-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 41.44
Std Dev 2.851
Gaus mean : 41.5138
Gaus Width: 3.3883
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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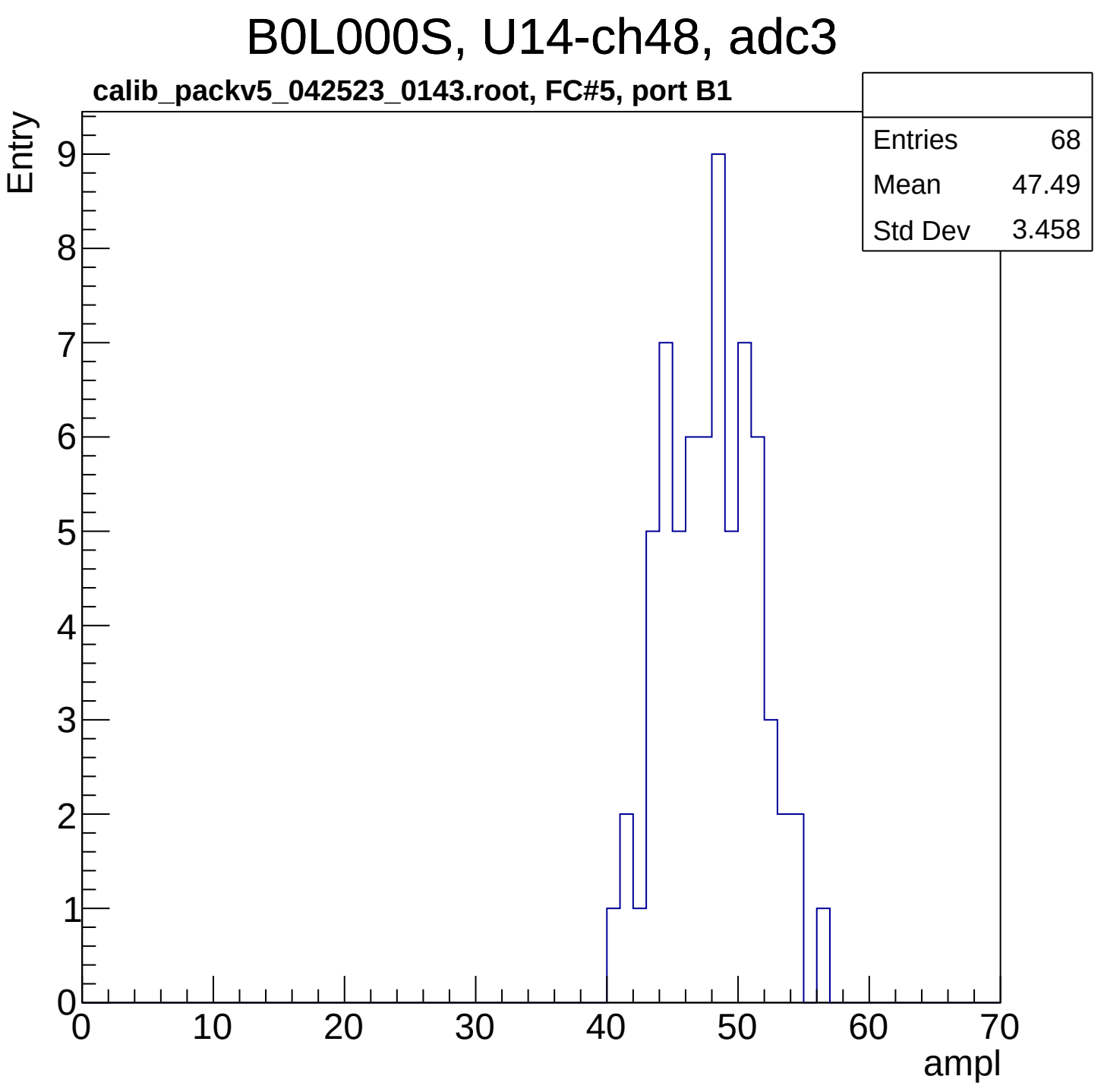
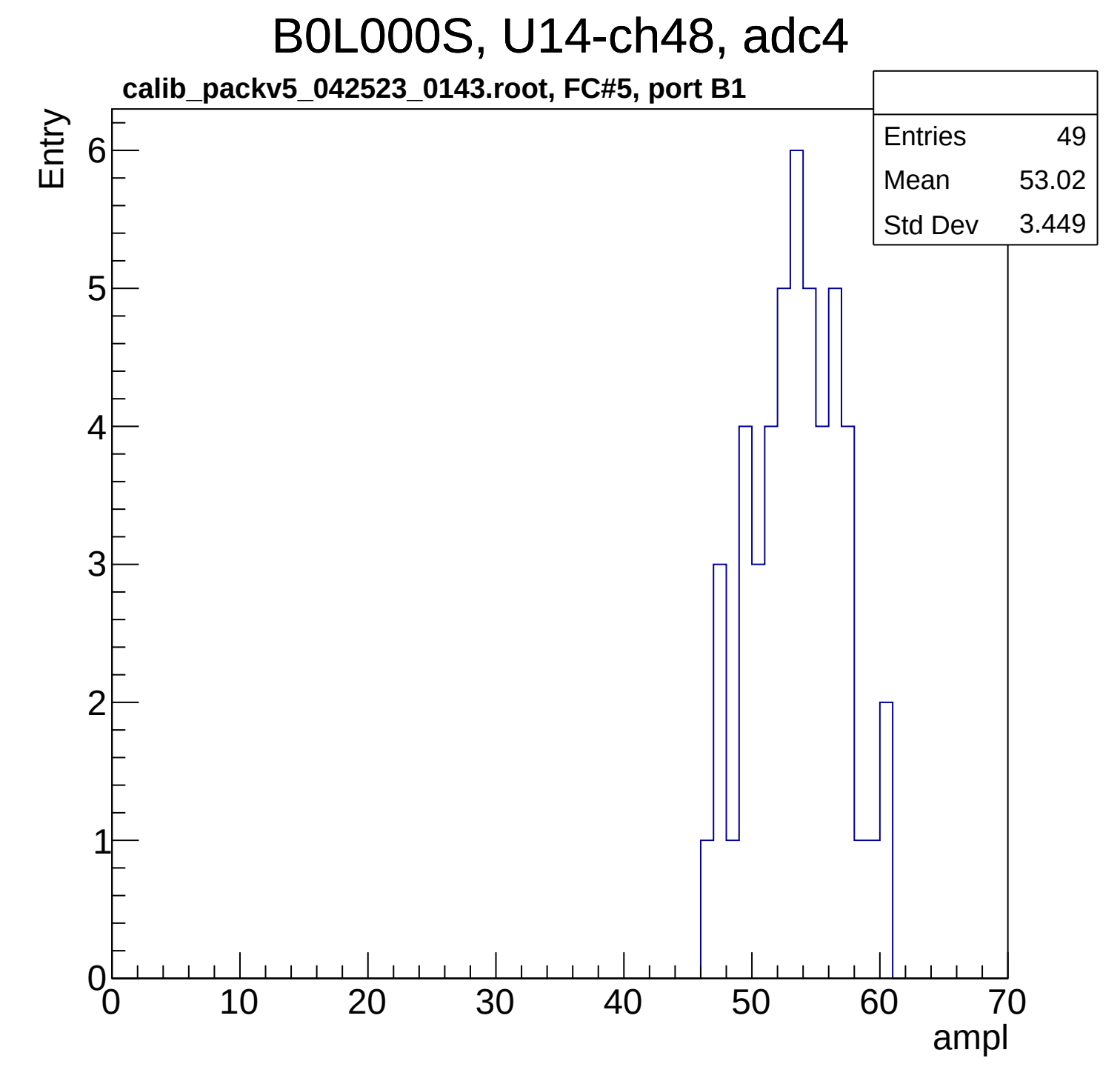
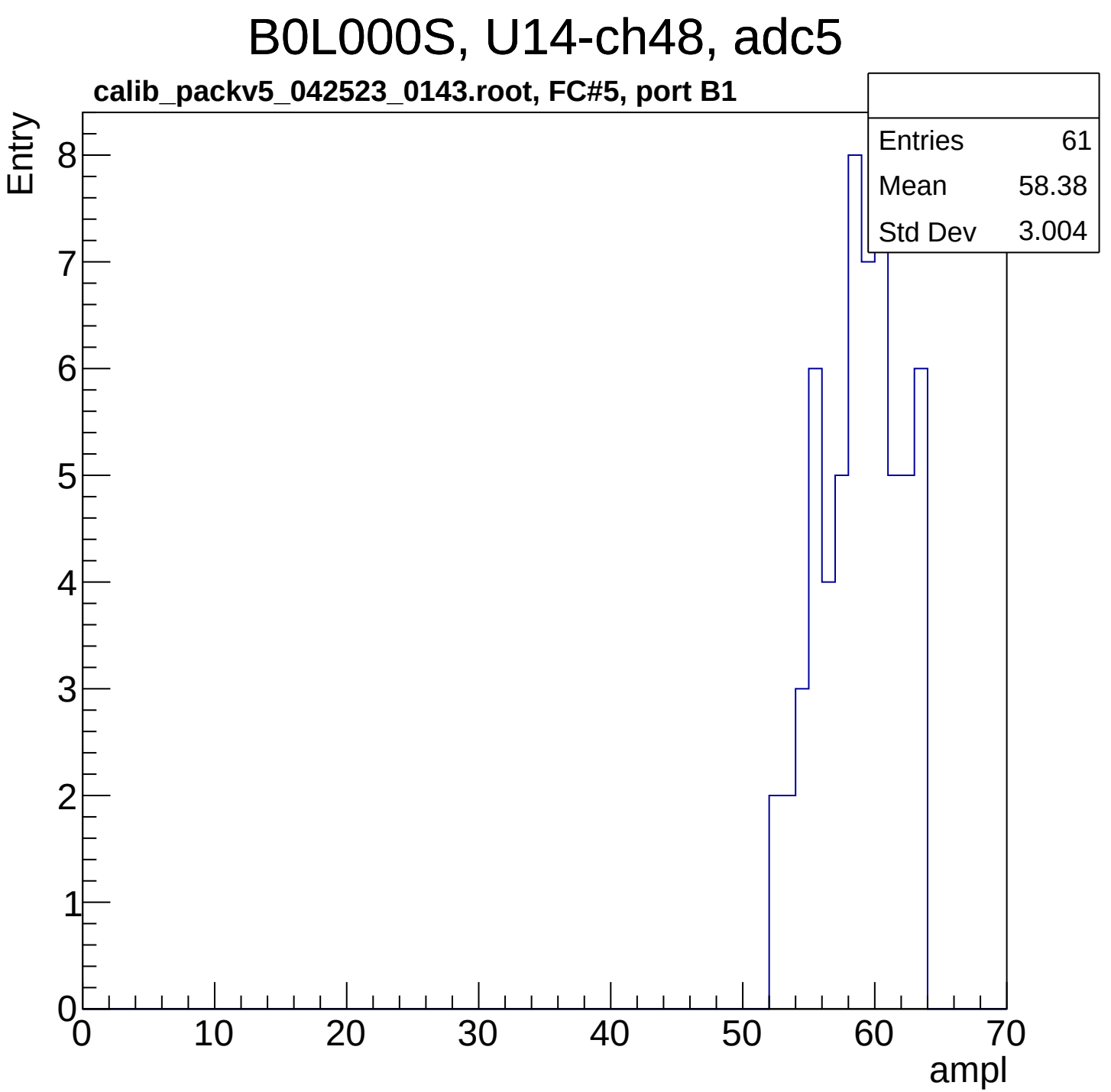

B0L000S, U14-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 47.49
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 53.02
Std Dev 3.449
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 58.38
Std Dev 3.004
Entry
ampl
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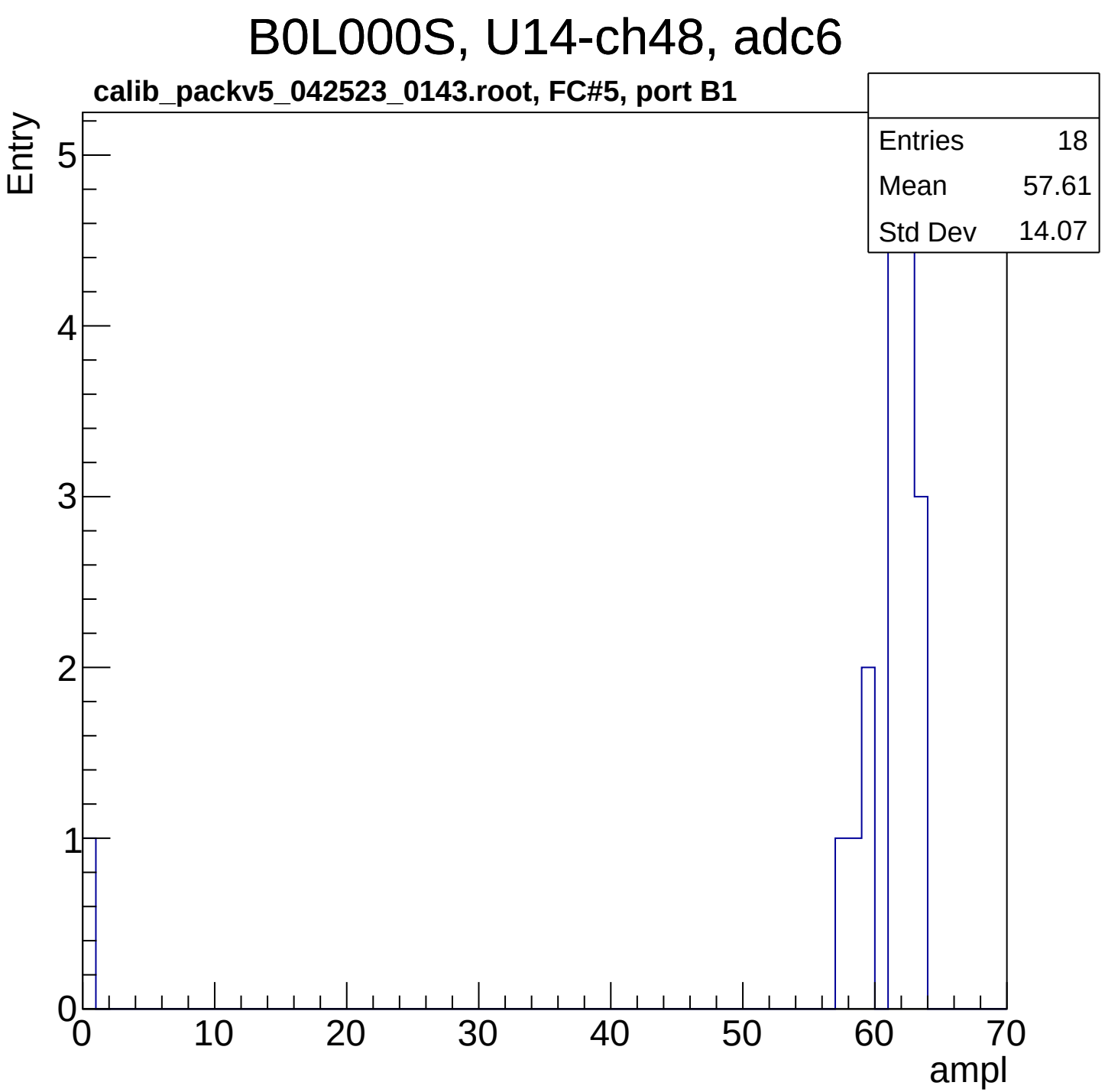
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 57.61
Std Dev 14.07
Entry
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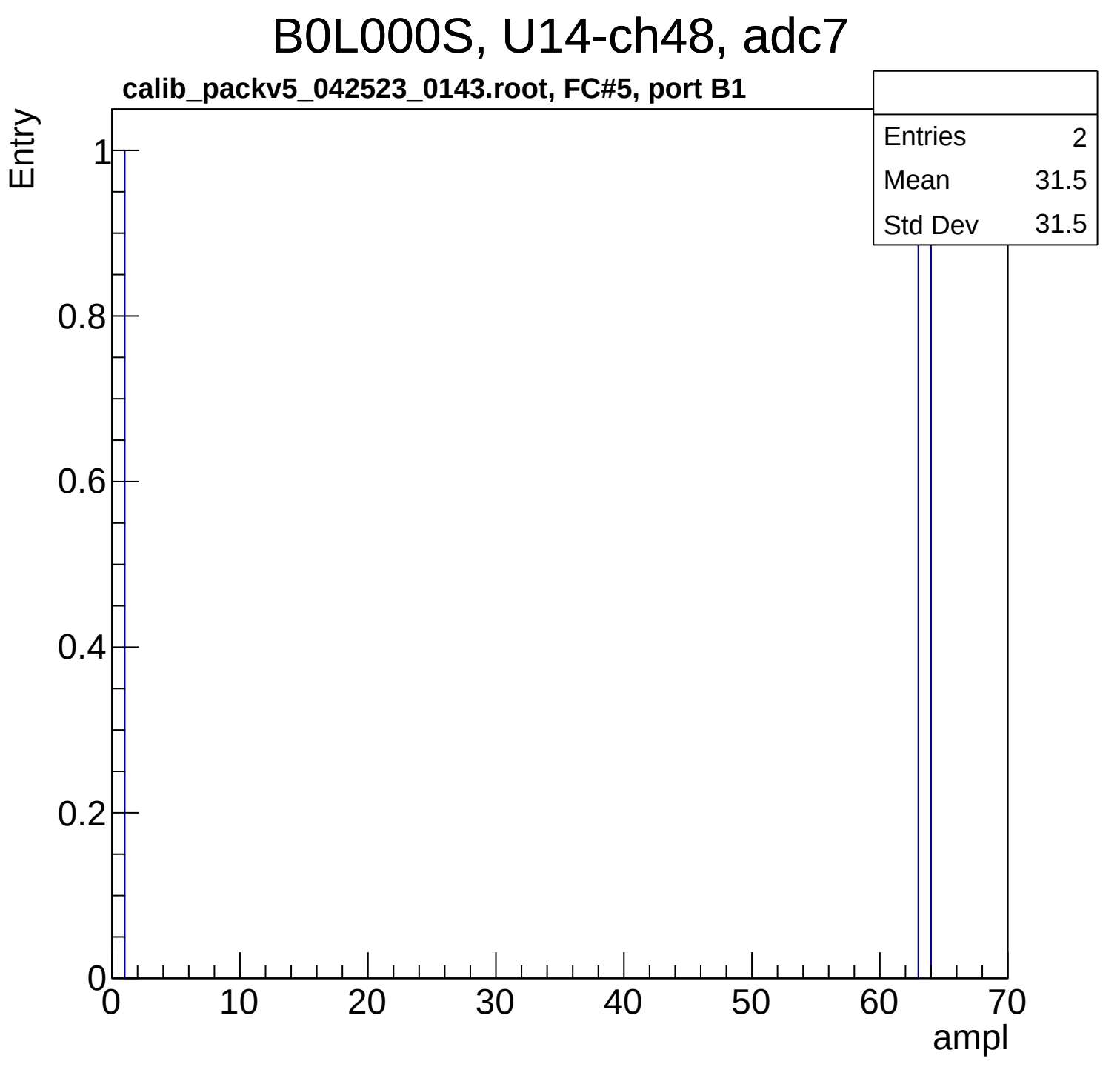
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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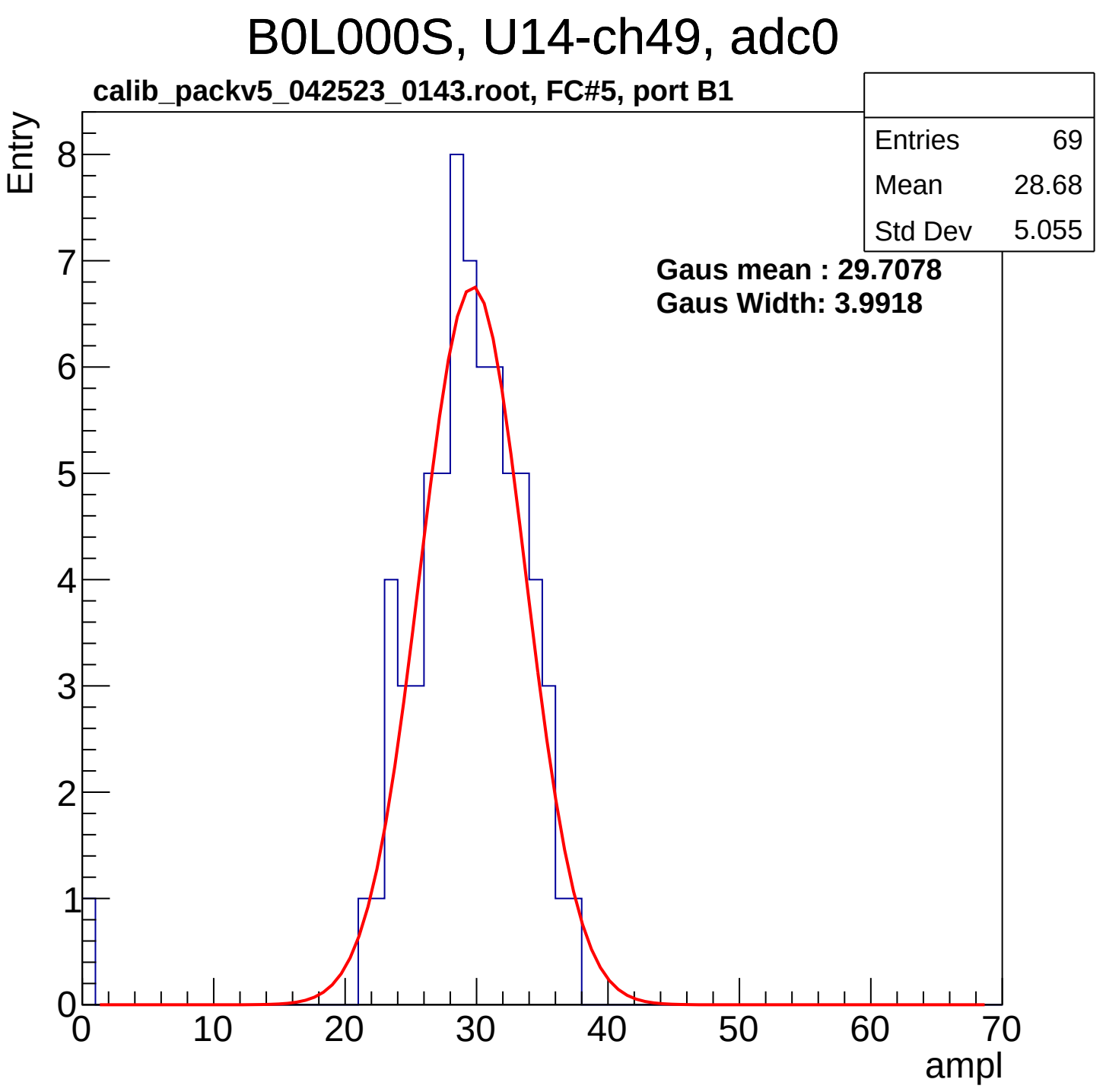
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 28.68
Std Dev 5.055
Gaus mean : 29.7078
Gaus Width: 3.9918
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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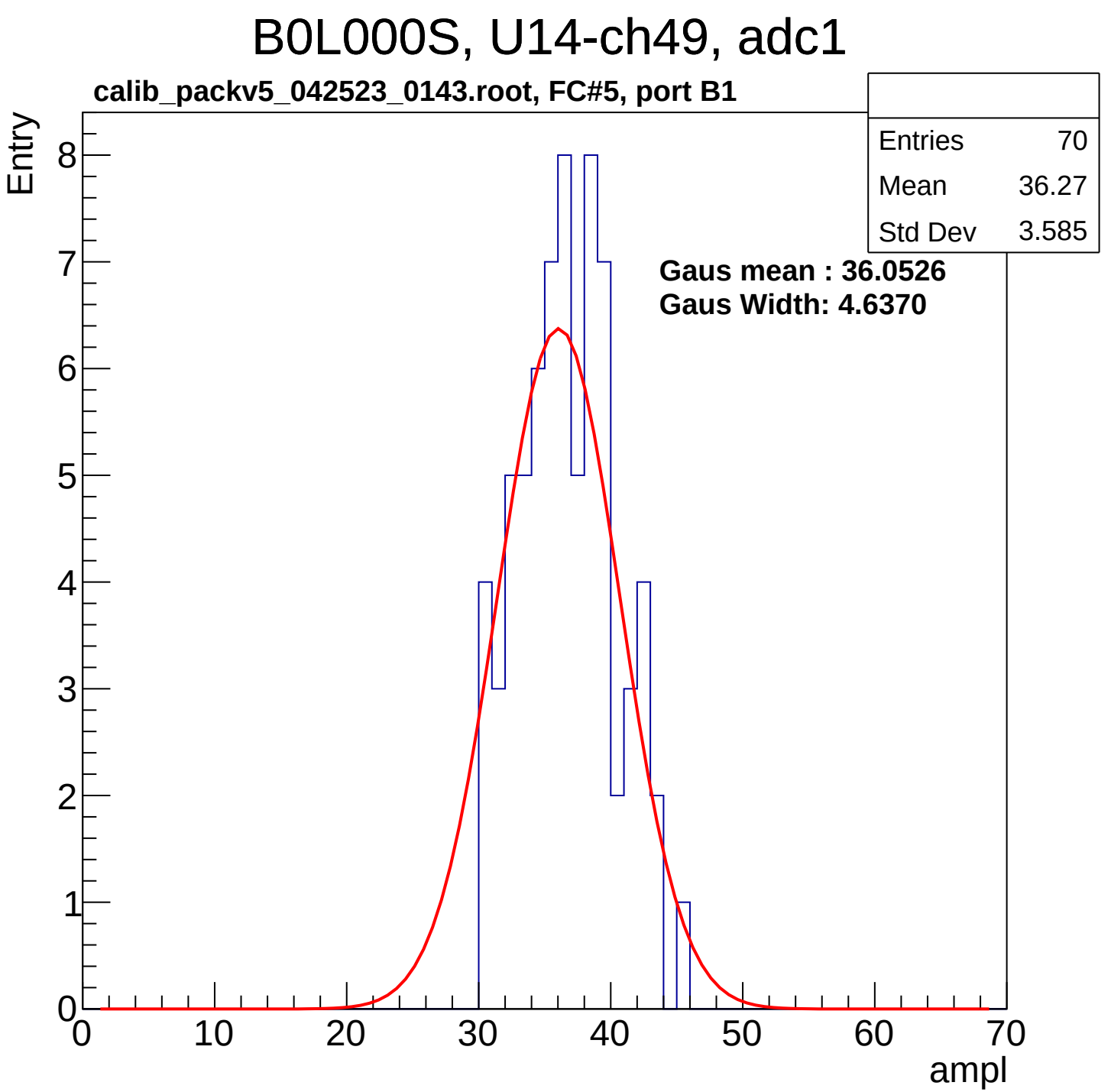
B0L000S, U14-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.27
Std Dev 3.585
Gaus mean : 36.0526
Gaus Width: 4.6370
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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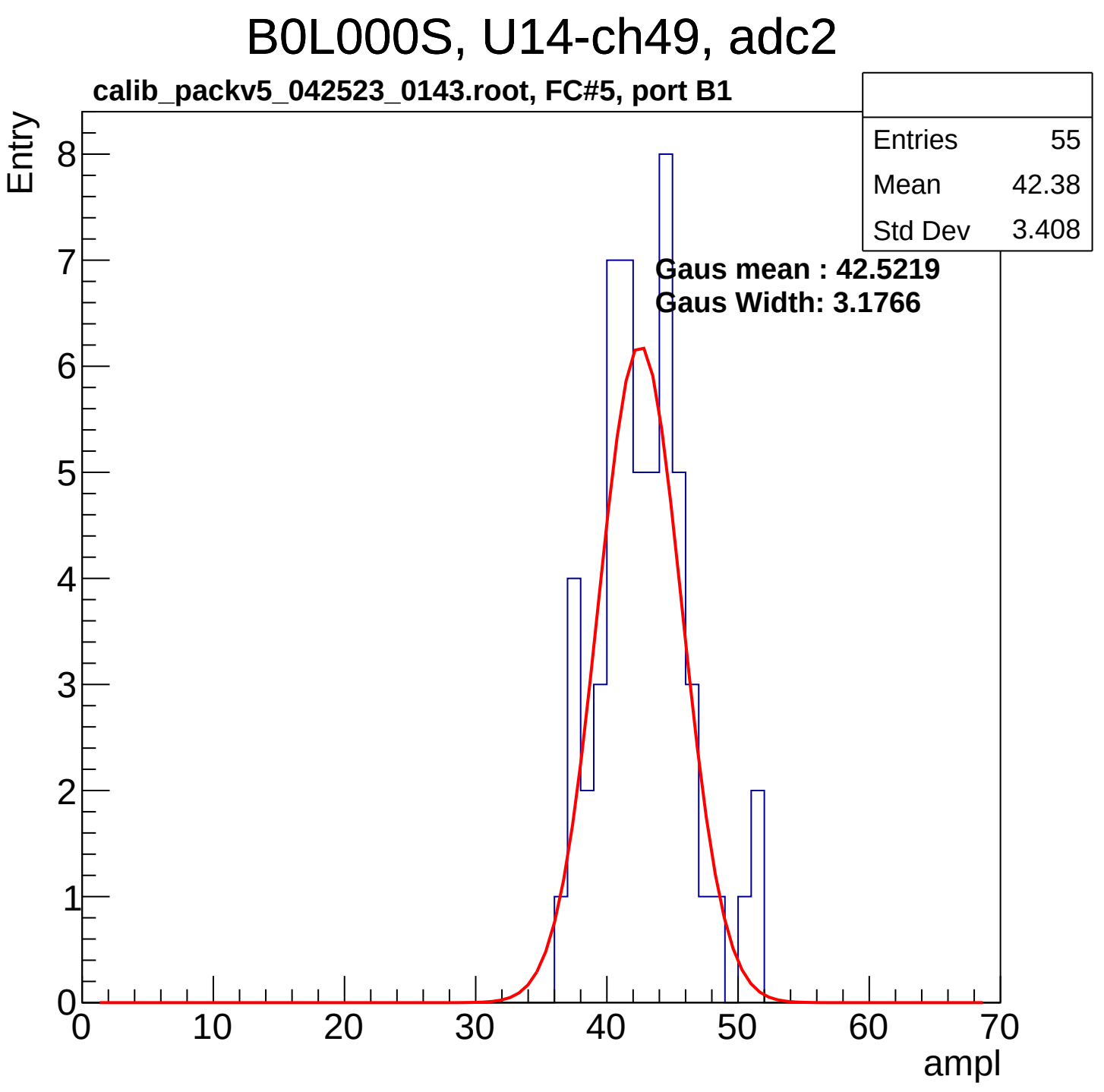
B0L000S, U14-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 42.38
Std Dev 3.408
Gaus mean : 42.5219
Gaus Width: 3.1766
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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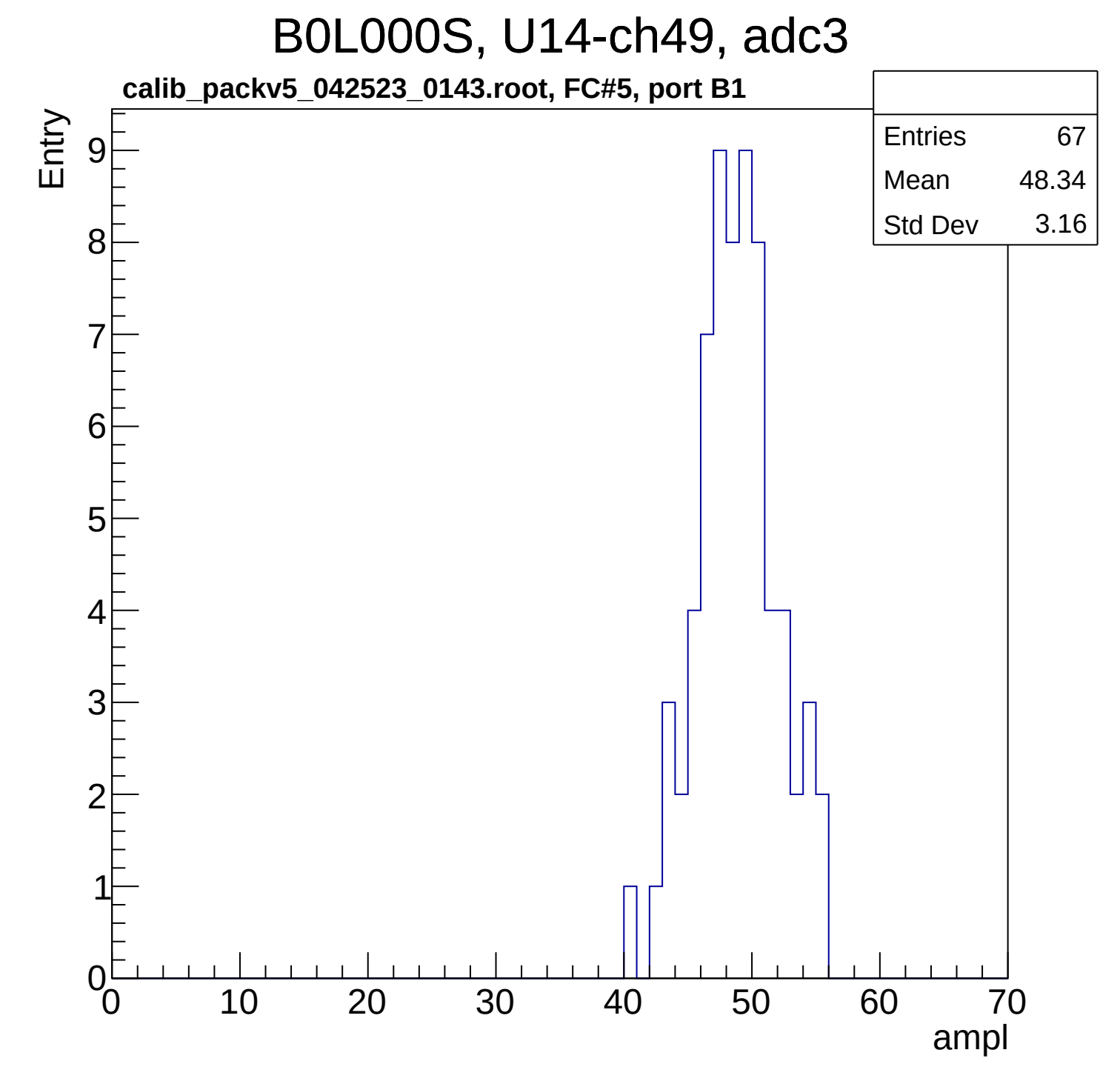
B0L000S, U14-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.34
Std Dev 3.16
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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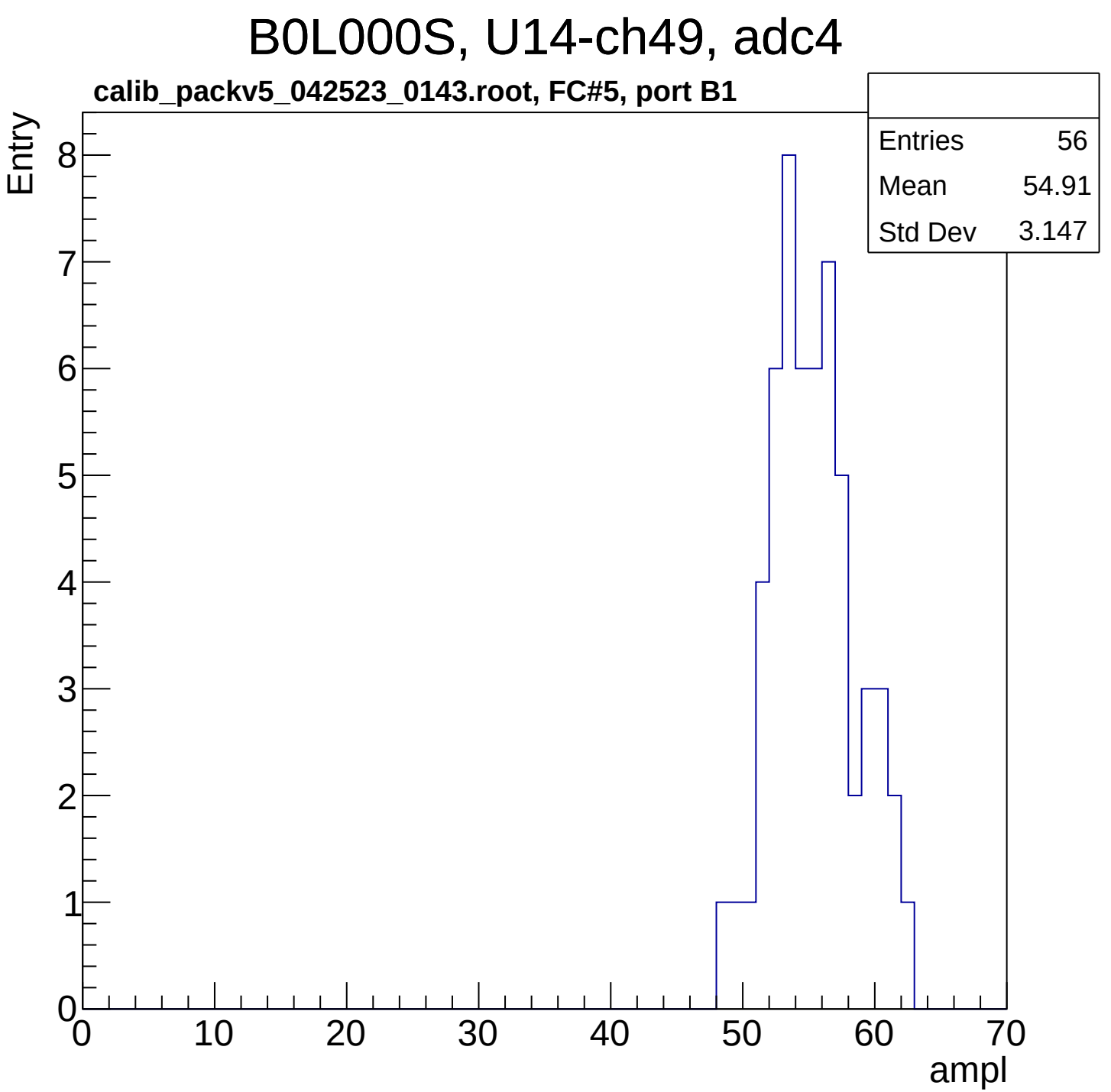
B0L000S, U14-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.91
Std Dev 3.147
Entry
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6
7
8
0
10
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50
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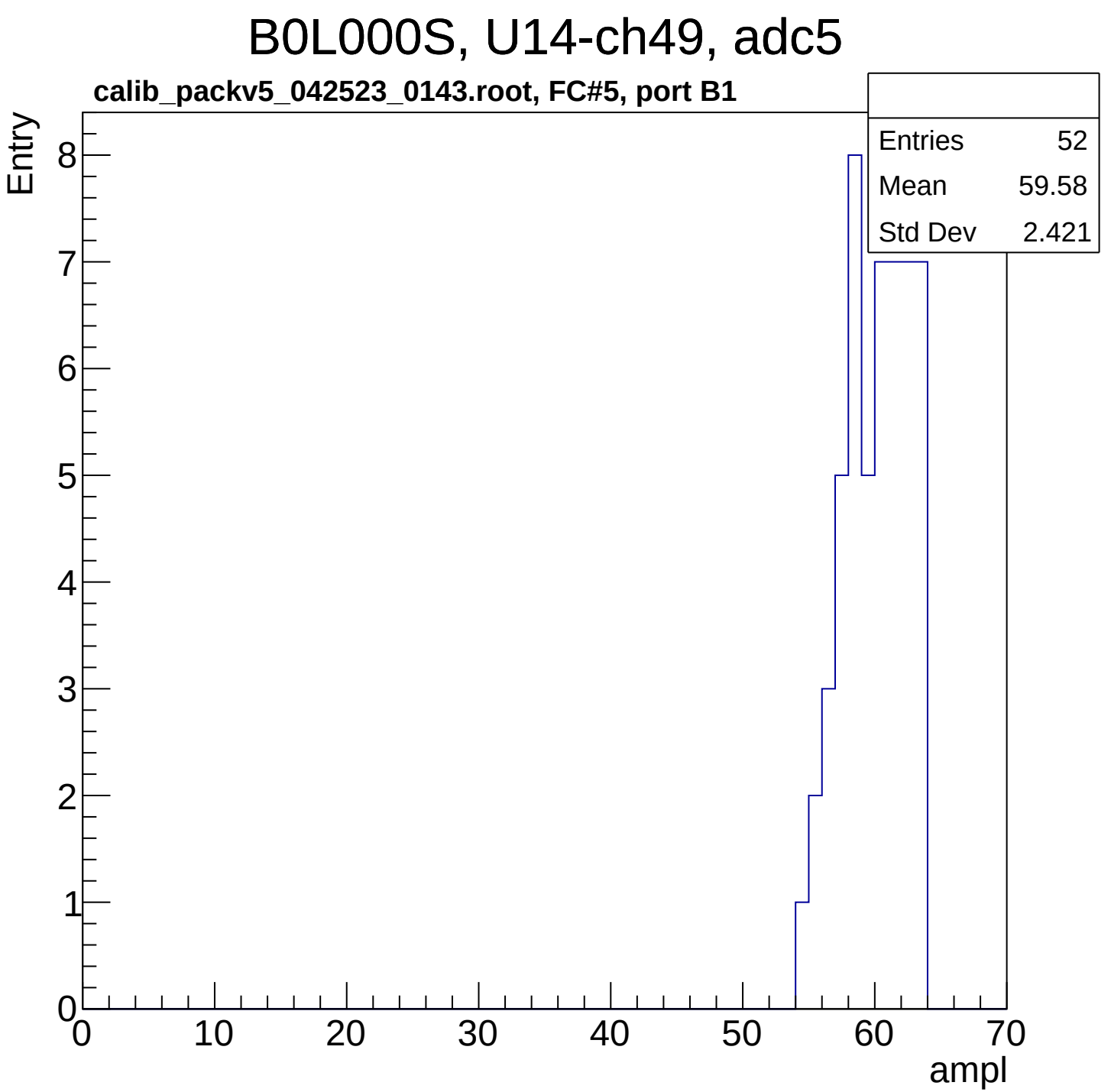
70
ampl


B0L000S, U14-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.58
Std Dev 2.421
Entry
ampl
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3
4
5
6
7
8
0
10
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40
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60
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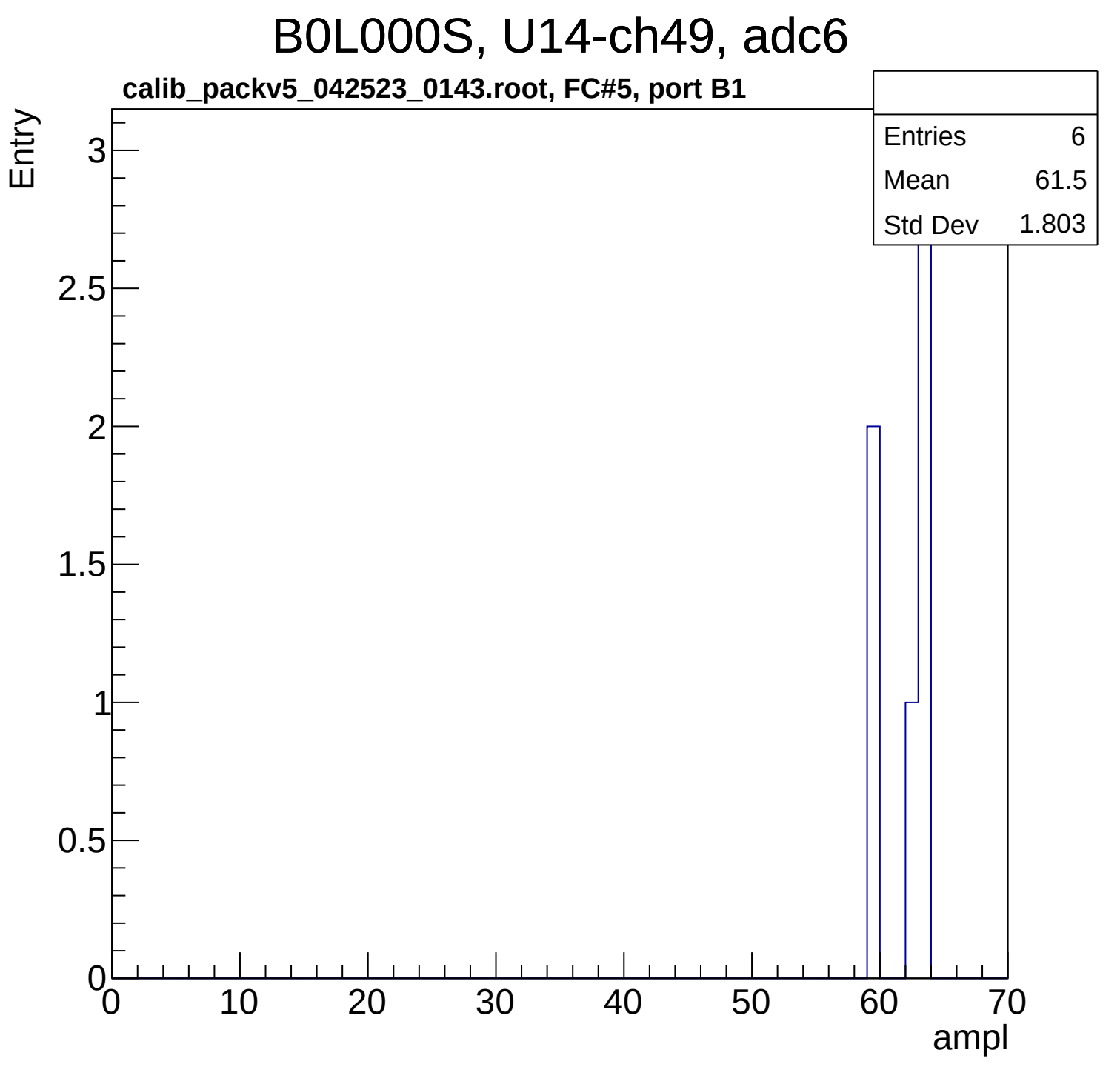
B0L000S, U14-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.5
Std Dev 1.803
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
30
40
50
60
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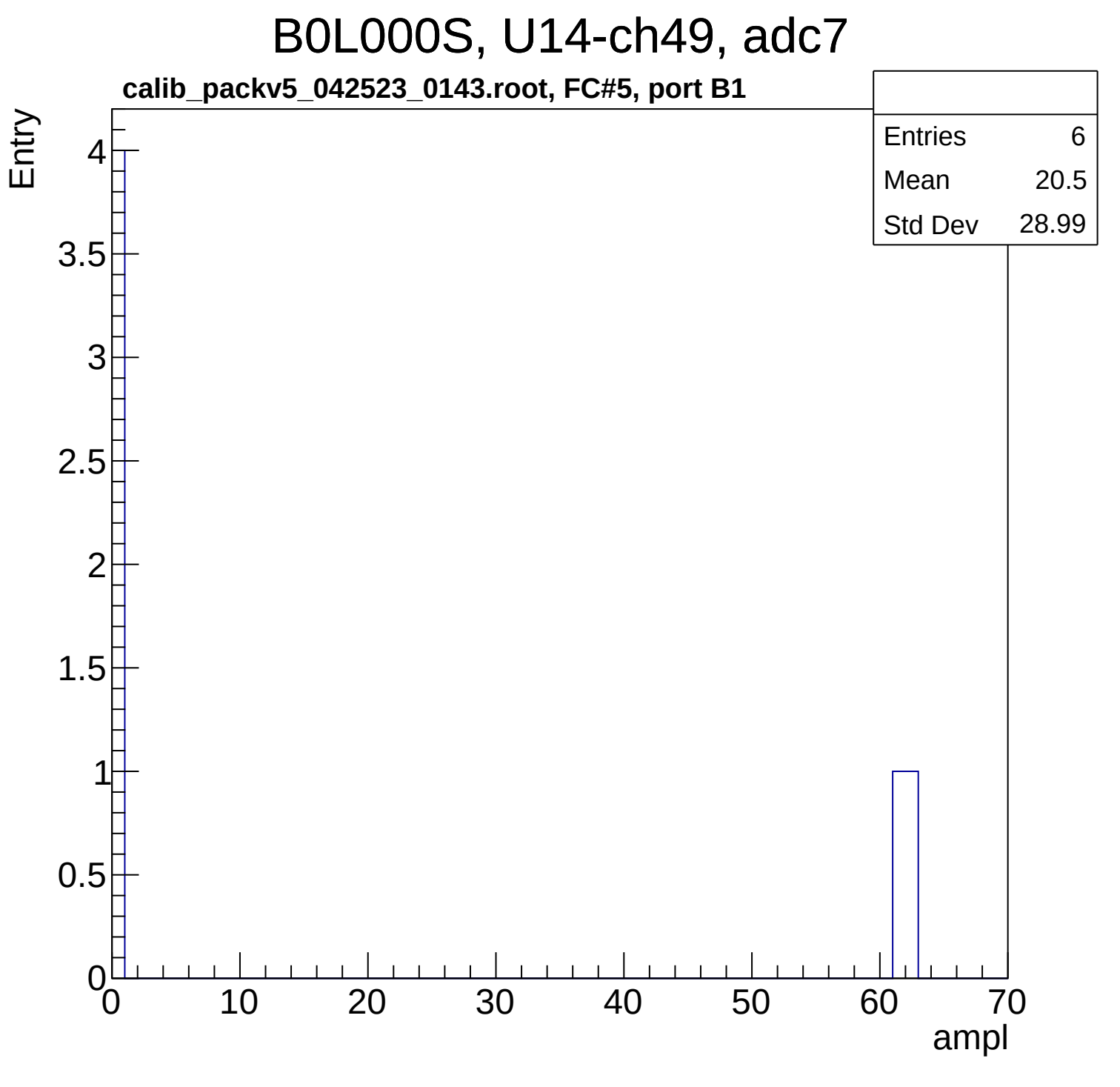
B0L000S, U14-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 20.5
Std Dev 28.99
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
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40
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60
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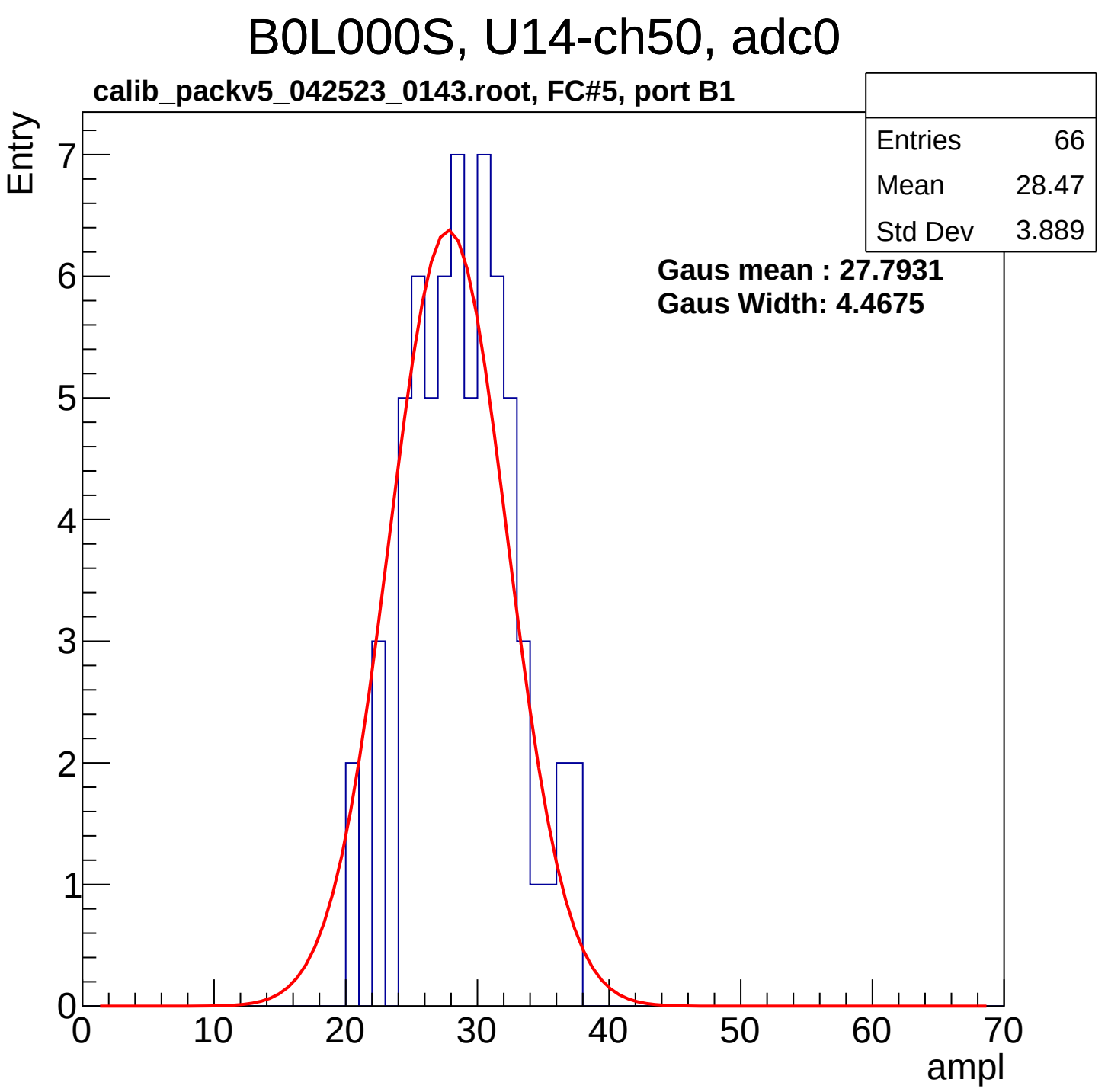
B0L000S, U14-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.47
Std Dev 3.889
Gaus mean : 27.7931
Gaus Width: 4.4675
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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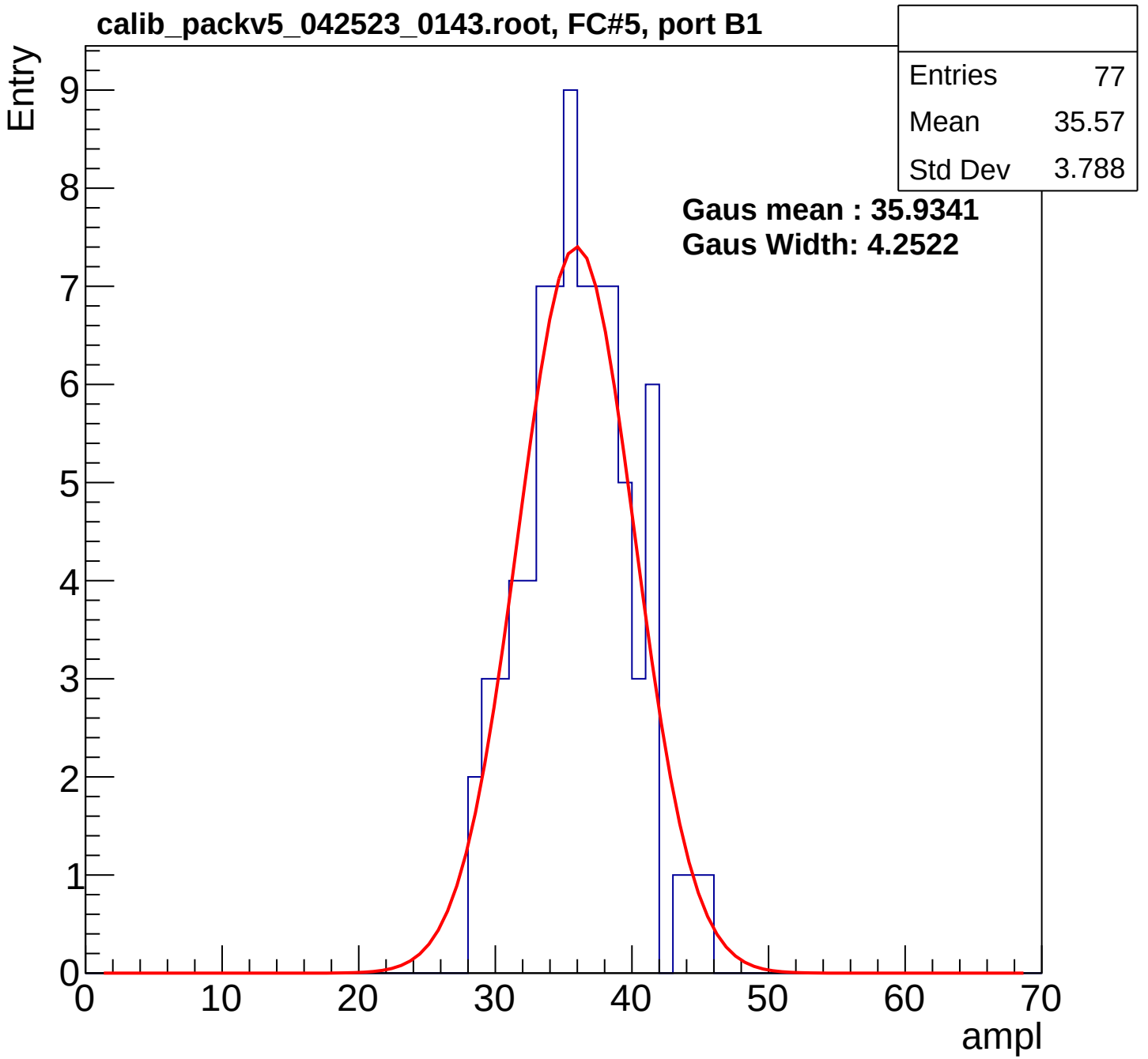
B0L000S, U14-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 35.57
Std Dev 3.788
Gaus mean : 35.9341
Gaus Width: 4.2522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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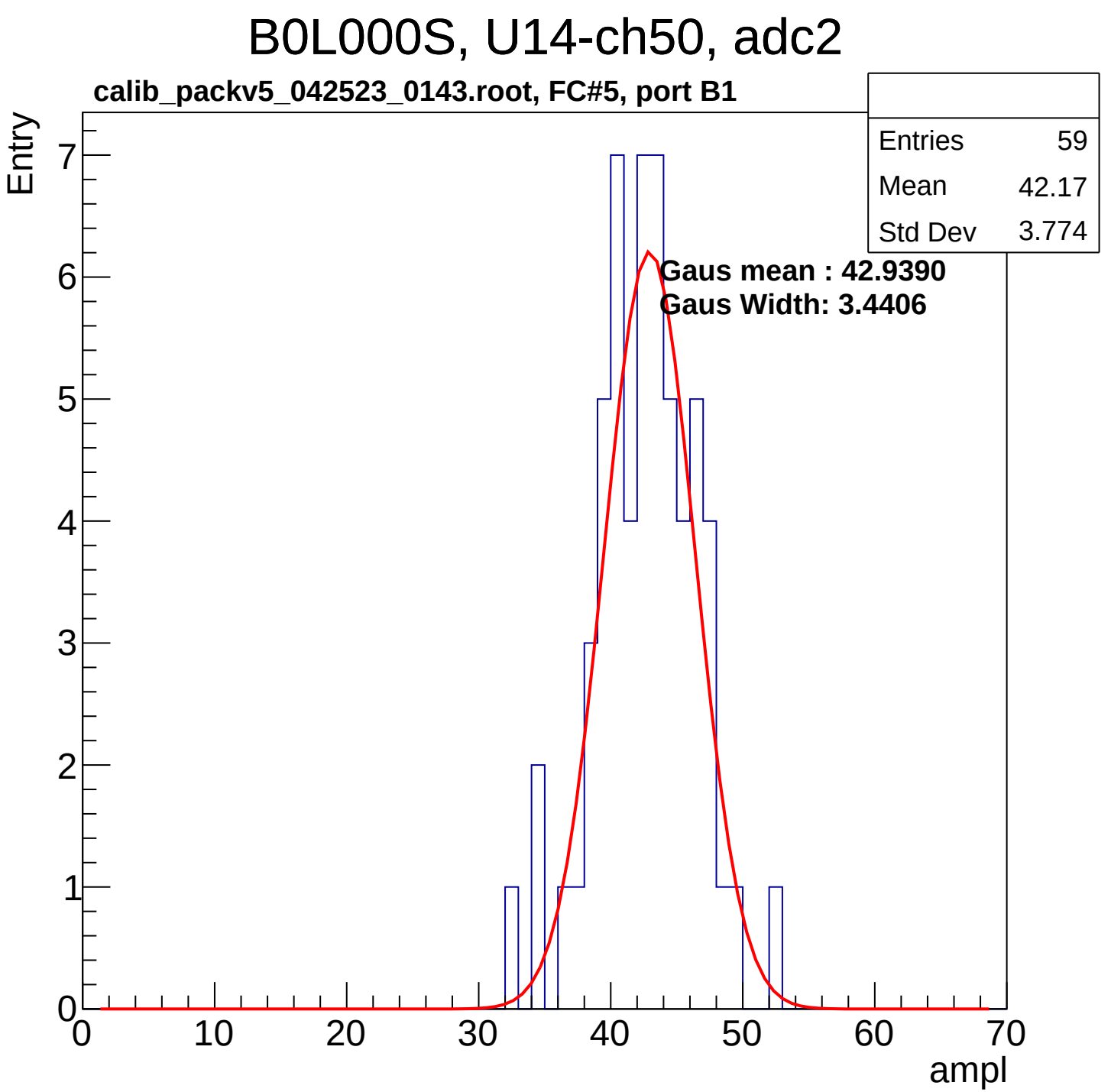
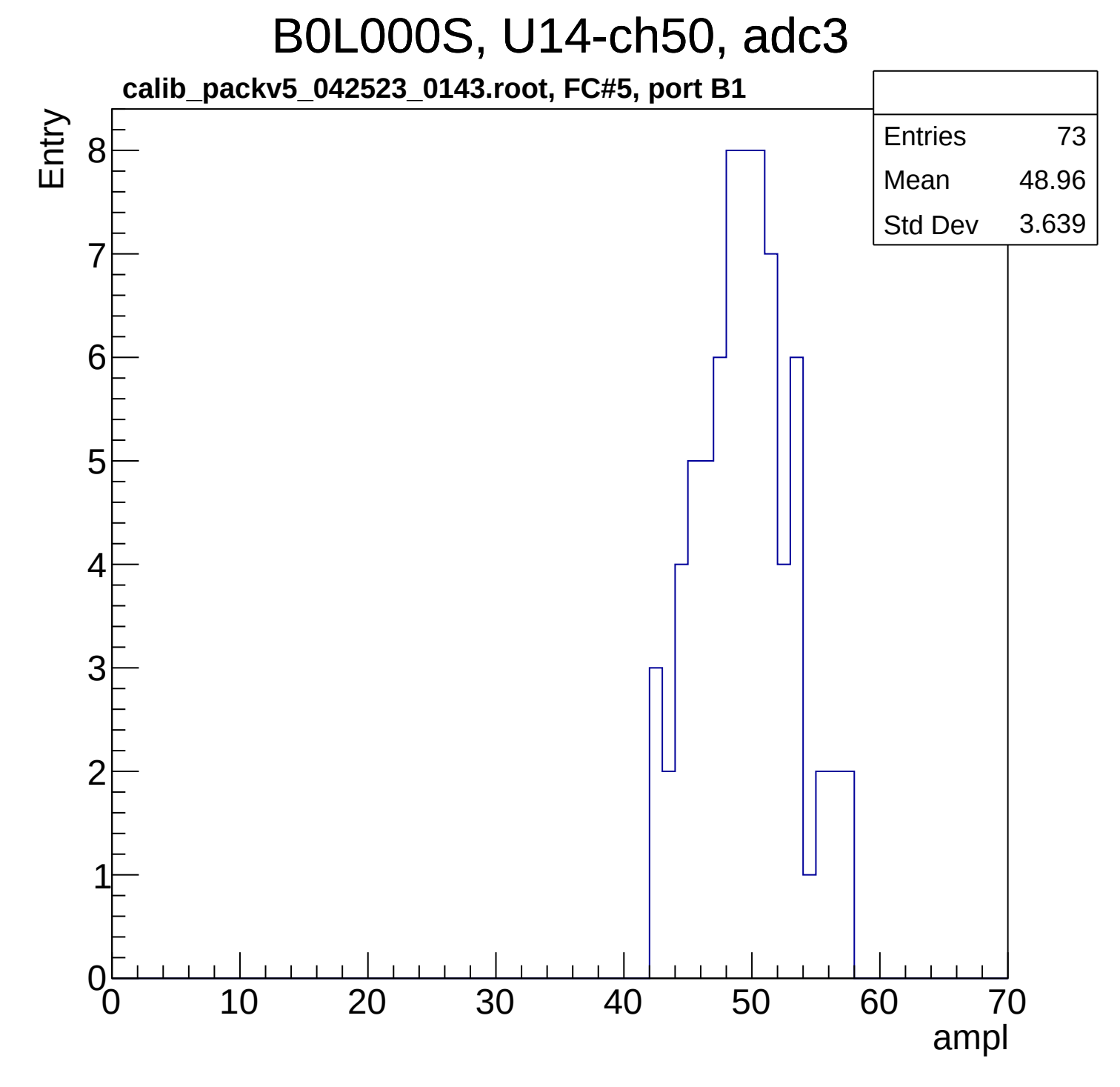

B0L000S, U14-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.17
Std Dev 3.774
Gaus mean : 42.9390
Gaus Width: 3.4406
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.96
Std Dev 3.639
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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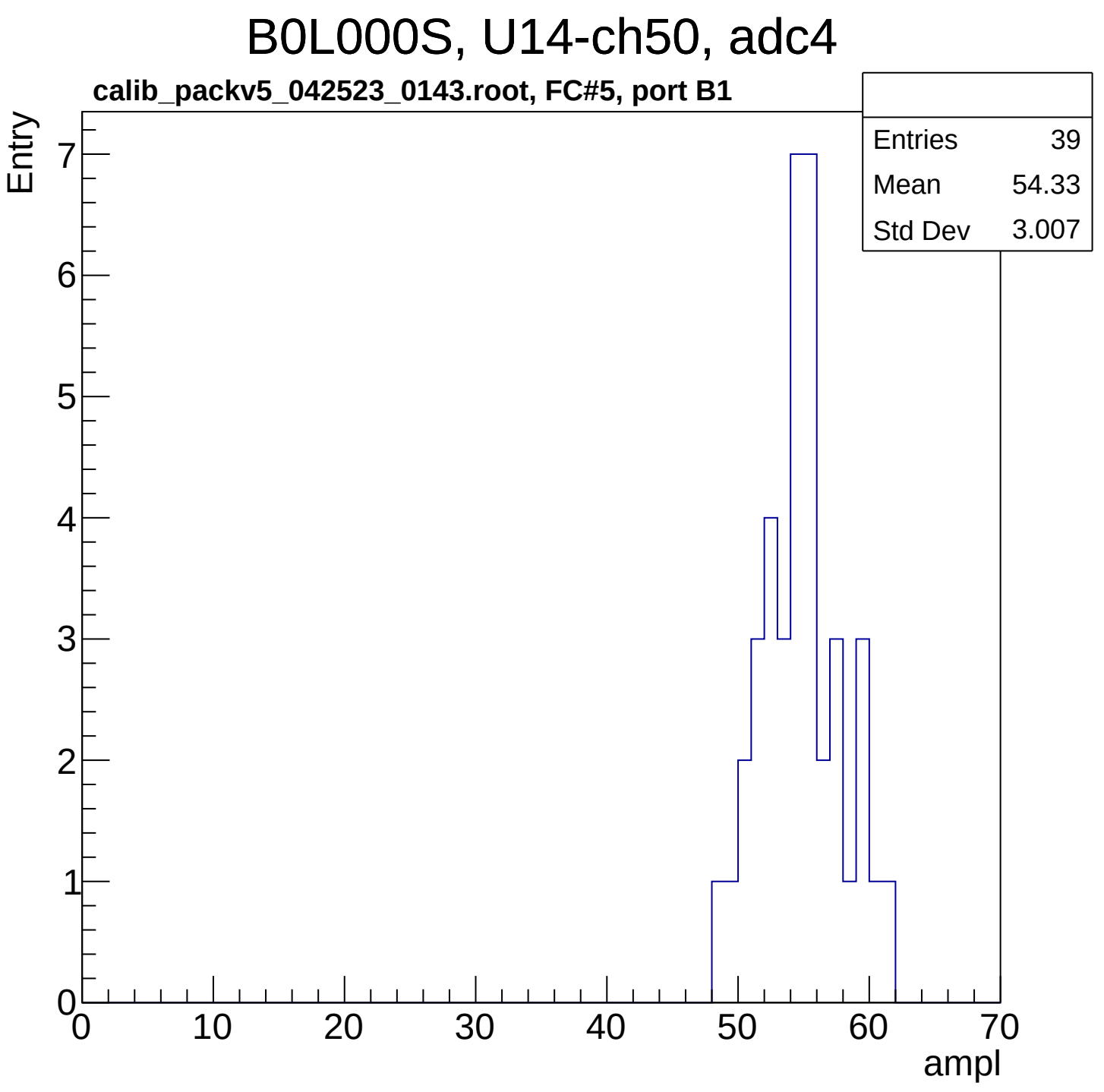
B0L000S, U14-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 54.33
Std Dev 3.007
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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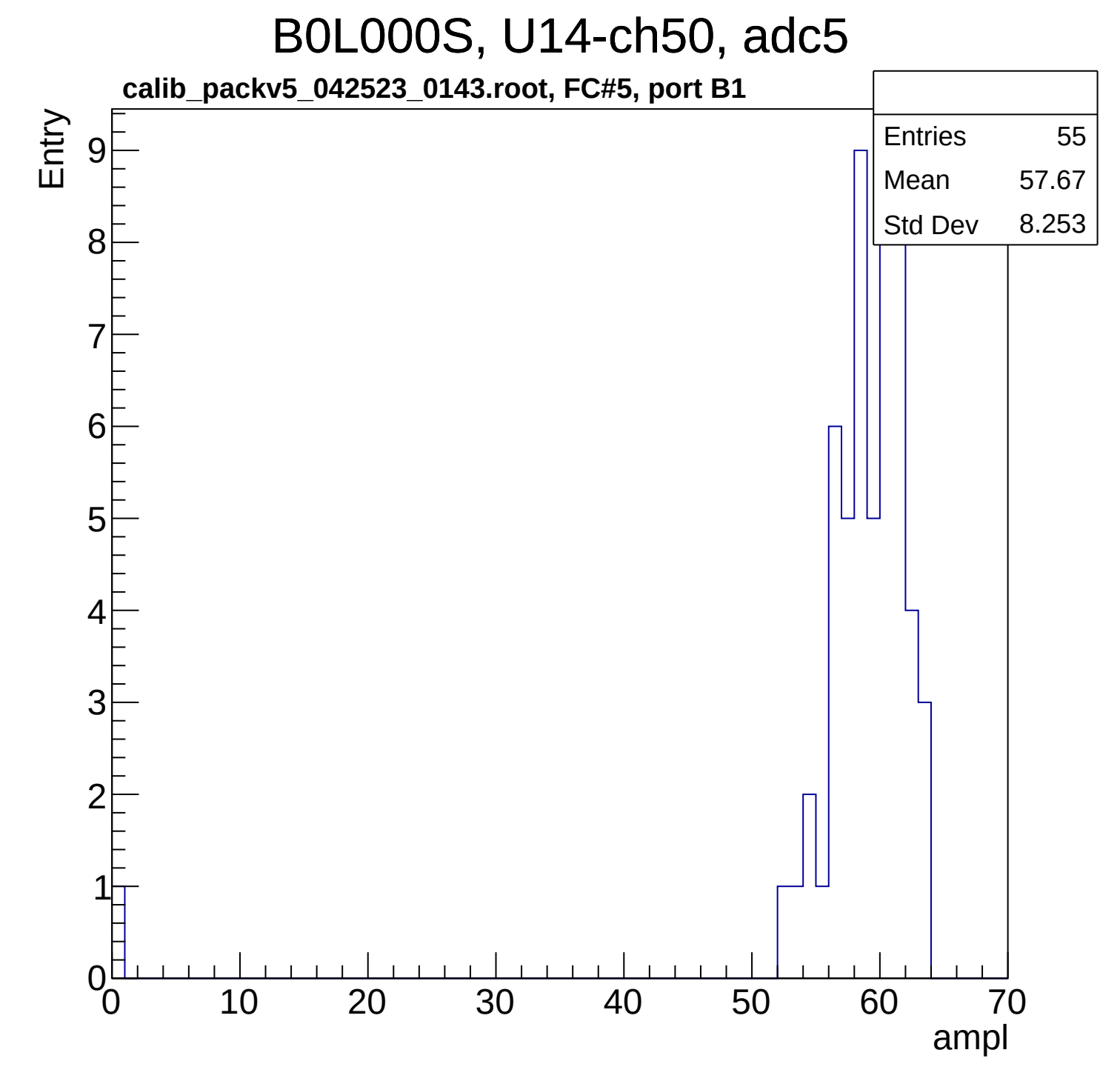
B0L000S, U14-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.67
Std Dev 8.253
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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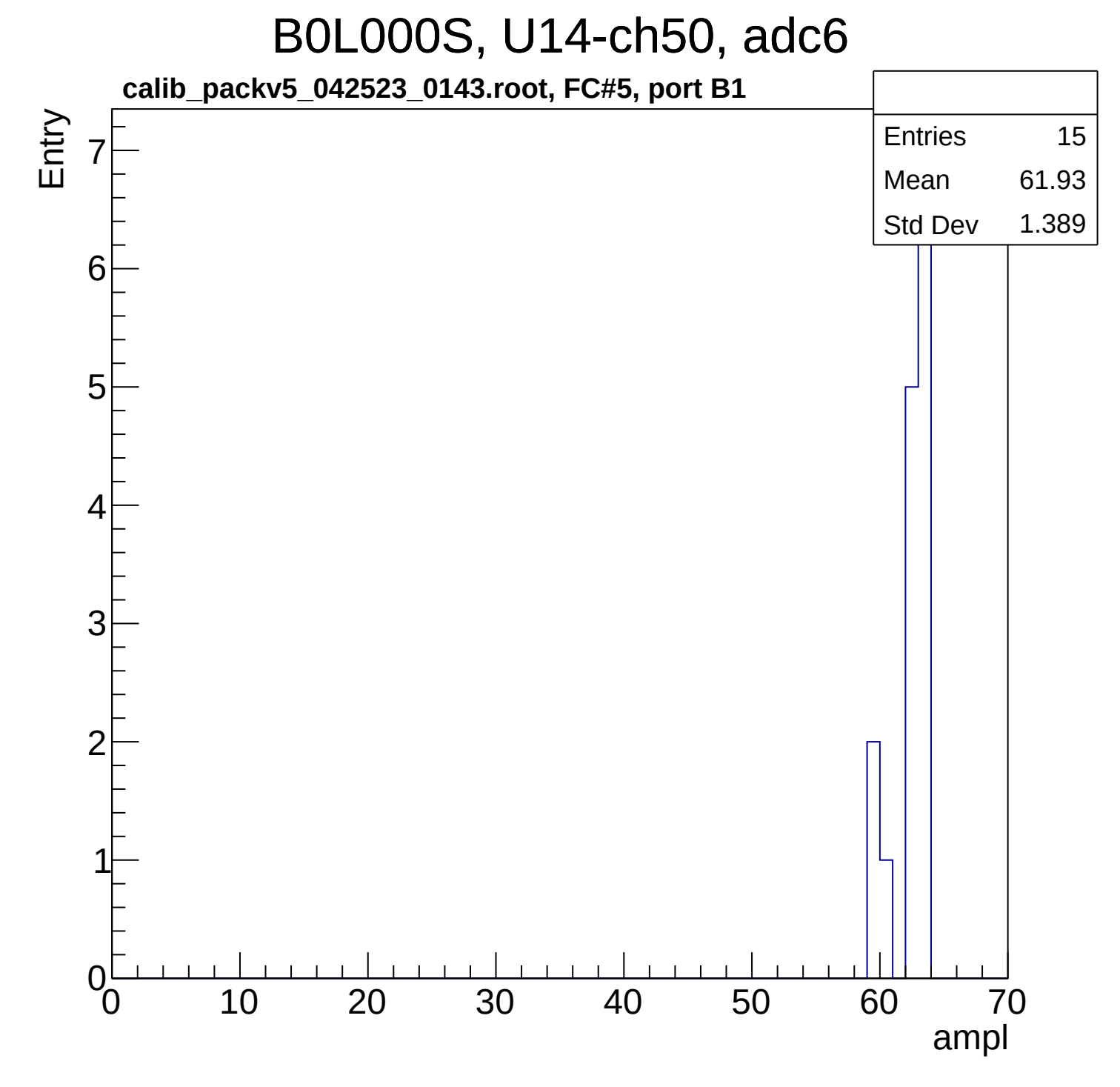
B0L000S, U14-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.93
Std Dev 1.389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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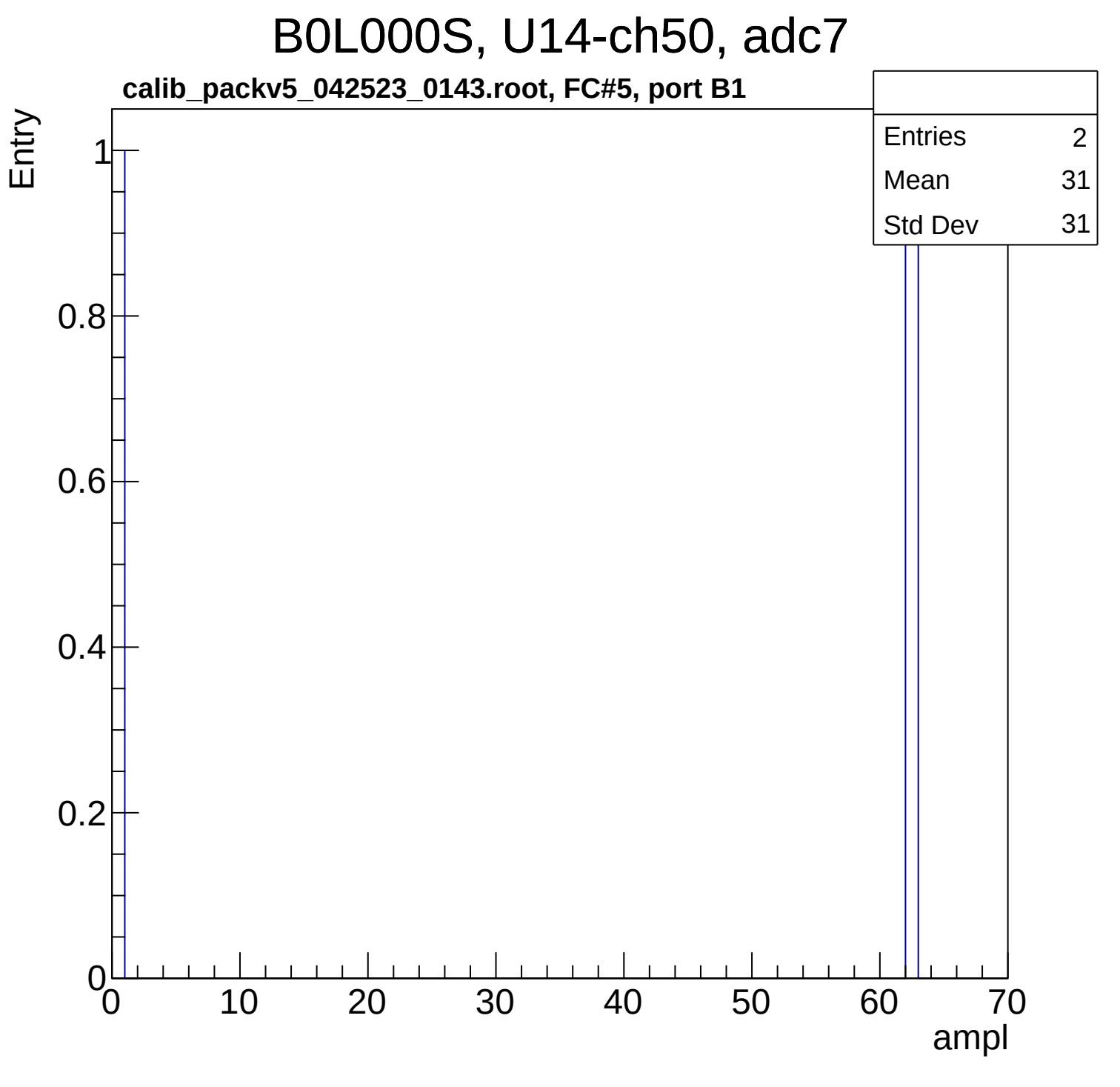
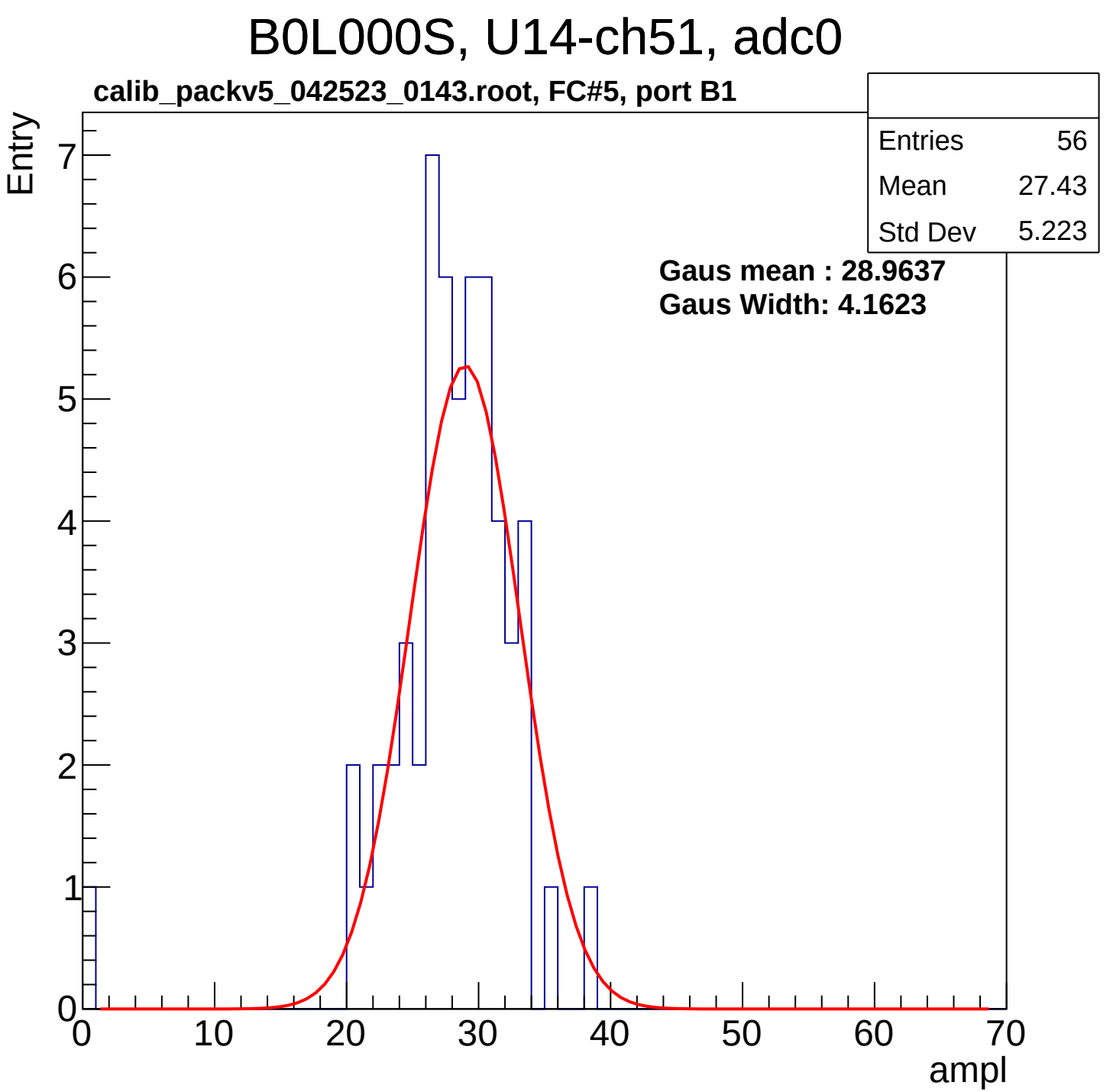
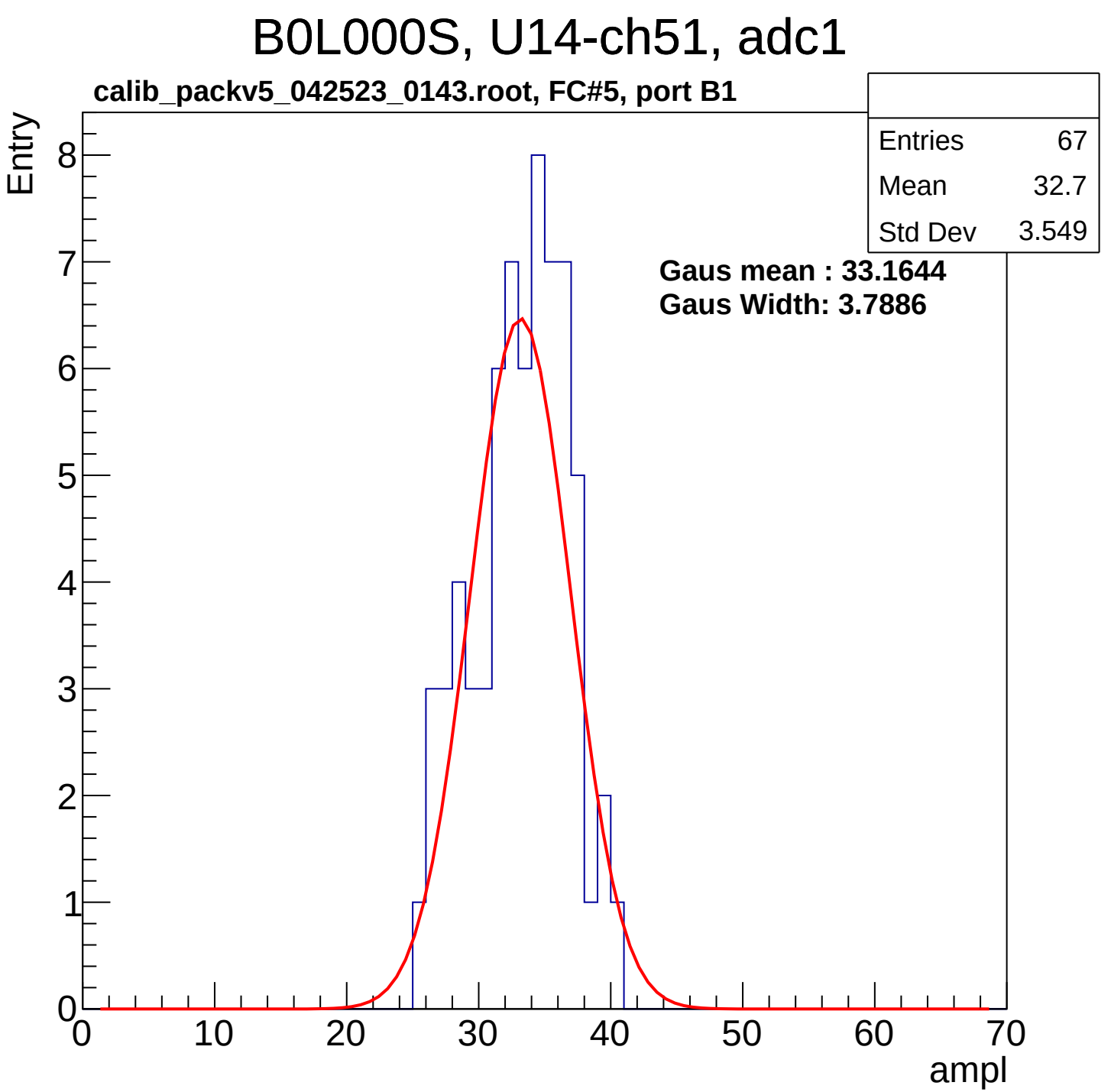

B0L000S, U14-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 27.43
Std Dev 5.223
Gaus mean : 28.9637
Gaus Width: 4.1623
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 32.7
Std Dev 3.549
Gaus mean : 33.1644
Gaus Width: 3.7886
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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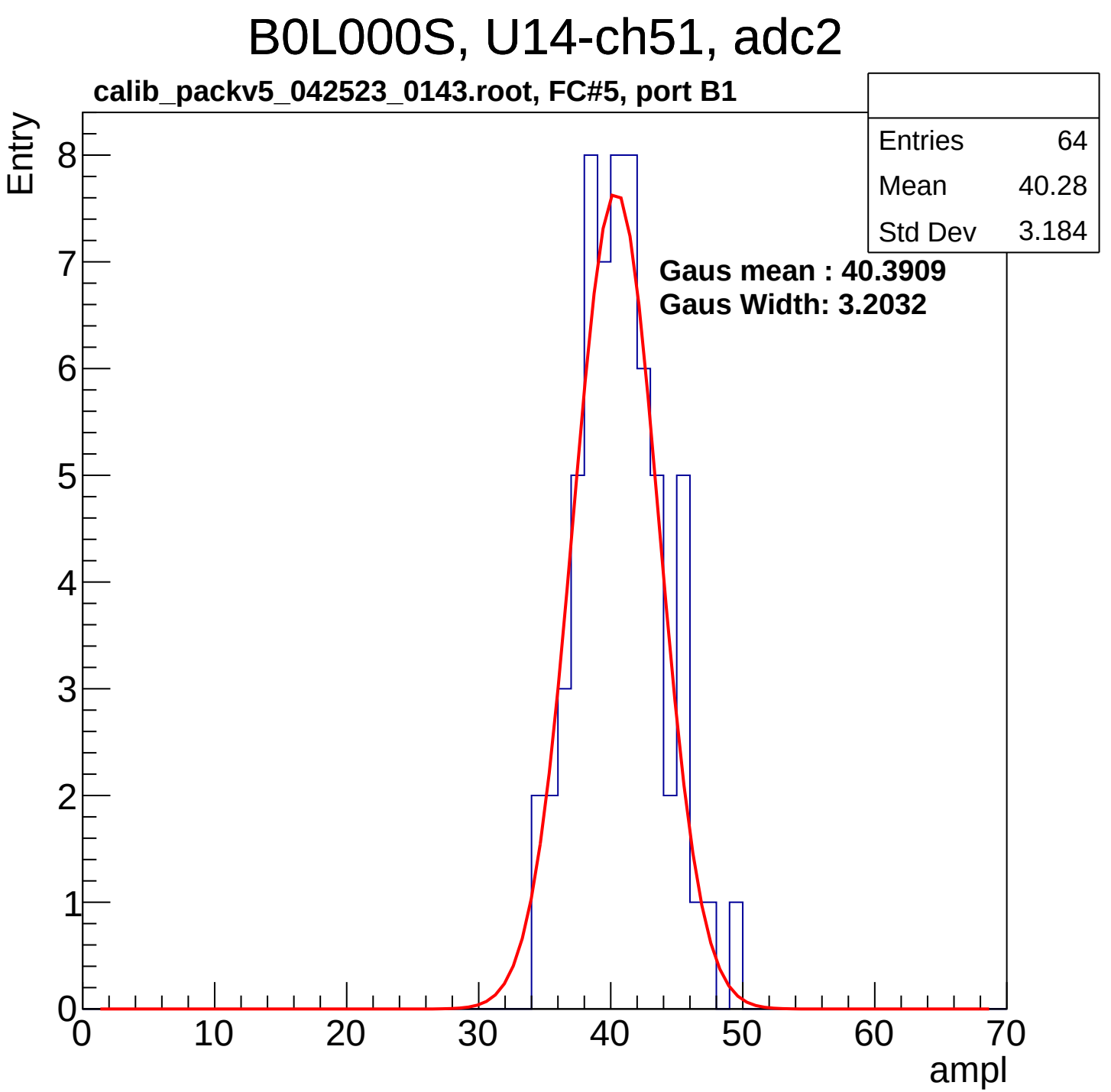
B0L000S, U14-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 40.28
Std Dev 3.184
Gaus mean : 40.3909
Gaus Width: 3.2032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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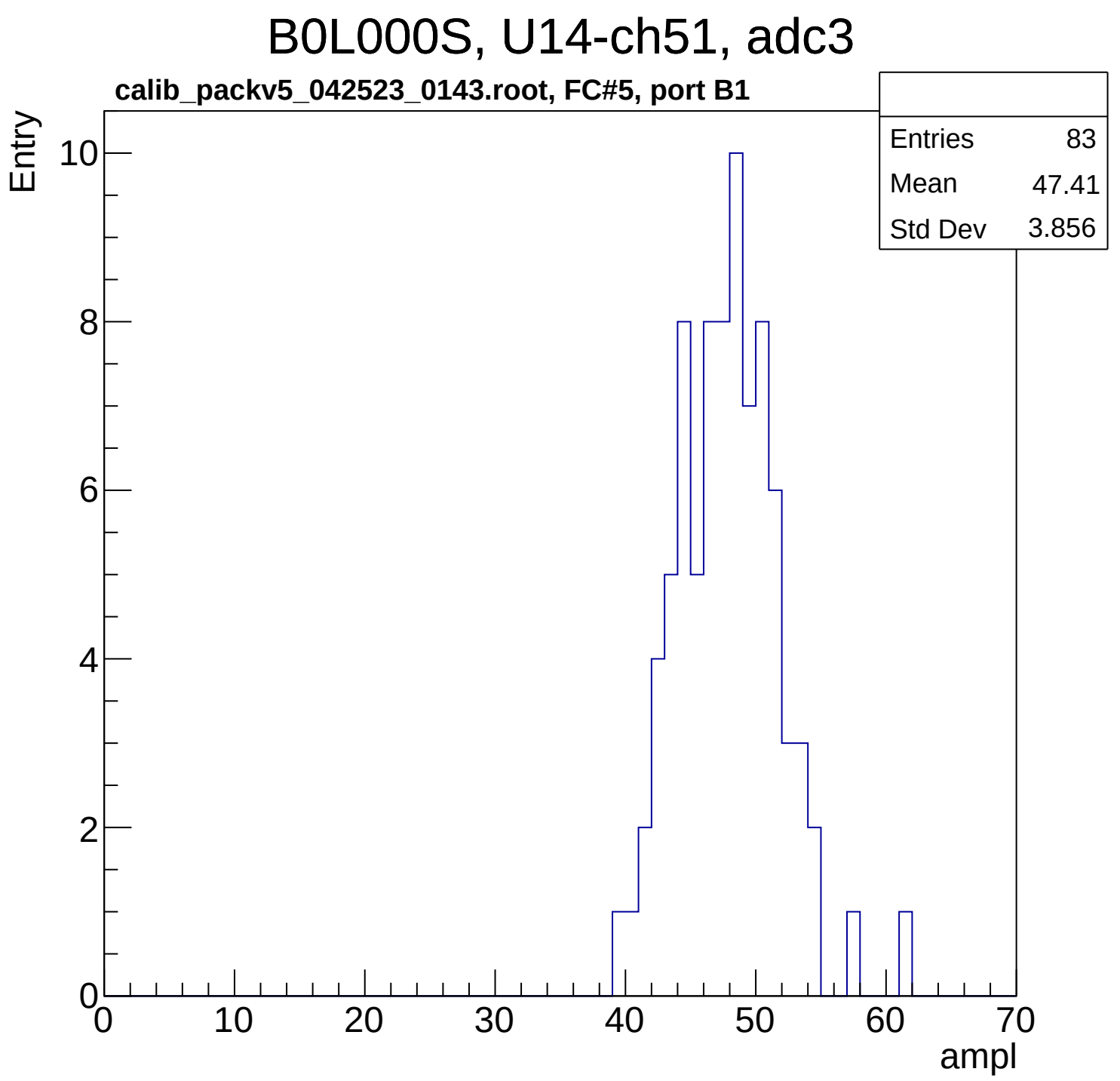
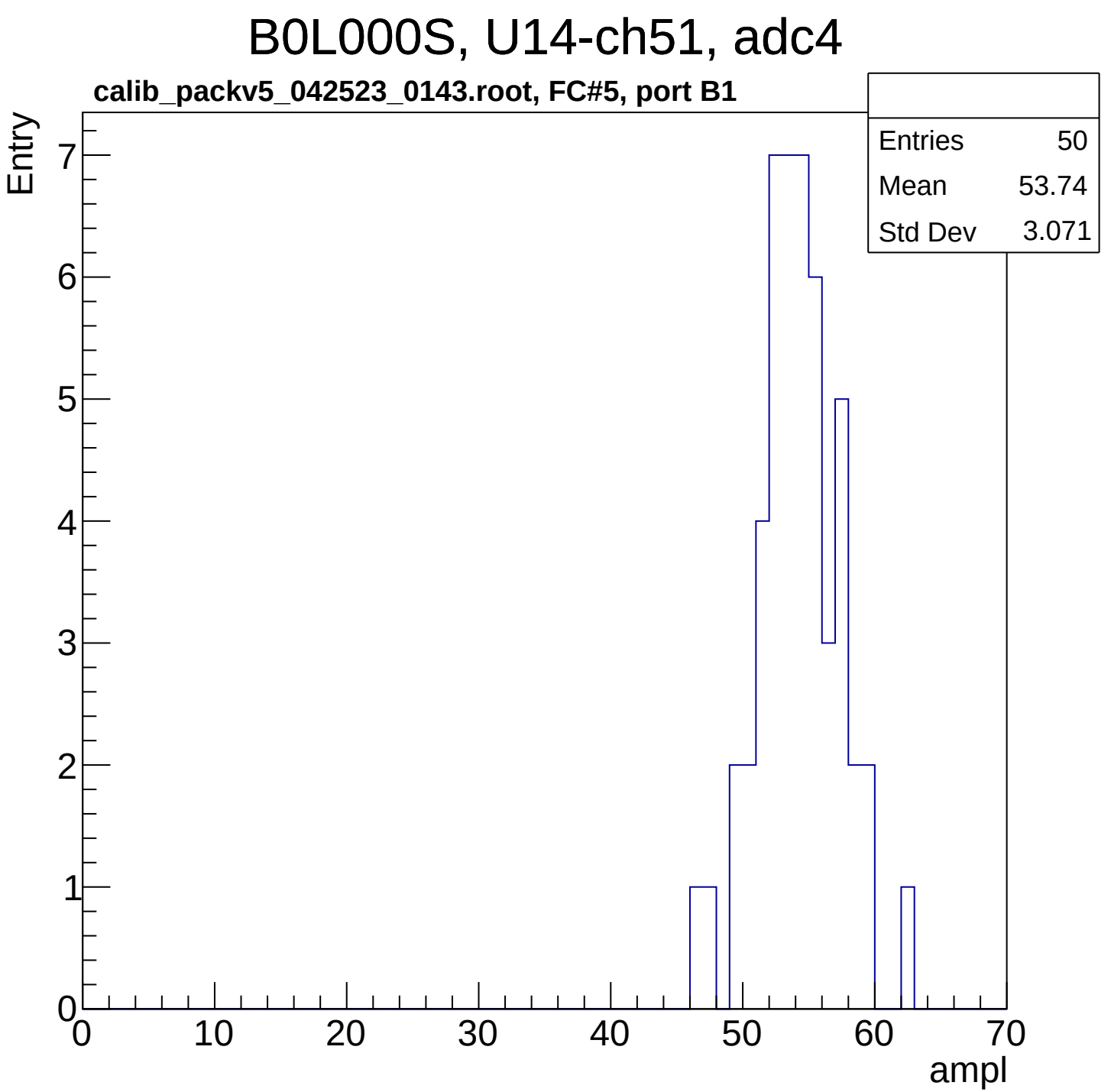
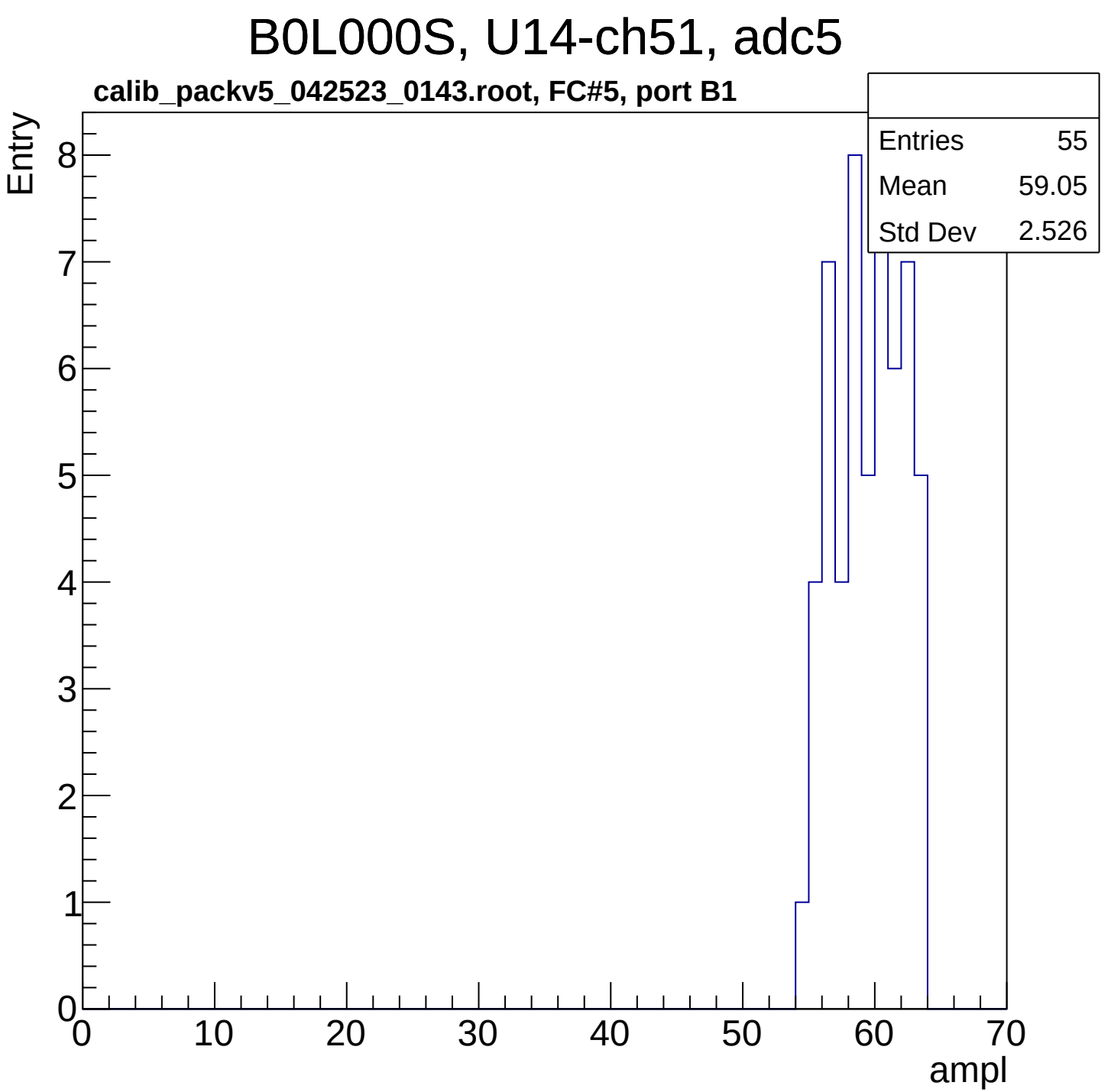

B0L000S, U14-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 47.41
Std Dev 3.856
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 53.74
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 59.05
Std Dev 2.526
Entry
ampl
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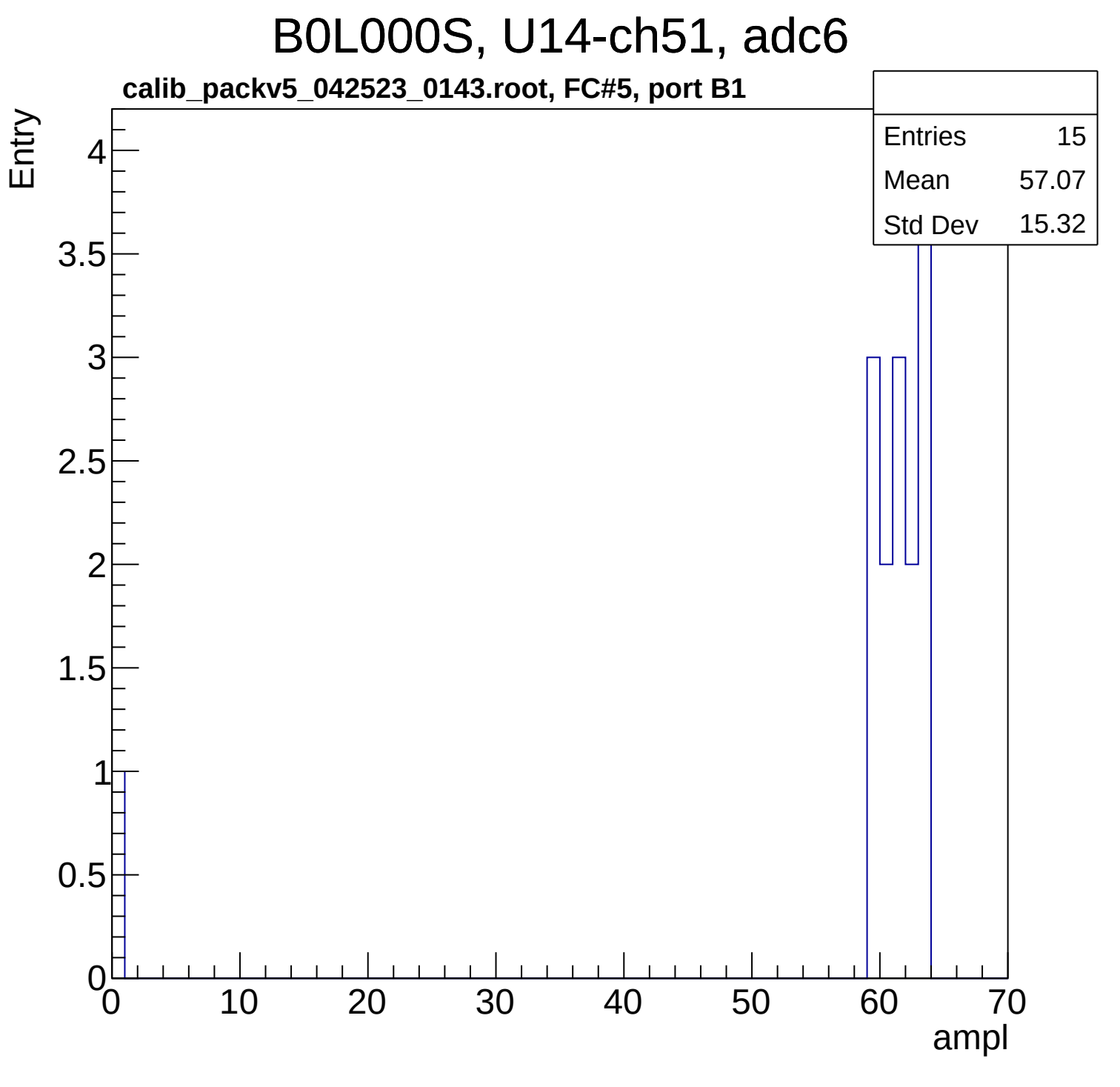
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.07
Std Dev 15.32
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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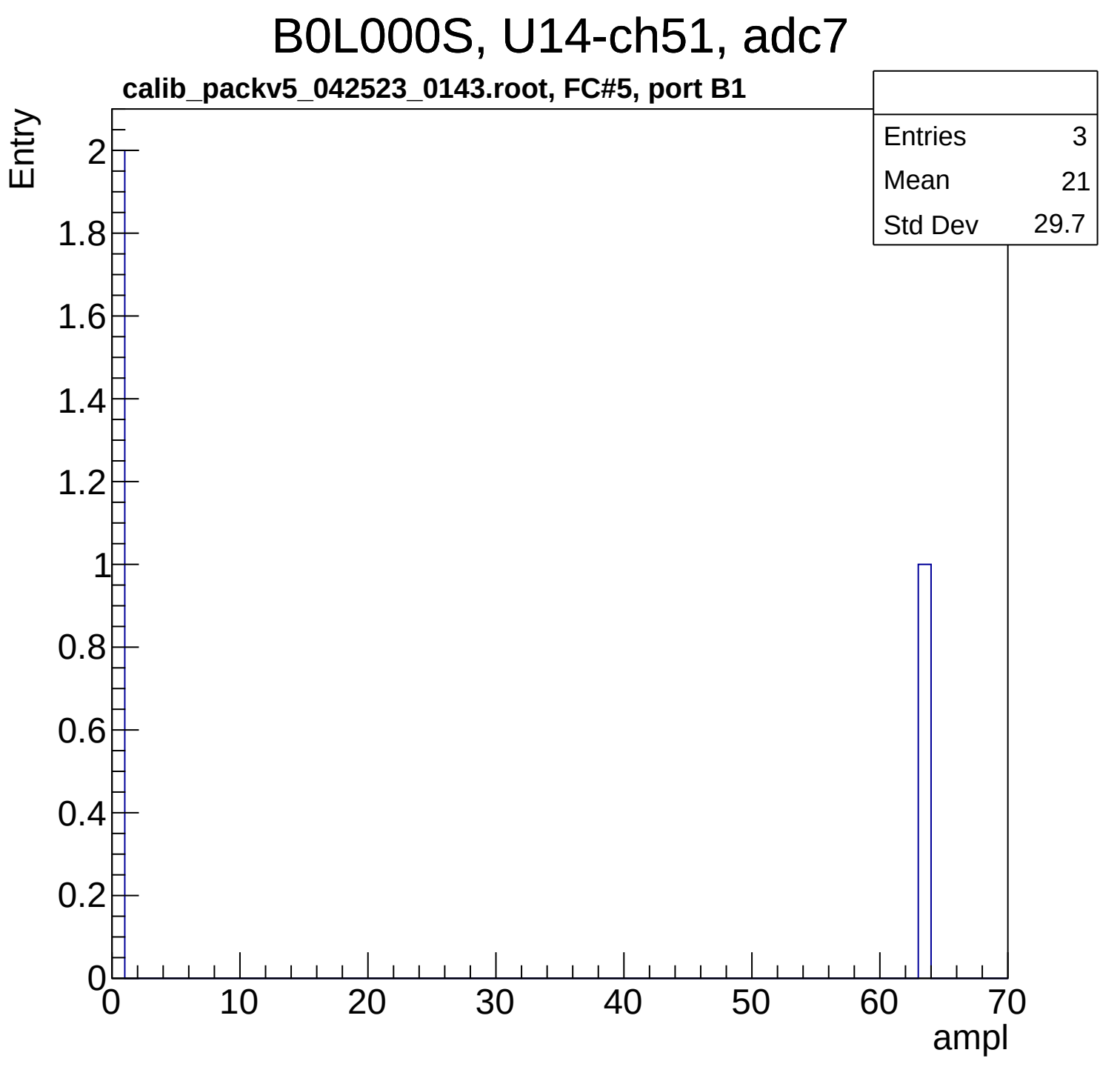
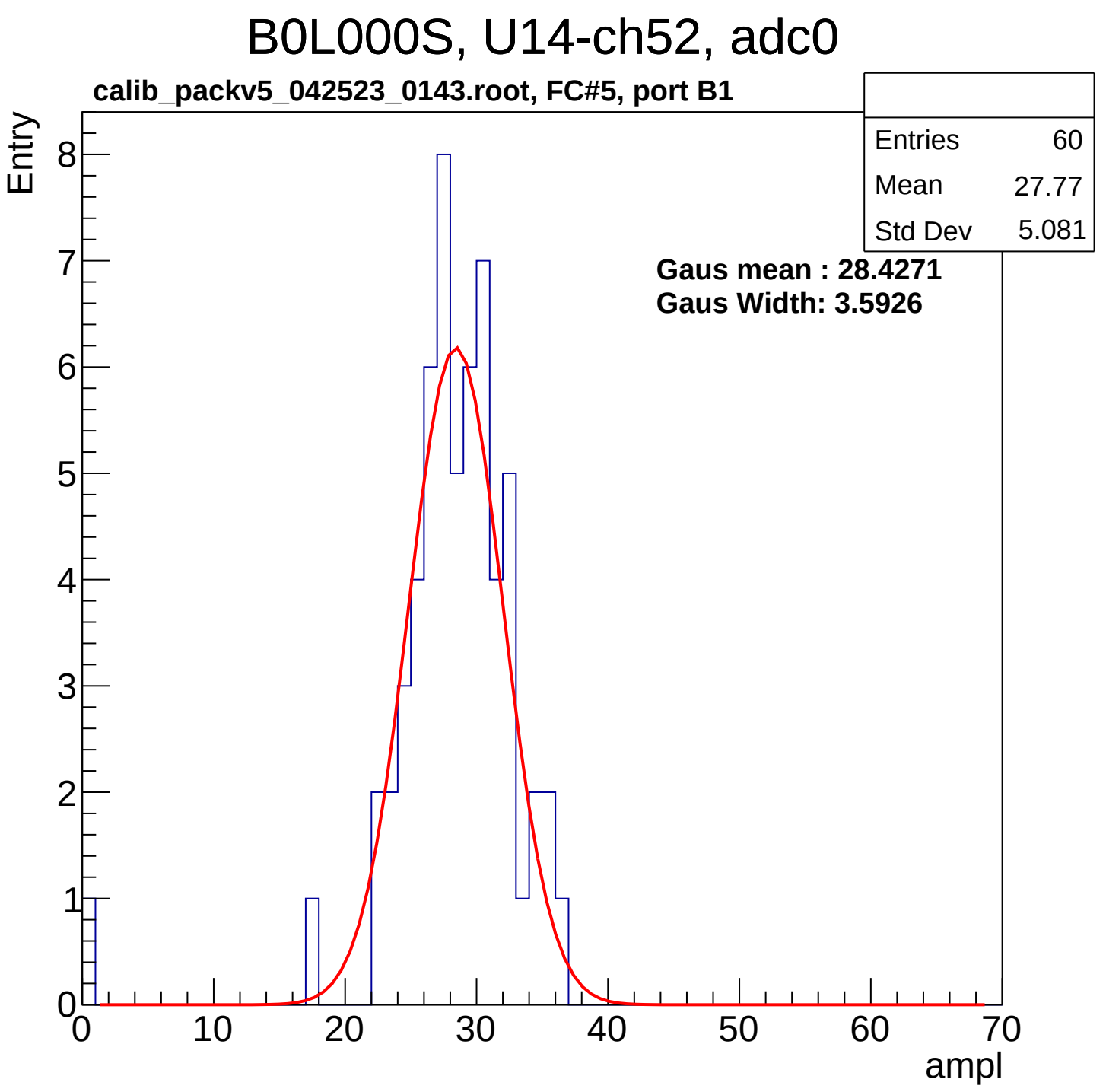
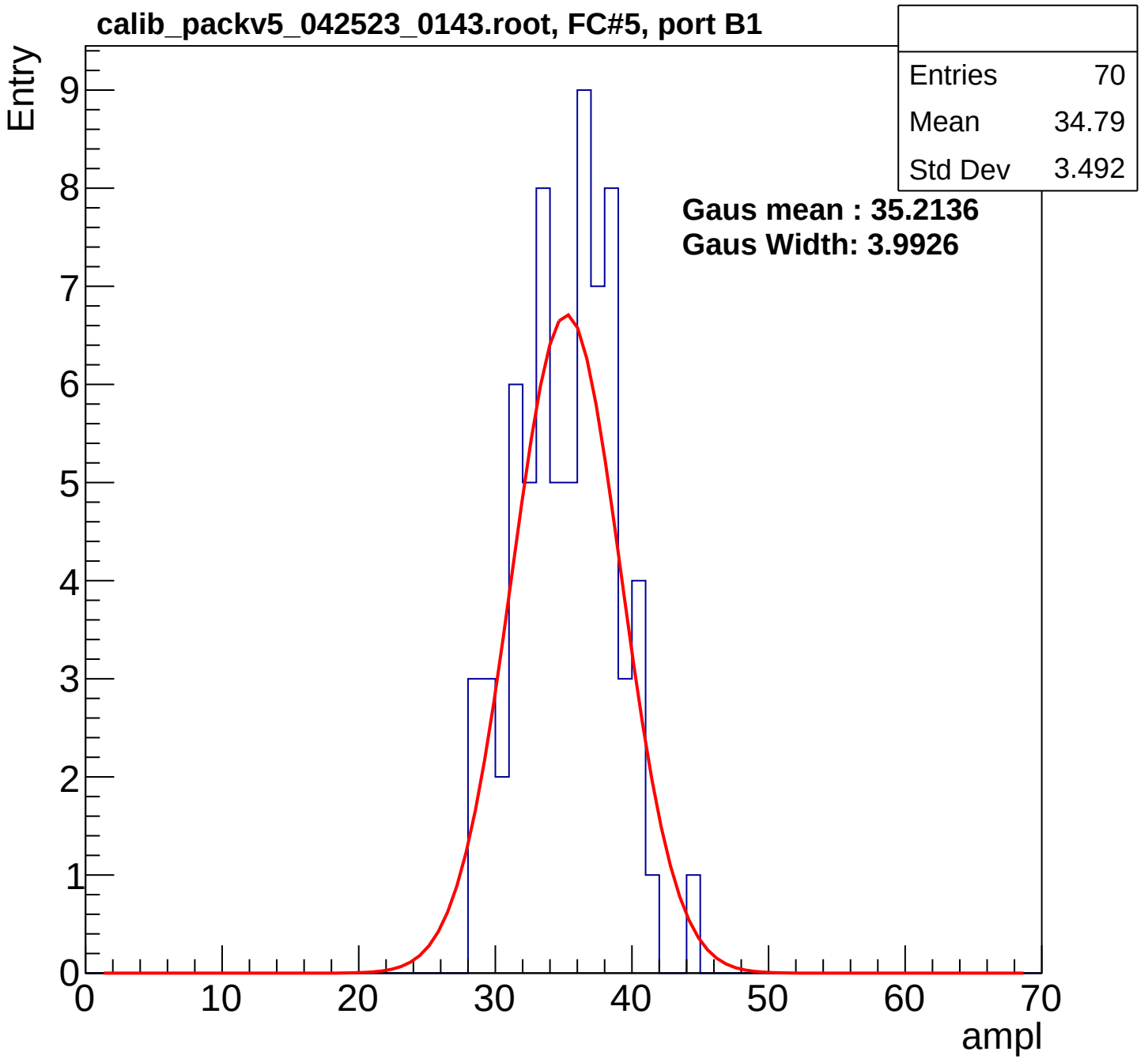

B0L000S, U14-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 27.77
Std Dev 5.081
Gaus mean : 28.4271
Gaus Width: 3.5926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 34.79
Std Dev 3.492
Gaus mean : 35.2136
Gaus Width: 3.9926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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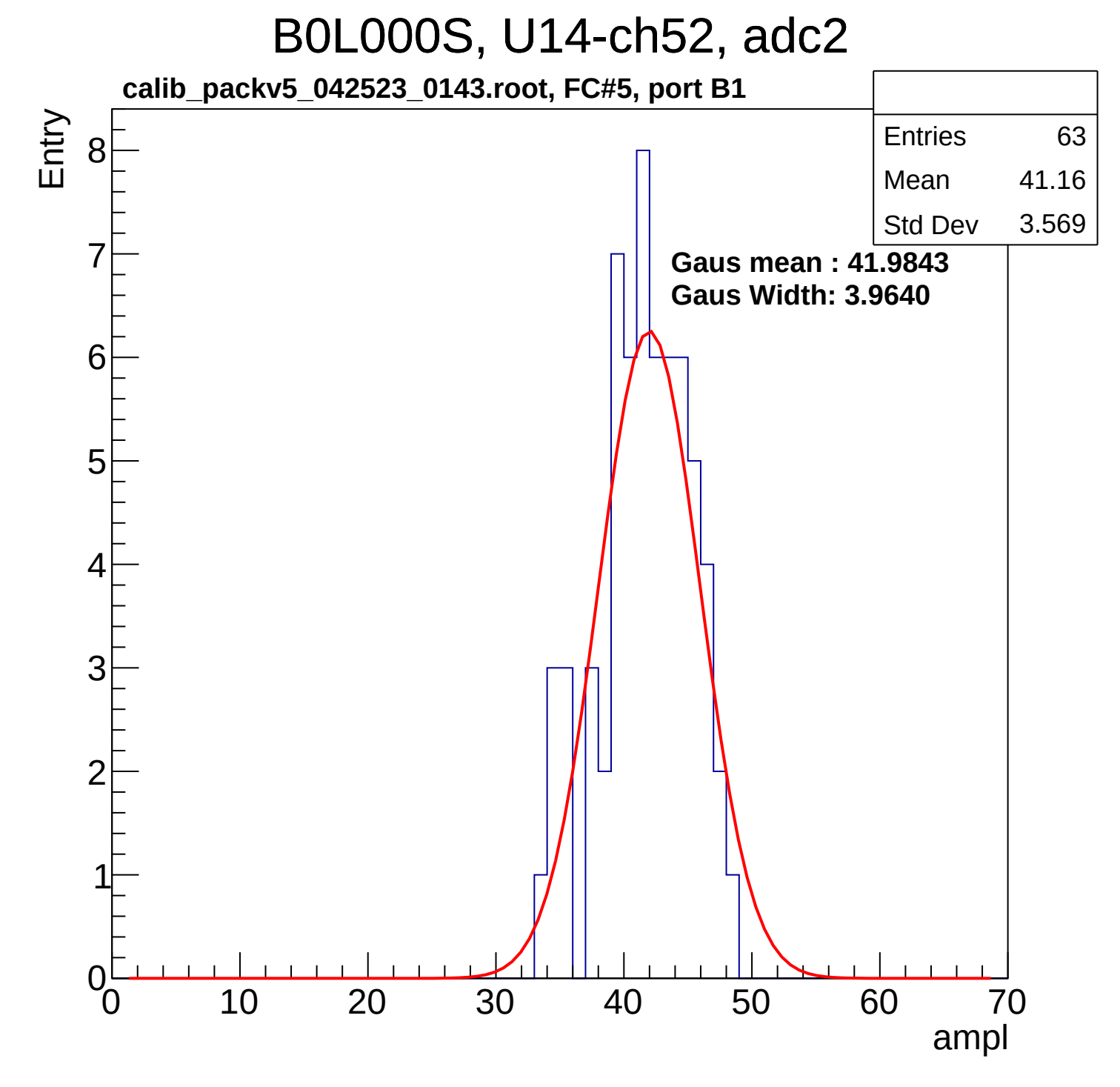
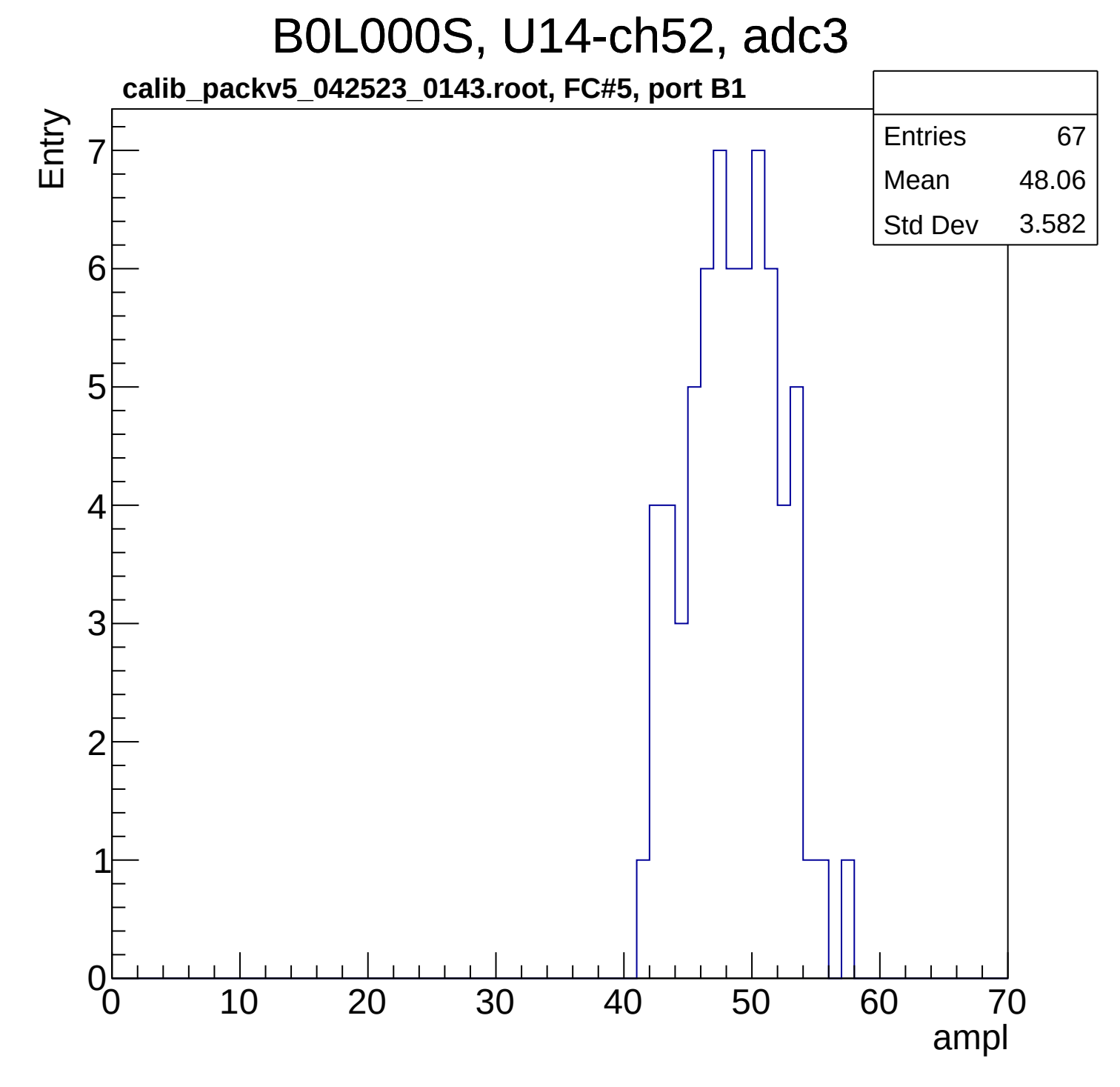

B0L000S, U14-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.16
Std Dev 3.569
Gaus mean : 41.9843
Gaus Width: 3.9640
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.06
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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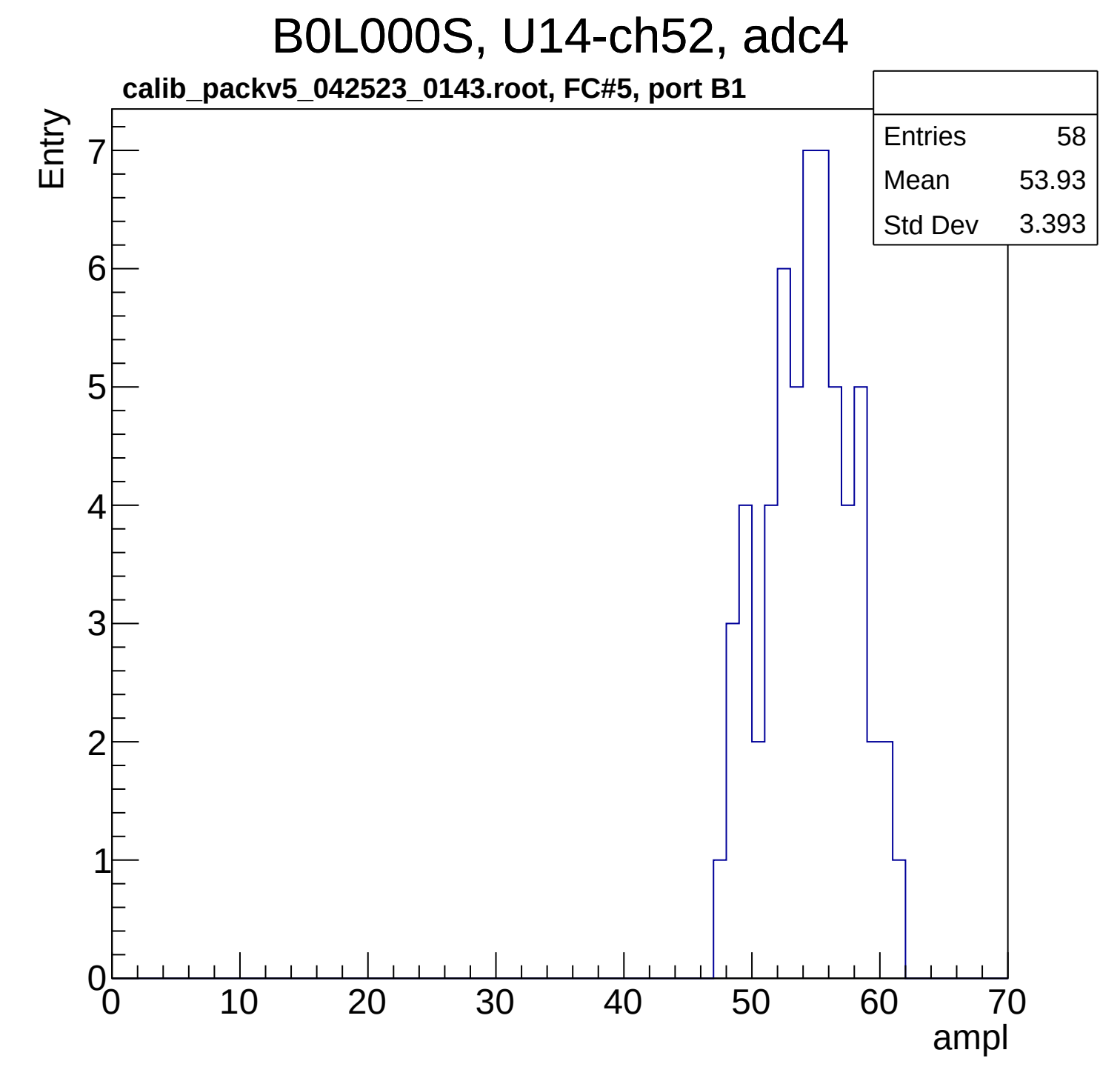
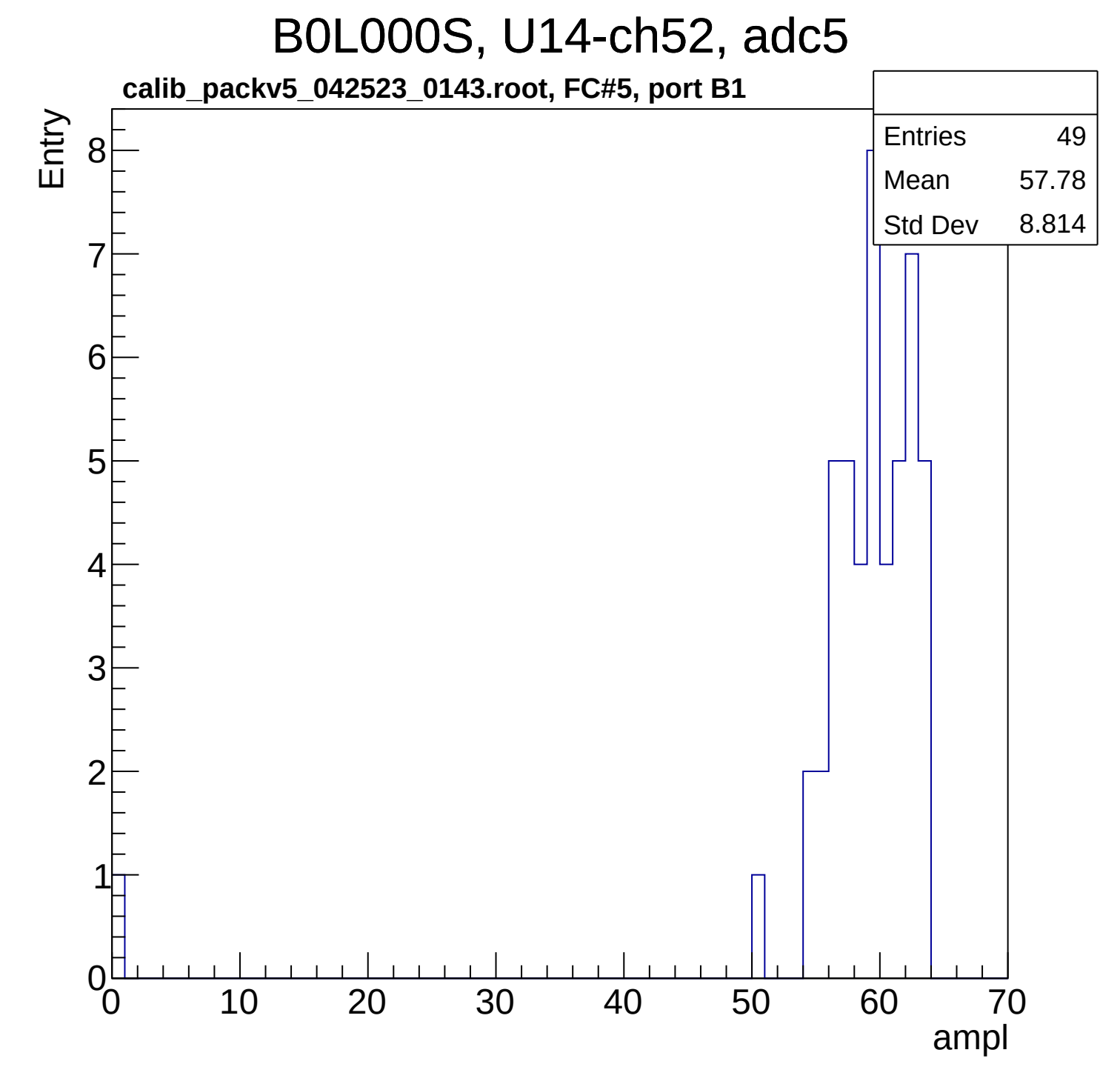
40
50
60
70


B0L000S, U14-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.93
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 57.78
Std Dev 8.814
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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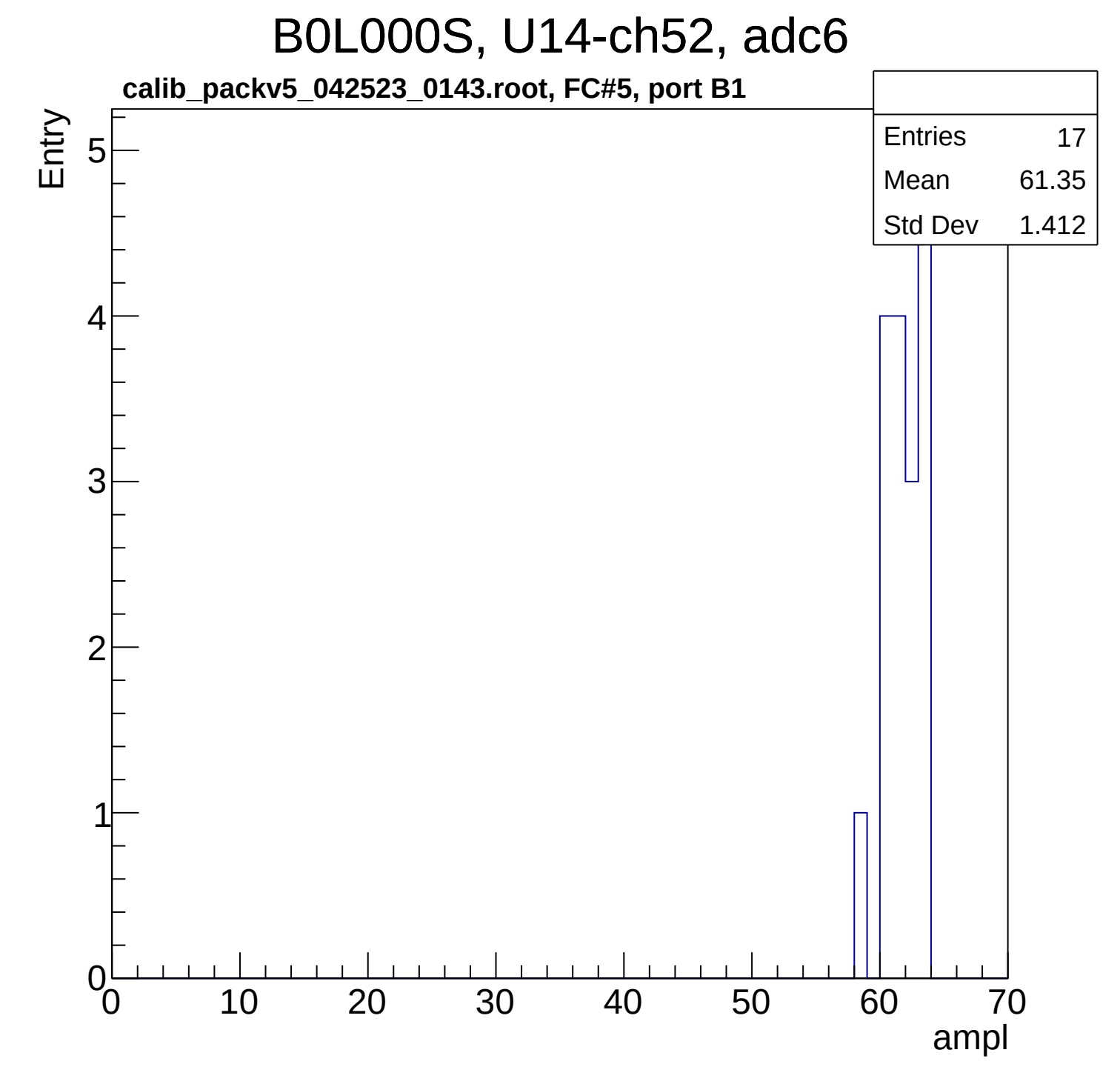
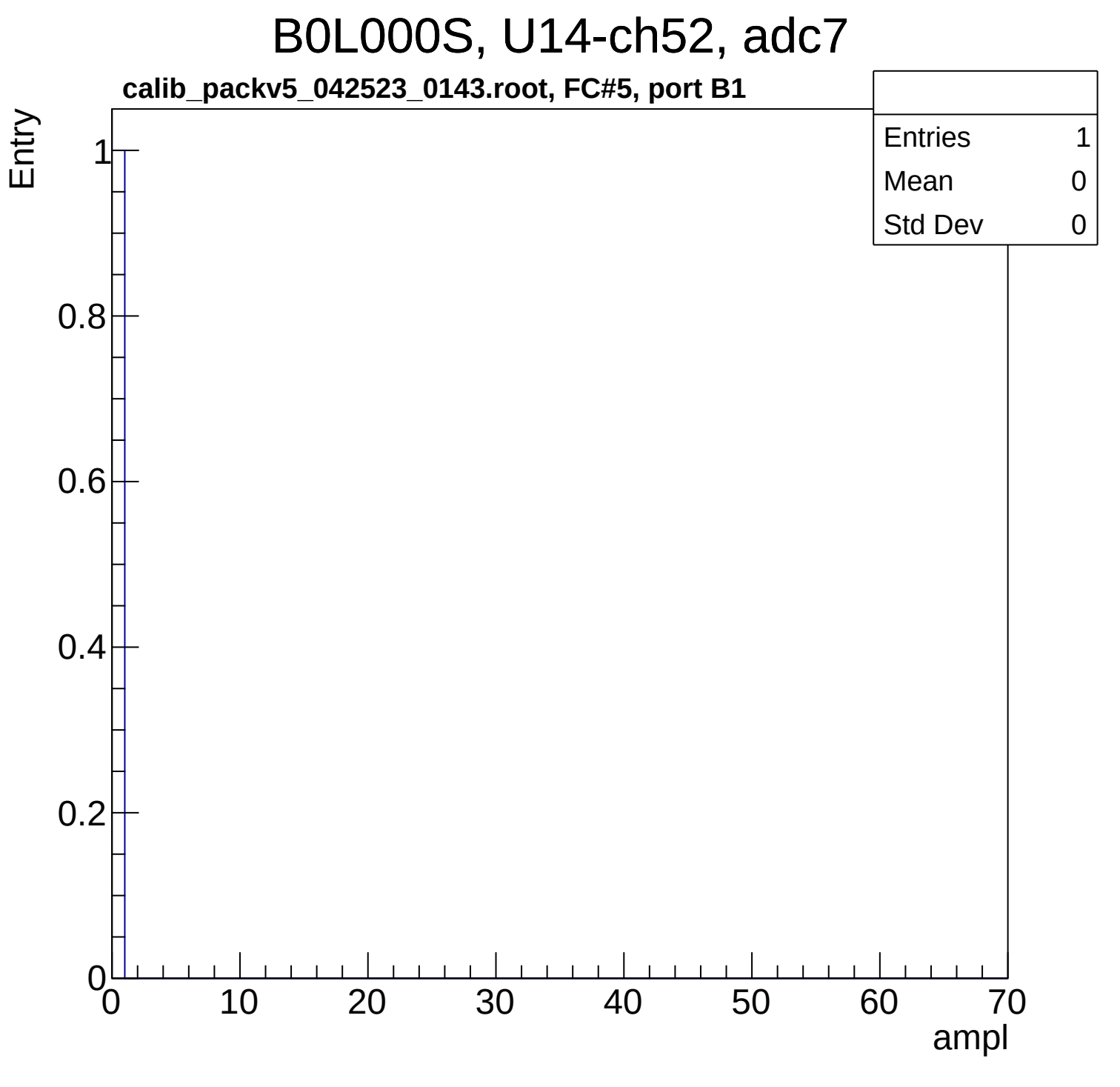
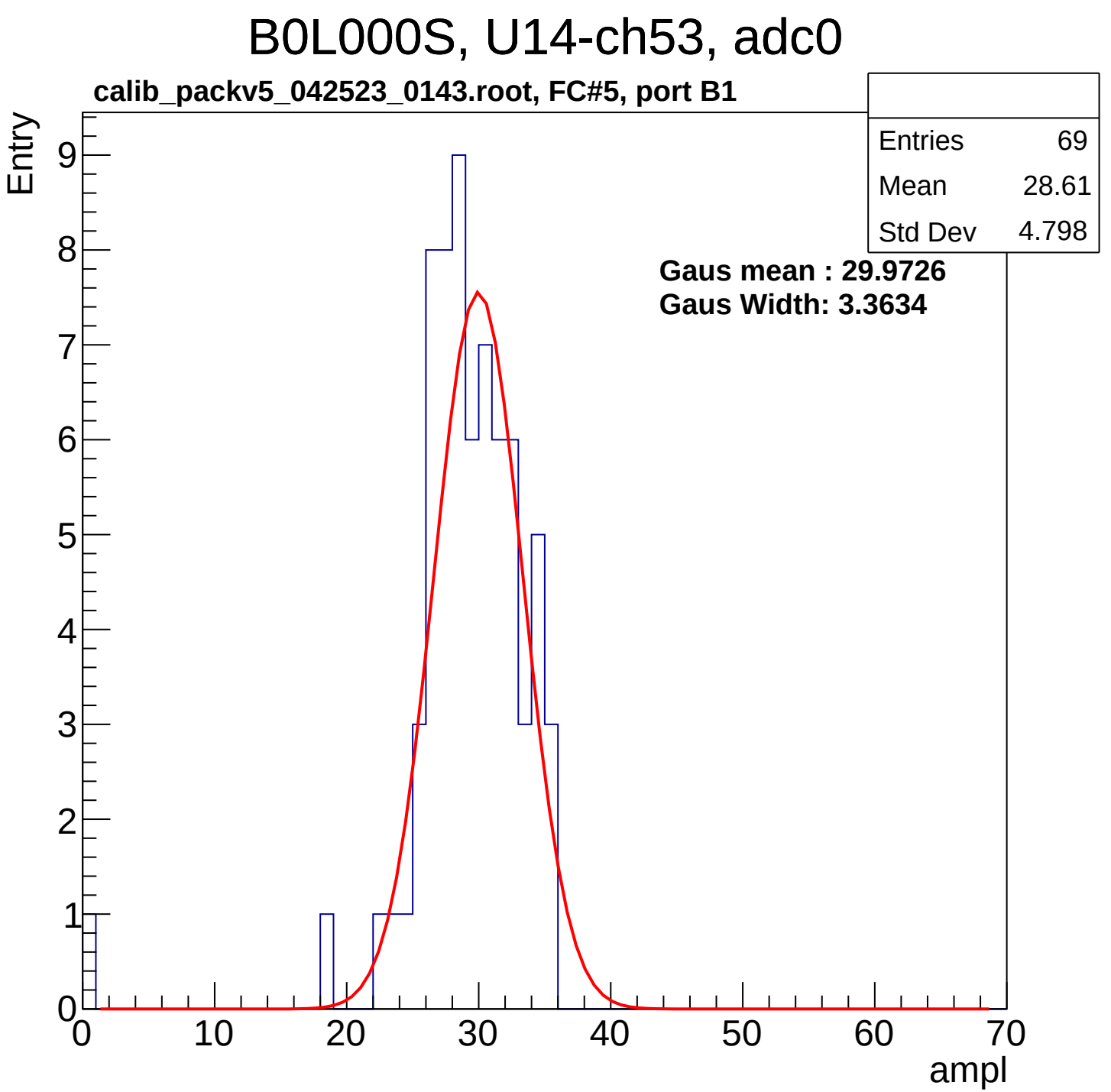
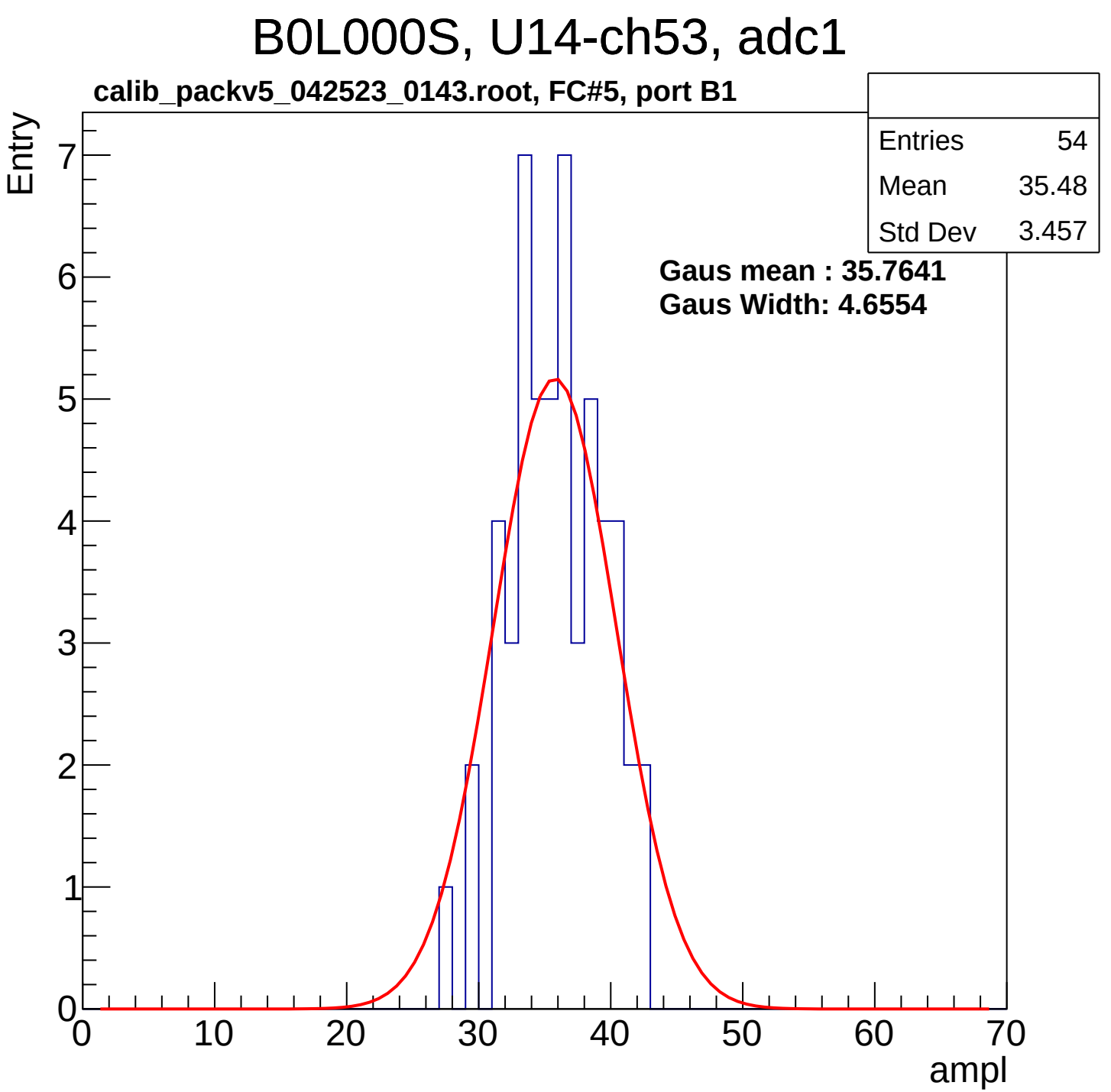
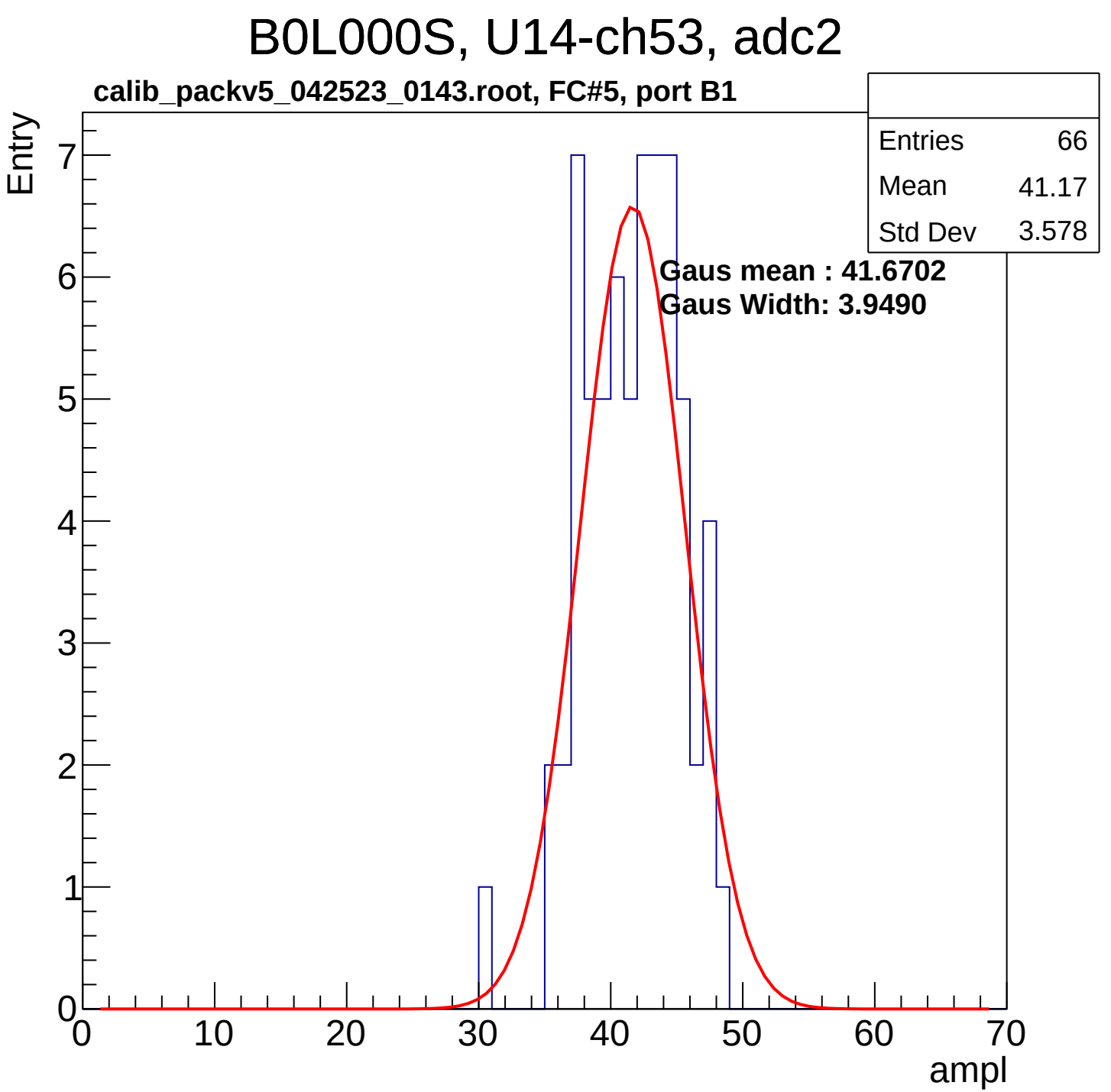

B0L000S, U14-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.35
Std Dev 1.412
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 28.61
Std Dev 4.798
Gaus mean : 29.9726
Gaus Width: 3.3634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 35.48
Std Dev 3.457
Gaus mean : 35.7641
Gaus Width: 4.6554
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.17
Std Dev 3.578
Gaus mean : 41.6702
Gaus Width: 3.9490
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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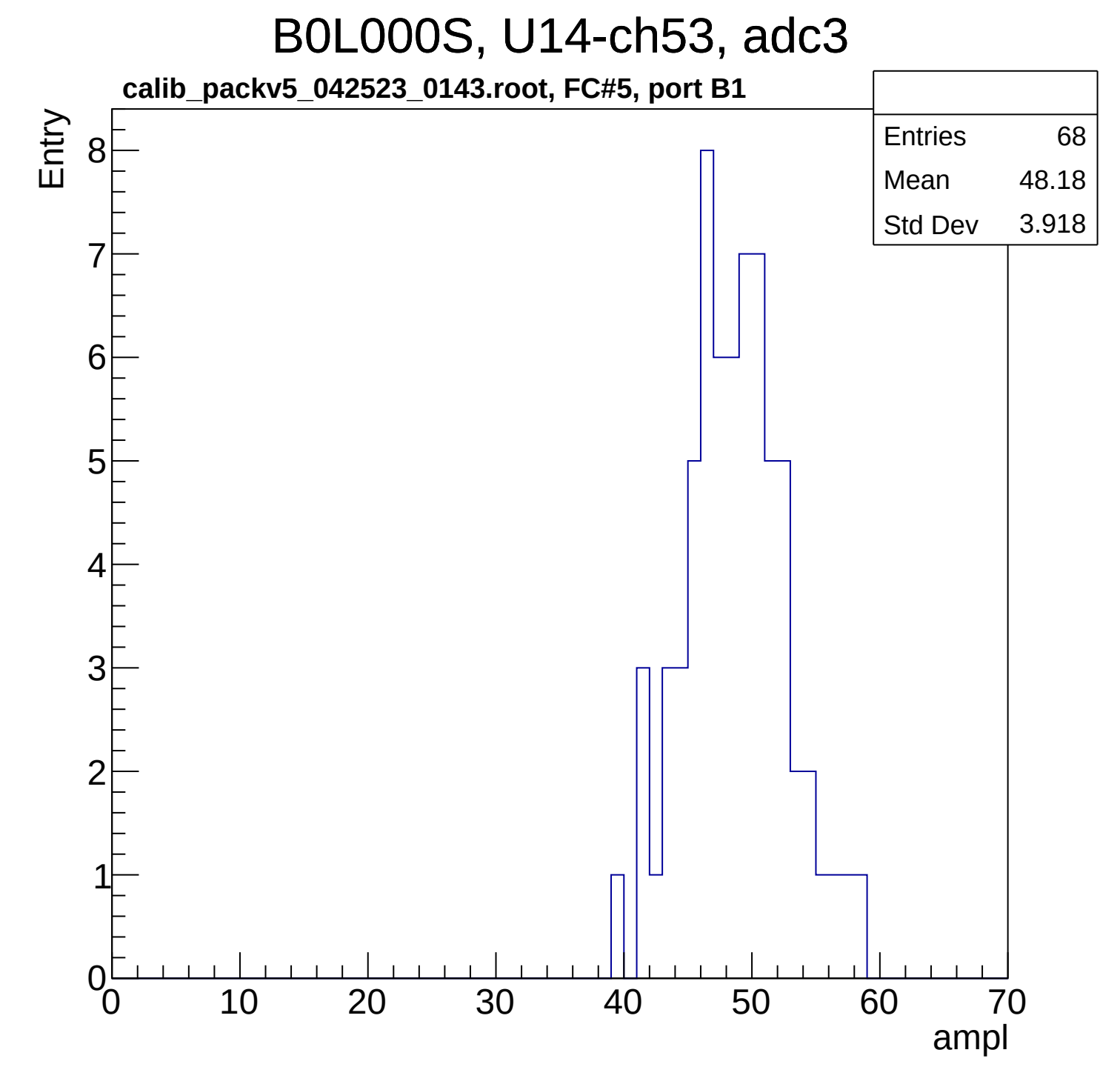
B0L000S, U14-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.18
Std Dev 3.918
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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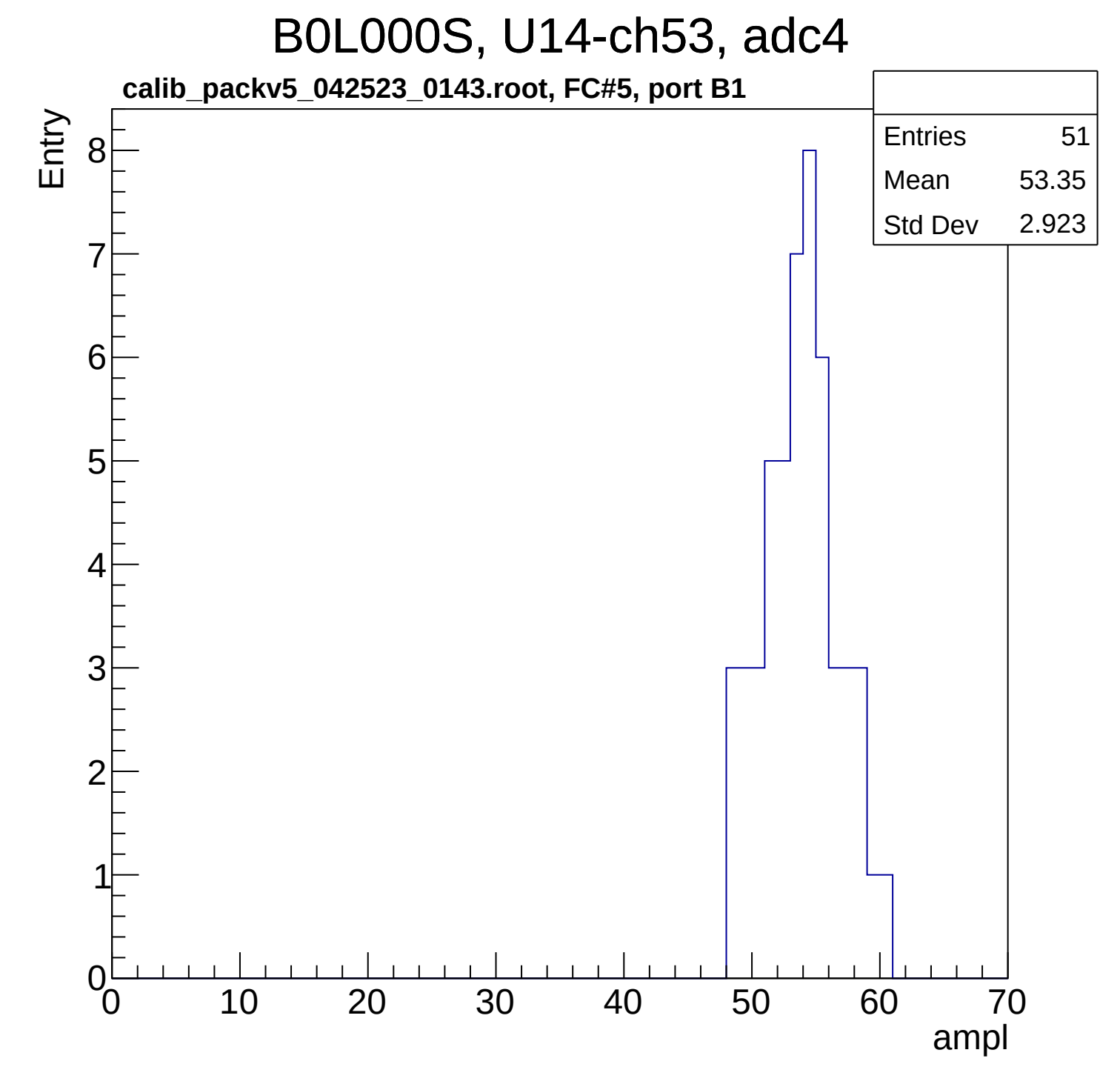
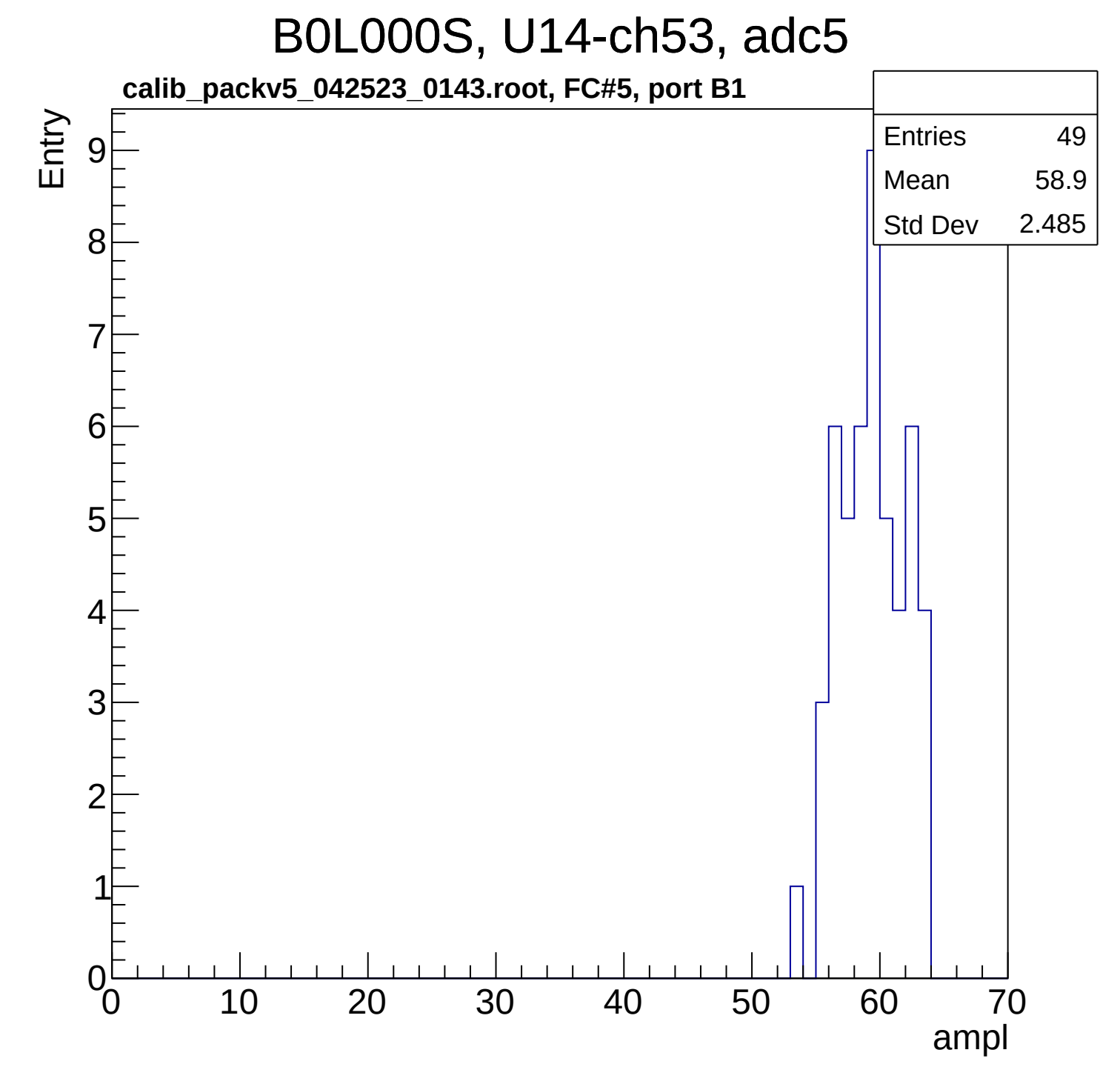

B0L000S, U14-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.35
Std Dev 2.923
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.9
Std Dev 2.485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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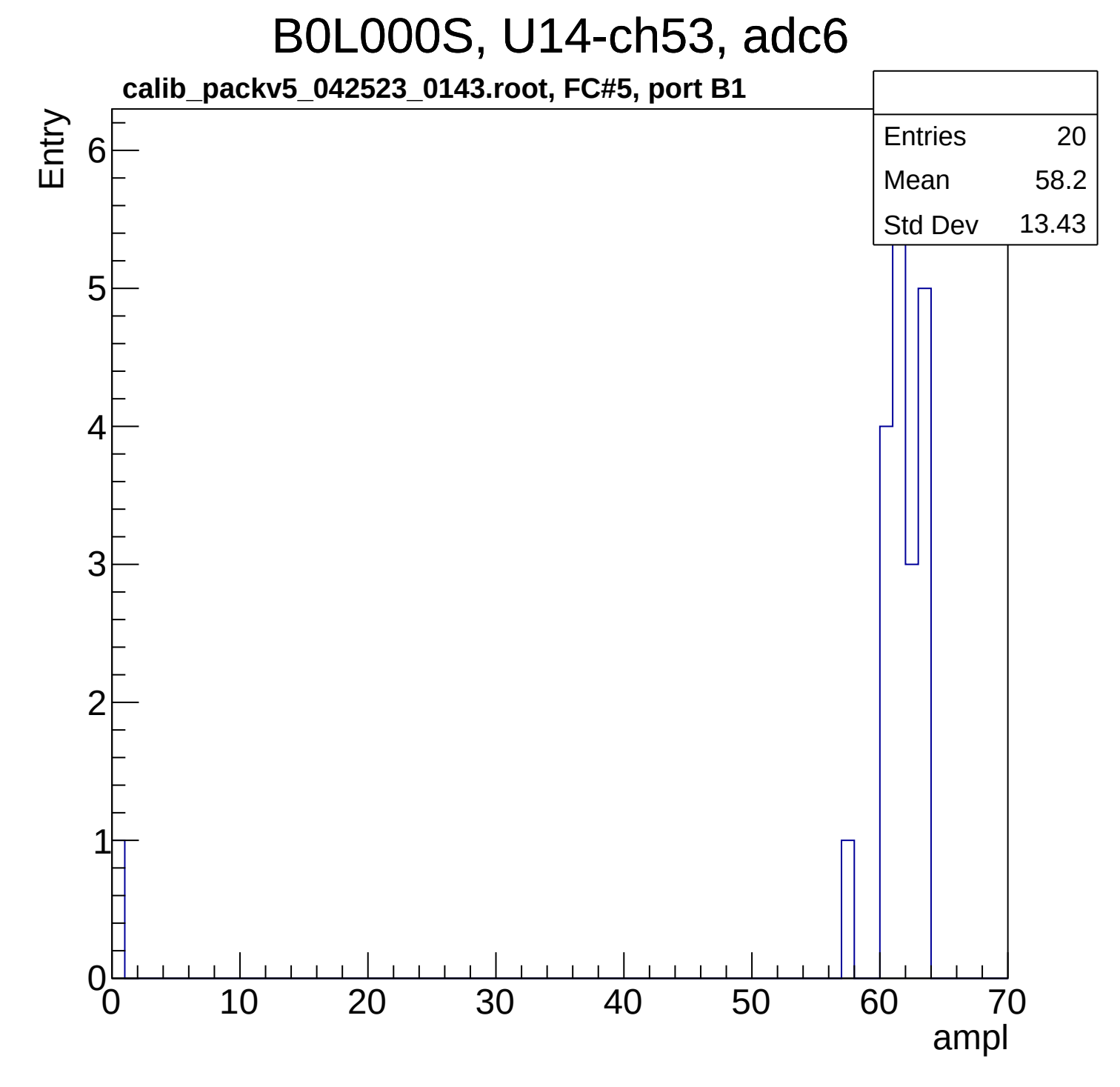
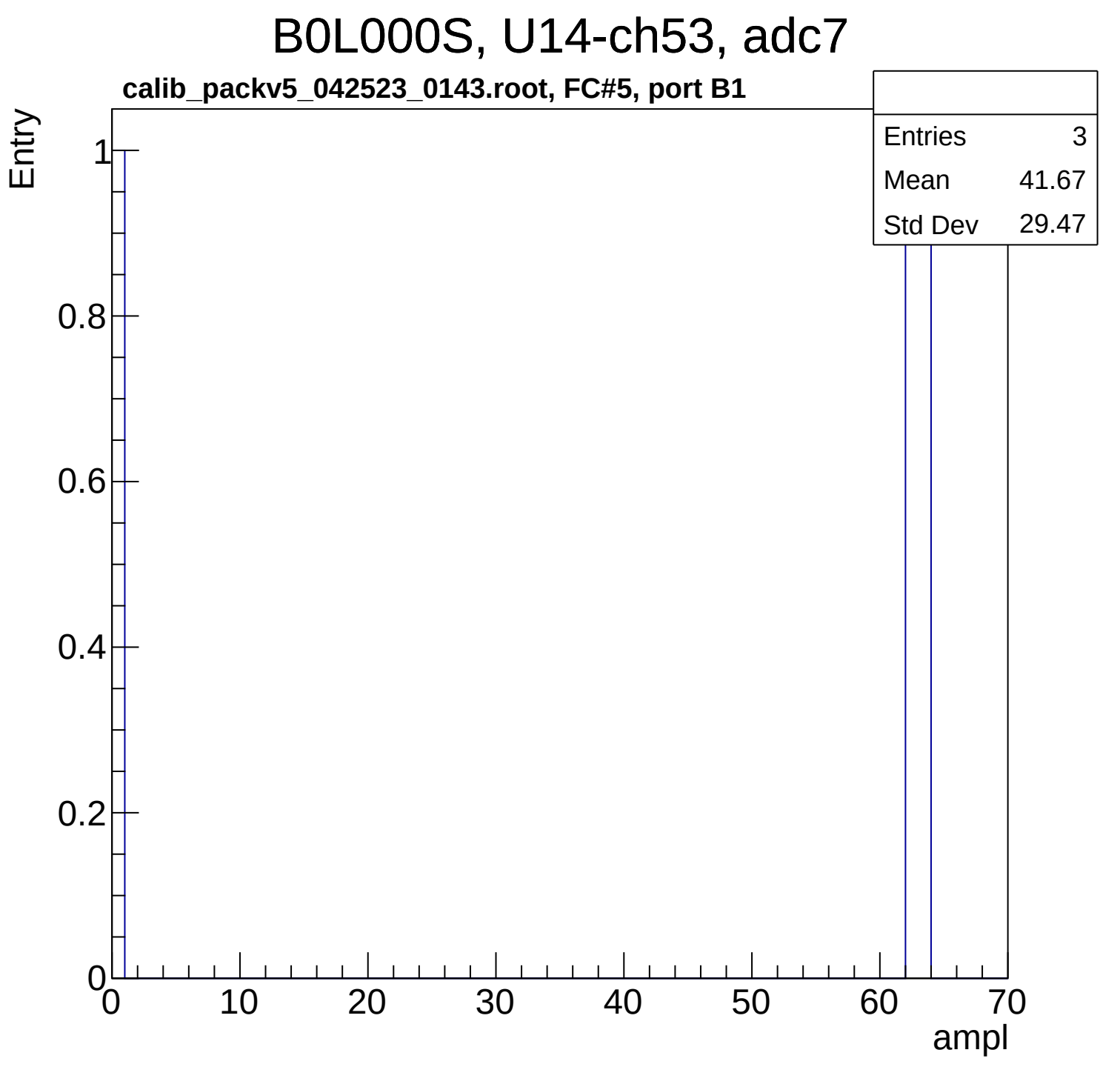

B0L000S, U14-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.2
Std Dev 13.43
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
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0
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10
20
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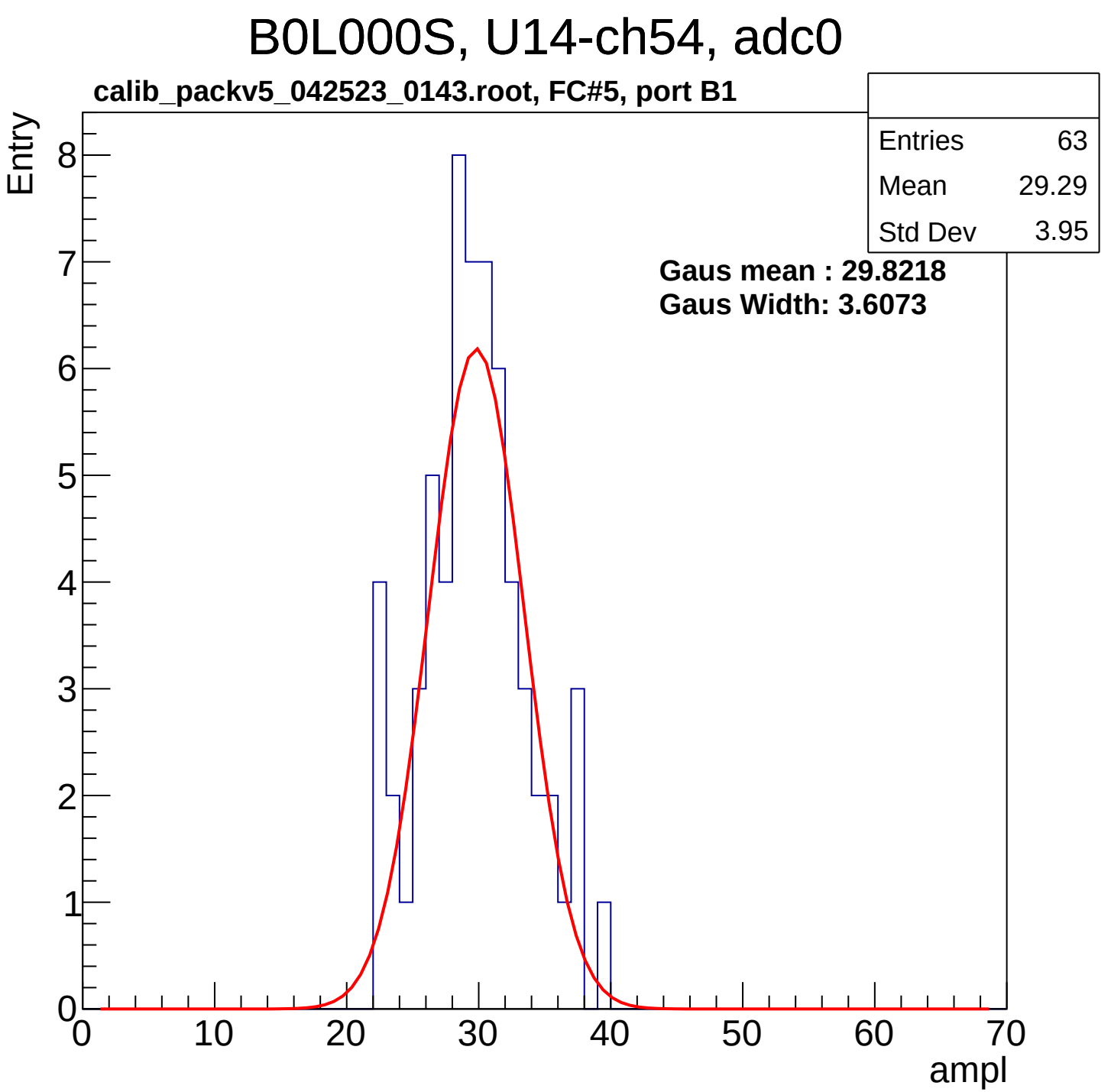
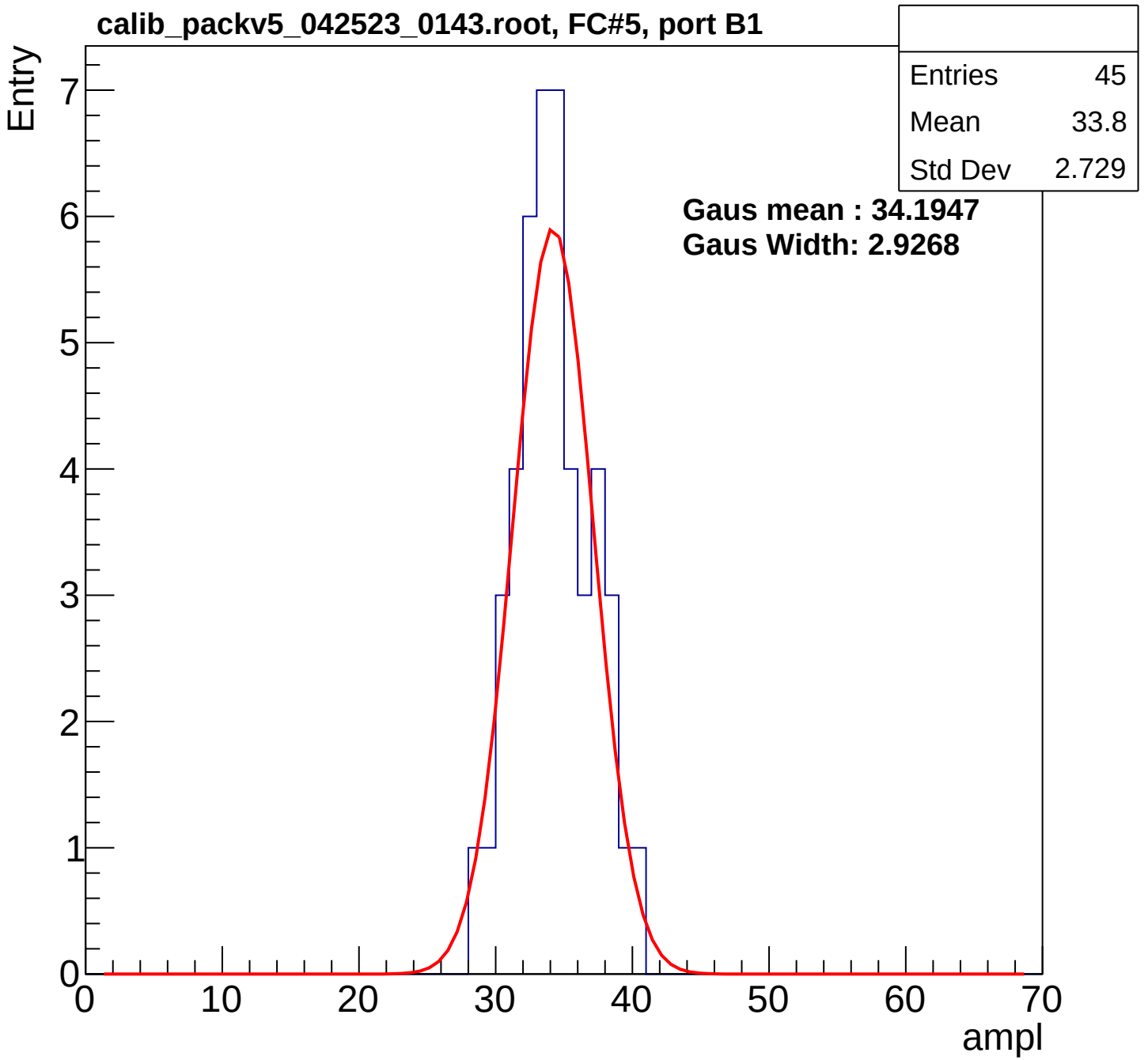
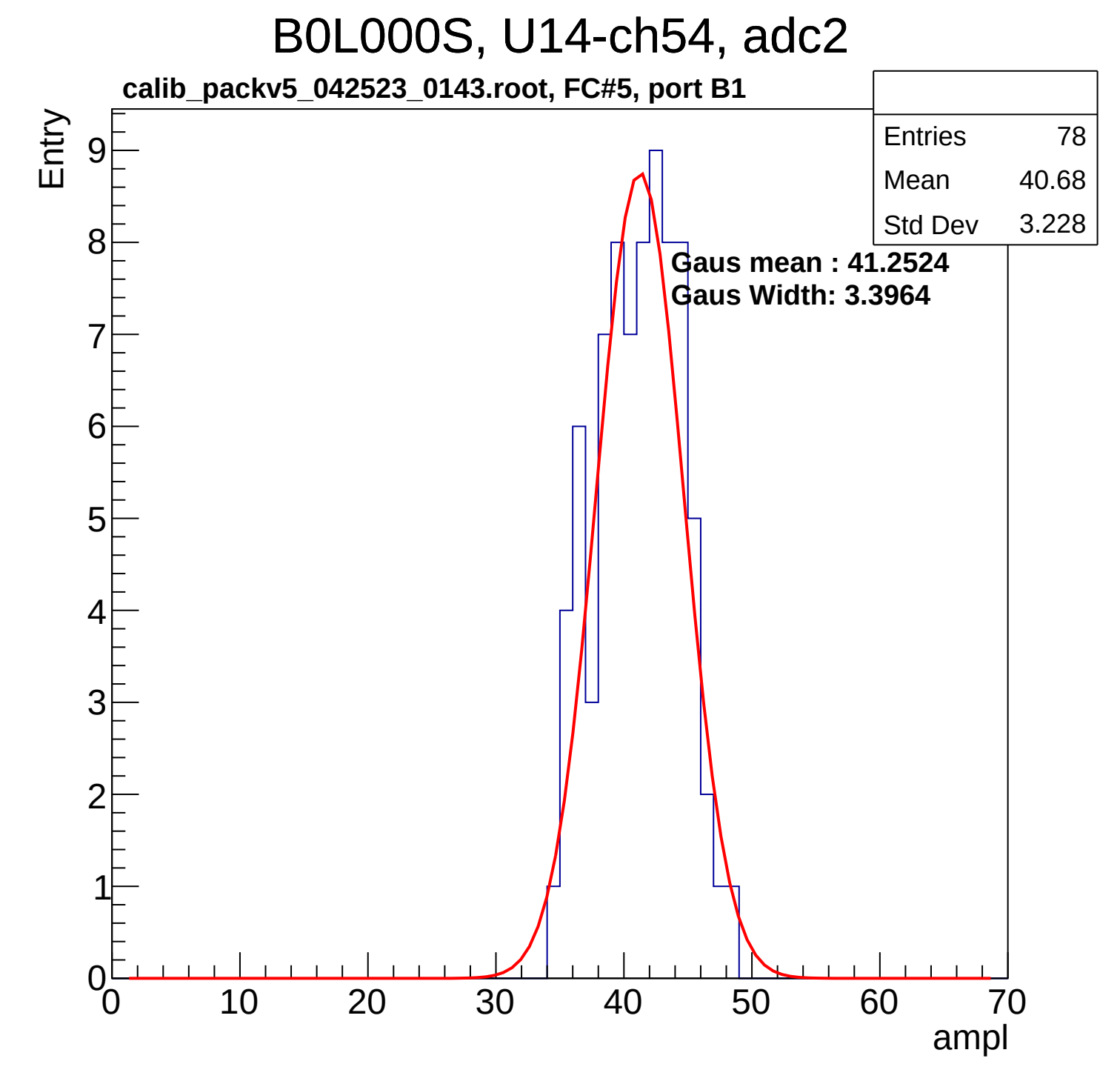
40
50
60
70
ampl


B0L000S, U14-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.29
Std Dev 3.95
Gaus mean : 29.8218
Gaus Width: 3.6073
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 33.8
Std Dev 2.729
Gaus mean : 34.1947
Gaus Width: 2.9268
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 40.68
Std Dev 3.228
Gaus mean : 41.2524
Gaus Width: 3.3964
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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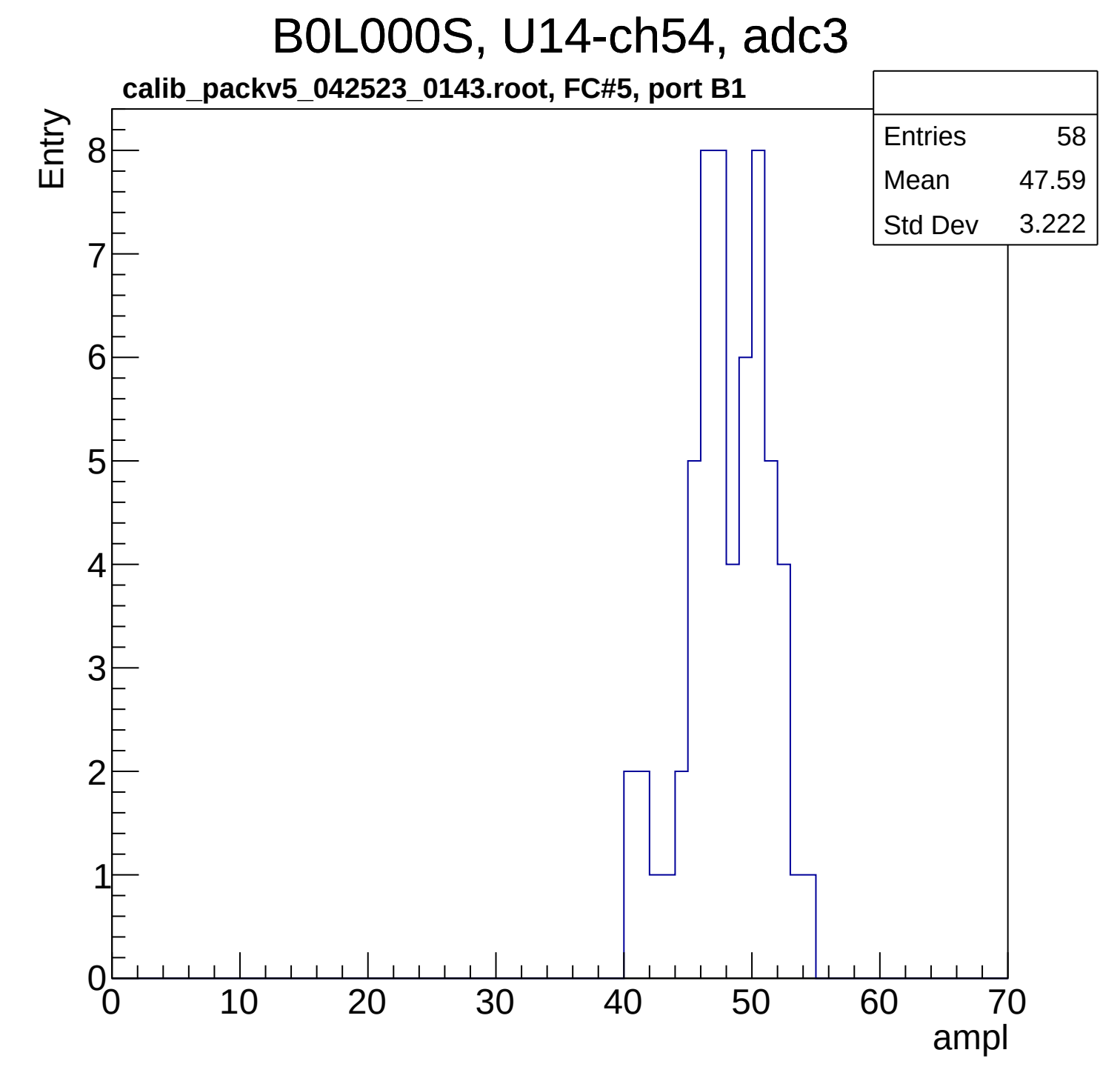
B0L000S, U14-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 47.59
Std Dev 3.222
Entry
ampl
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1
2
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4
5
6
7
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0
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60
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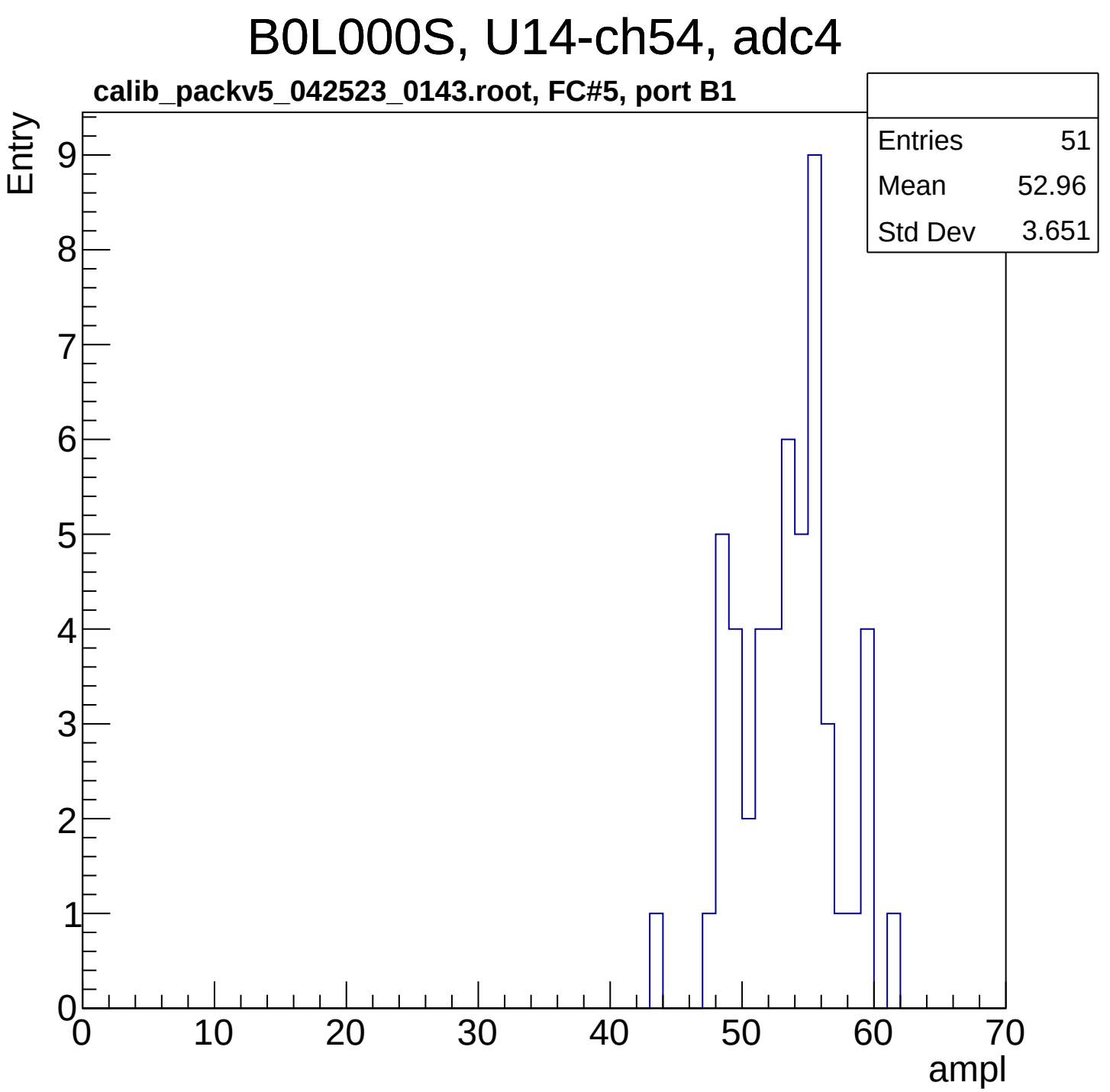
B0L000S, U14-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 52.96
Std Dev 3.651
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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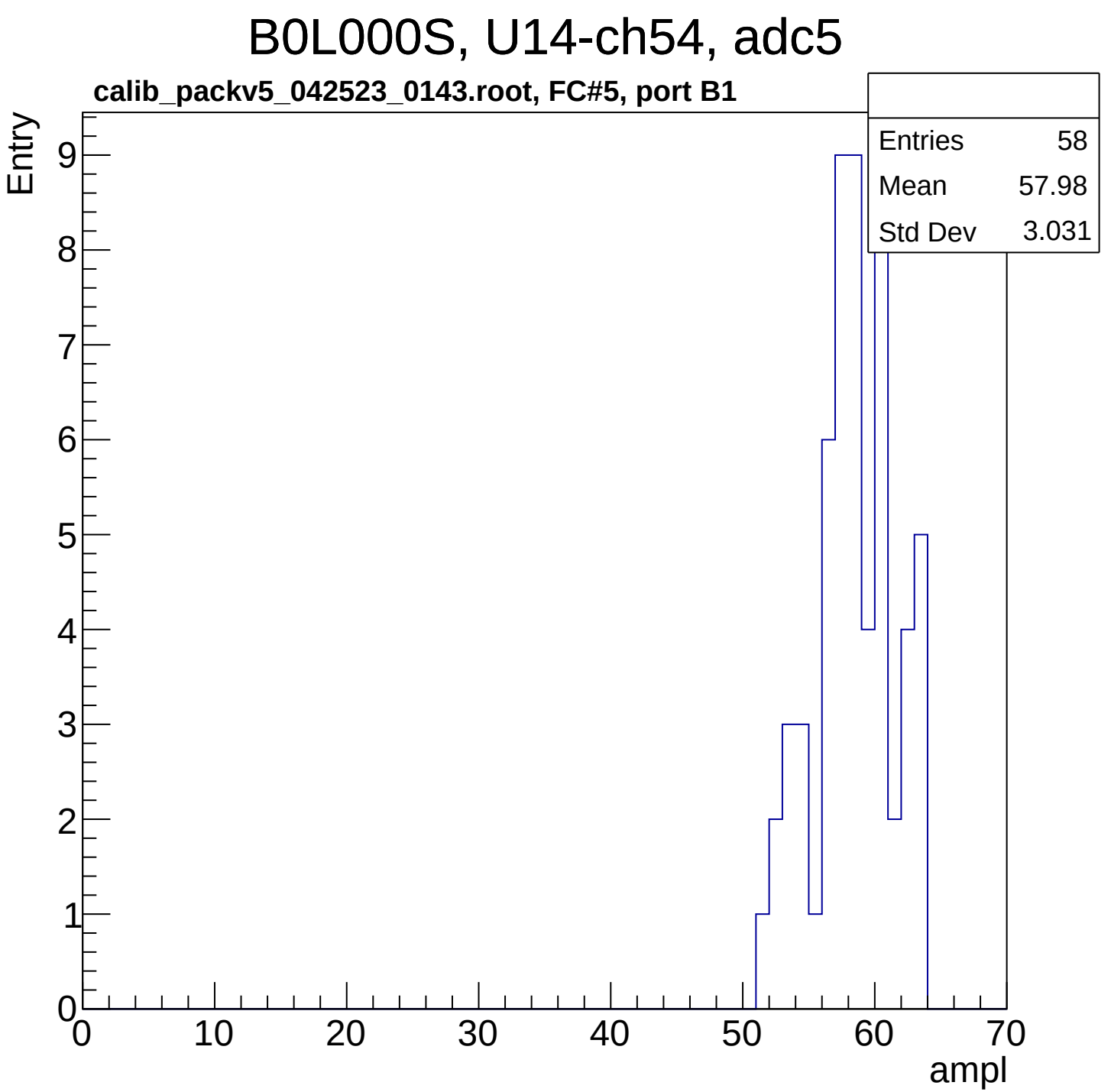
B0L000S, U14-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.98
Std Dev 3.031
Entry
ampl
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2
3
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0
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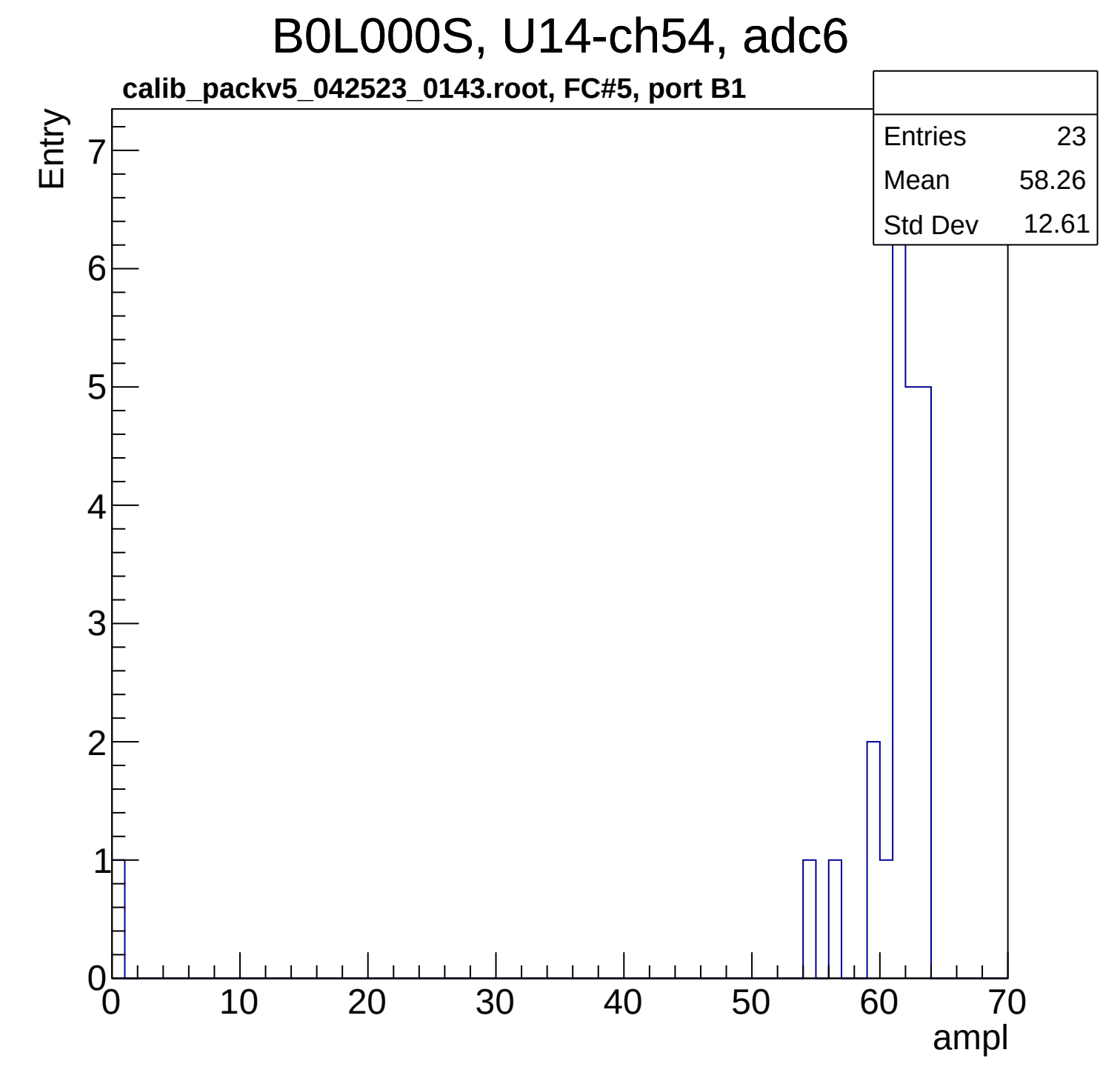
B0L000S, U14-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.26
Std Dev 12.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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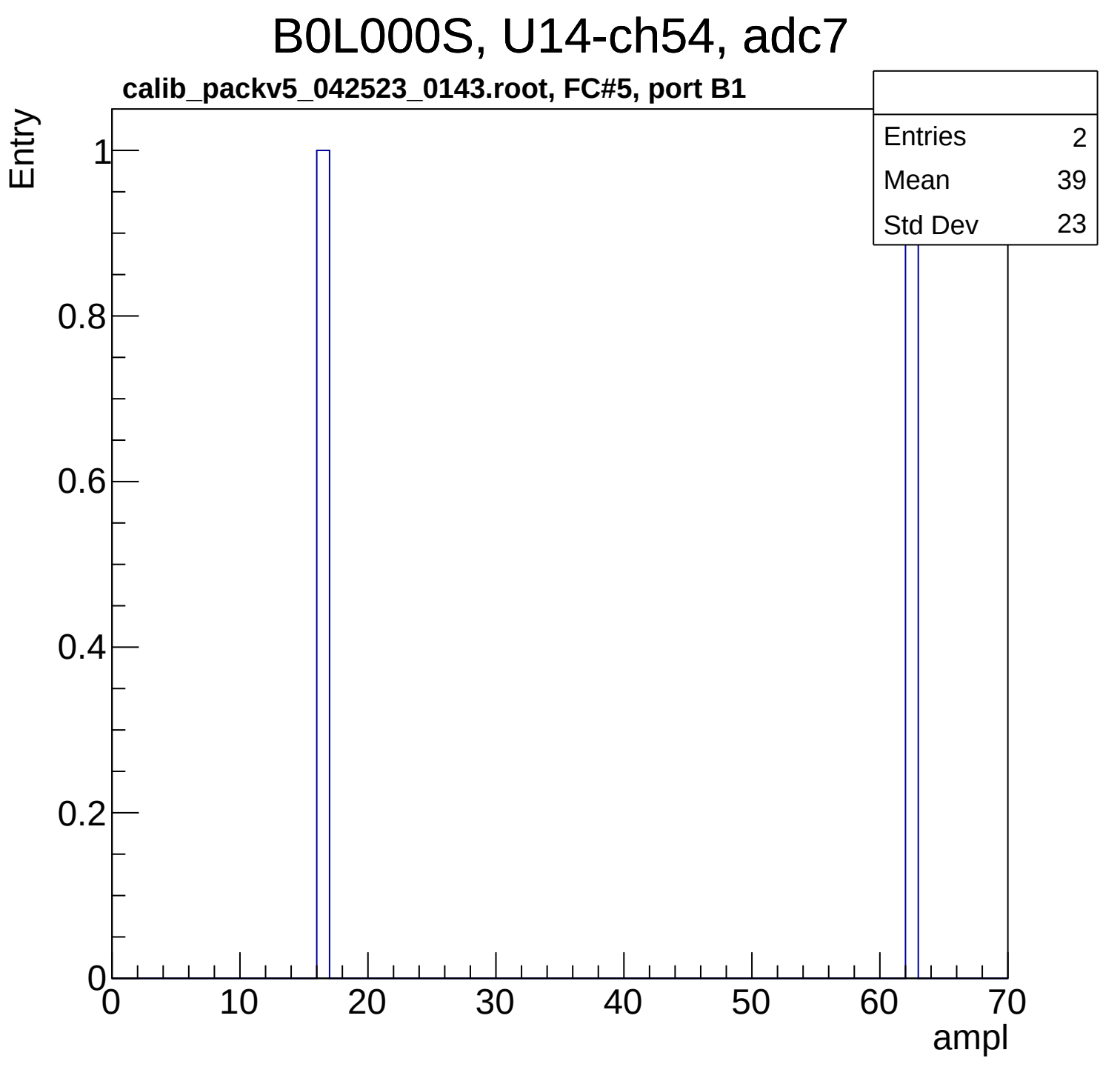
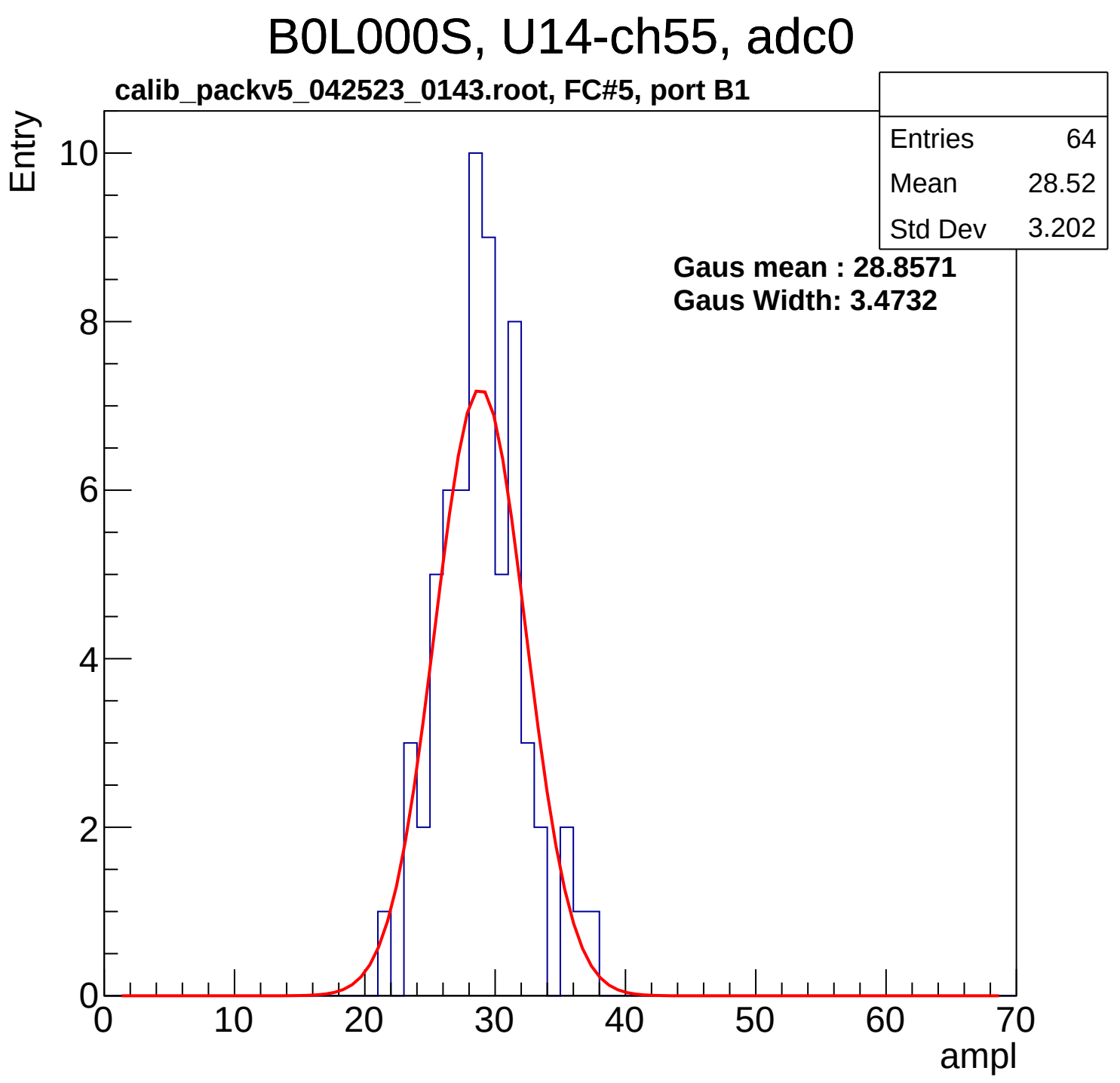
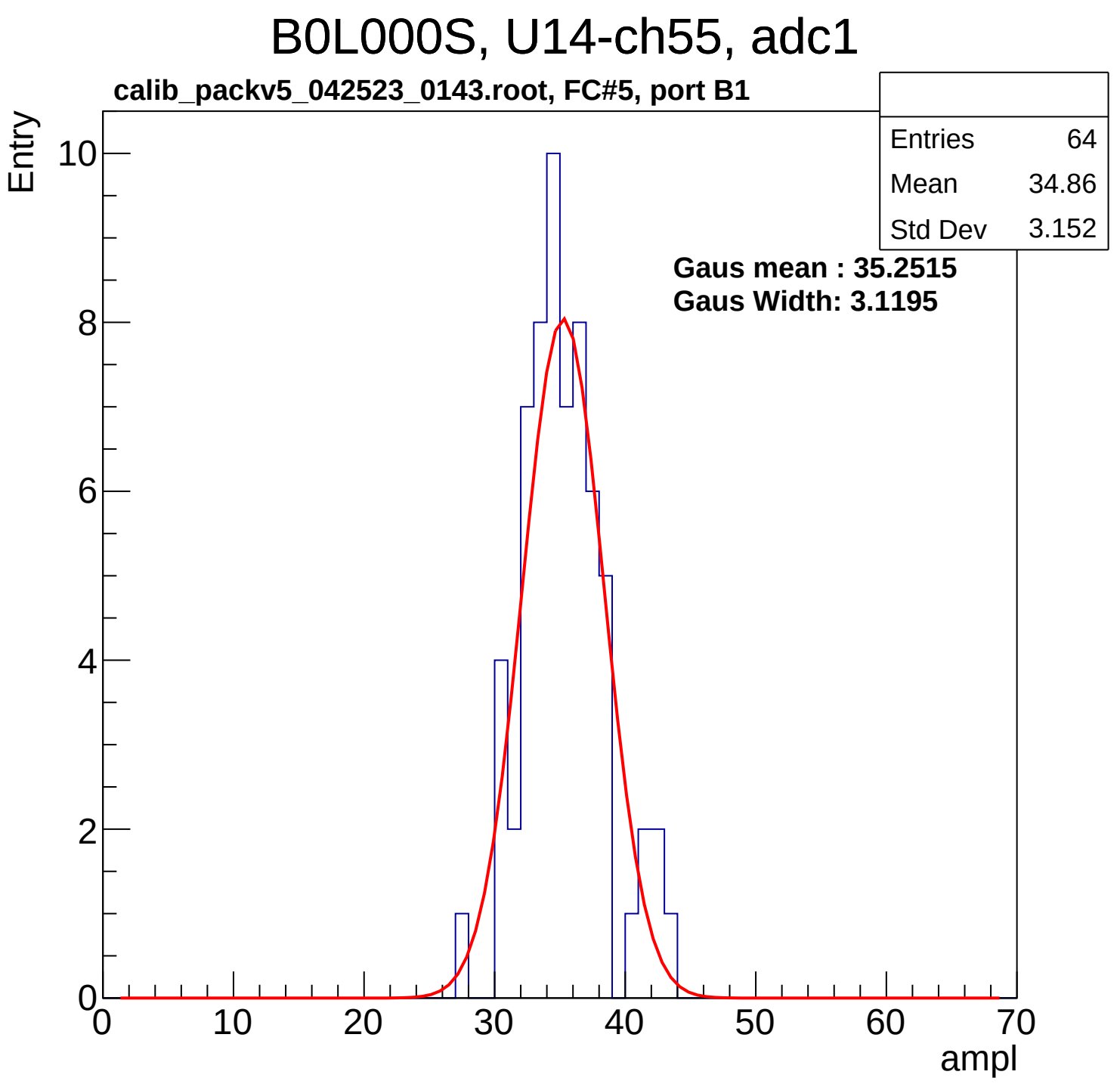

B0L000S, U14-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 39
Std Dev 23
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.52
Std Dev 3.202
Gaus mean : 28.8571
Gaus Width: 3.4732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 34.86
Std Dev 3.152
Gaus mean : 35.2515
Gaus Width: 3.1195
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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60
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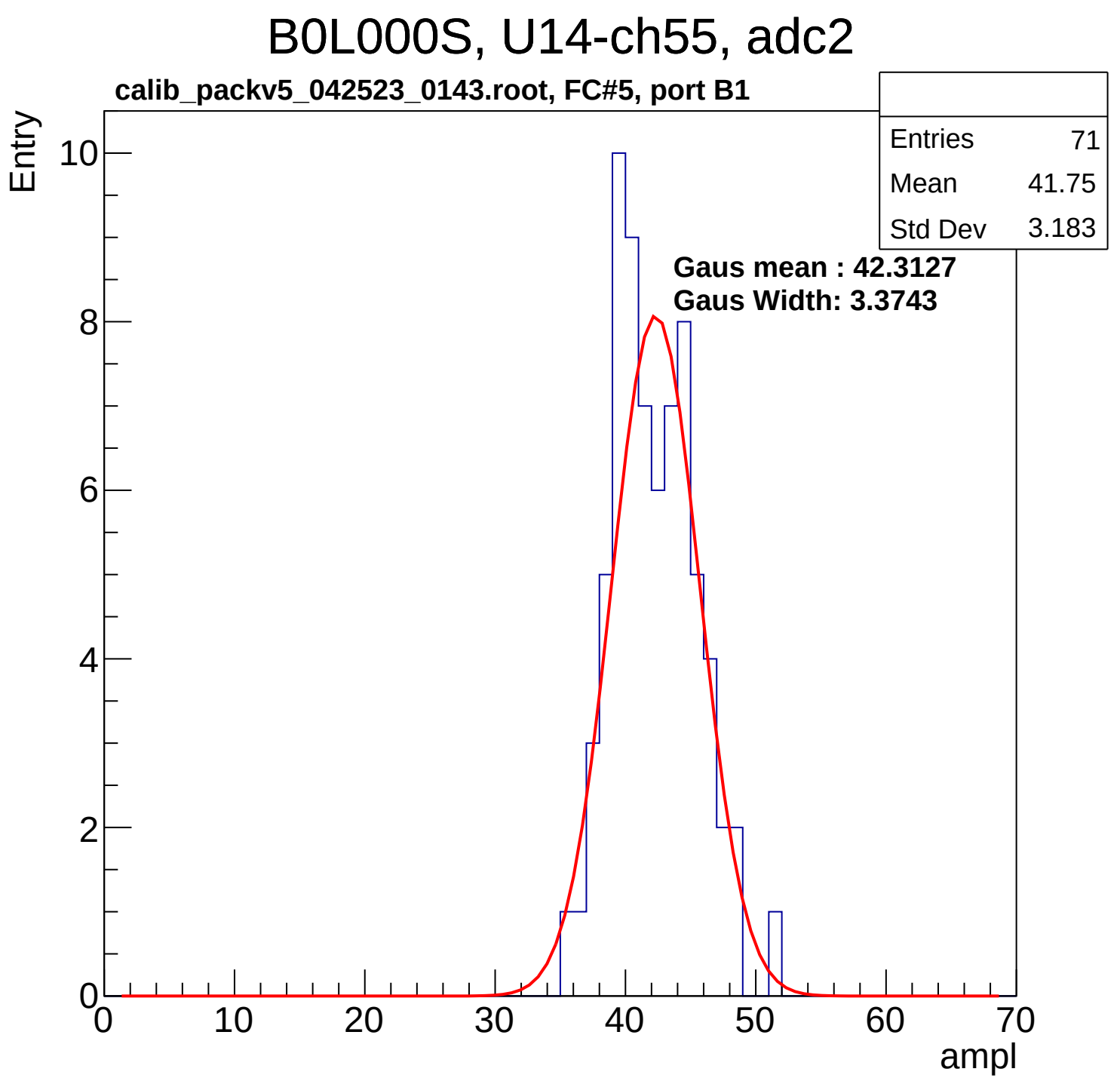
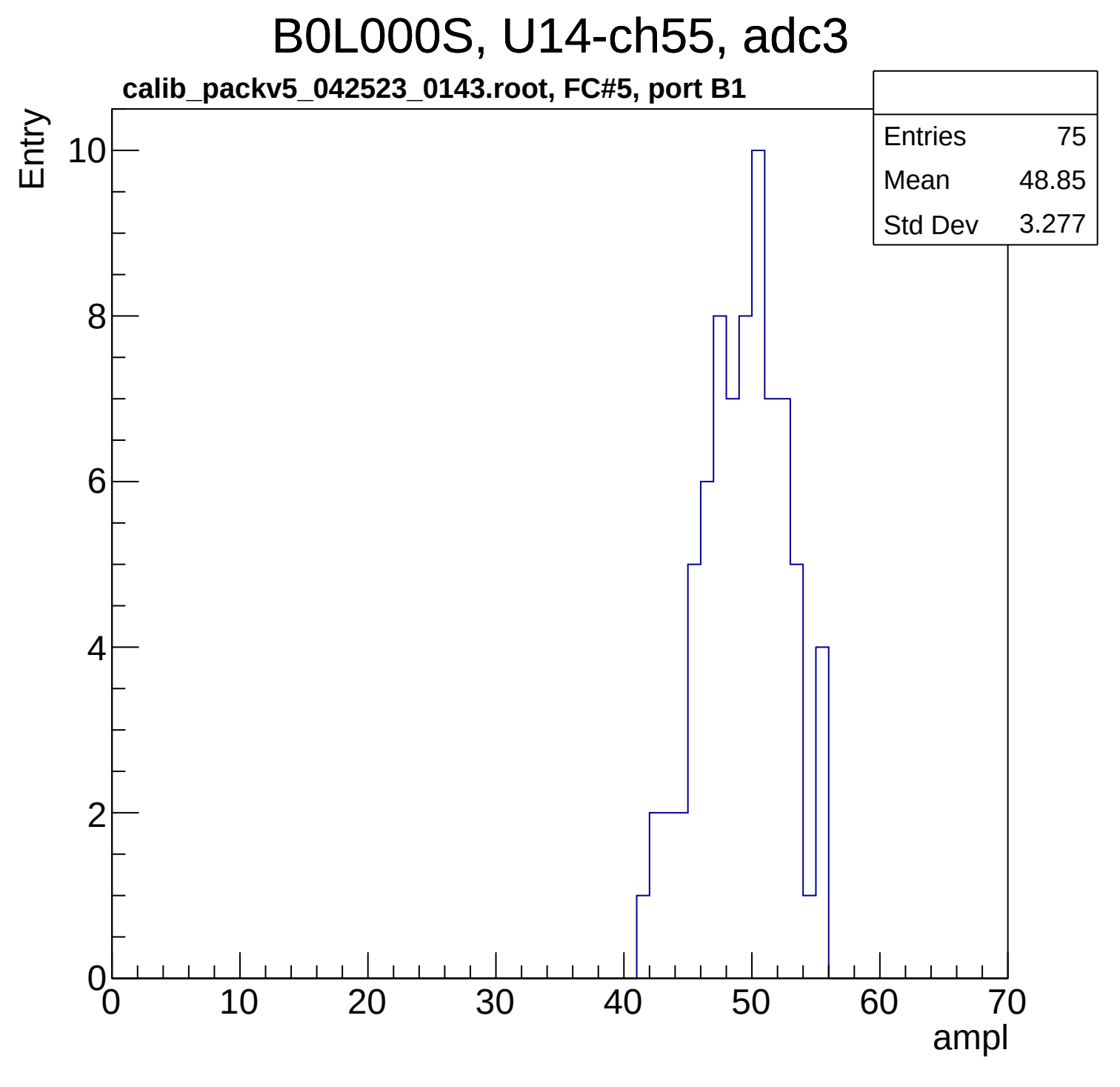
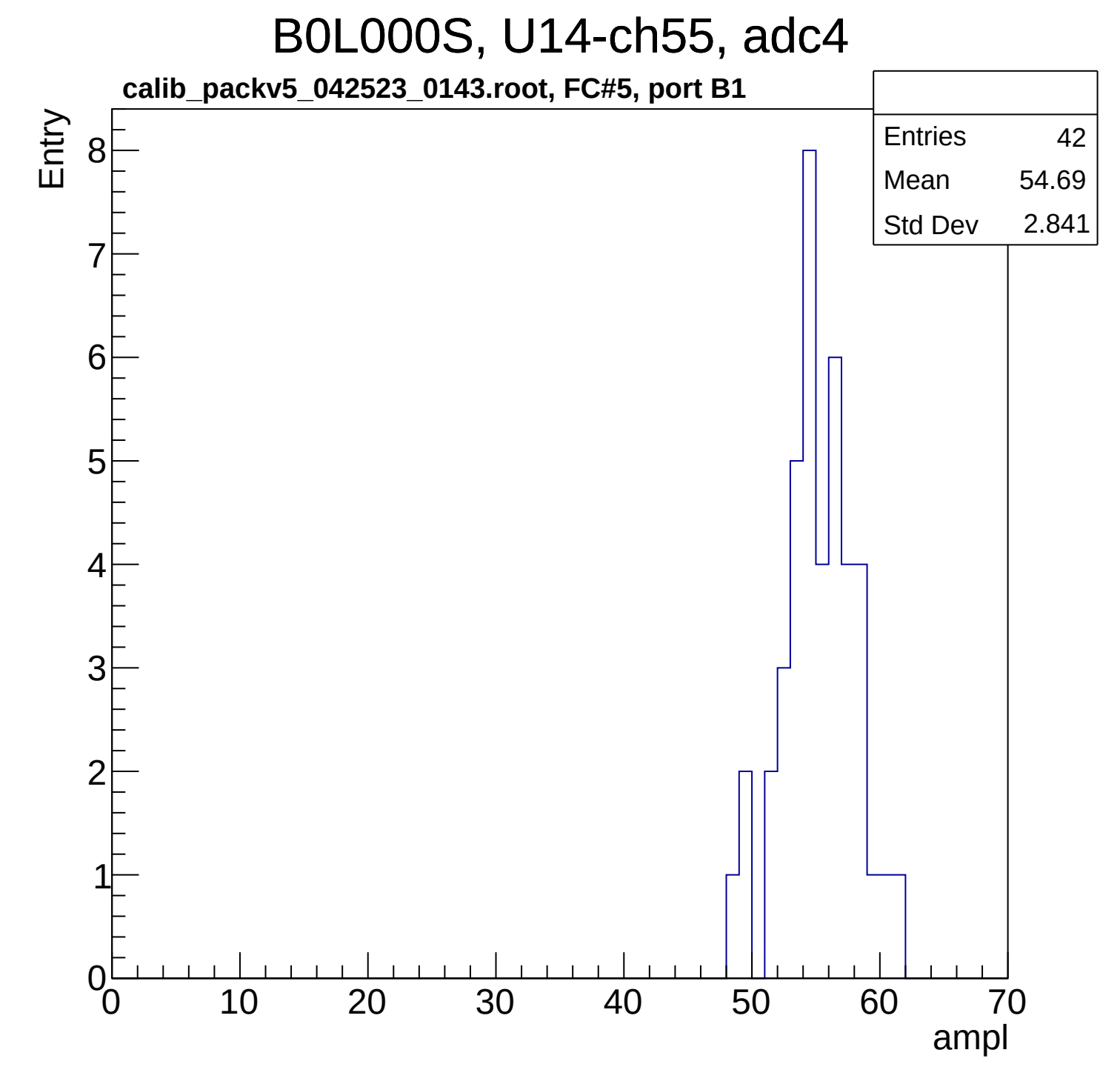
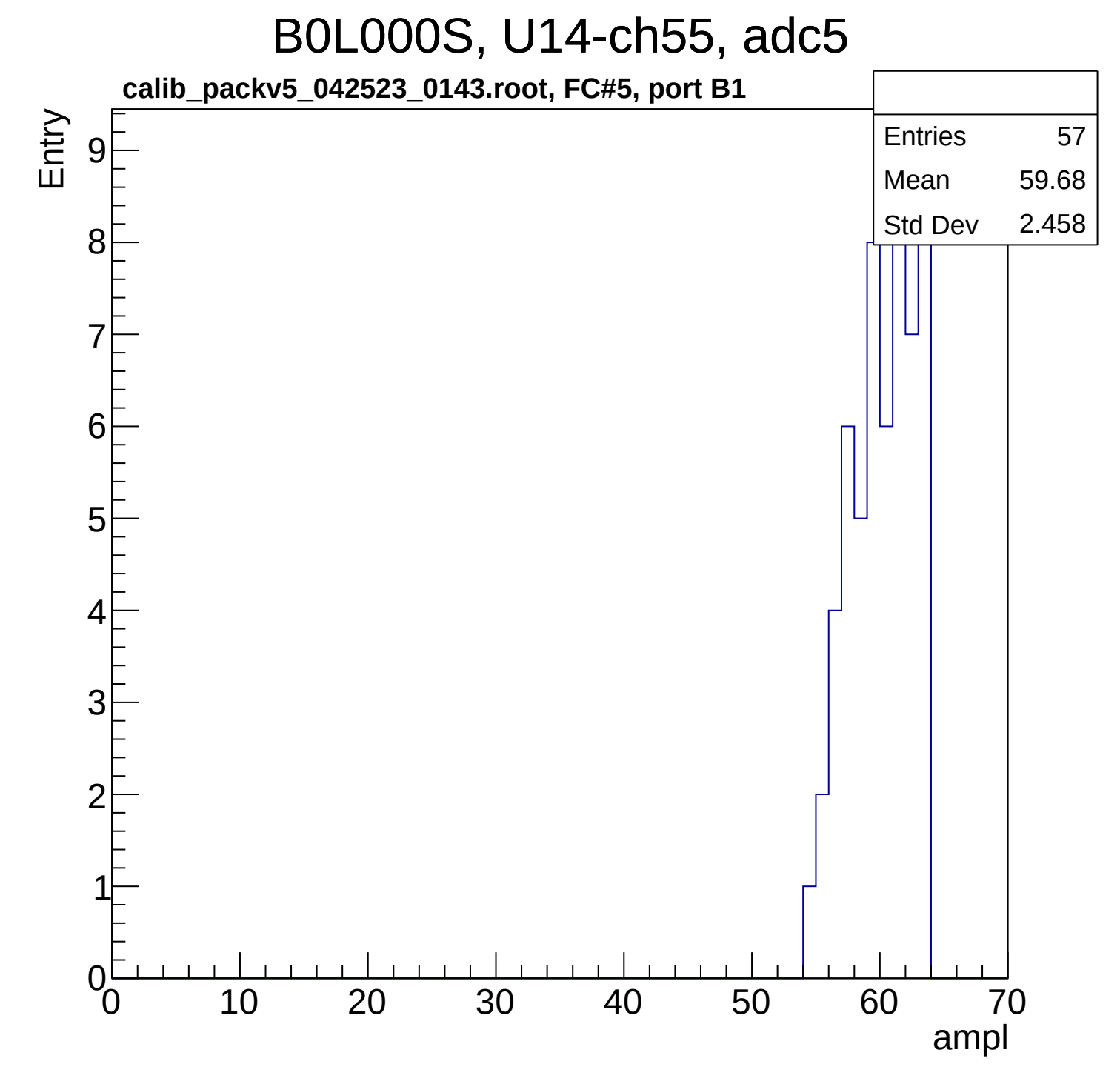

B0L000S, U14-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 41.75
Std Dev 3.183
Gaus mean : 42.3127
Gaus Width: 3.3743
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 48.85
Std Dev 3.277
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 54.69
Std Dev 2.841
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 59.68
Std Dev 2.458
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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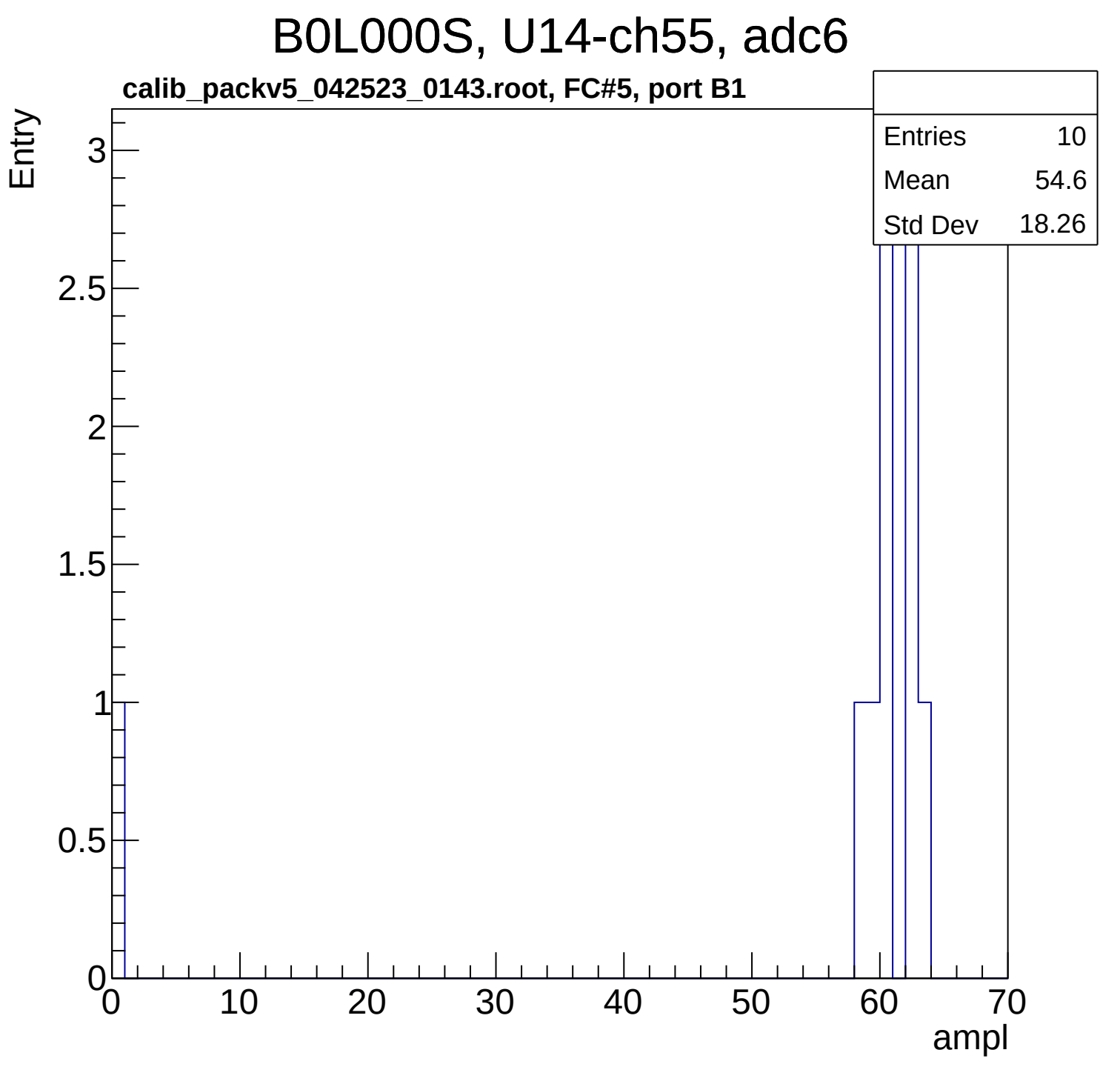
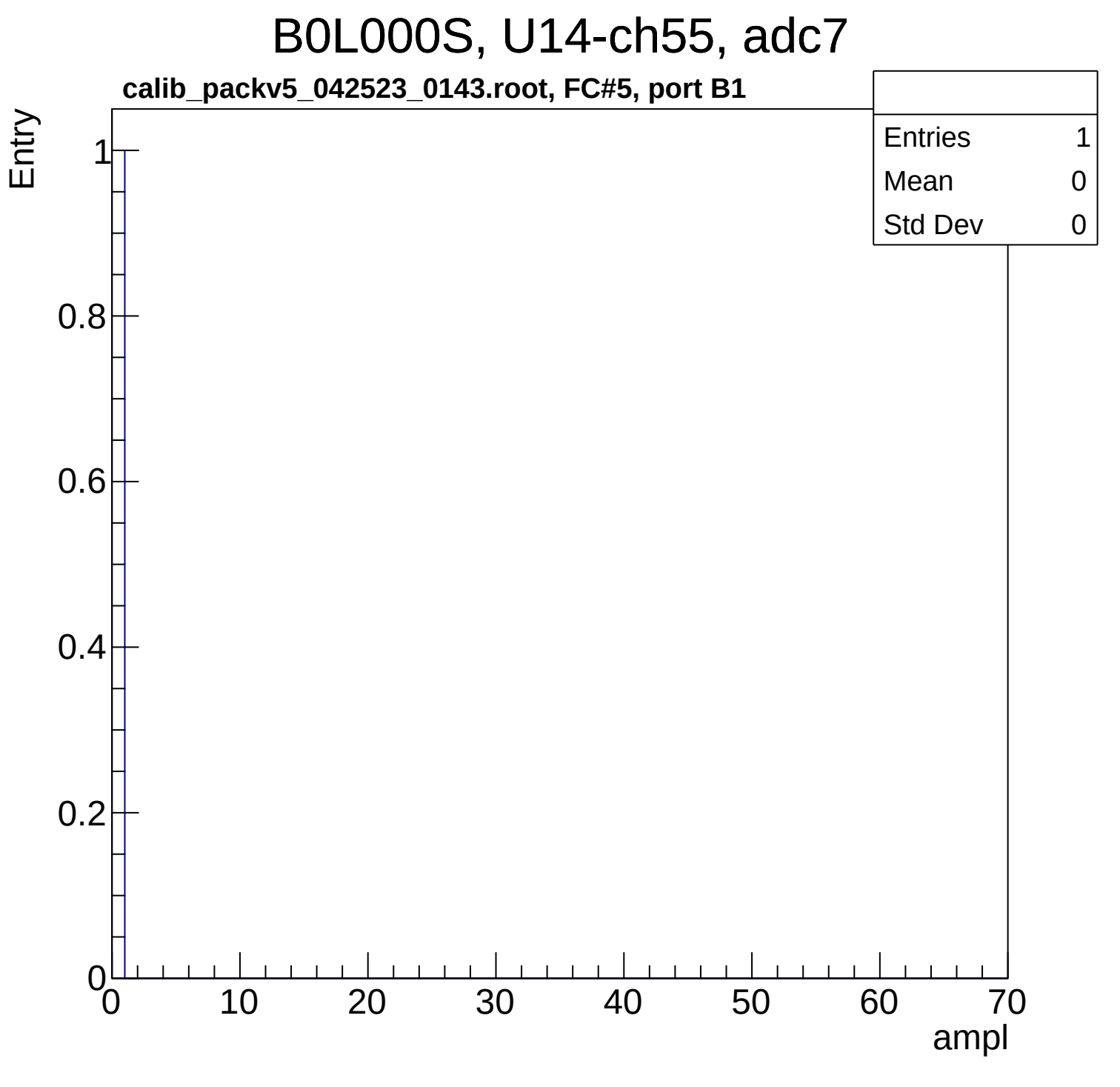
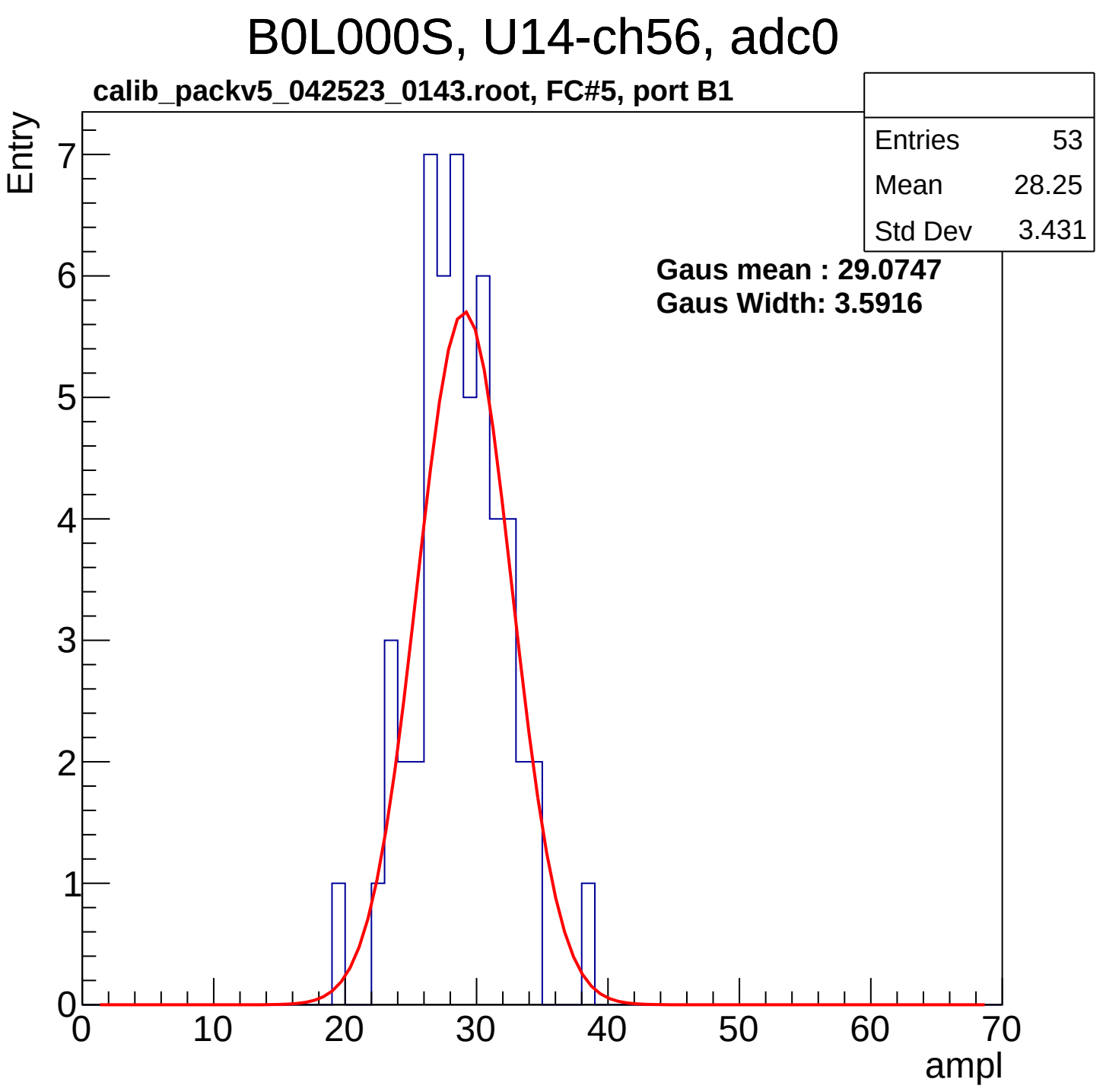
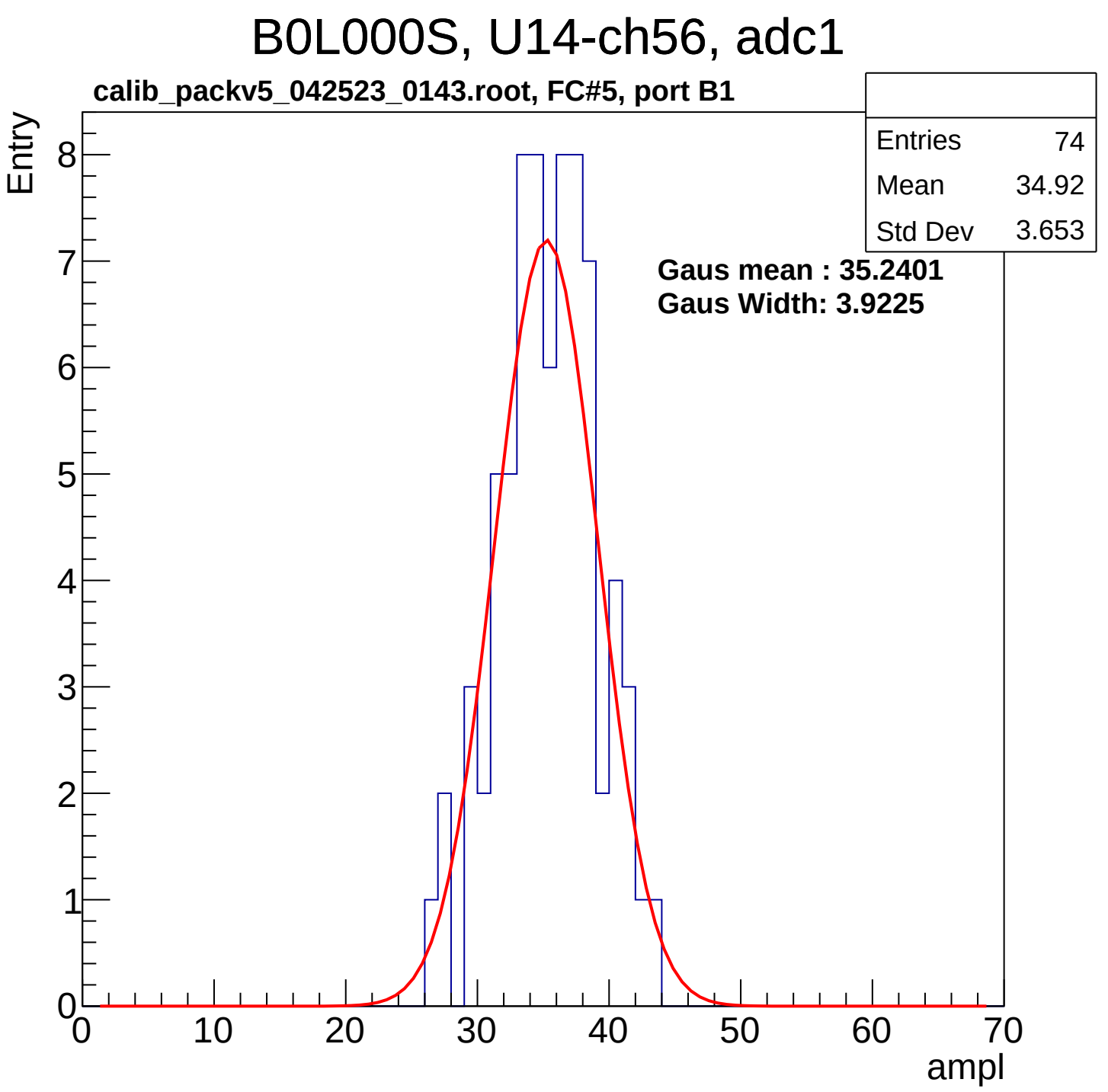

B0L000S, U14-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 54.6
Std Dev 18.26
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 28.25
Std Dev 3.431
Gaus mean : 29.0747
Gaus Width: 3.5916
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 34.92
Std Dev 3.653
Gaus mean : 35.2401
Gaus Width: 3.9225
Entry
ampl
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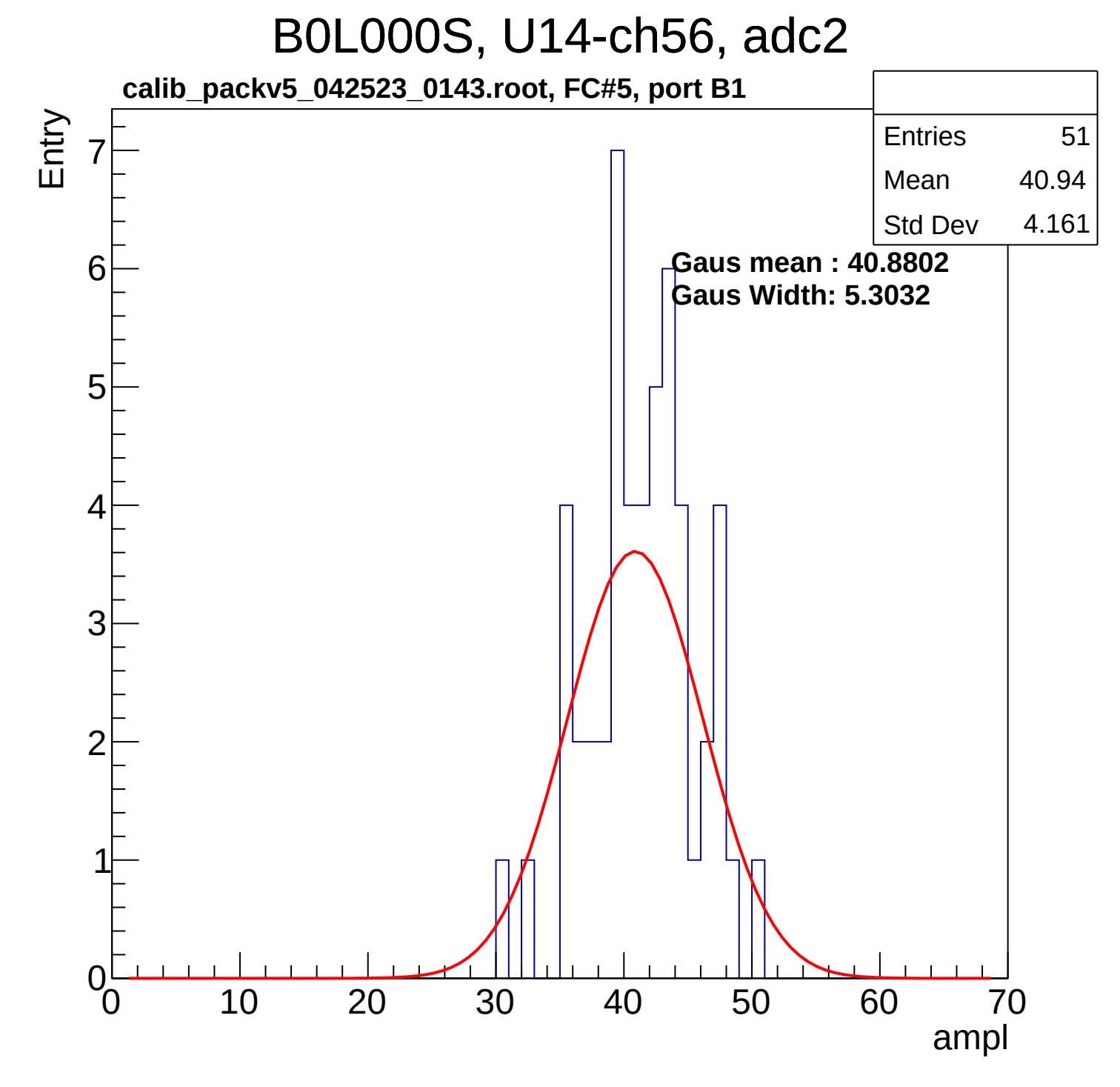
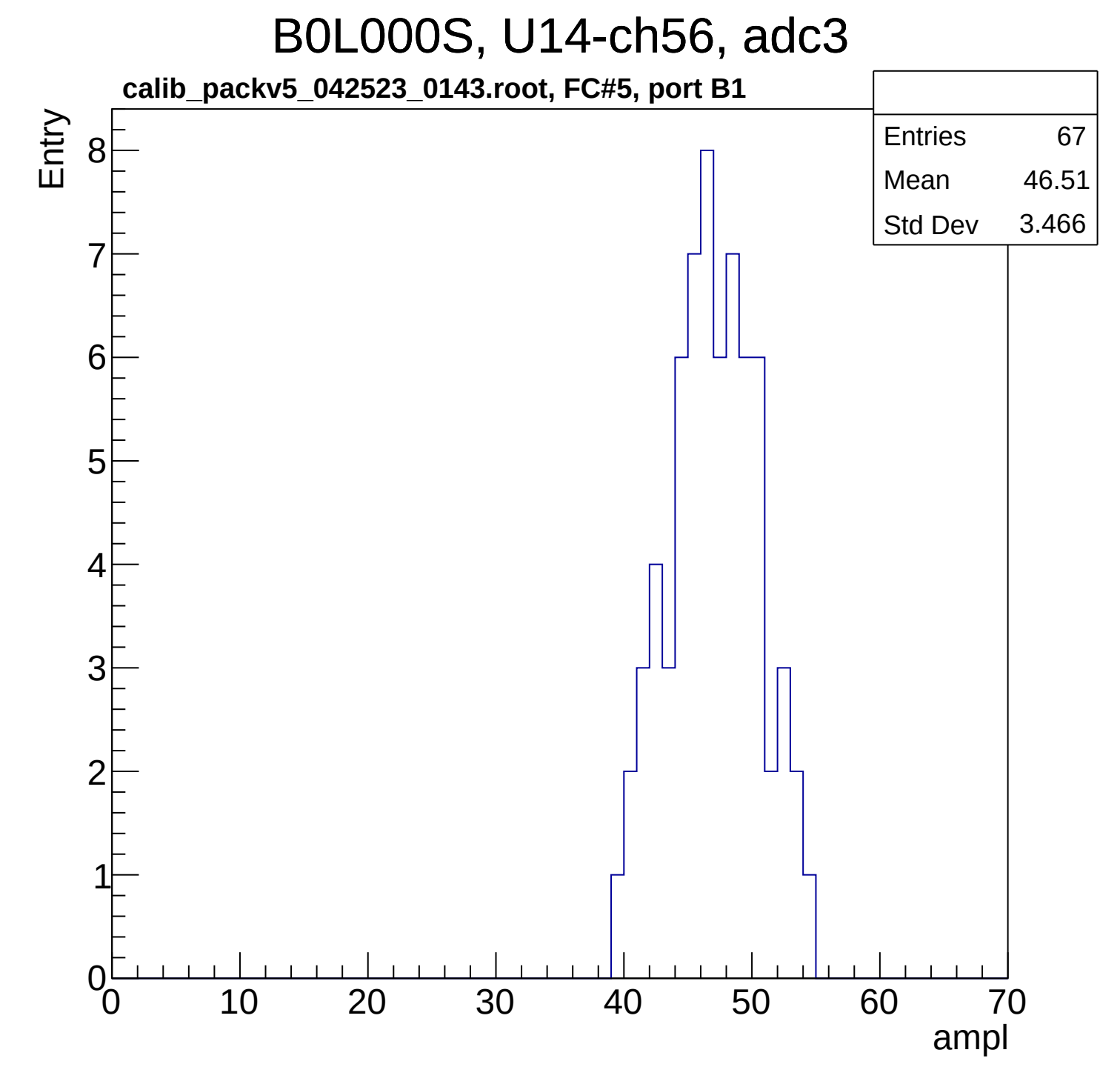
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 40.94
Std Dev 4.161
Gaus mean : 40.8802
Gaus Width: 5.3032
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 46.51
Std Dev 3.466
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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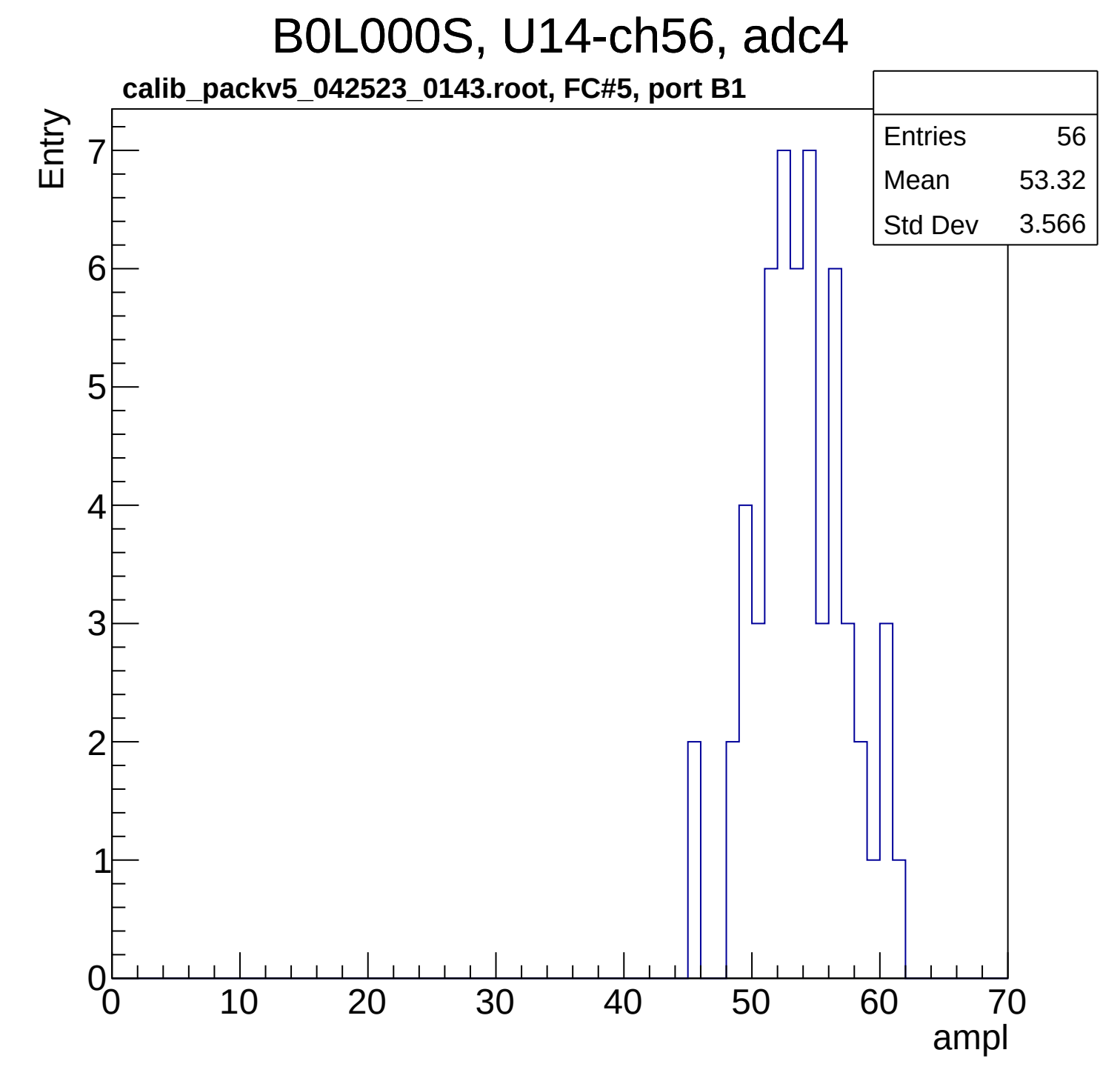
40
50
60
70


B0L000S, U14-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.32
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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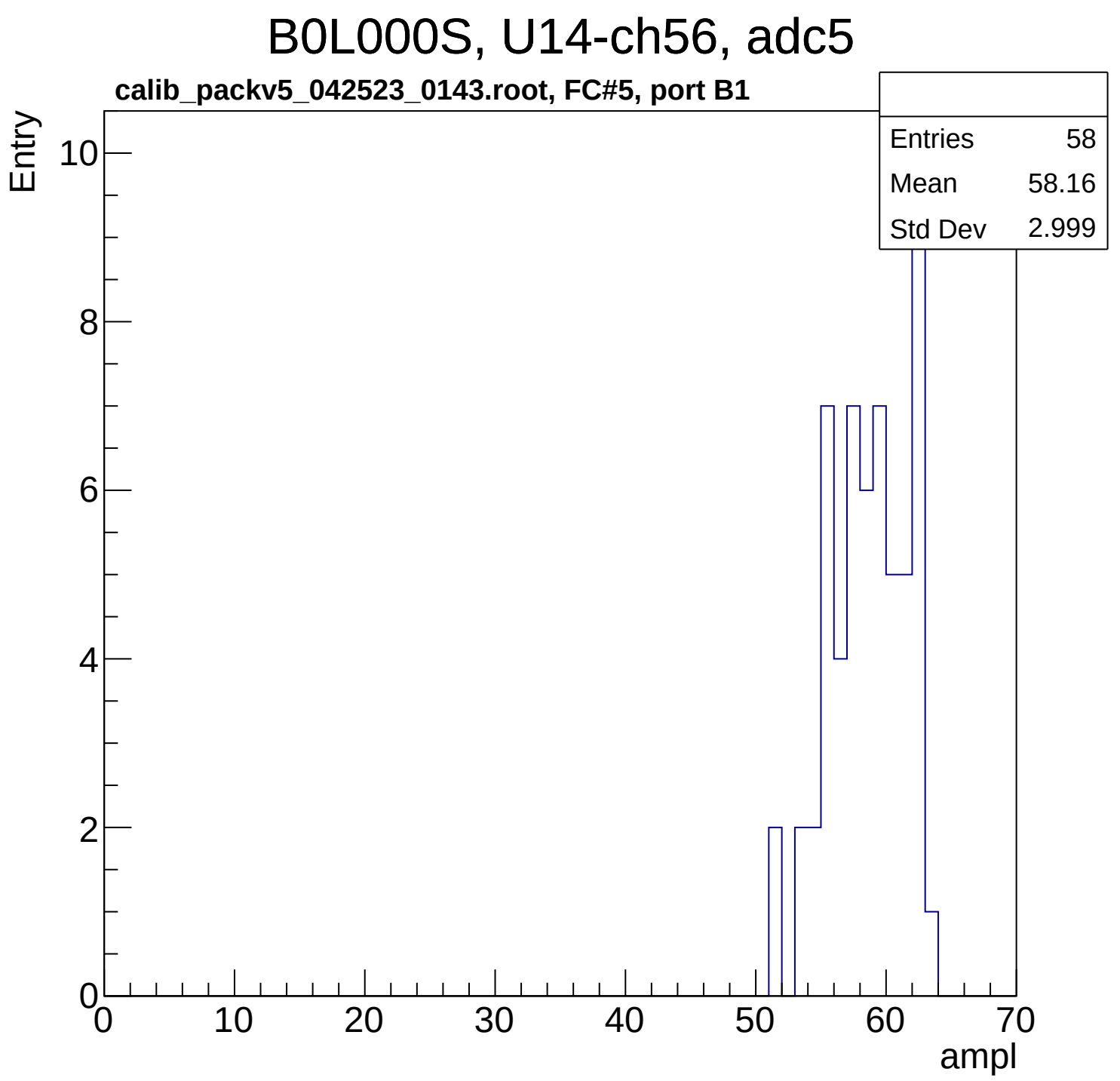
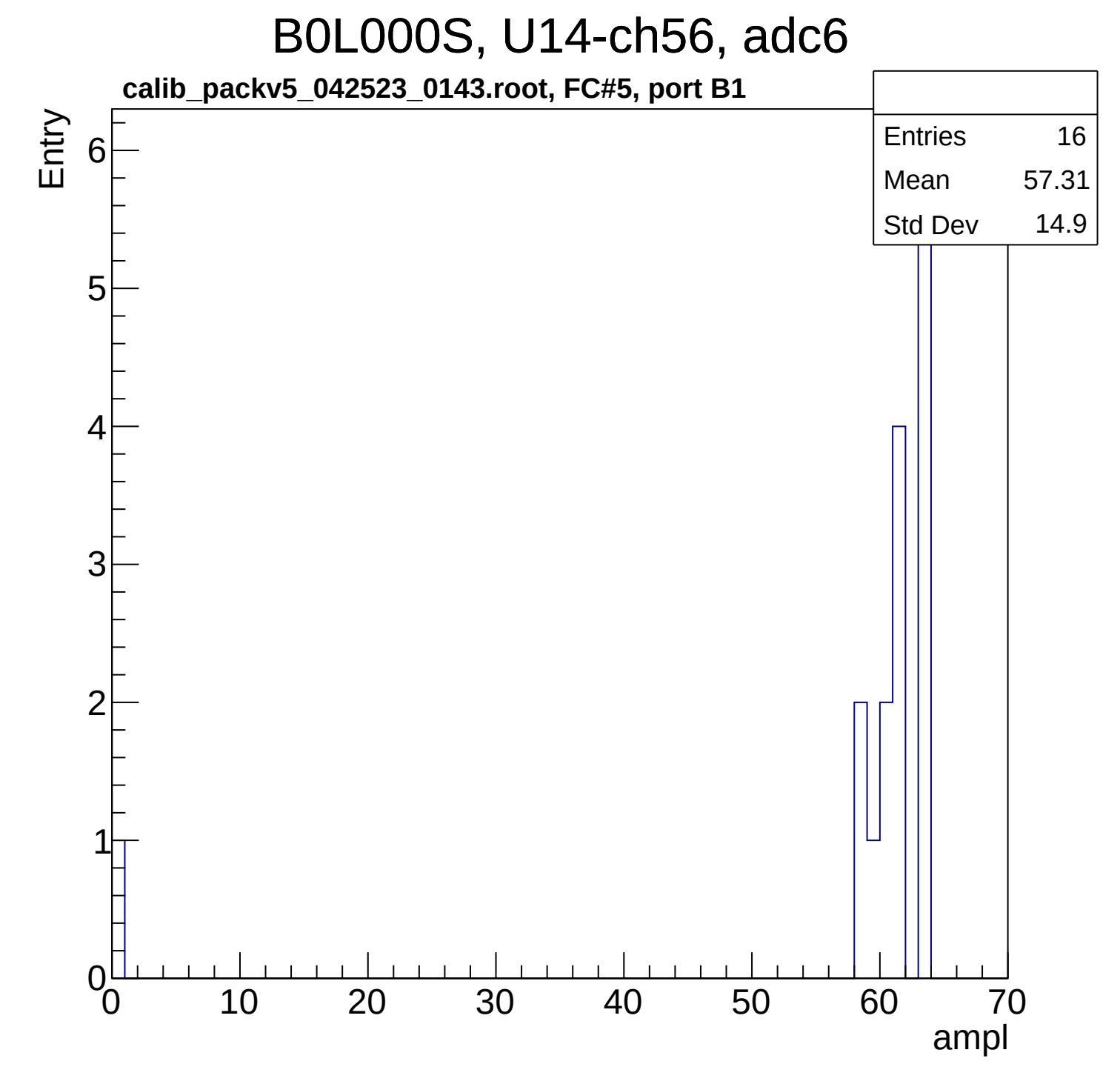
40
50
60
70


B0L000S, U14-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.16
Std Dev 2.999
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.31
Std Dev 14.9
Entry
0
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2
3
4
5
6
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10
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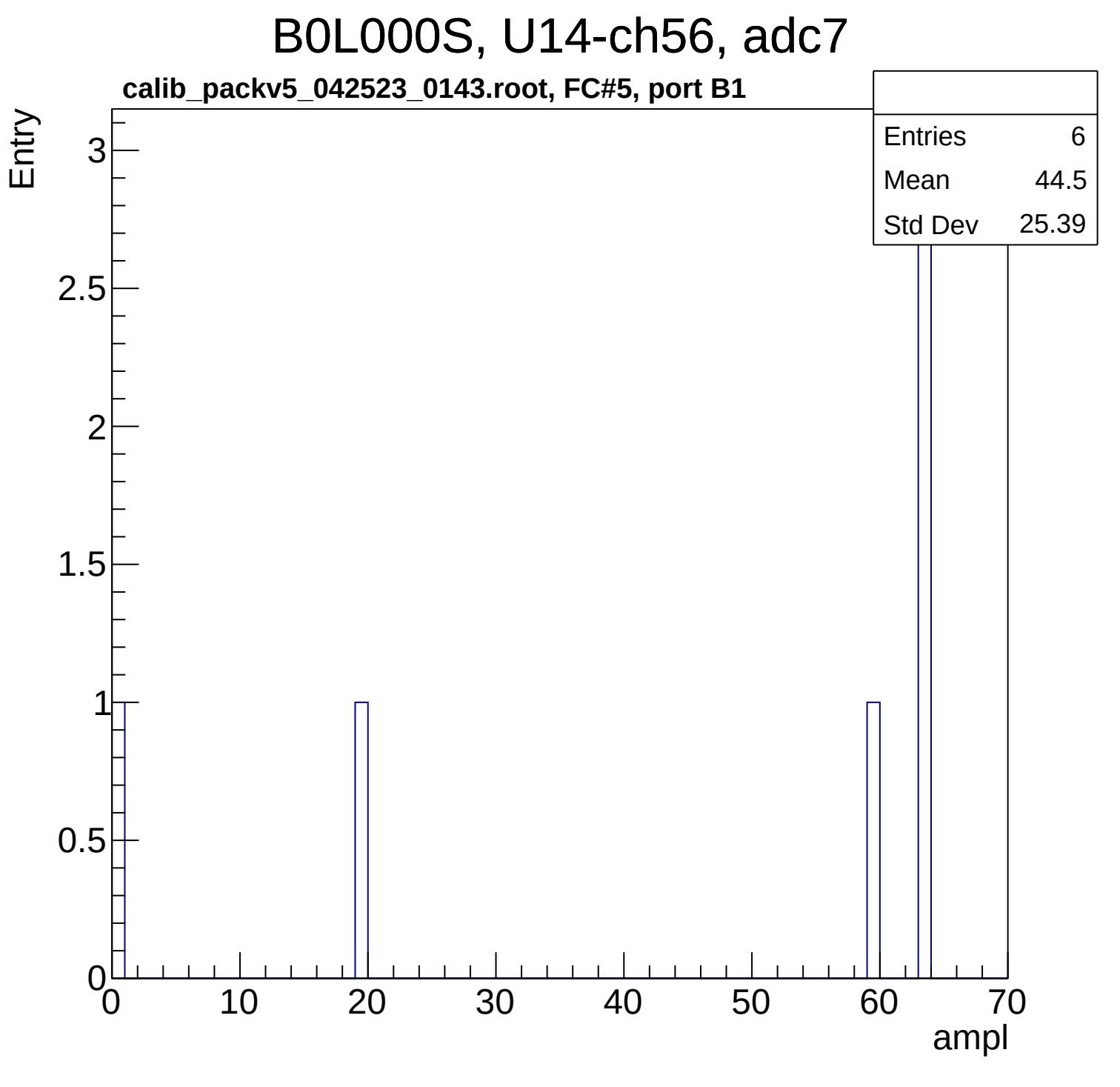
50
60
70
ampl


B0L000S, U14-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 44.5
Std Dev 25.39
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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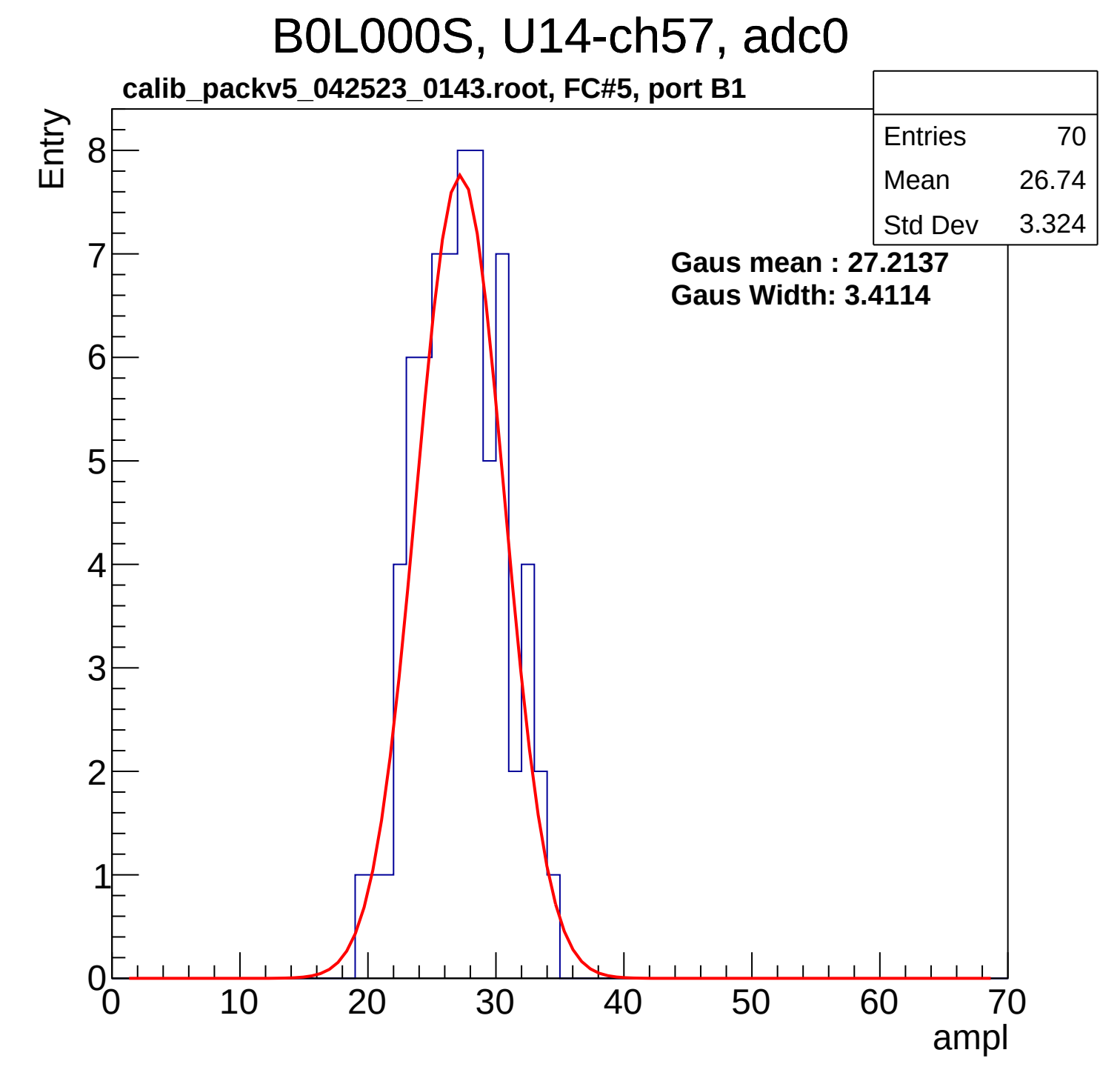
B0L000S, U14-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 26.74
Std Dev 3.324
Gaus mean : 27.2137
Gaus Width: 3.4114
Entry
ampl
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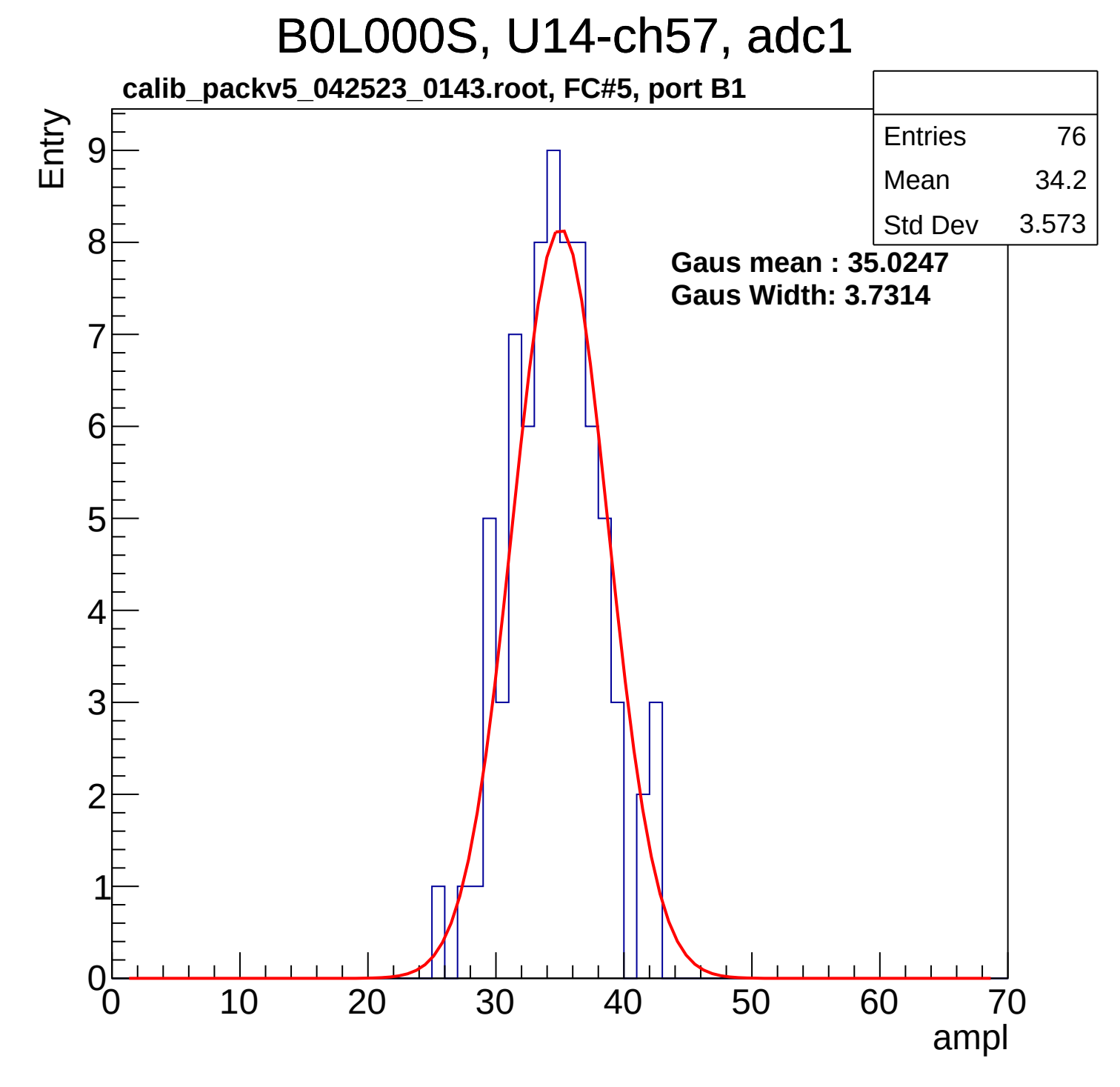
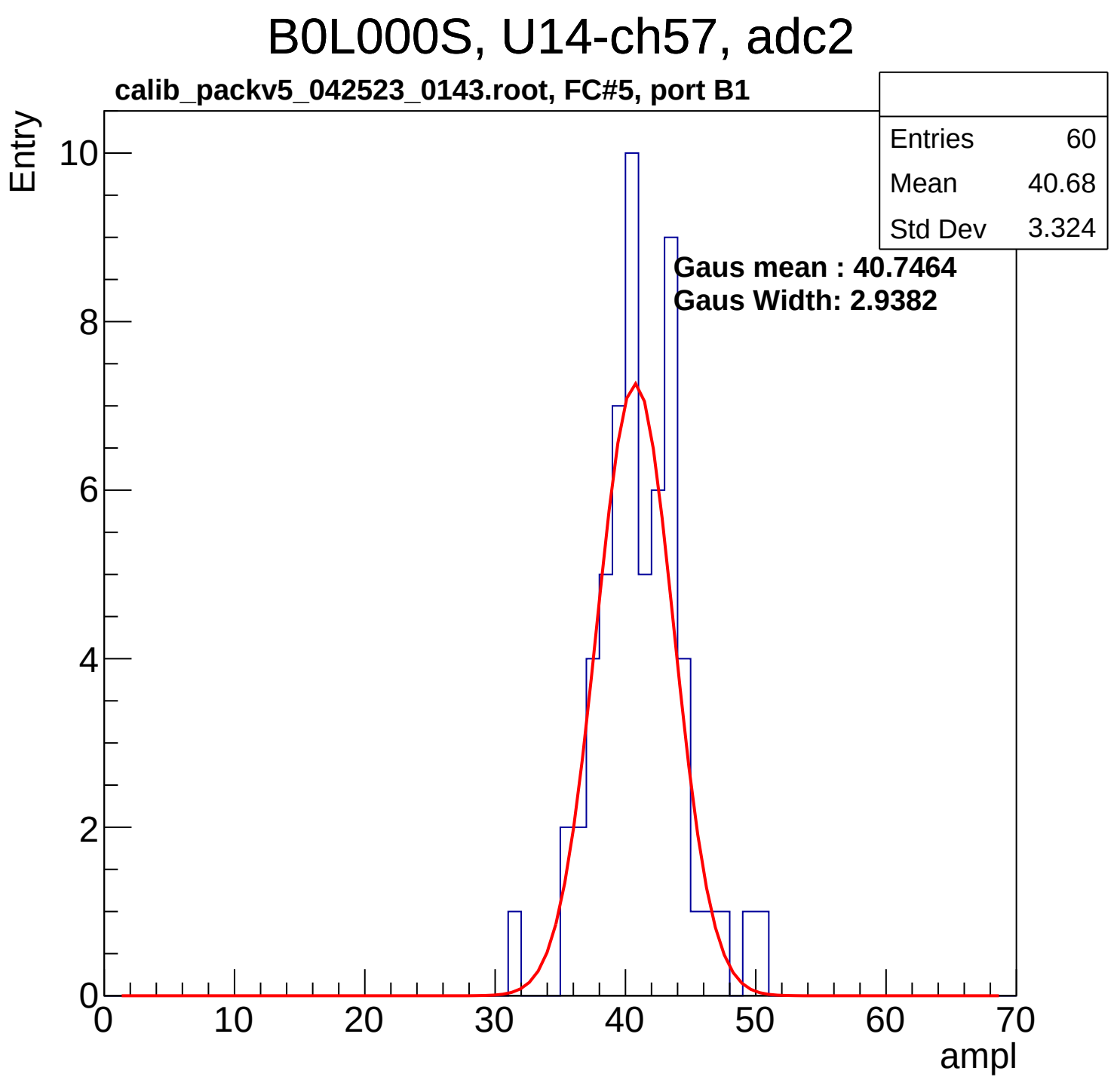
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 34.2
Std Dev 3.573
Gaus mean : 35.0247
Gaus Width: 3.7314
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 40.68
Std Dev 3.324
Gaus mean : 40.7464
Gaus Width: 2.9382
Entry
ampl
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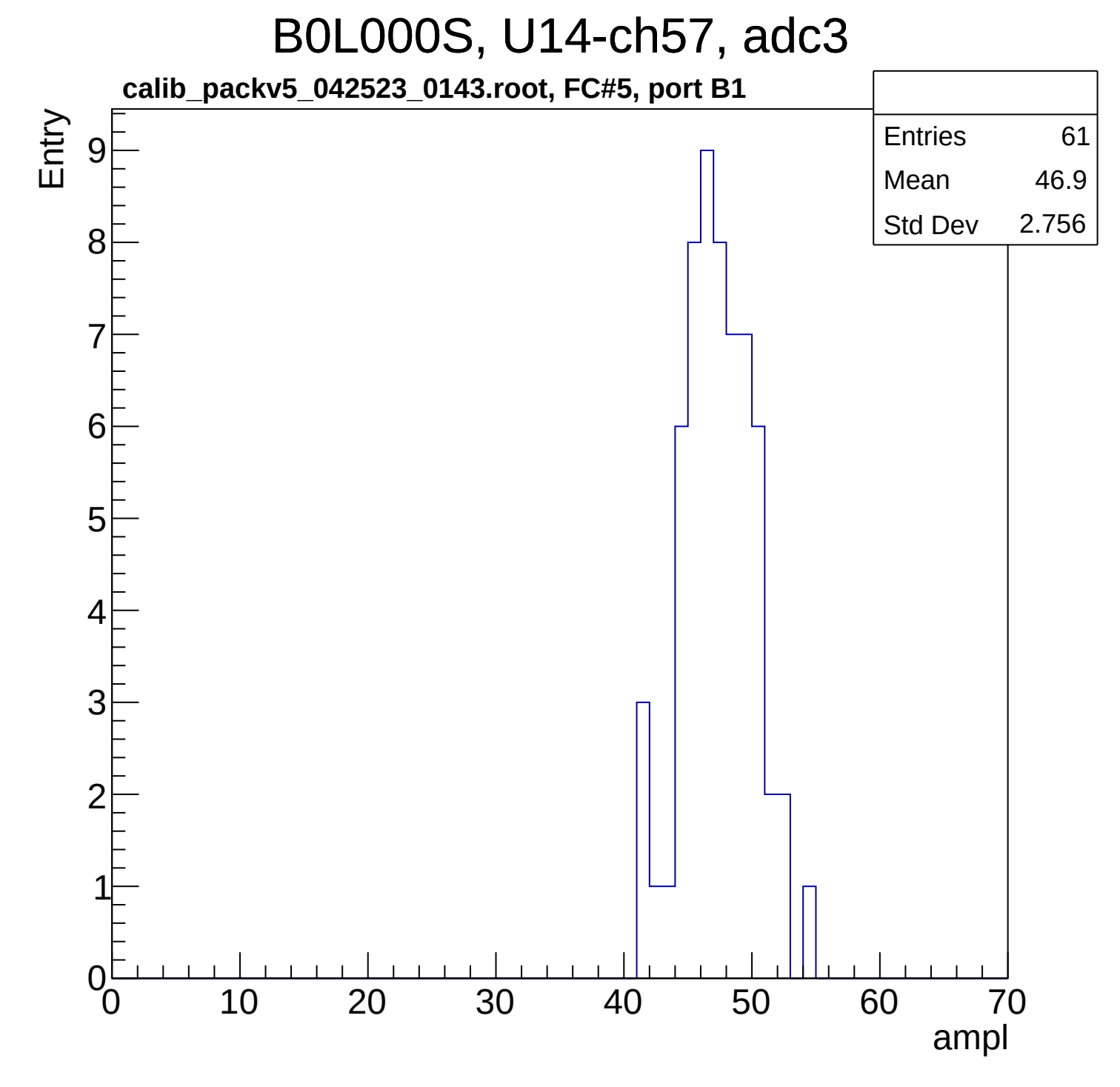
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 46.9
Std Dev 2.756
Entry
ampl
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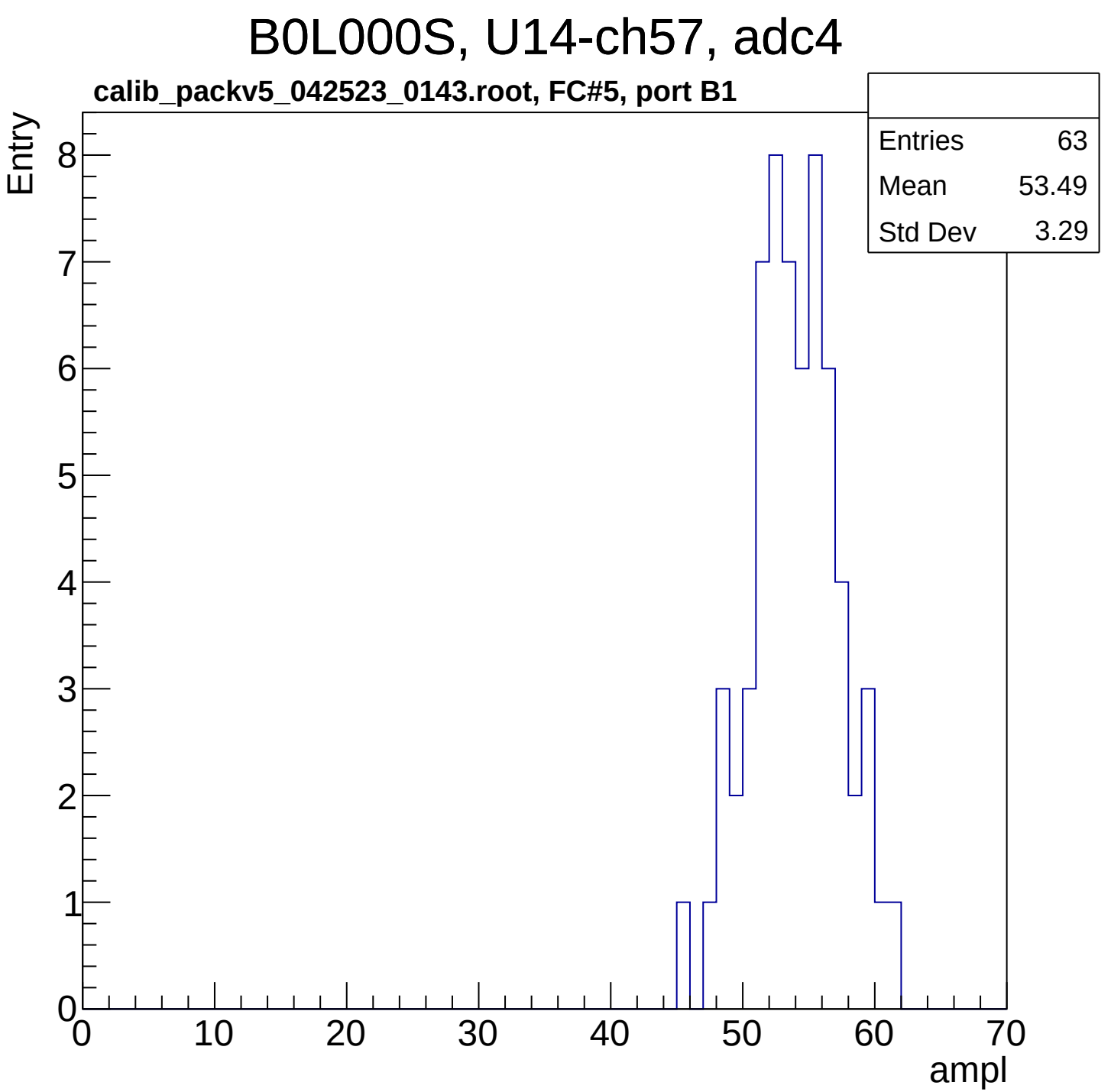
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 53.49
Std Dev 3.29
Entry
ampl
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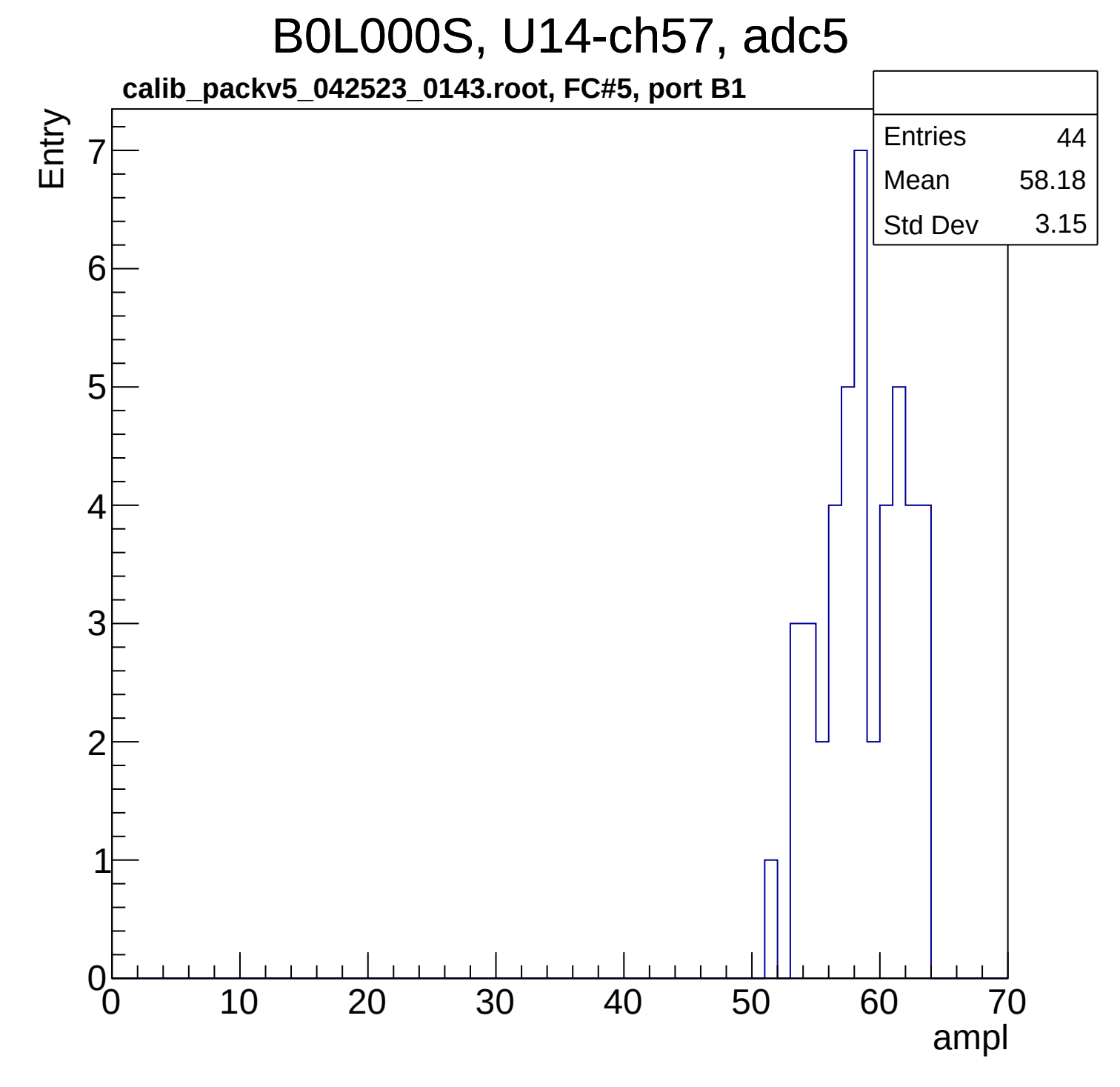
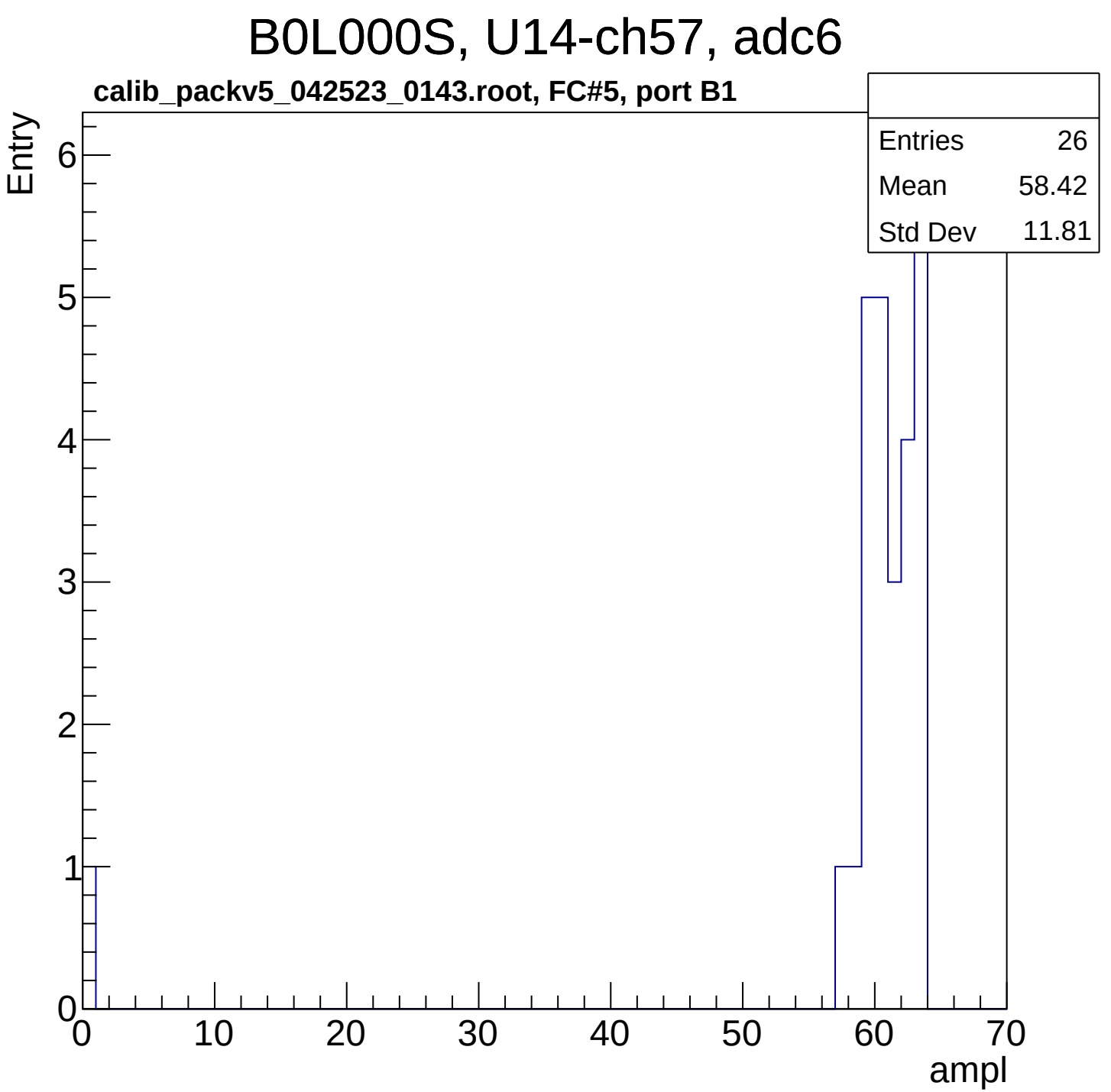
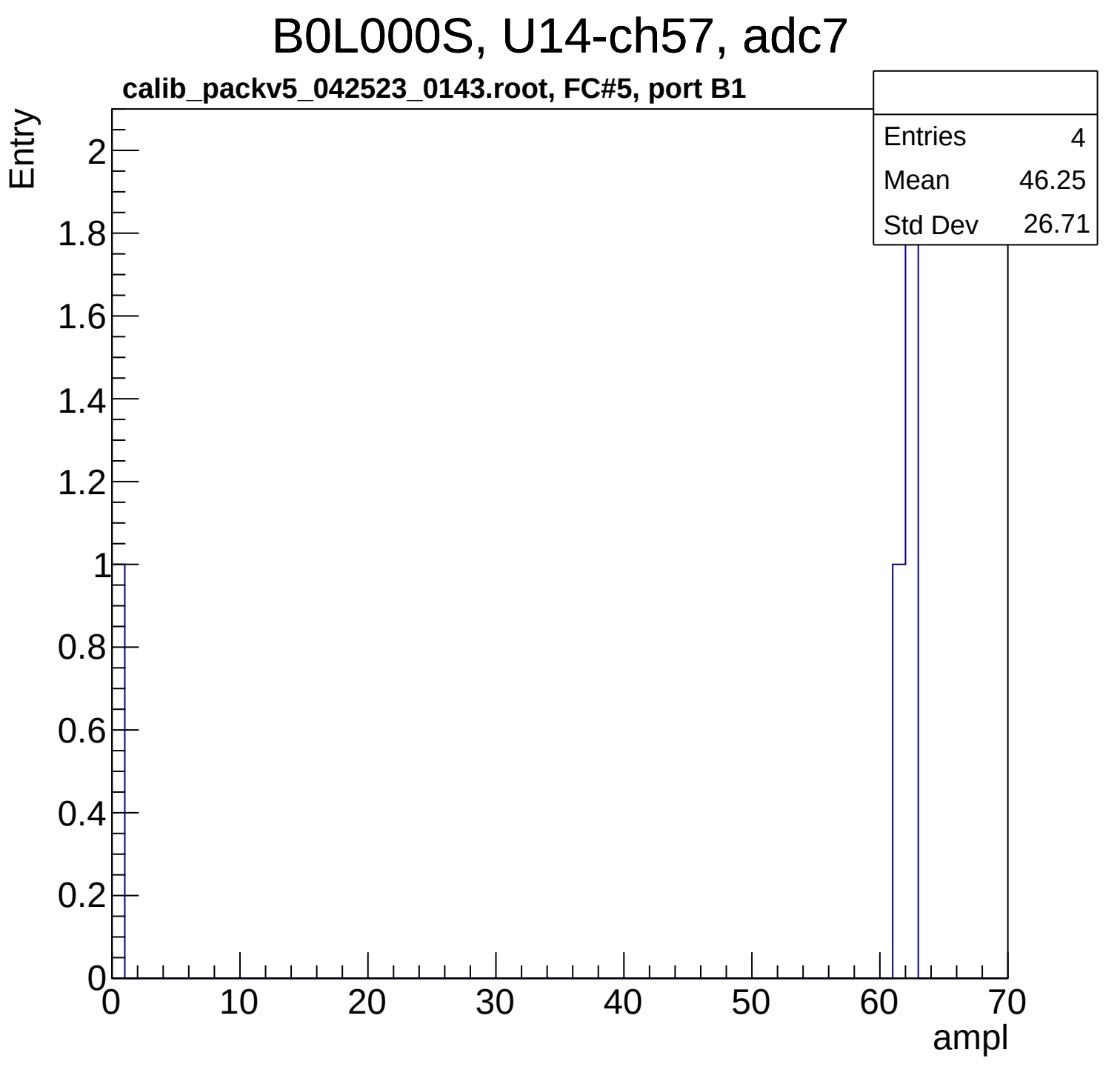
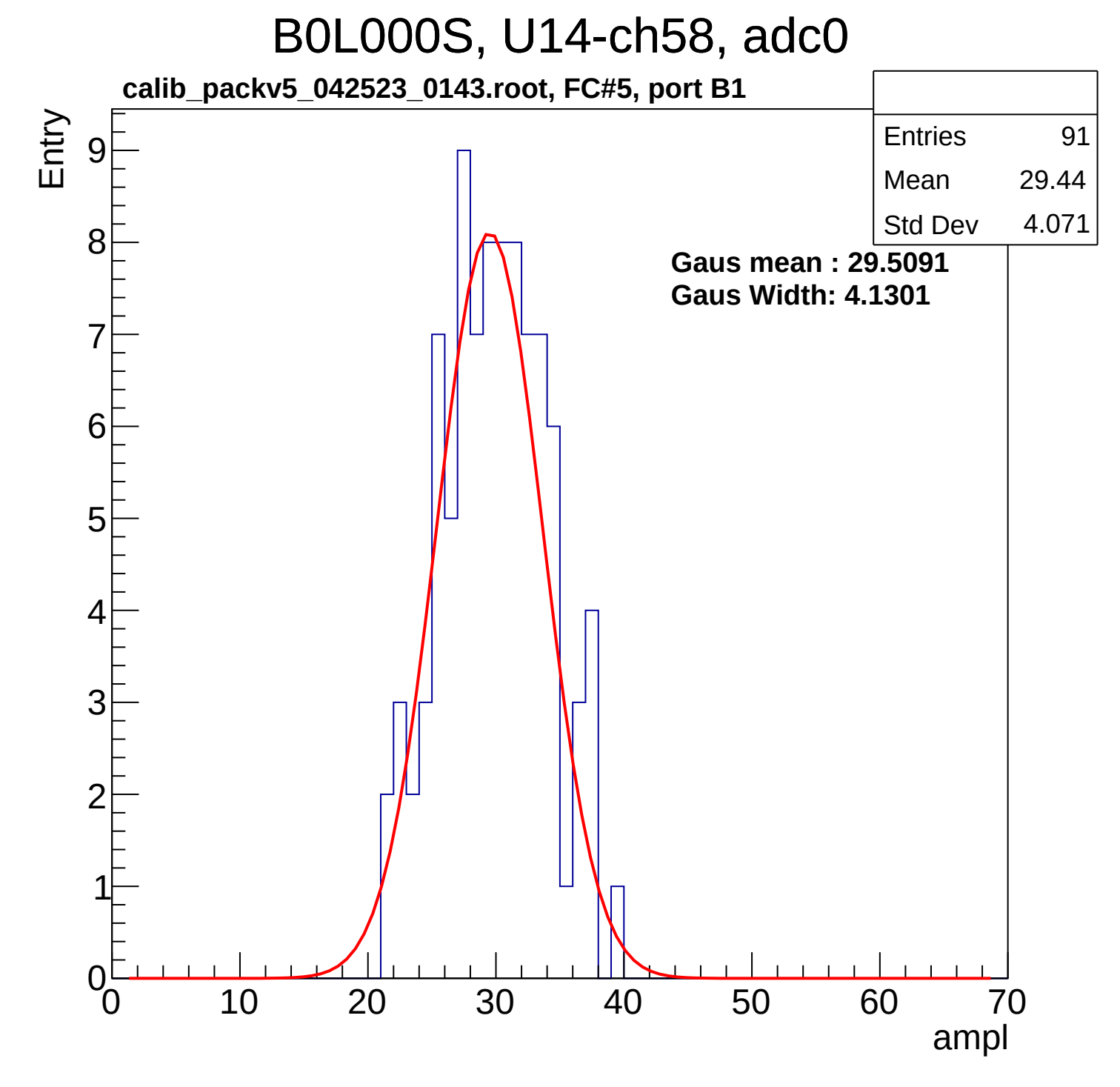
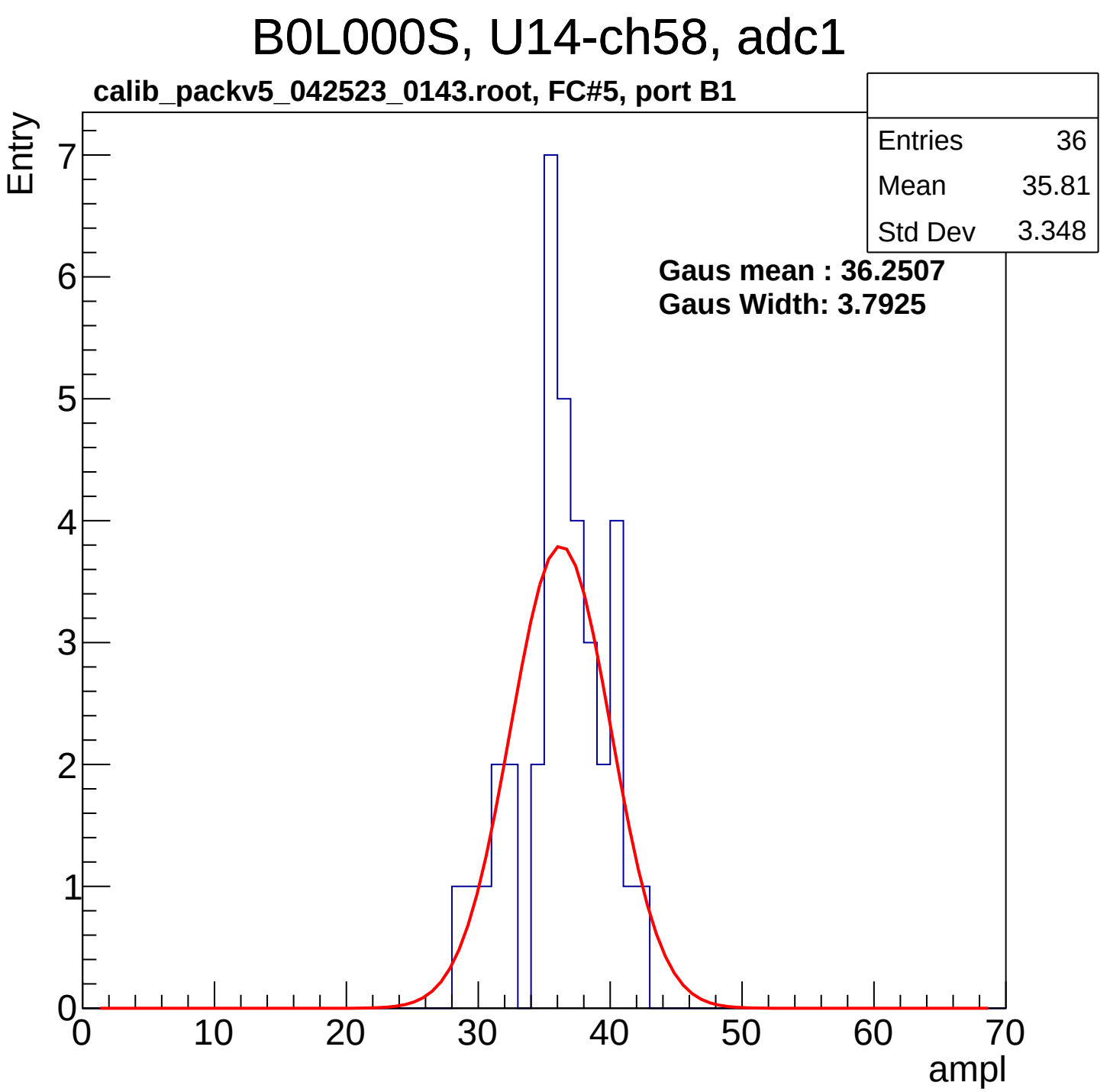
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.18
Std Dev 3.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 26
Mean 58.42
Std Dev 11.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.25
Std Dev 26.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 29.44
Std Dev 4.071
Gaus mean : 29.5091
Gaus Width: 4.1301
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 35.81
Std Dev 3.348
Gaus mean : 36.2507
Gaus Width: 3.7925
Entry
ampl
0
1
2
3
4
5
6
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0
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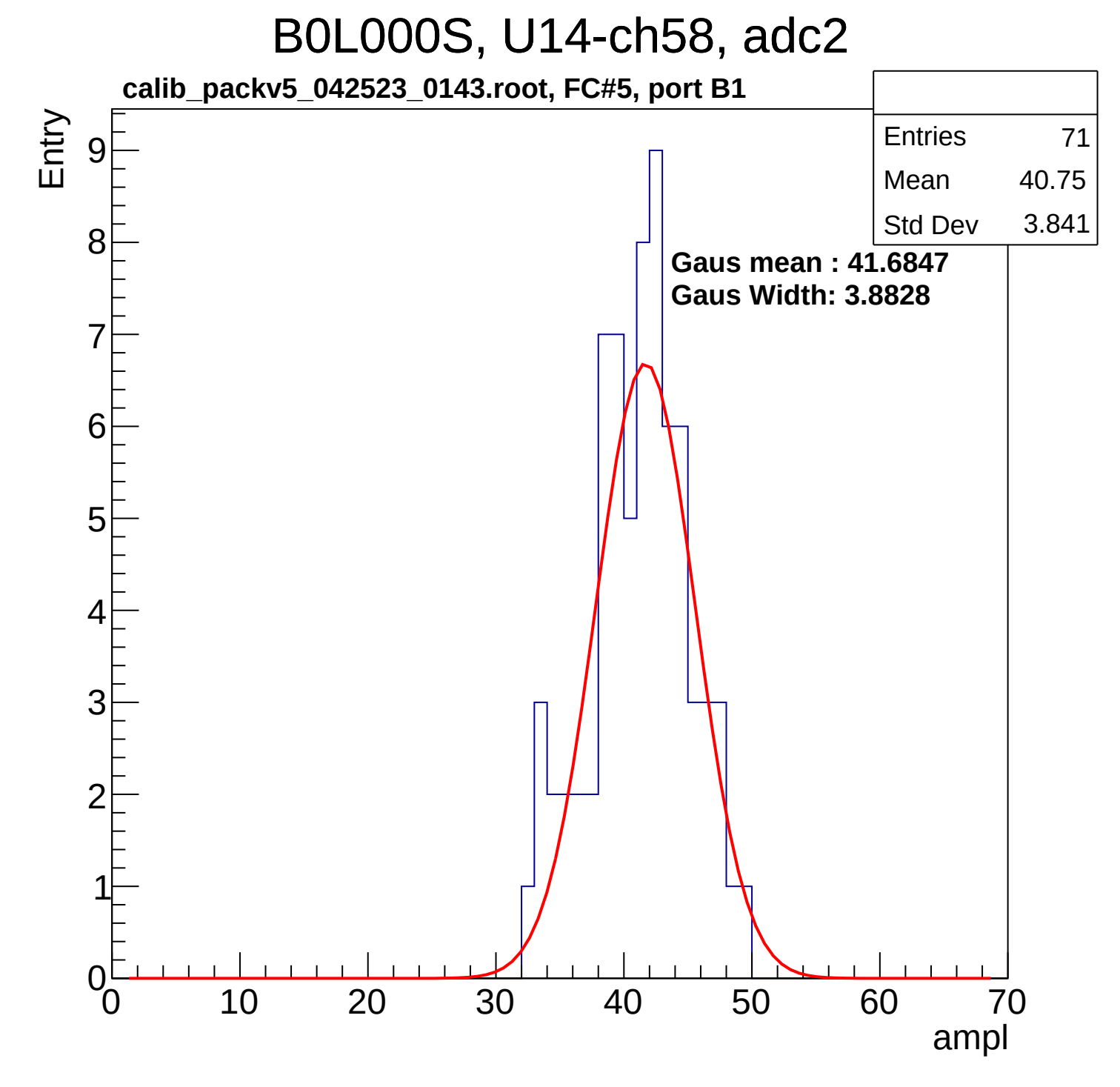
B0L000S, U14-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 40.75
Std Dev 3.841
Gaus mean : 41.6847
Gaus Width: 3.8828
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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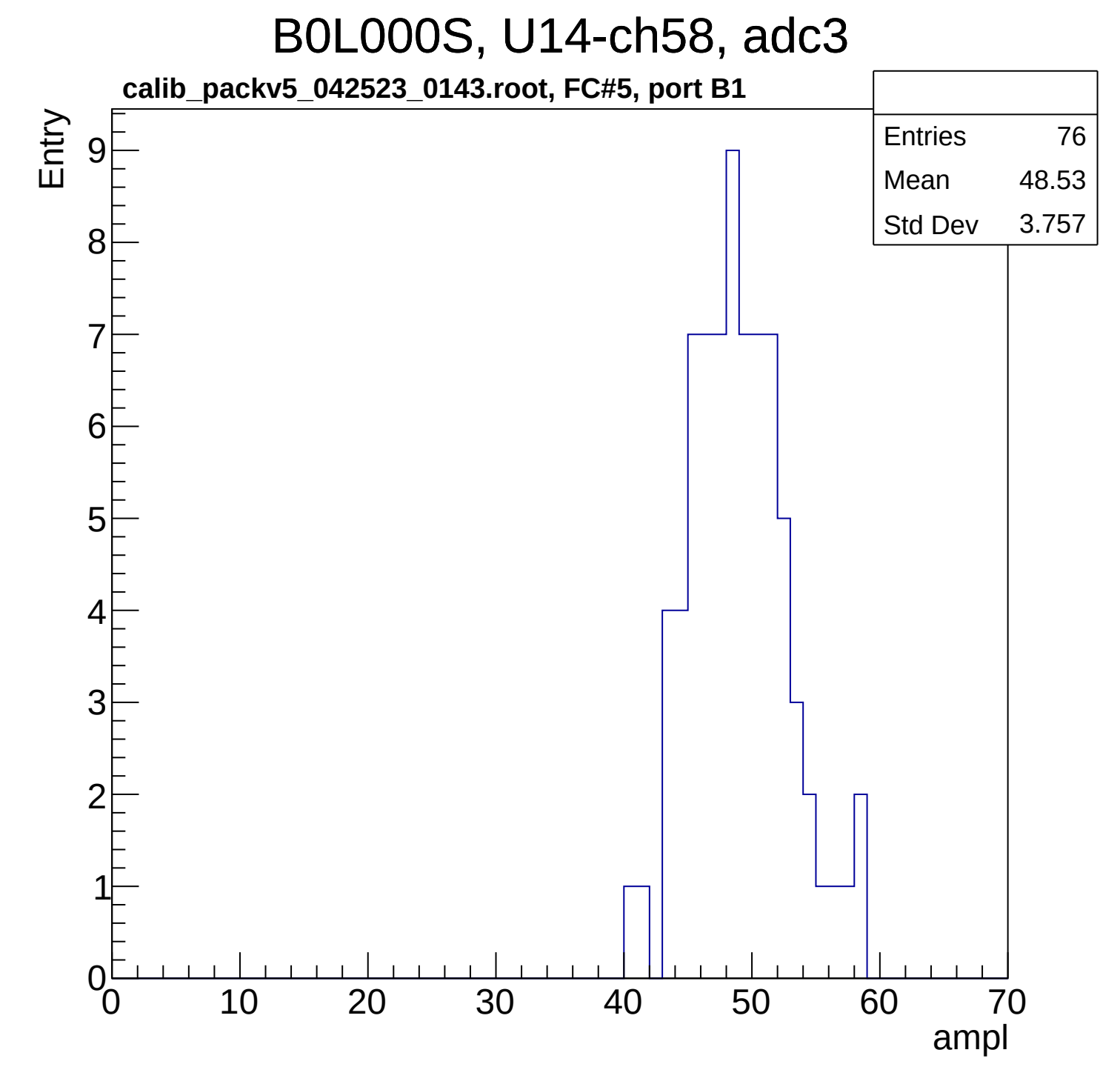
B0L000S, U14-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 48.53
Std Dev 3.757
Entry
ampl
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2
3
4
5
6
7
8
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0
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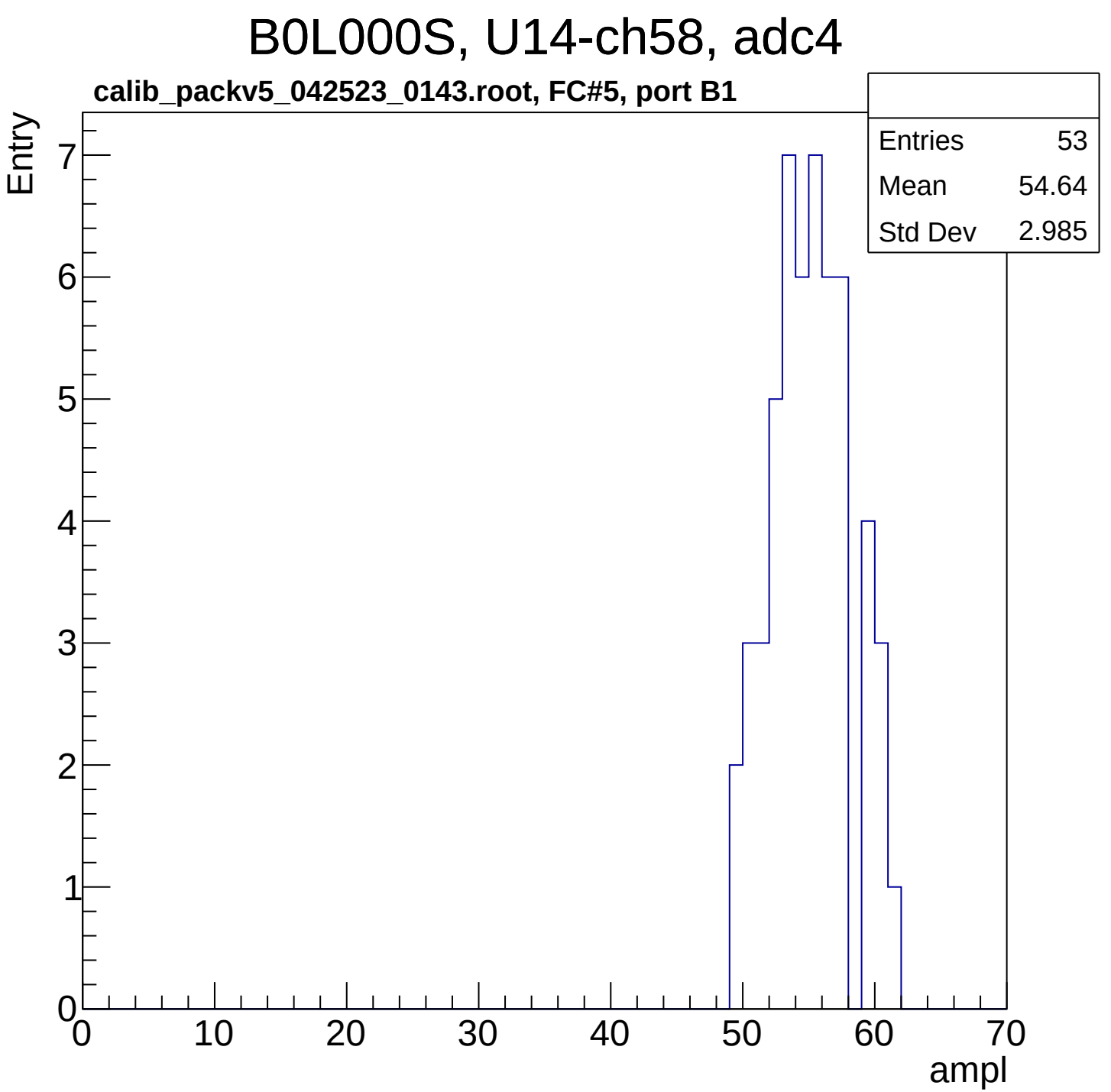
B0L000S, U14-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.64
Std Dev 2.985
Entry
ampl
0
1
2
3
4
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6
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0
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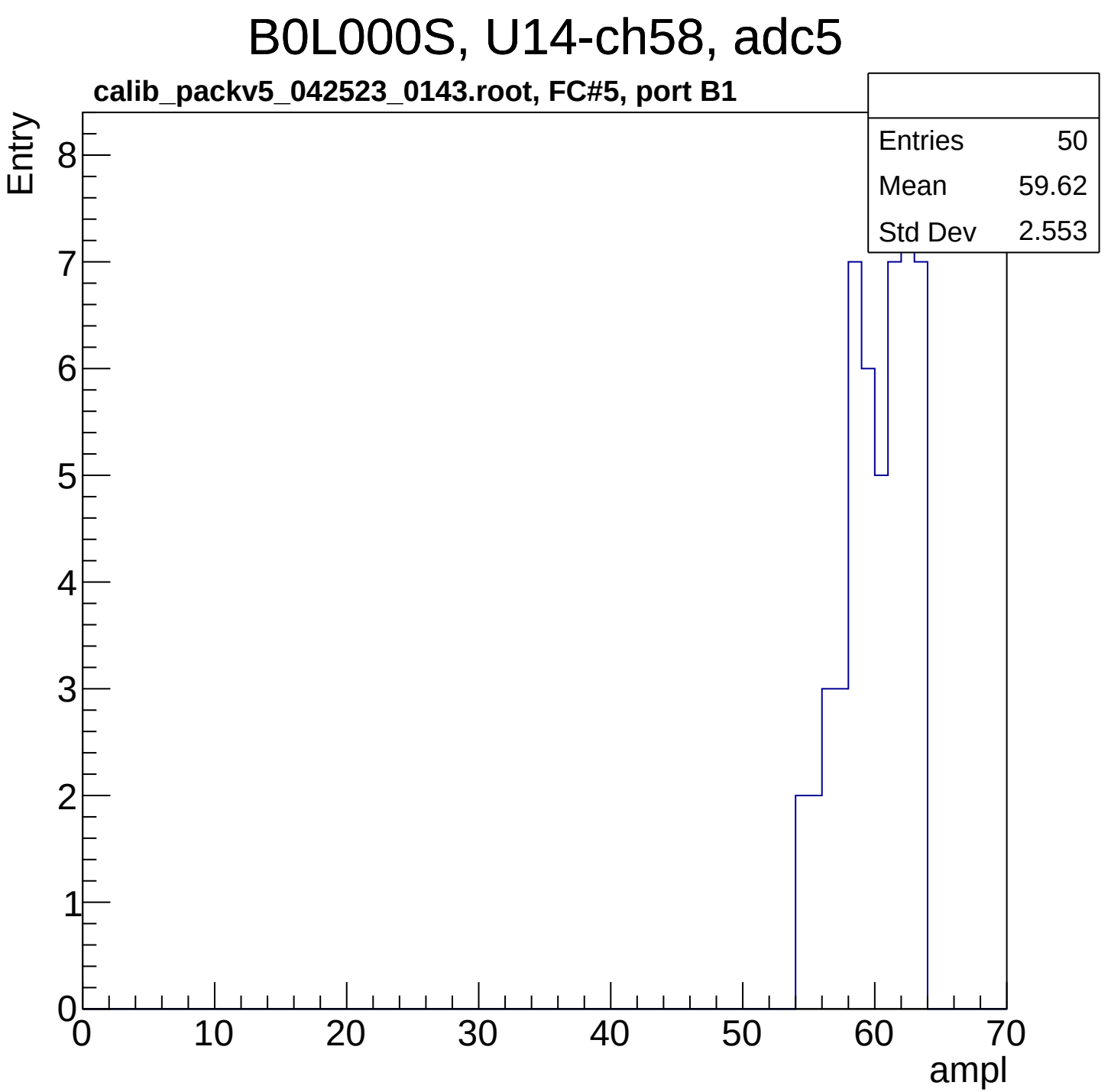
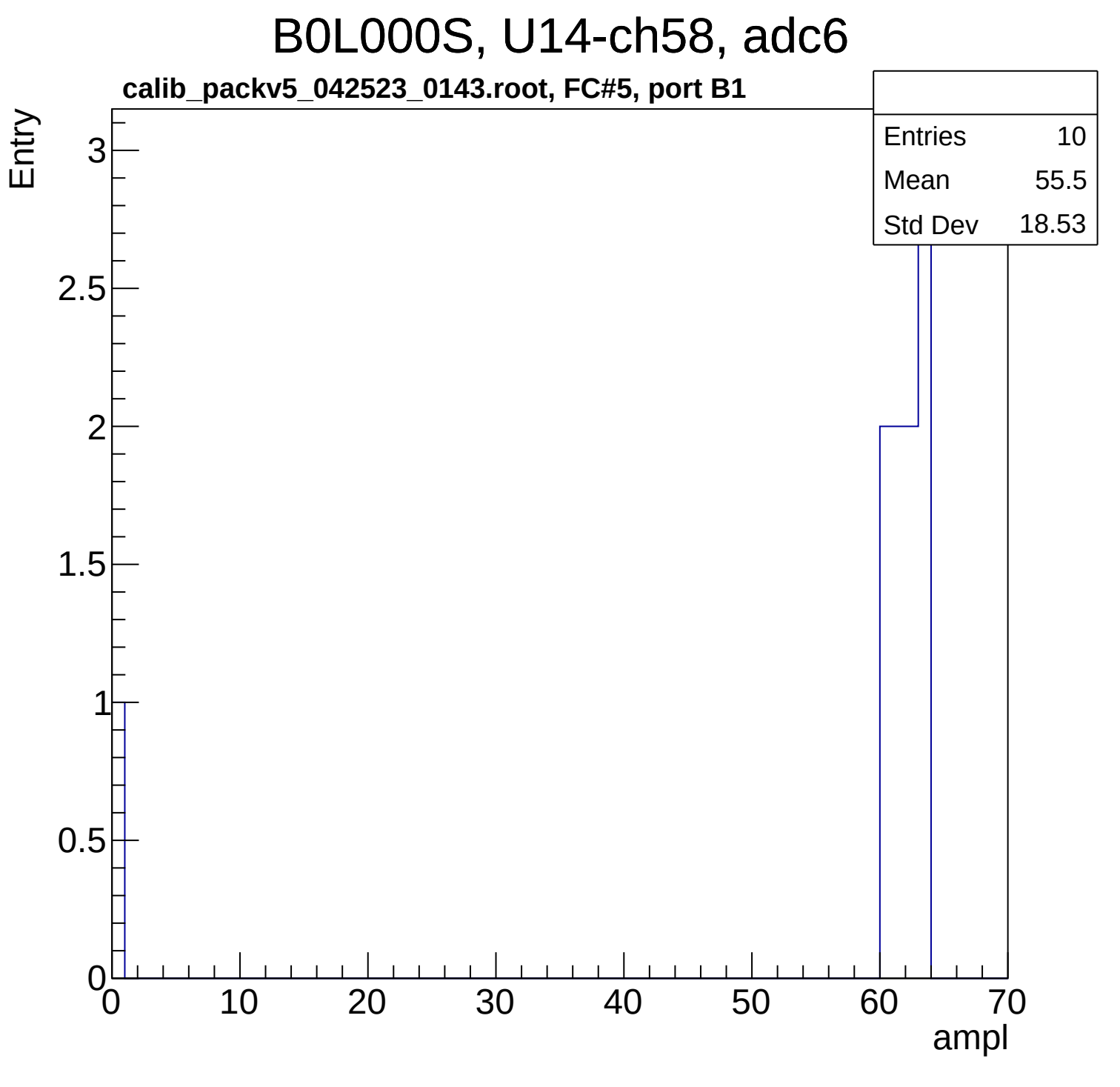
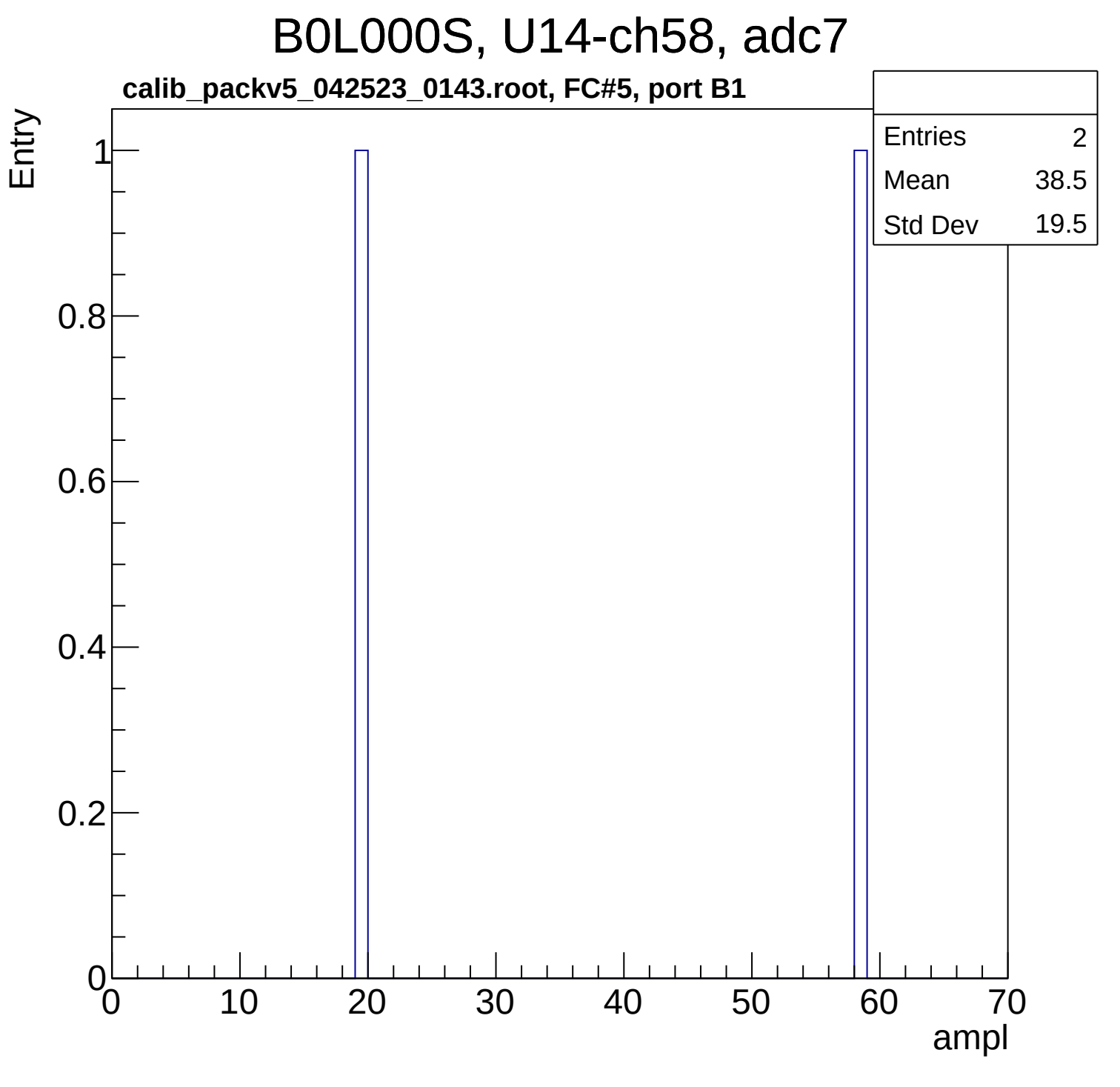

B0L000S, U14-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.62
Std Dev 2.553
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.5
Std Dev 18.53
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 38.5
Std Dev 19.5
Entry
1
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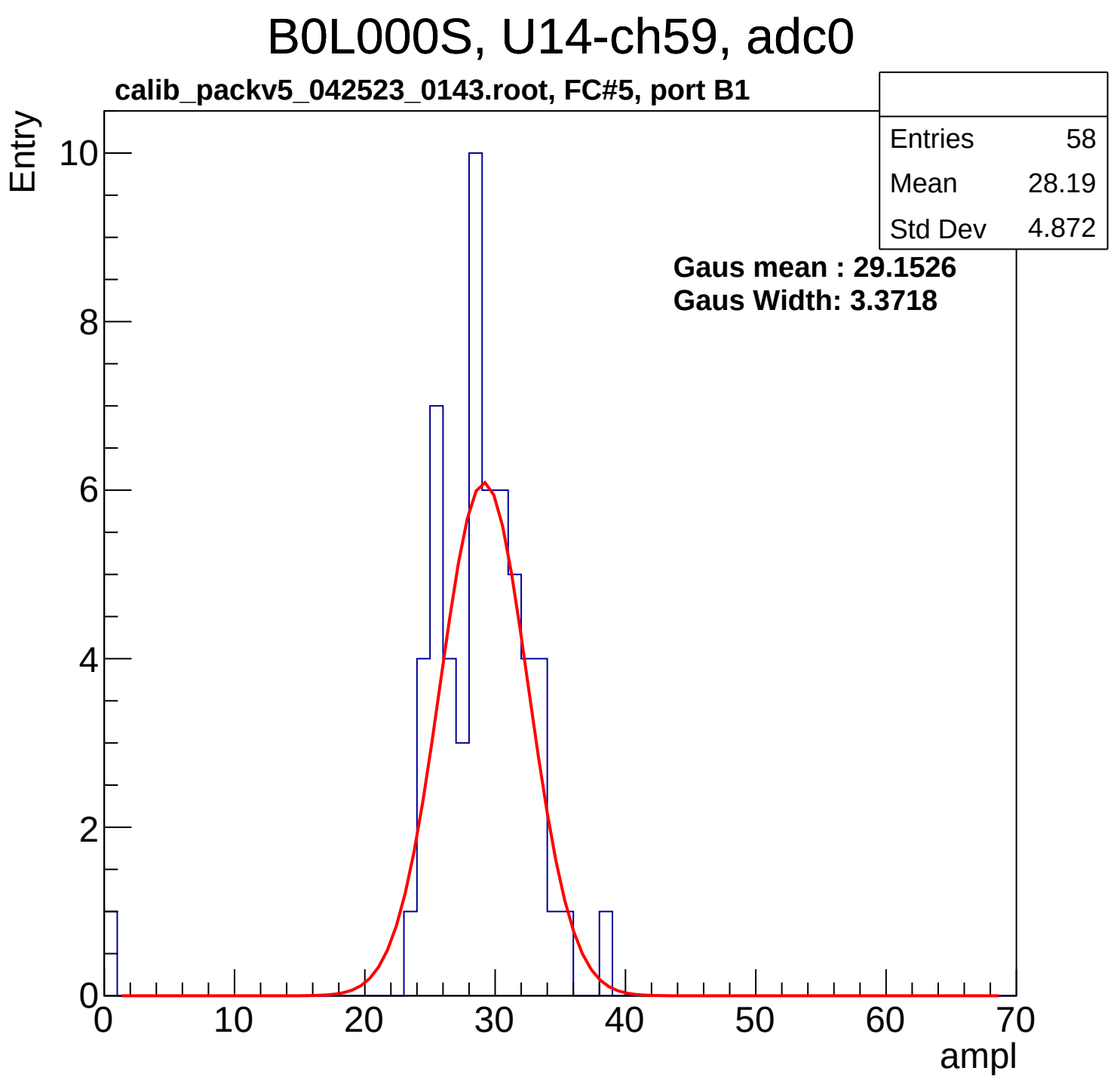
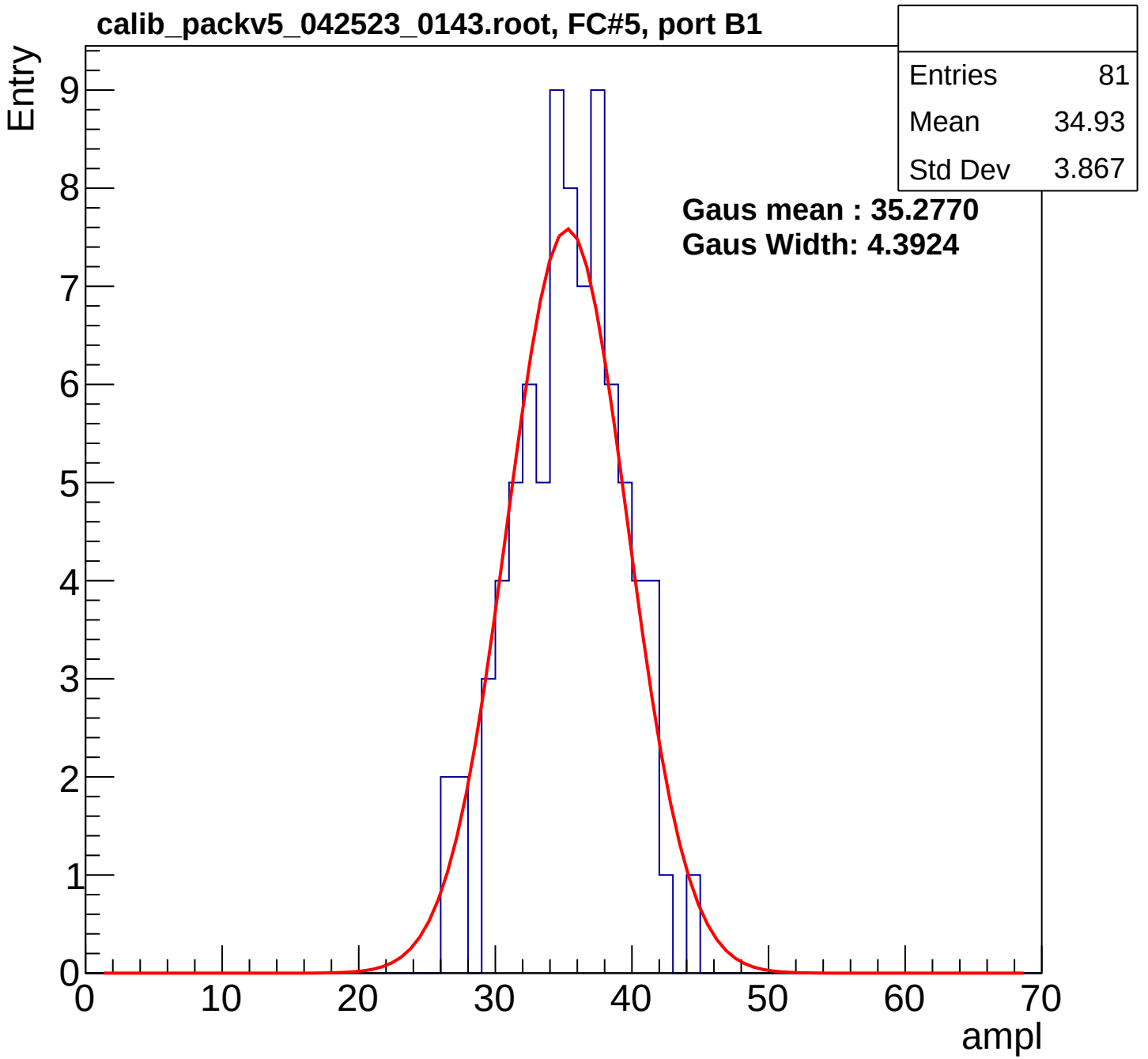
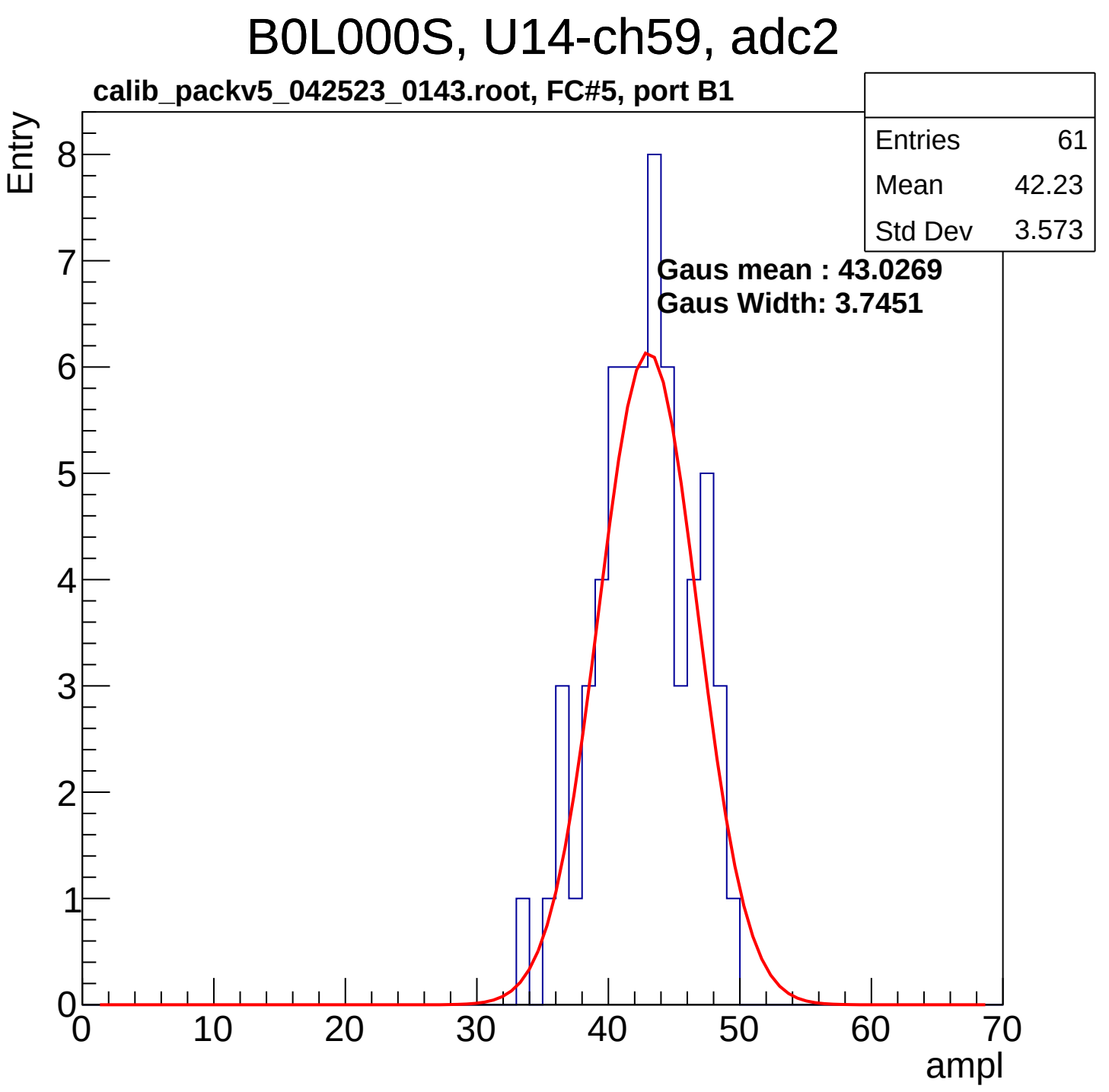
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 28.19
Std Dev 4.872
Gaus mean : 29.1526
Gaus Width: 3.3718
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 34.93
Std Dev 3.867
Gaus mean : 35.2770
Gaus Width: 4.3924
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.23
Std Dev 3.573
Gaus mean : 43.0269
Gaus Width: 3.7451
Entry
ampl
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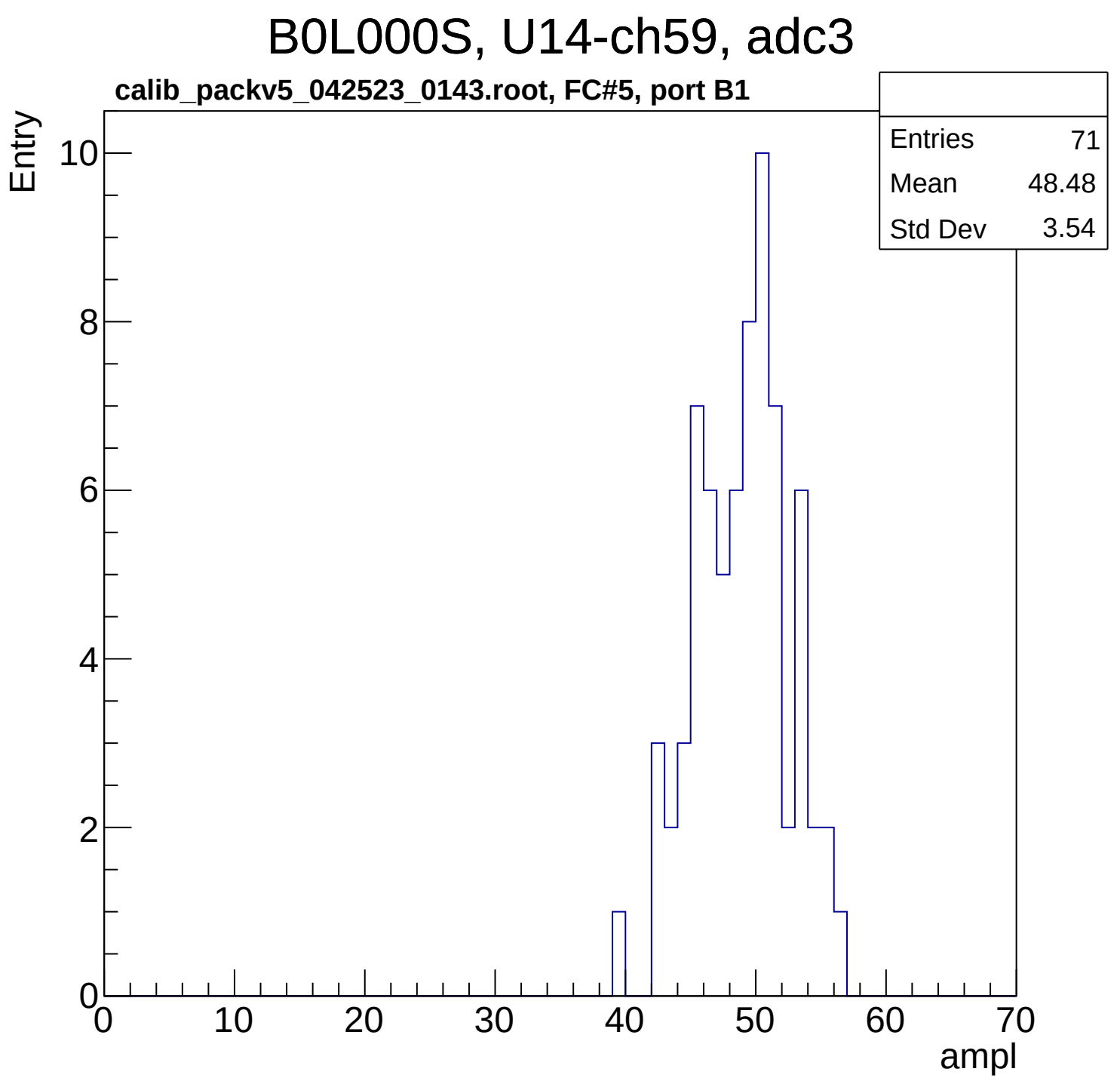
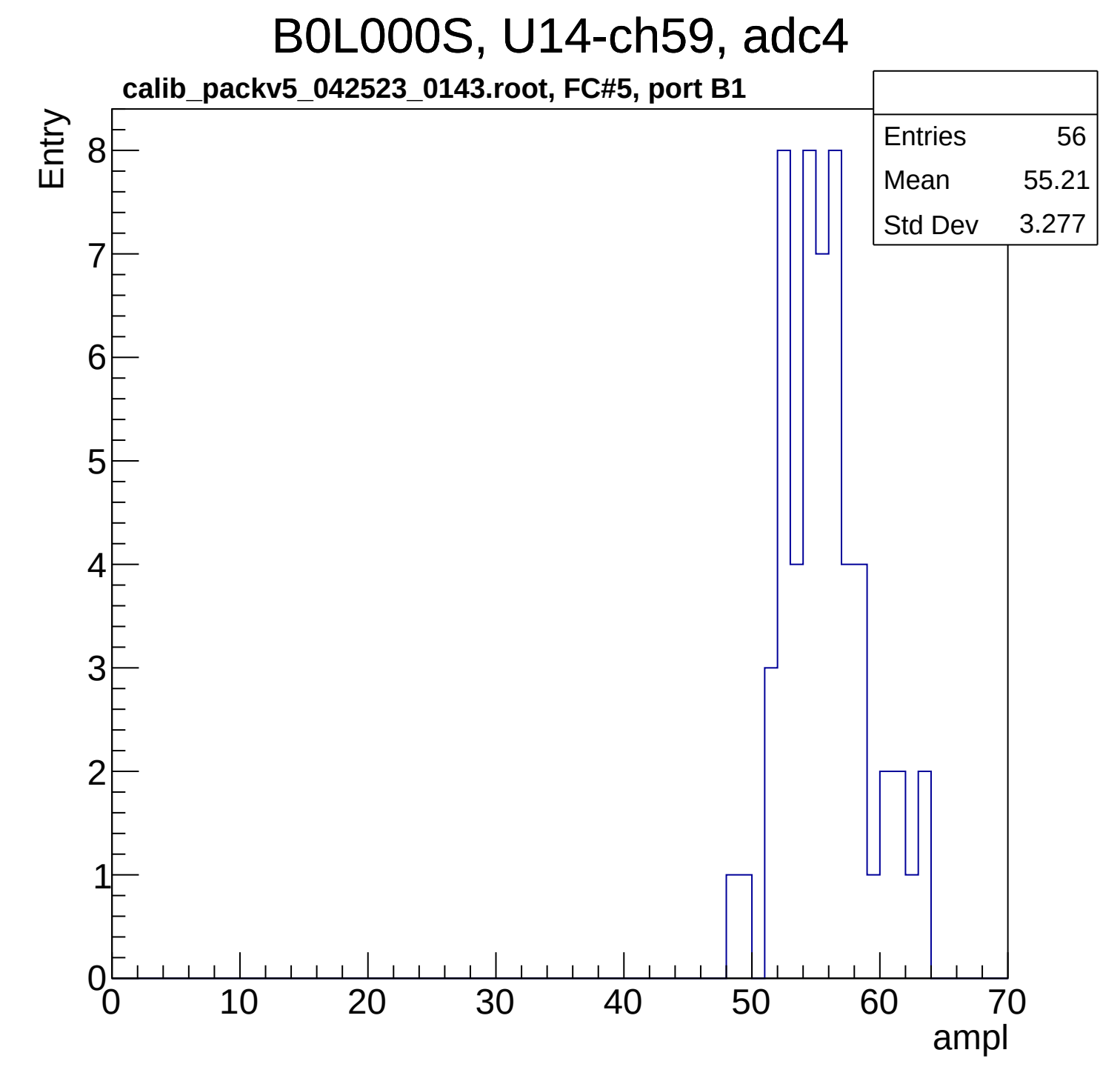
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 48.48
Std Dev 3.54
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.21
Std Dev 3.277
Entry
ampl
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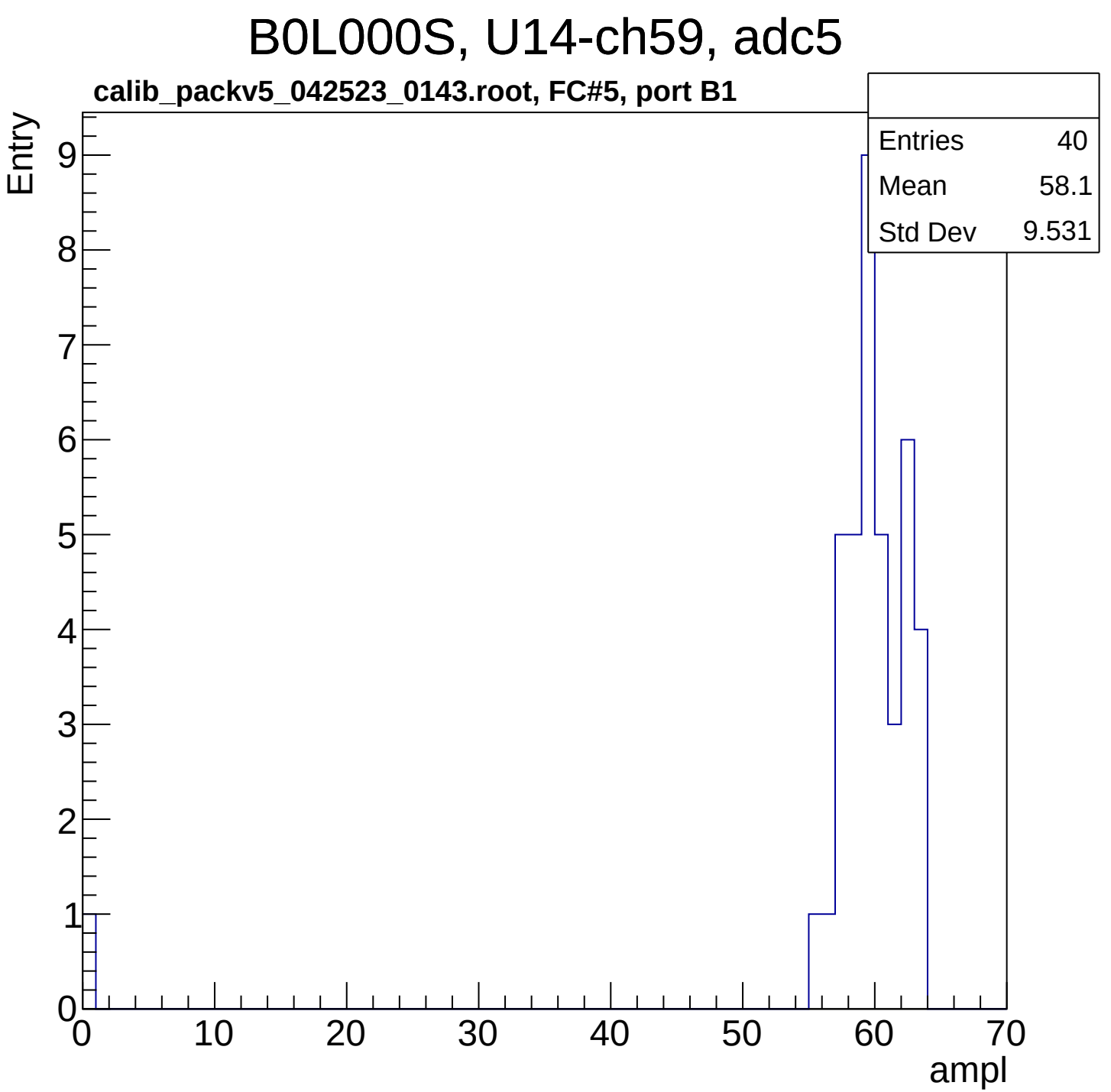
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.1
Std Dev 9.531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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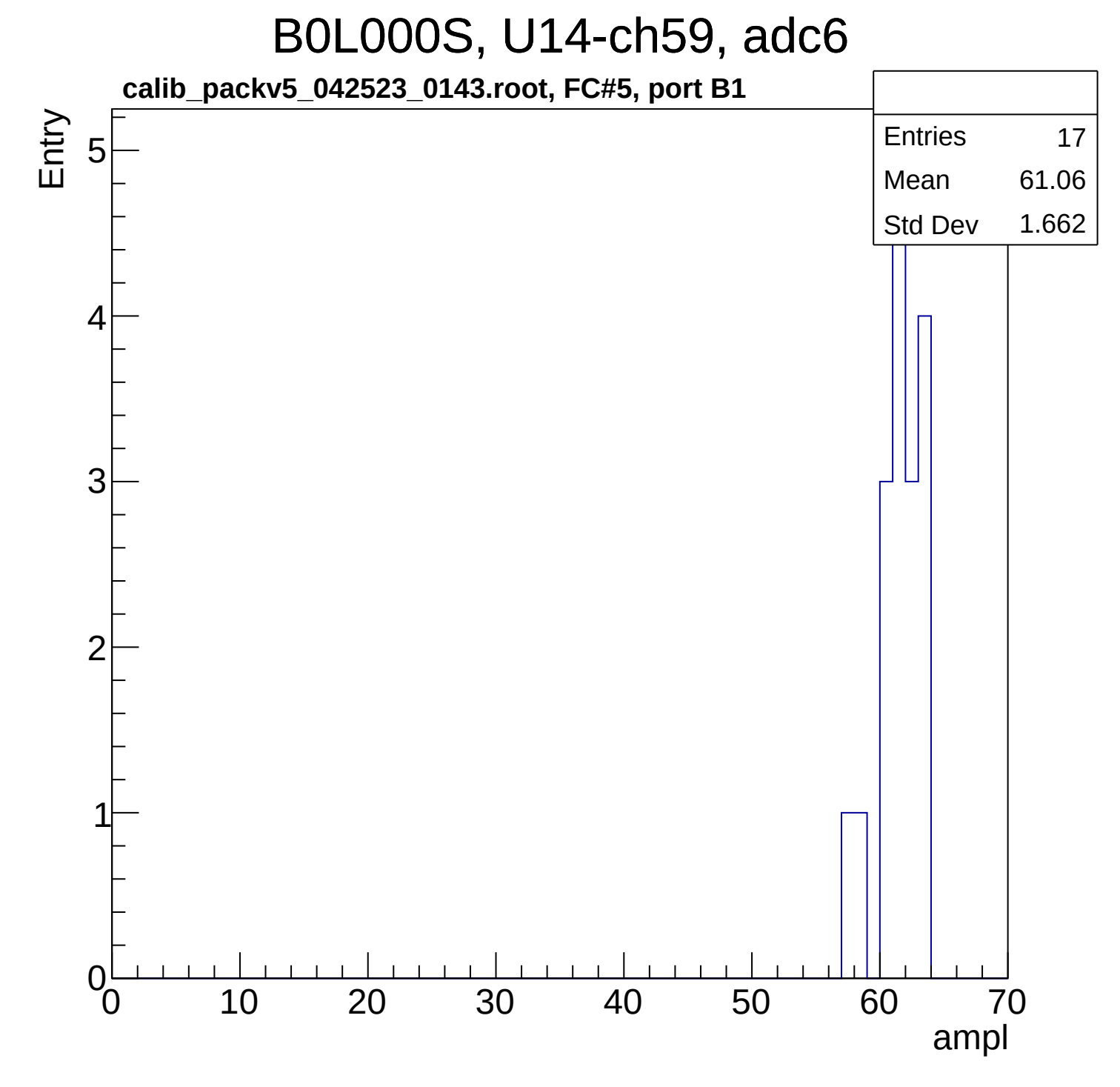
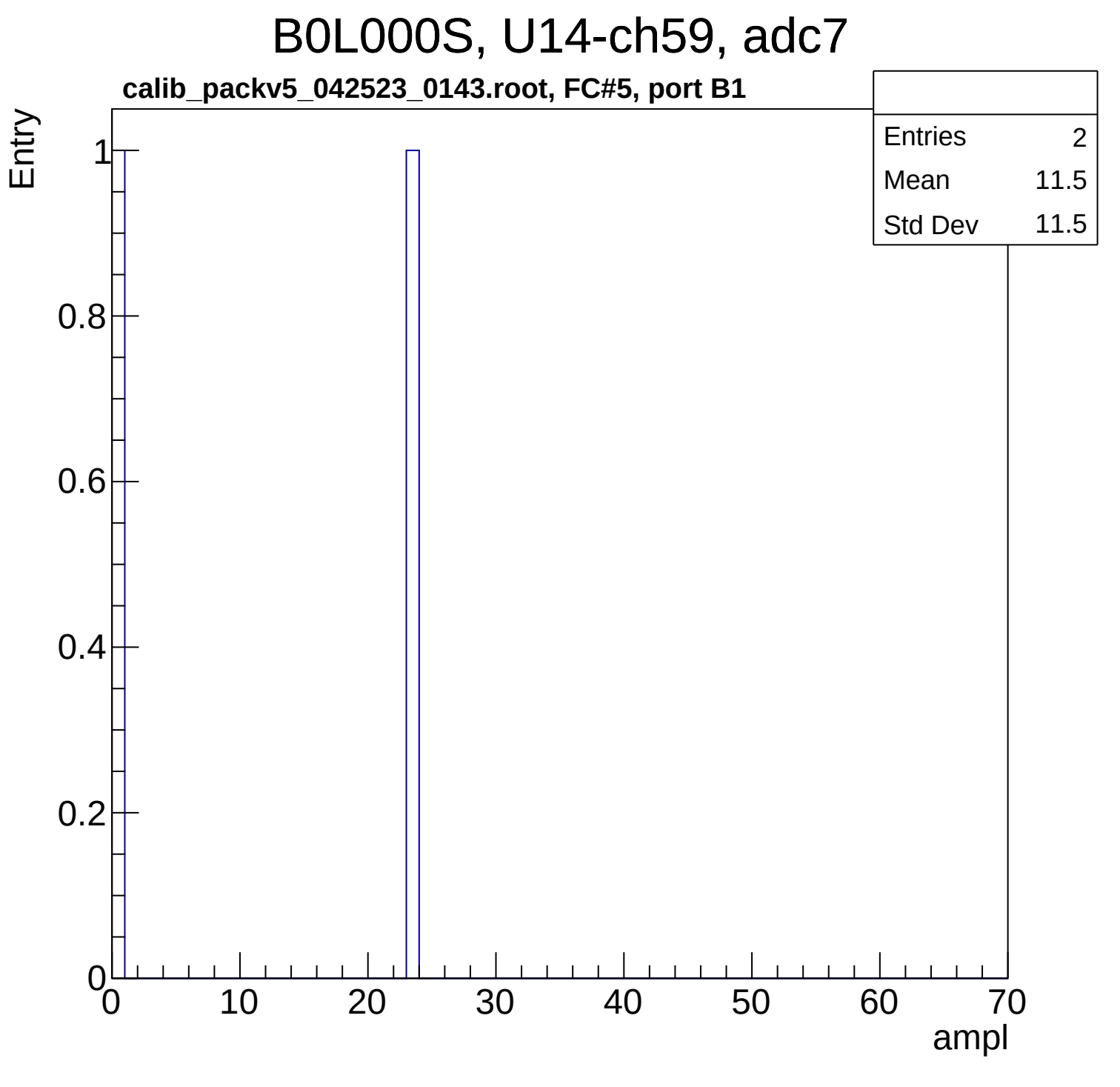
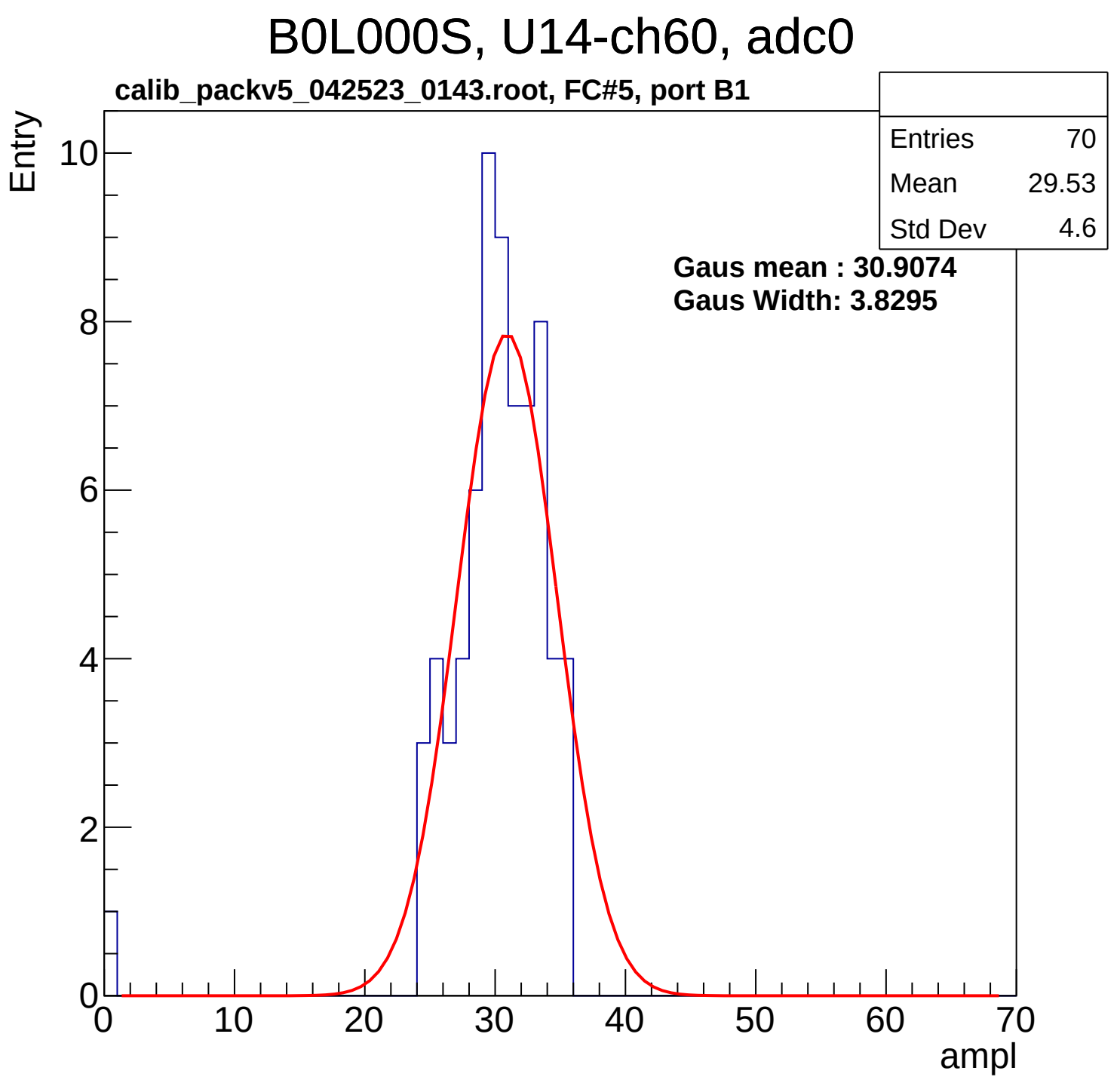
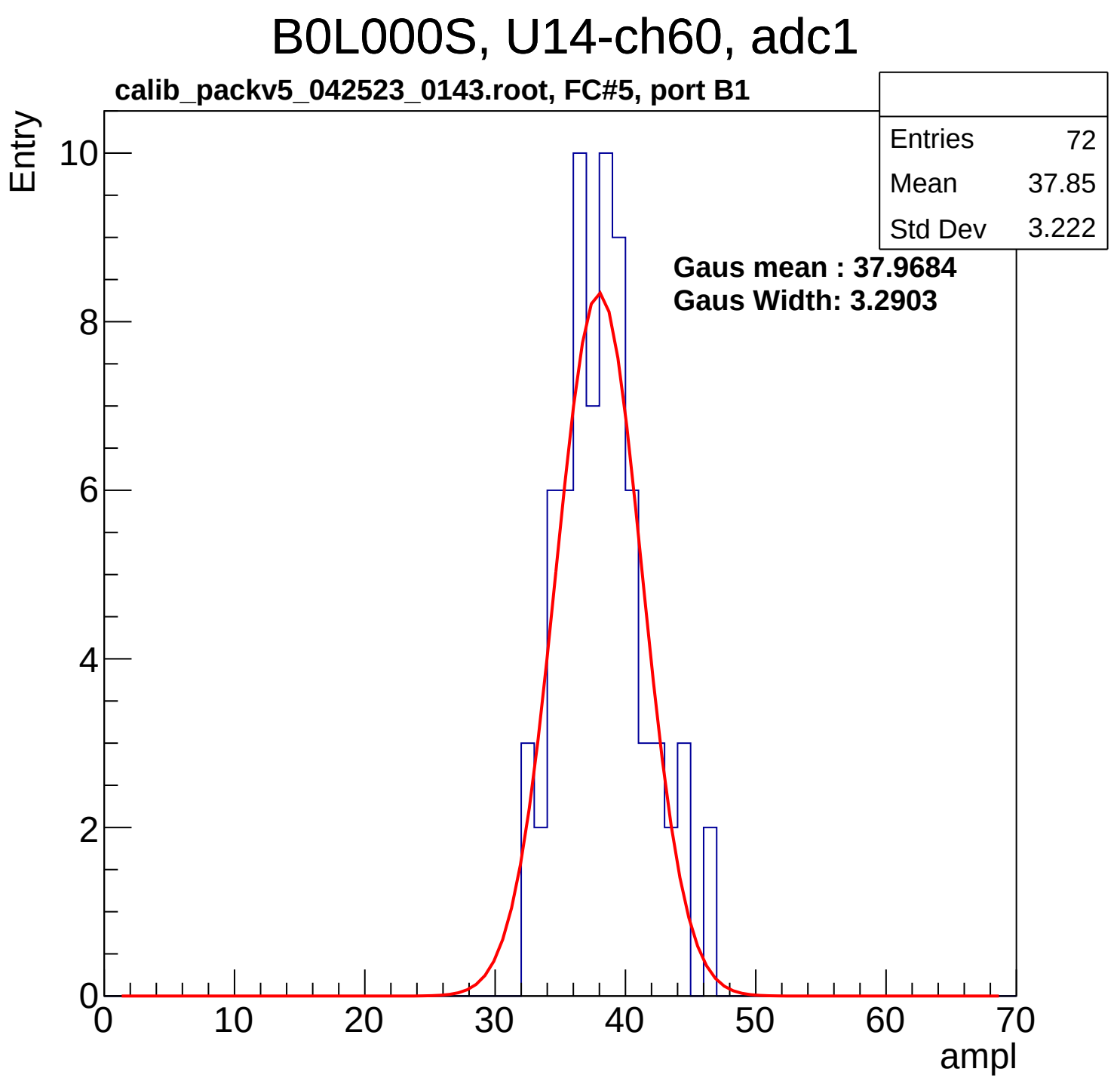
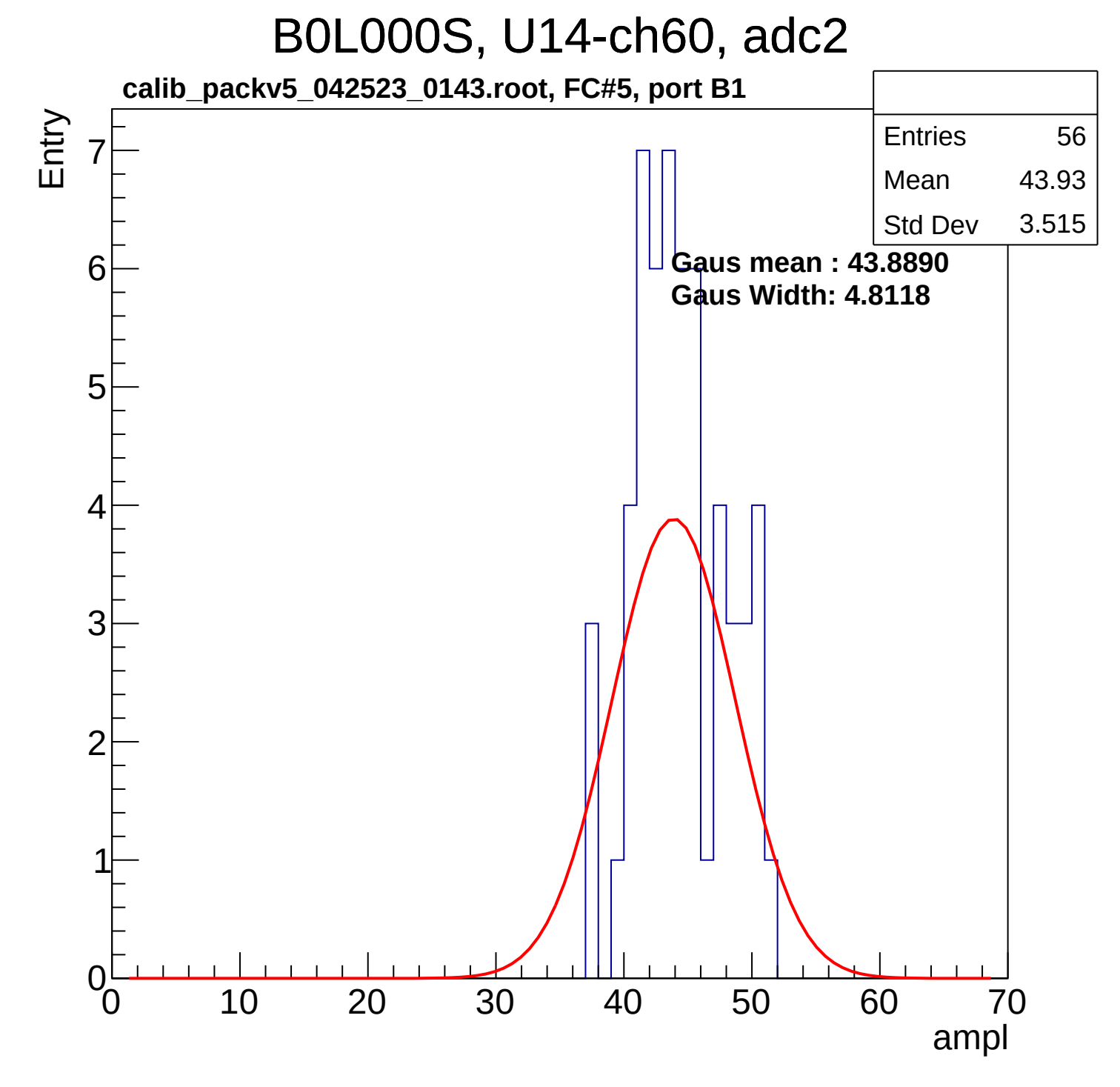
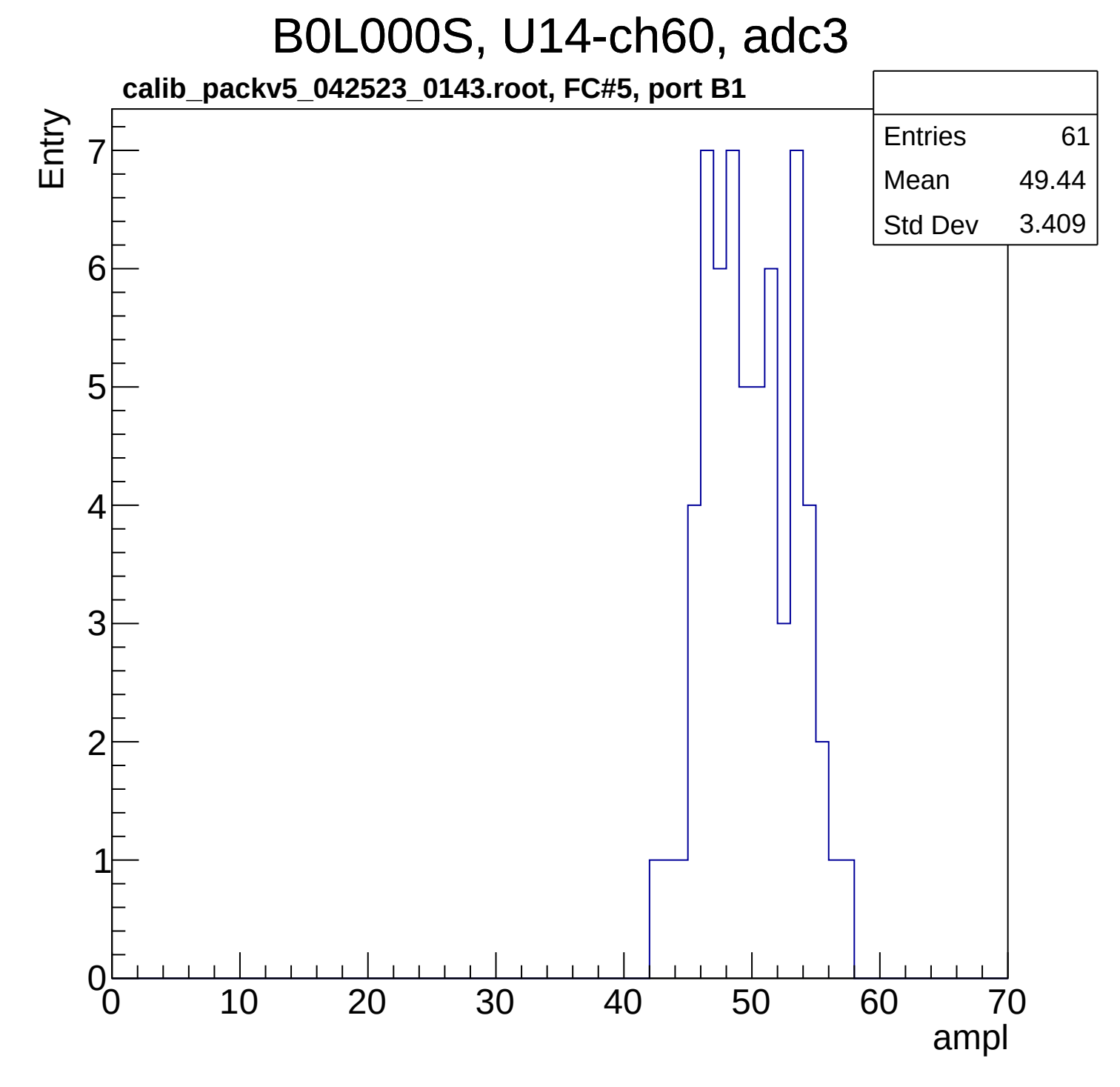

B0L000S, U14-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.06
Std Dev 1.662
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 29.53
Std Dev 4.6
Gaus mean : 30.9074
Gaus Width: 3.8295
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.85
Std Dev 3.222
Gaus mean : 37.9684
Gaus Width: 3.2903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.93
Std Dev 3.515
Gaus mean : 43.8890
Gaus Width: 4.8118
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 49.44
Std Dev 3.409
Entry
ampl
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1
2
3
4
5
6
7
0
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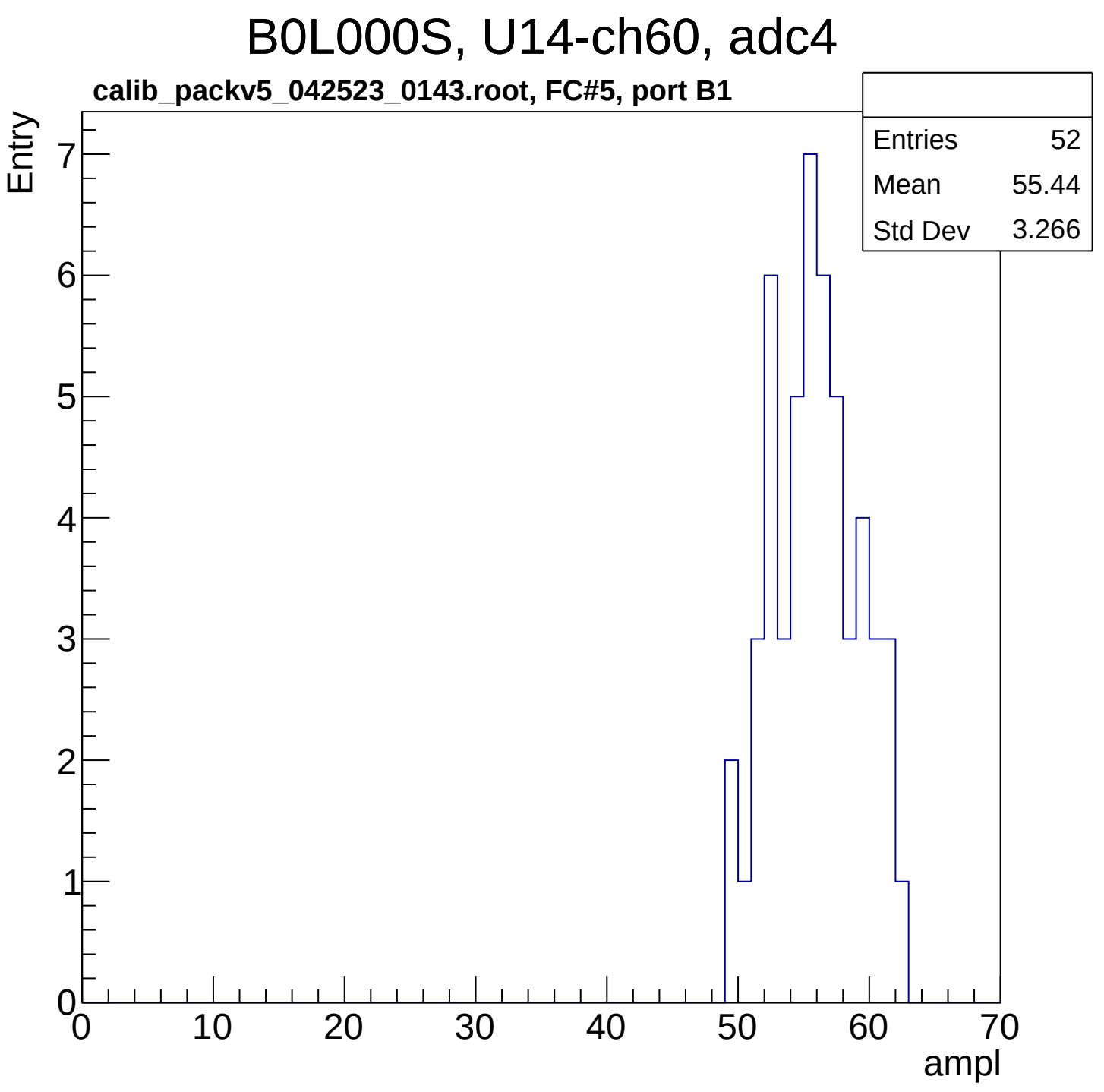
20
30
40
50
60
70


B0L000S, U14-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.44
Std Dev 3.266
Entry
ampl
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1
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0
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60
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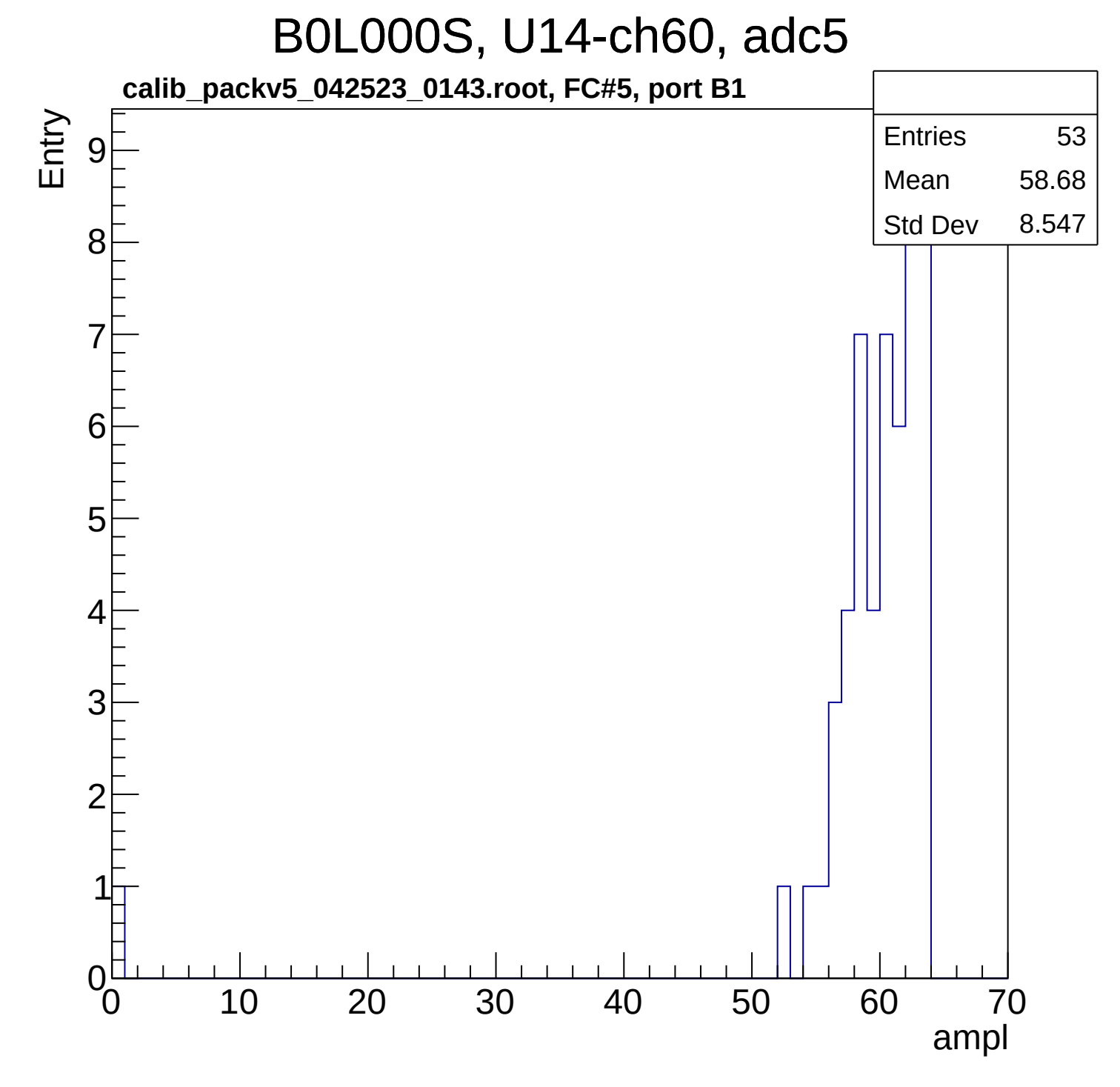
B0L000S, U14-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.68
Std Dev 8.547
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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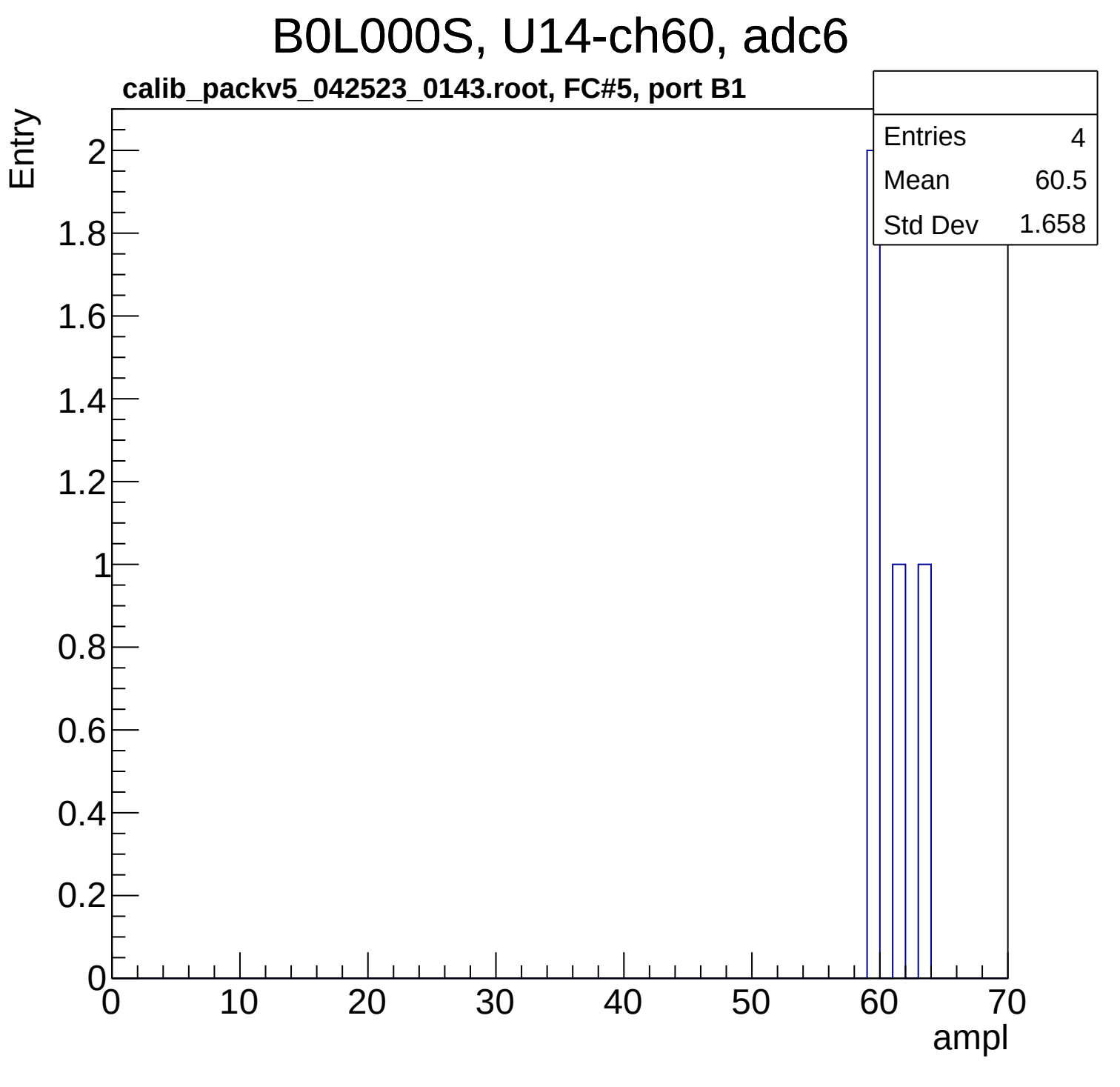
B0L000S, U14-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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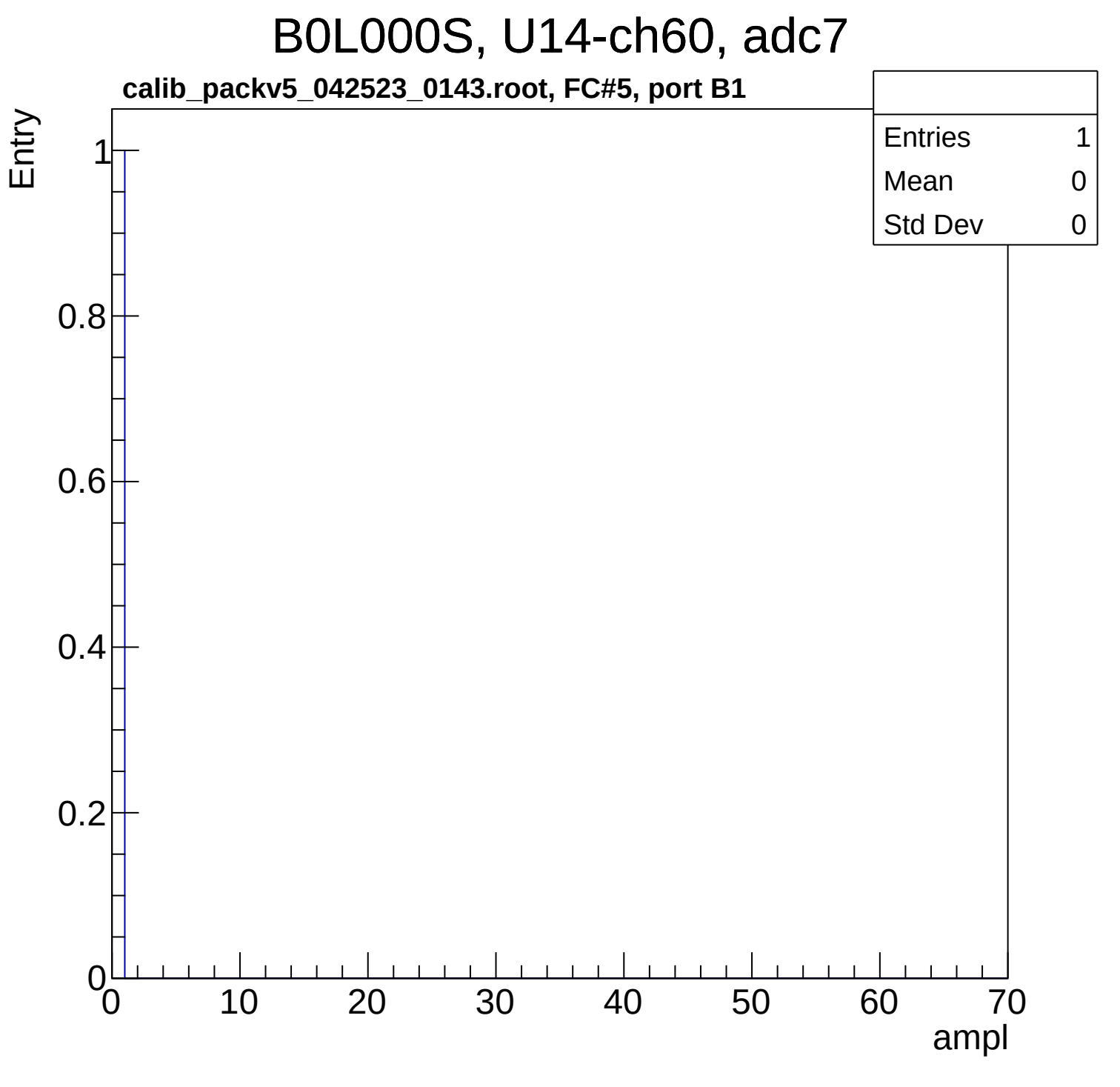
B0L000S, U14-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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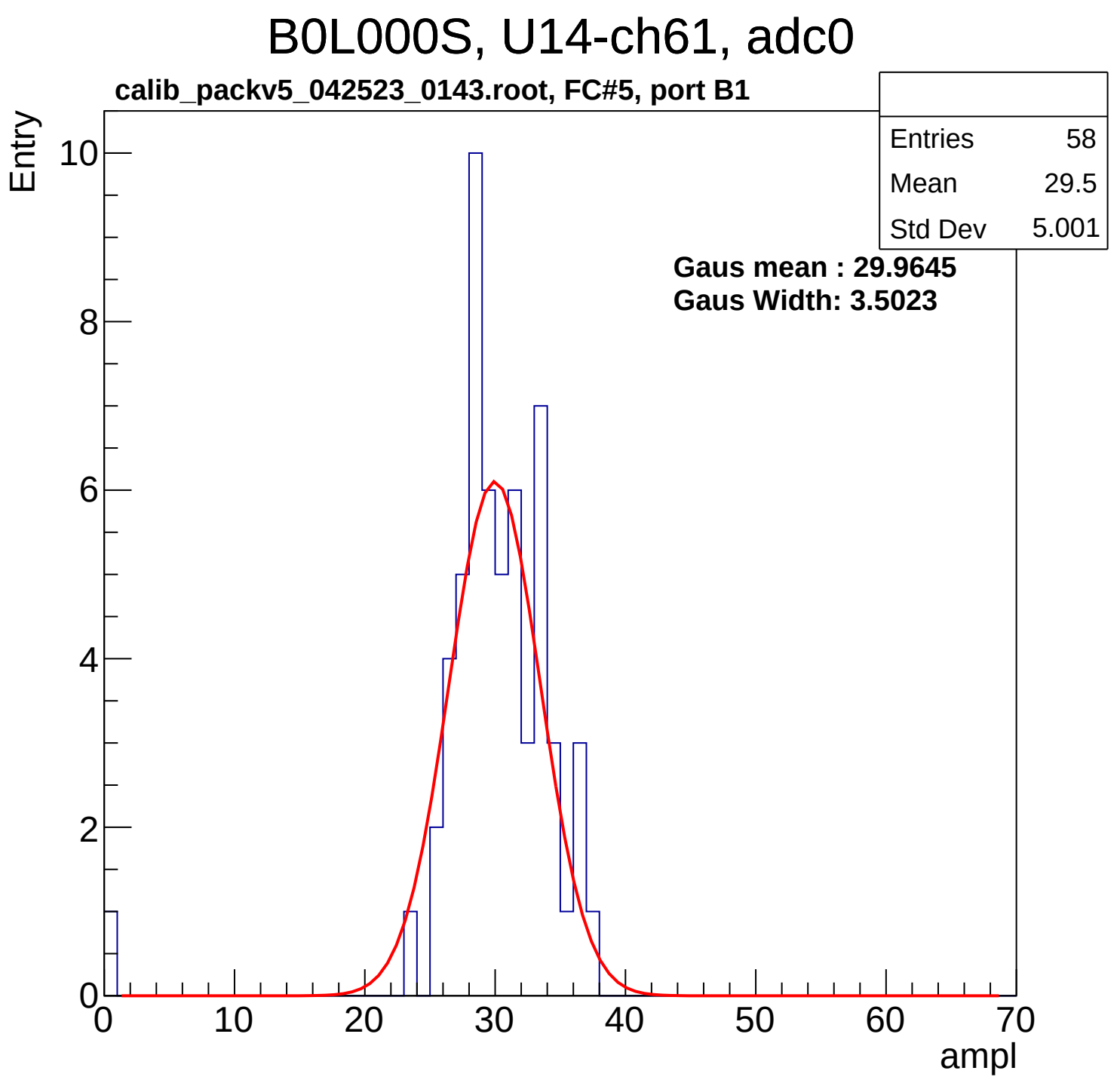
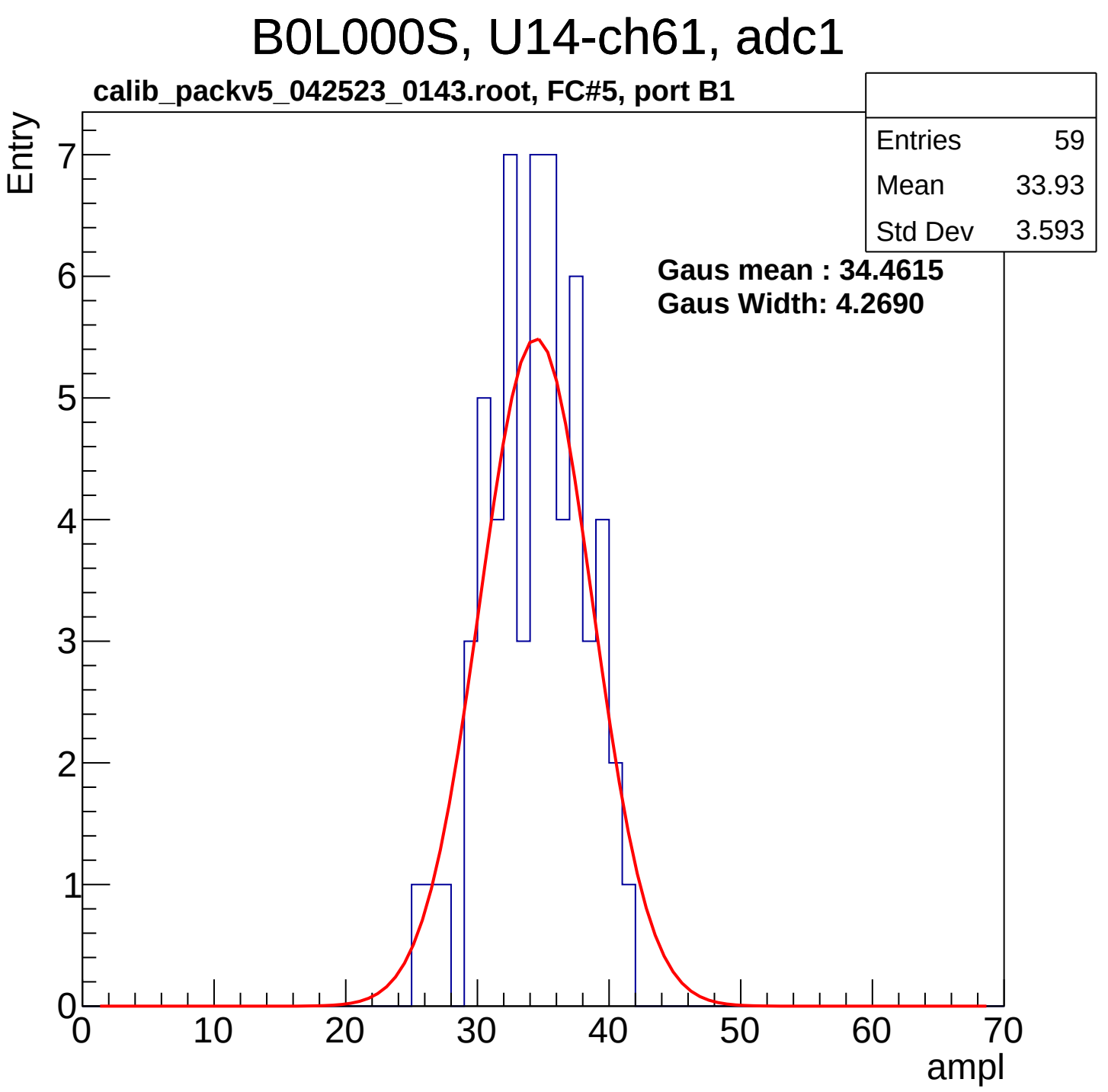
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 29.5
Std Dev 5.001
Gaus mean : 29.9645
Gaus Width: 3.5023
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 33.93
Std Dev 3.593
Gaus mean : 34.4615
Gaus Width: 4.2690
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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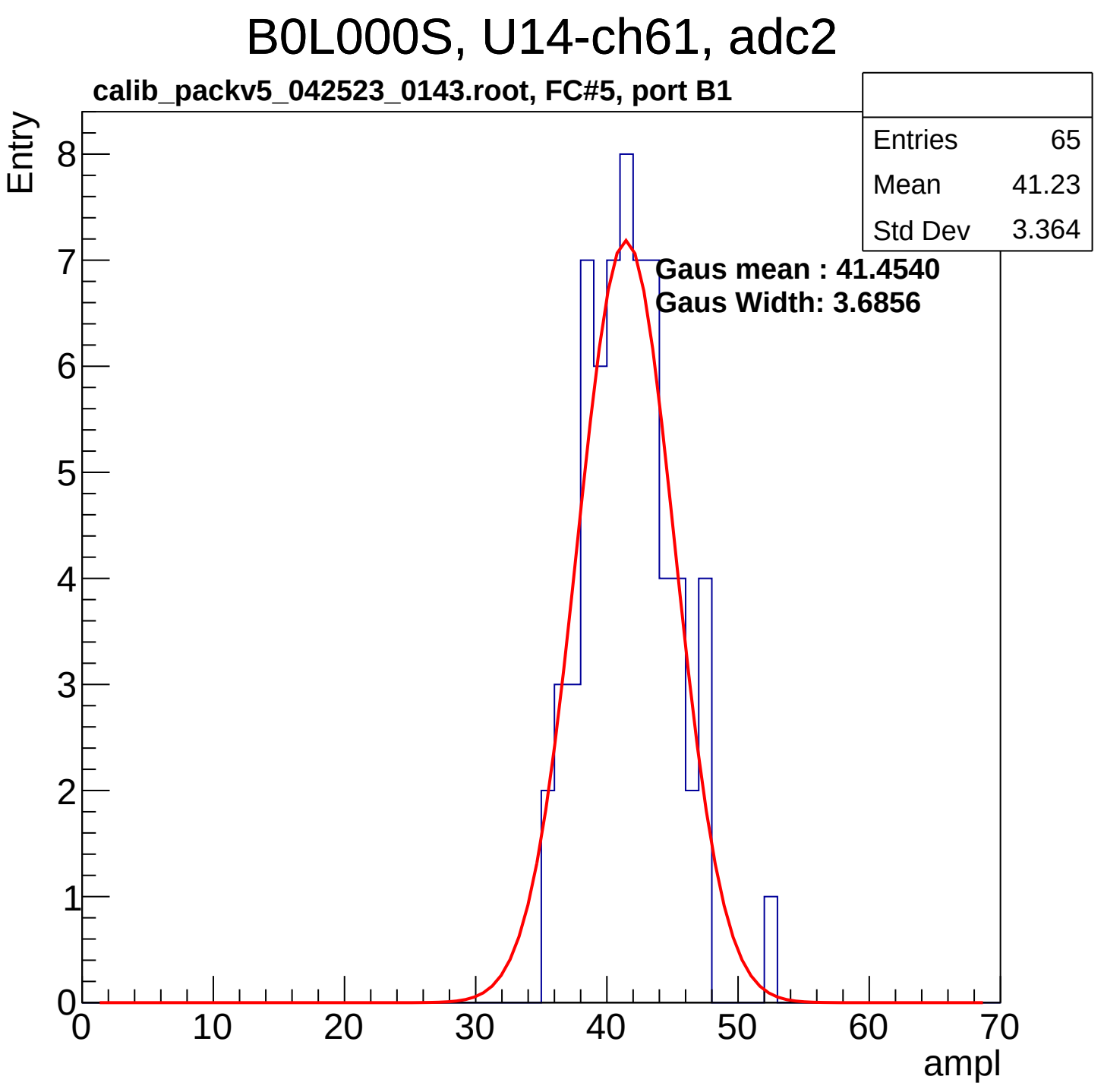
B0L000S, U14-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.23
Std Dev 3.364
Gaus mean : 41.4540
Gaus Width: 3.6856
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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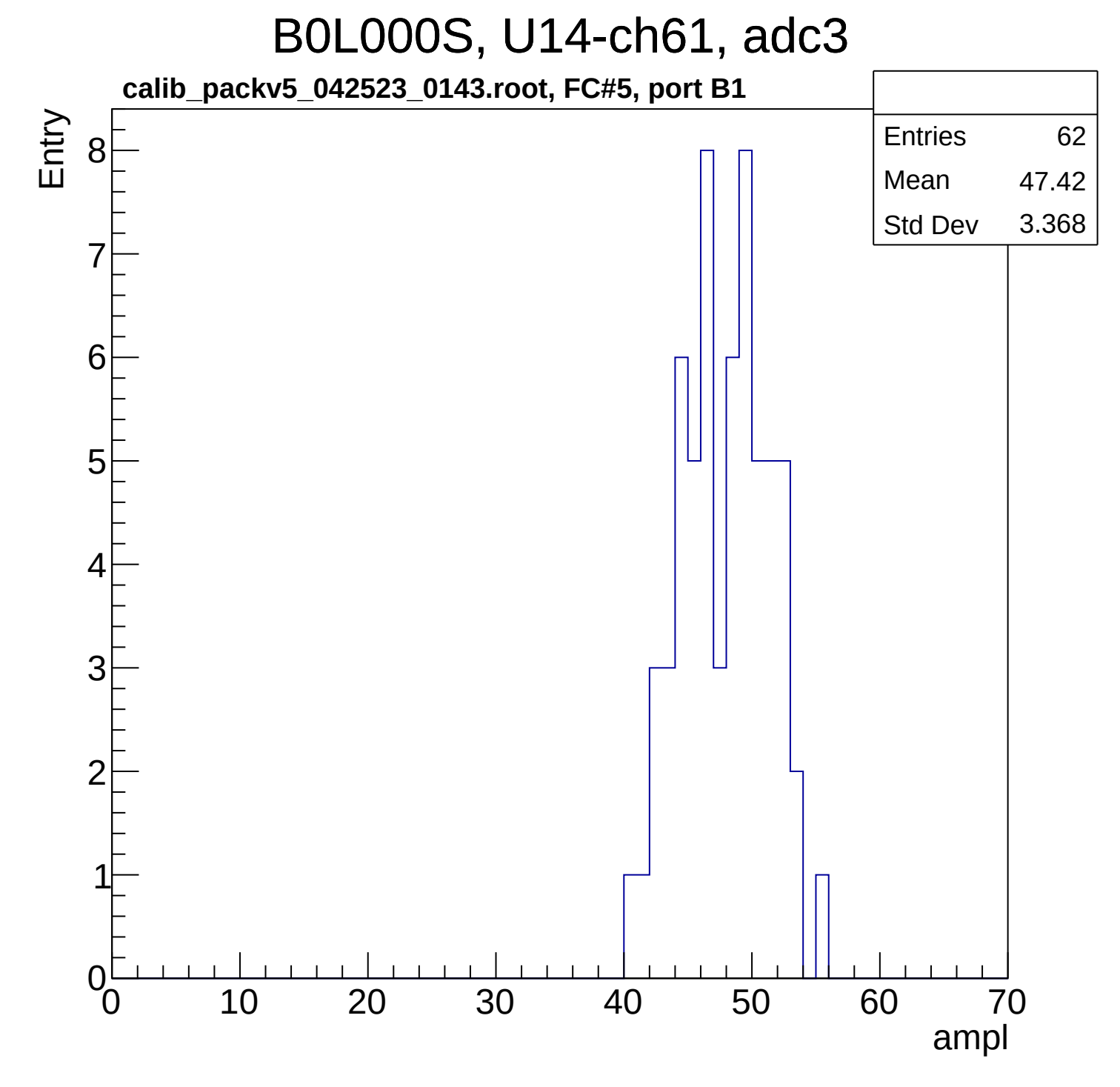
B0L000S, U14-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 47.42
Std Dev 3.368
Entry
ampl
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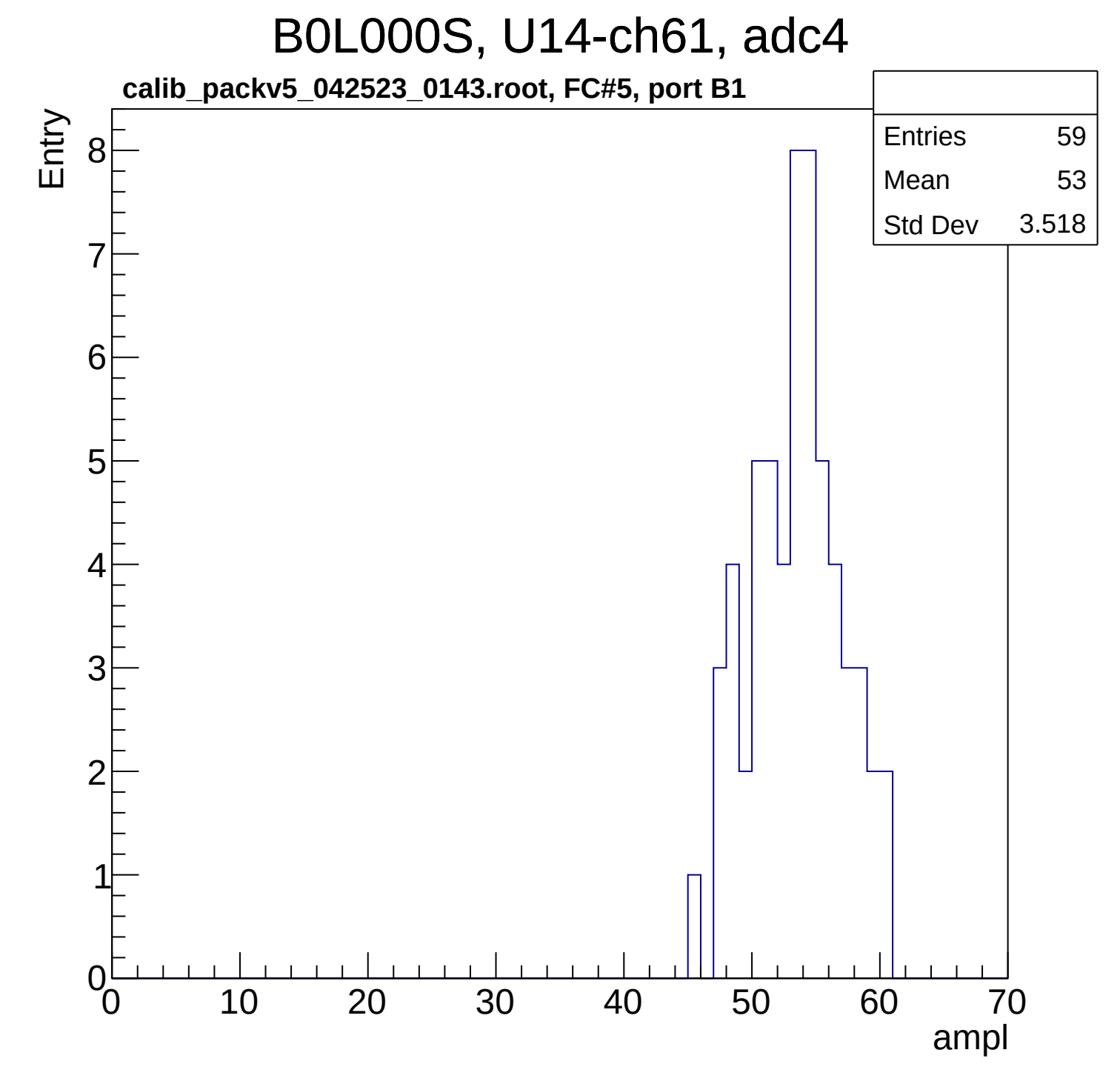
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53
Std Dev 3.518
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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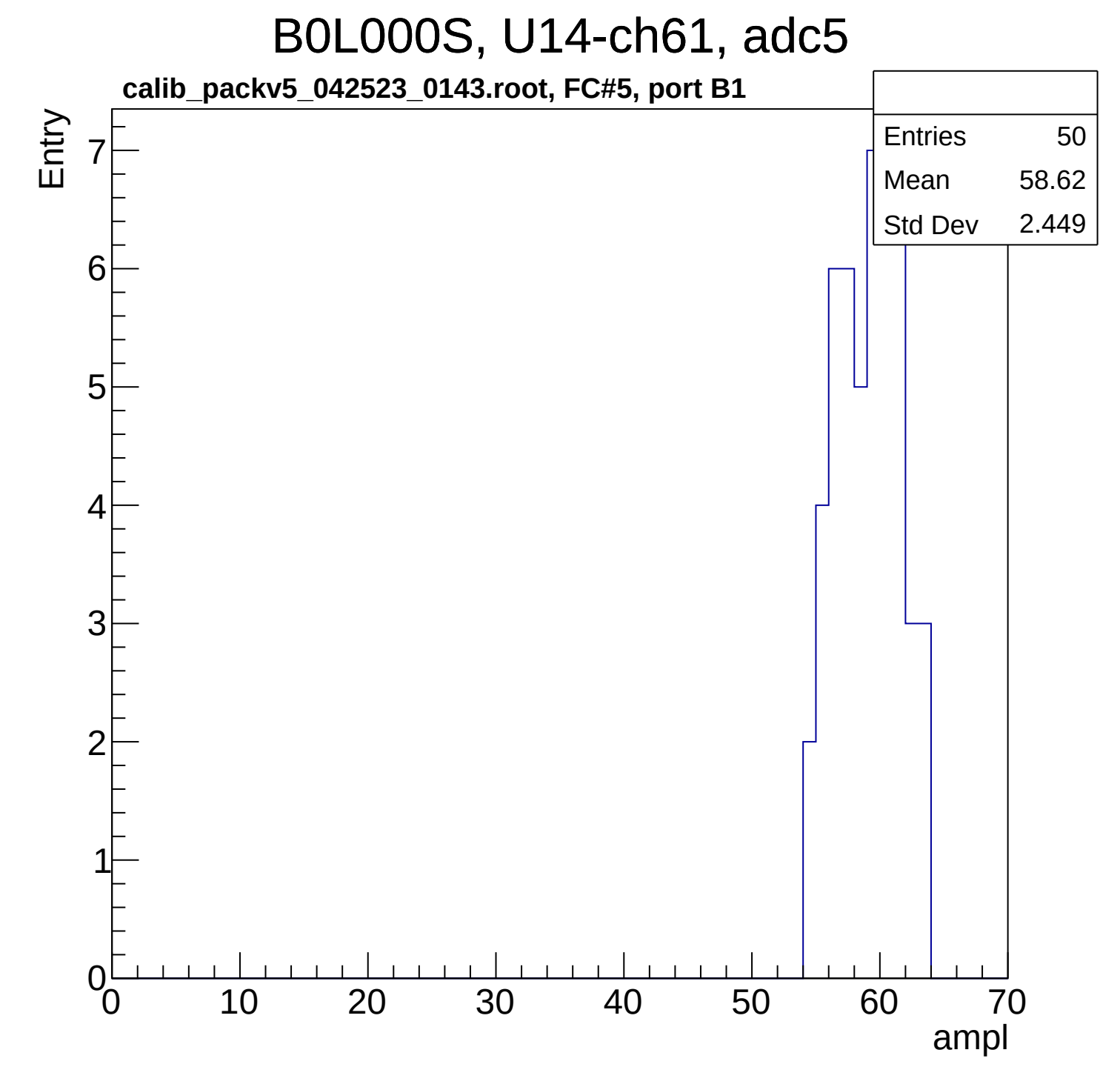
B0L000S, U14-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.62
Std Dev 2.449
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
40
50
60
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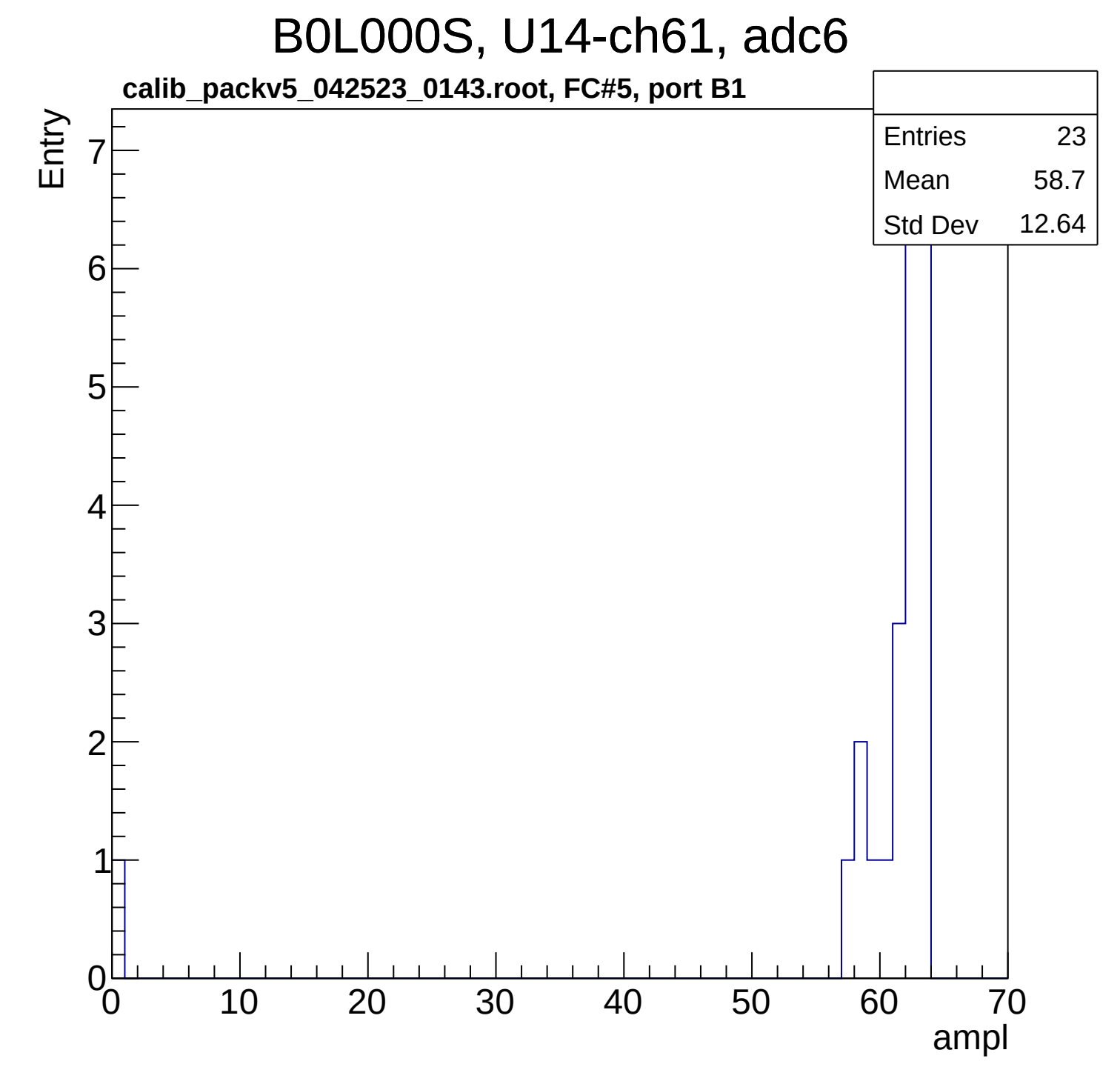
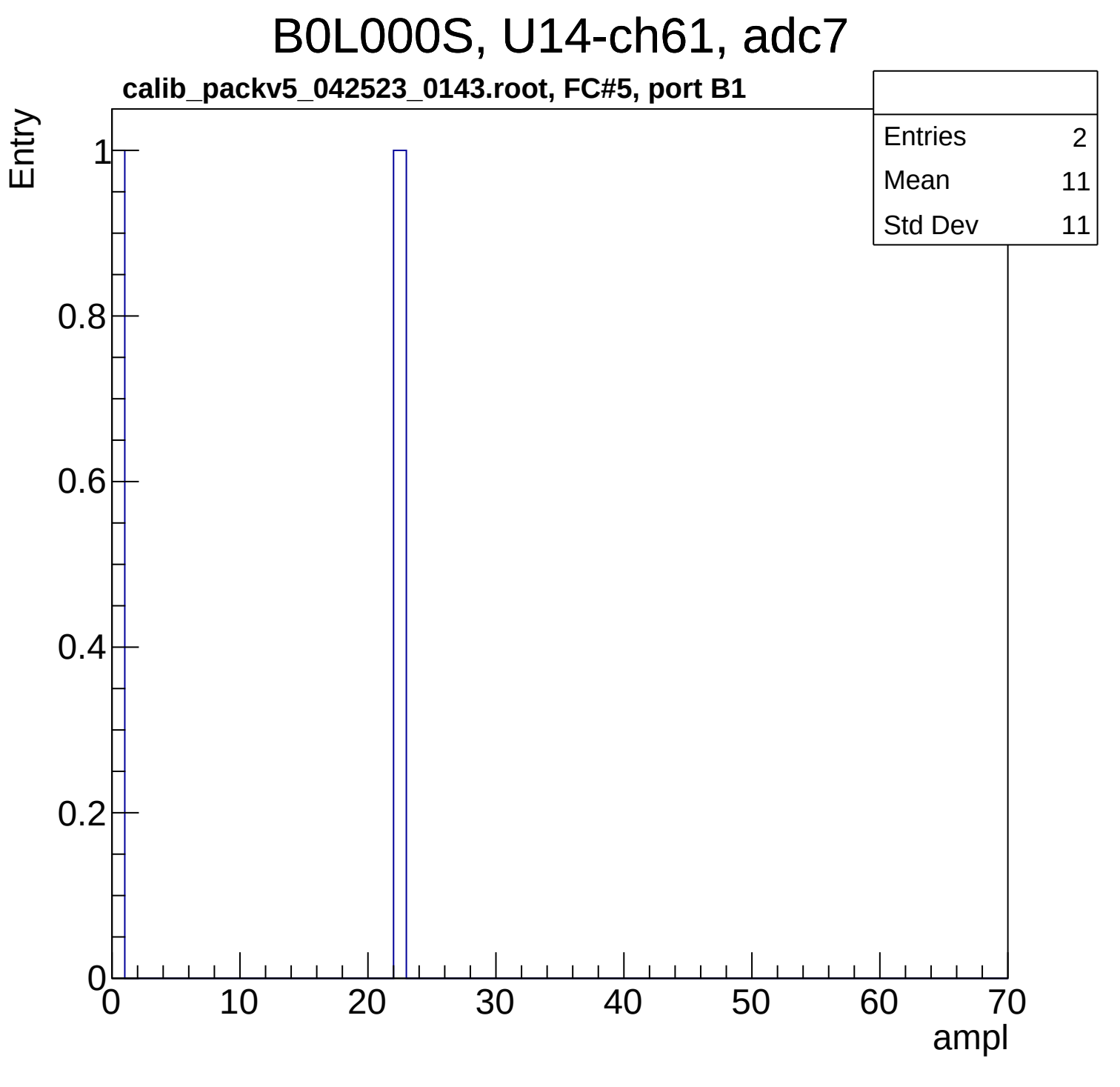
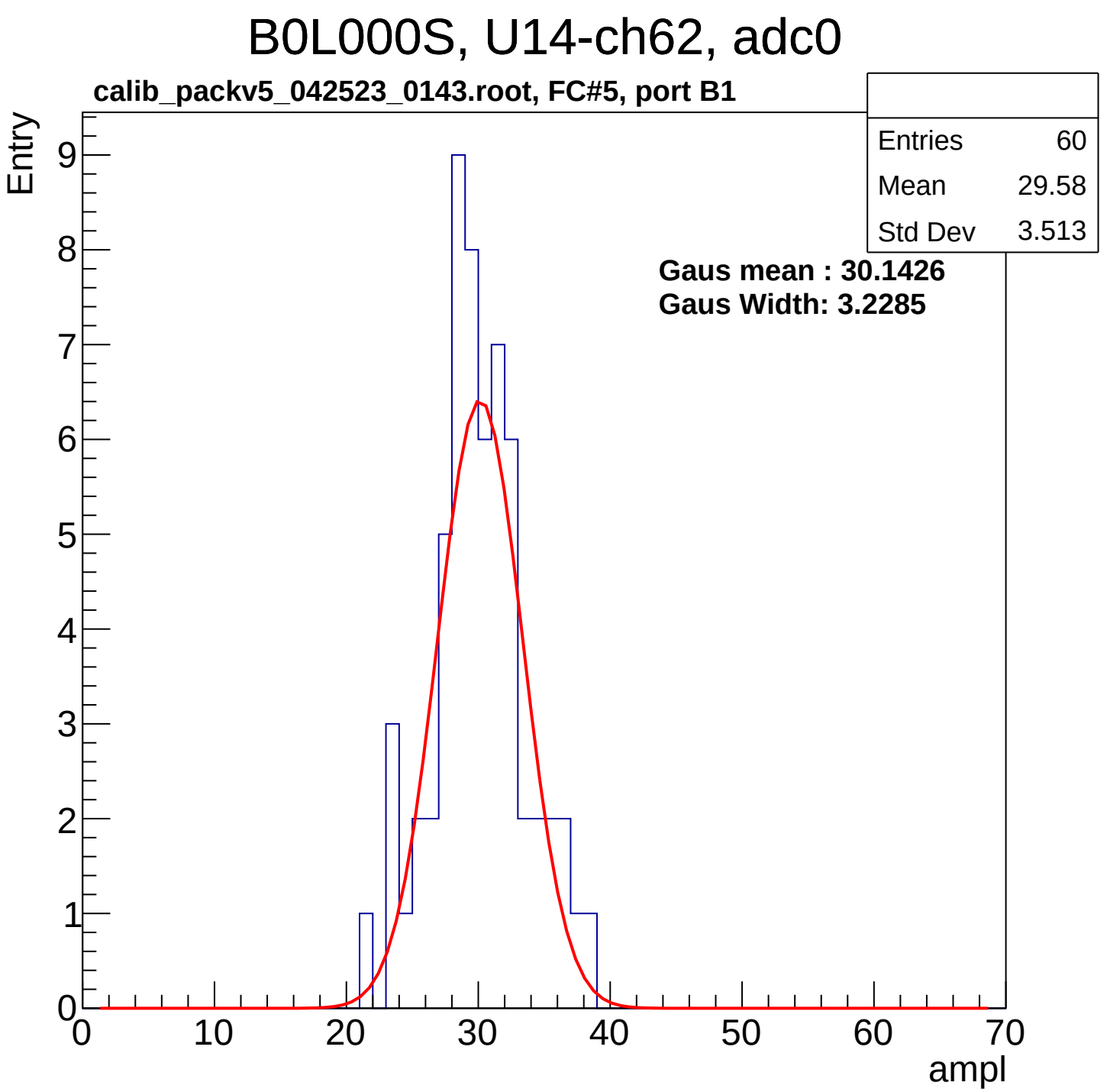
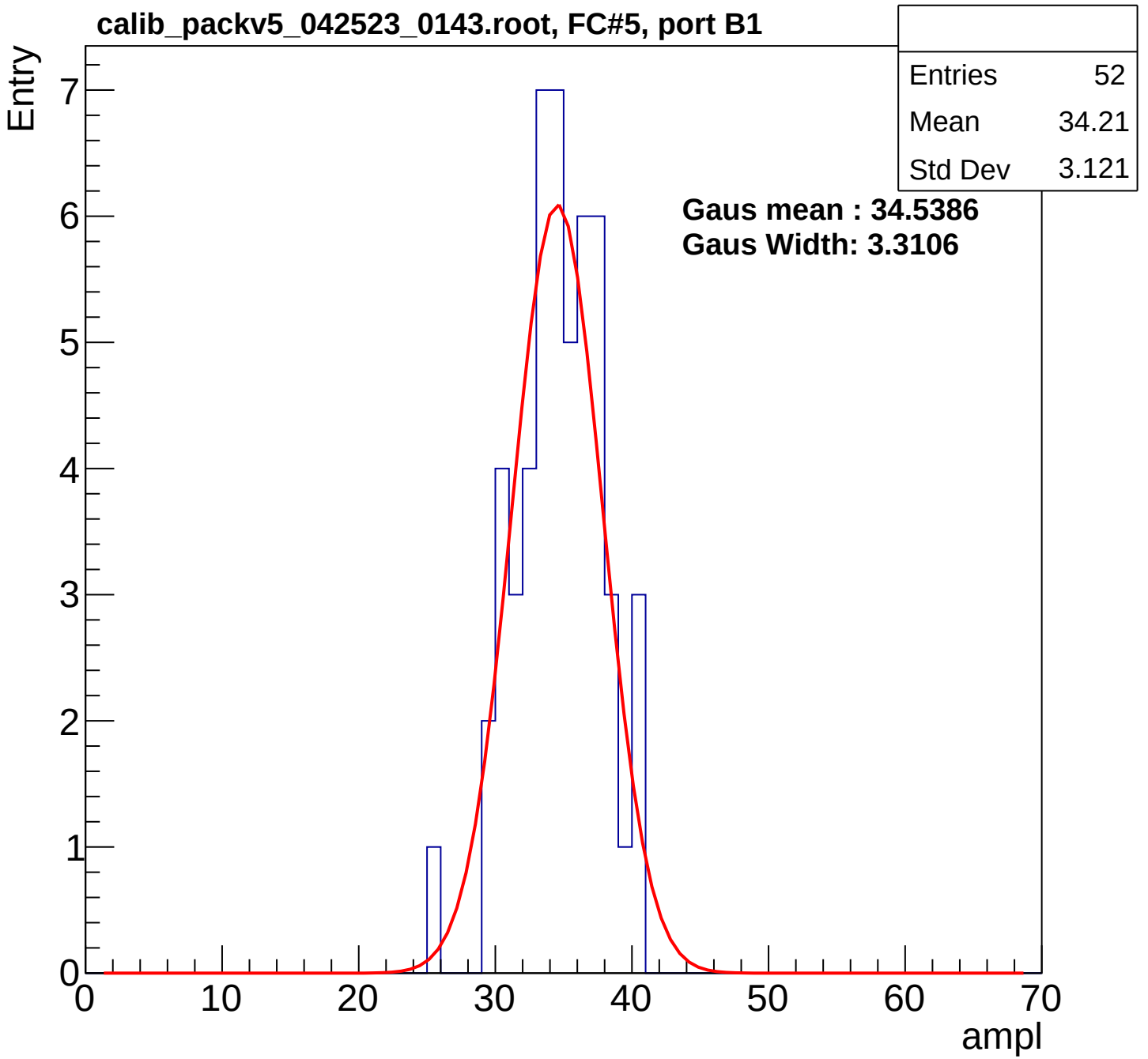
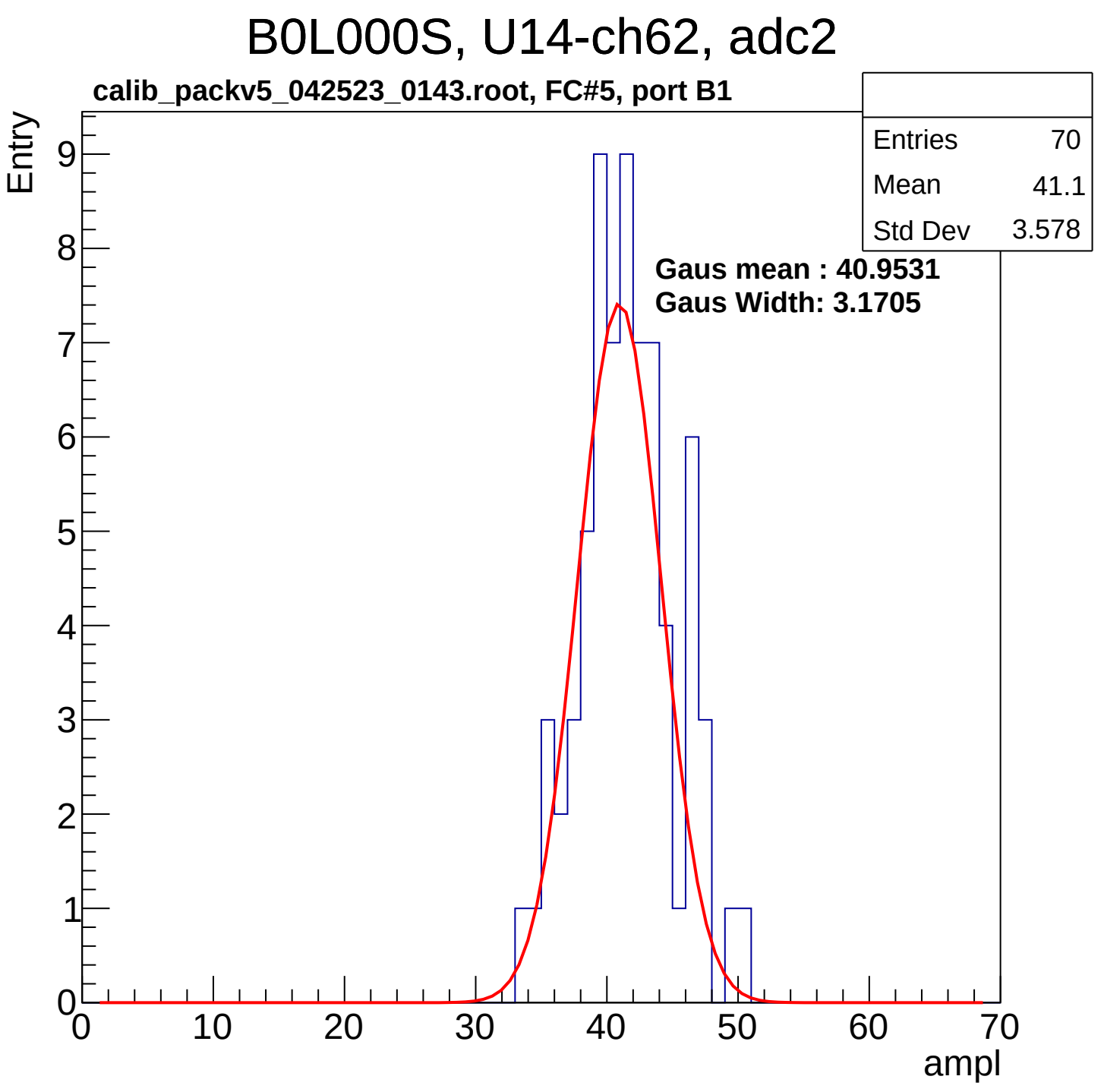

B0L000S, U14-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 58.7
Std Dev 12.64
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.58
Std Dev 3.513
Gaus mean : 30.1426
Gaus Width: 3.2285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 34.21
Std Dev 3.121
Gaus mean : 34.5386
Gaus Width: 3.3106
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 41.1
Std Dev 3.578
Gaus mean : 40.9531
Gaus Width: 3.1705
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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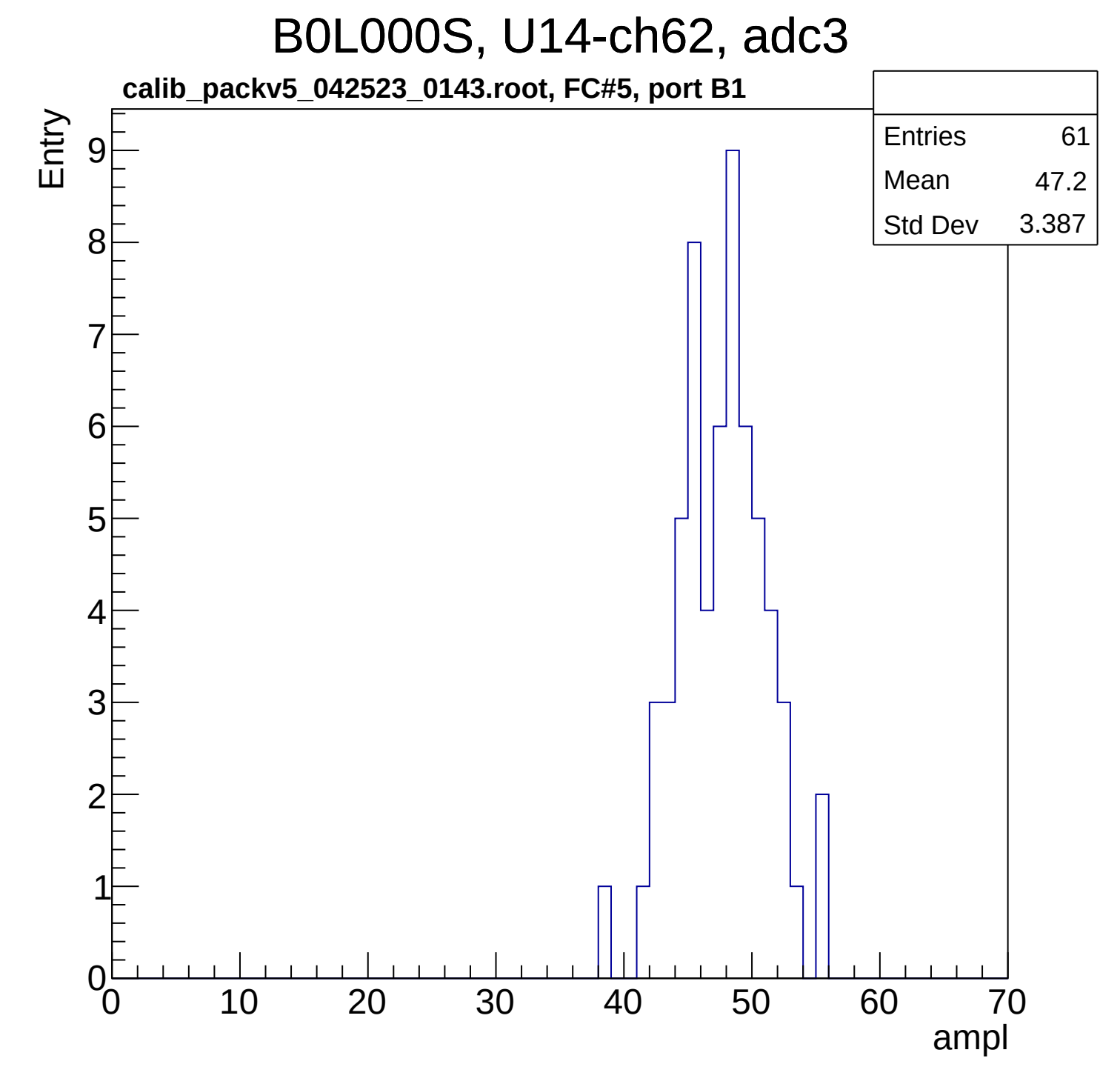
B0L000S, U14-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.2
Std Dev 3.387
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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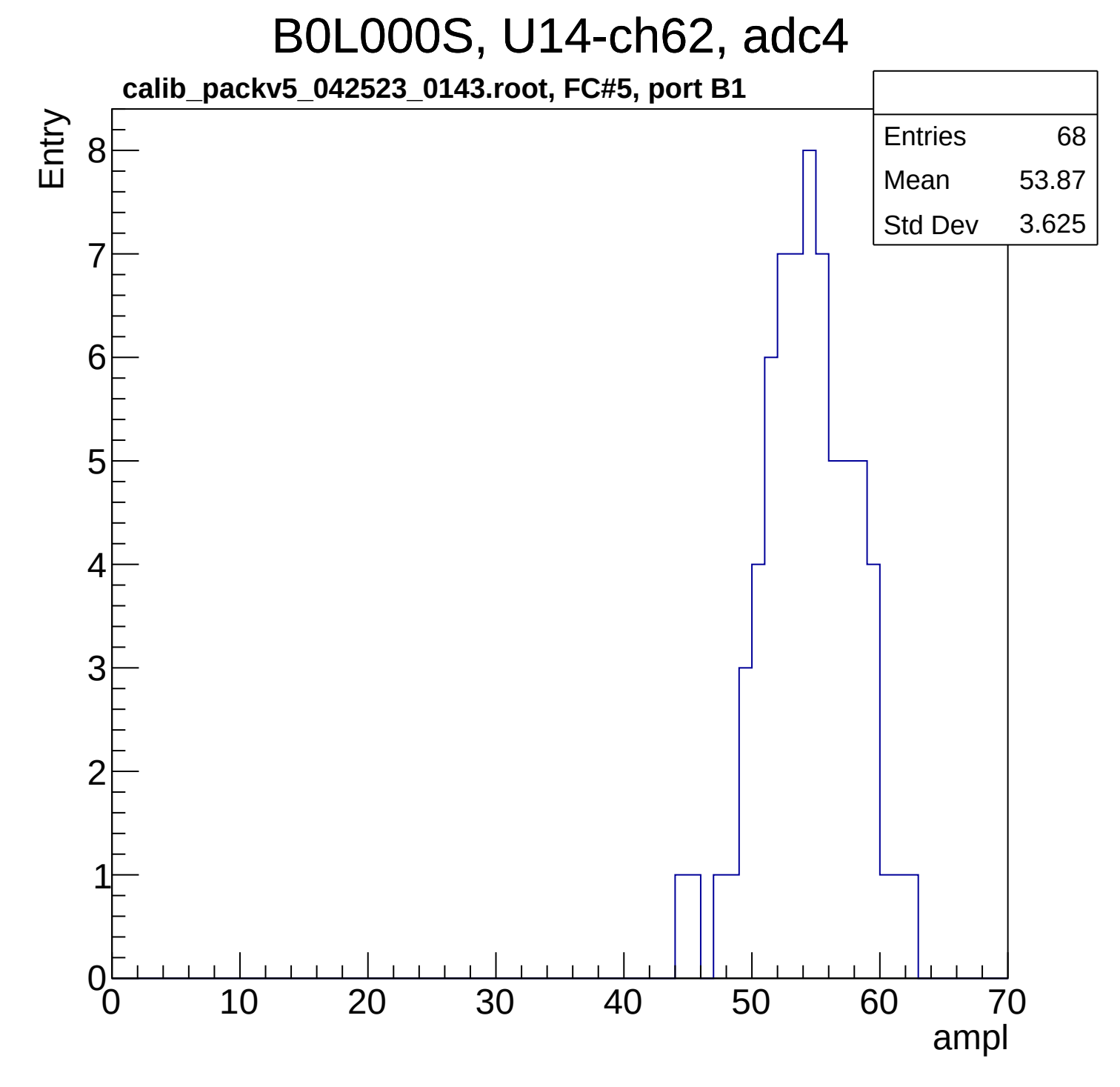
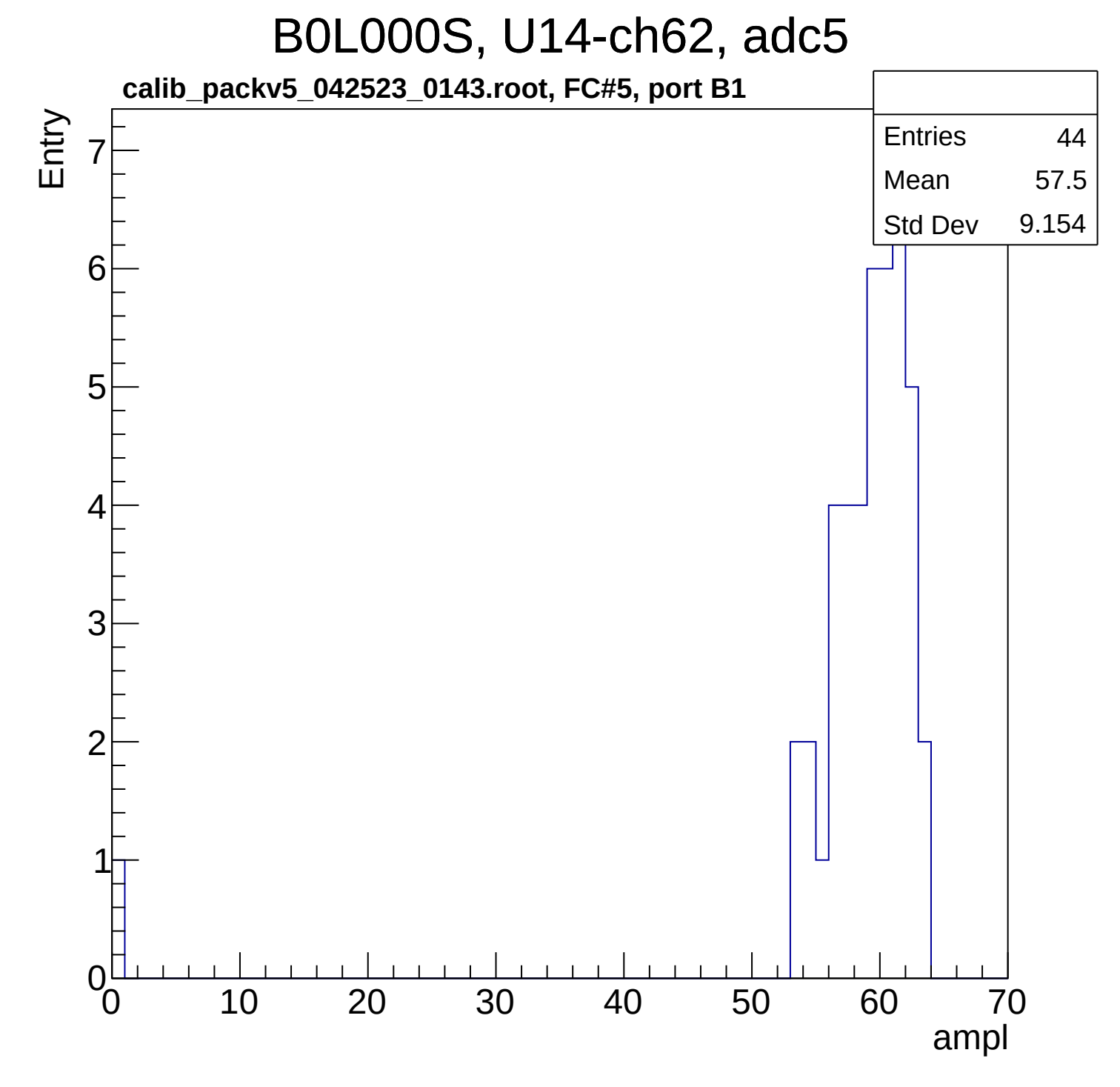
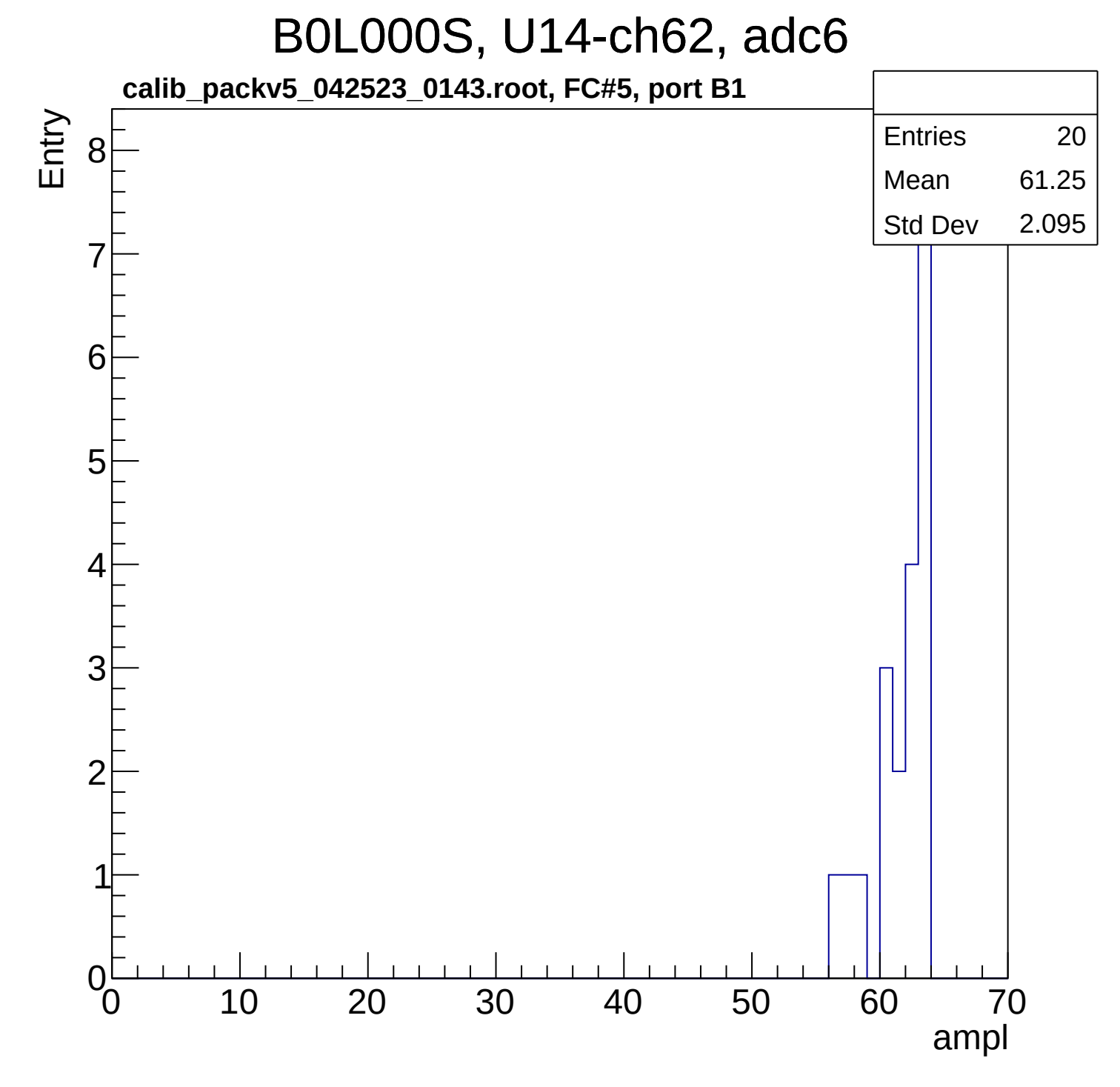
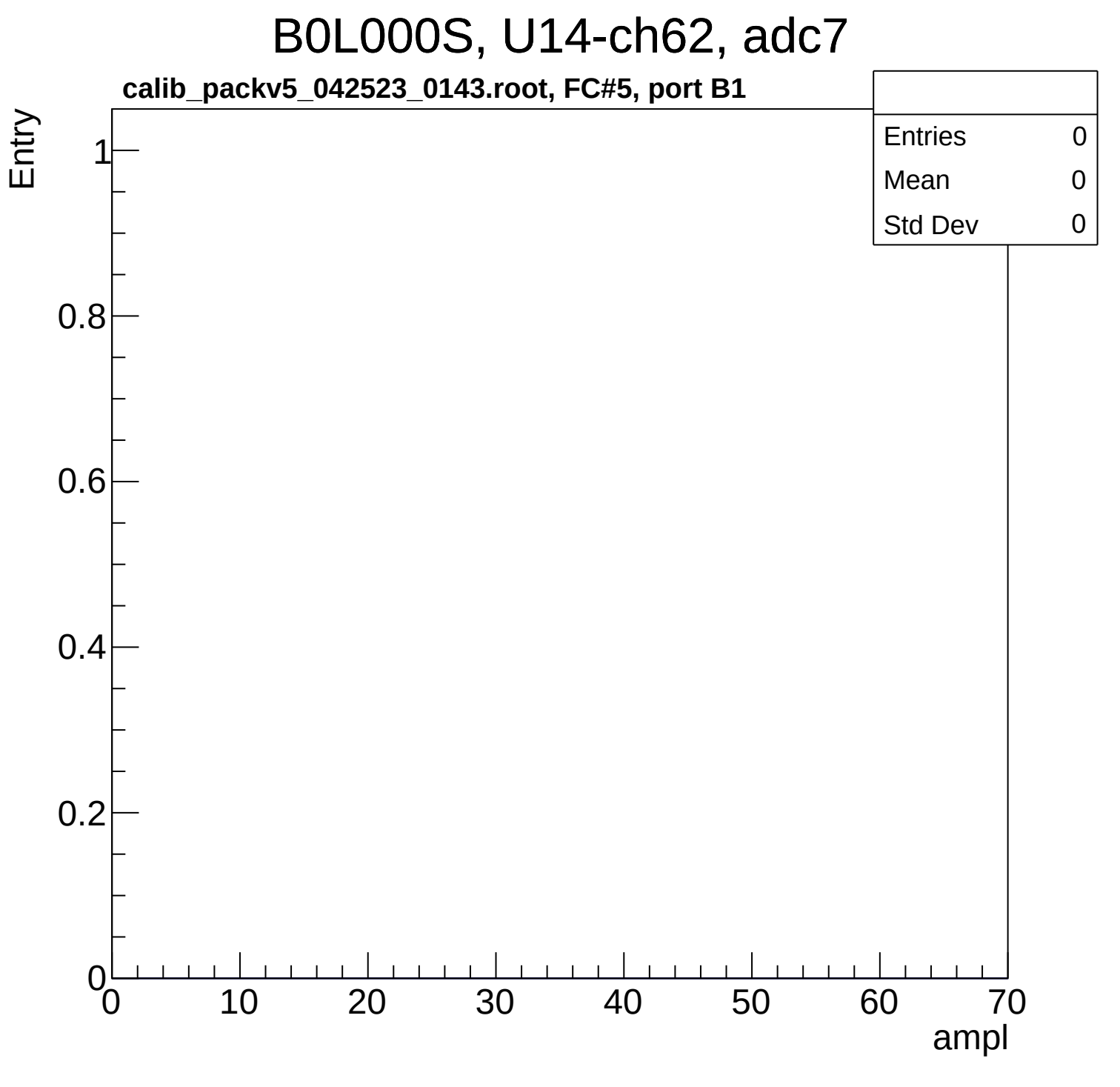
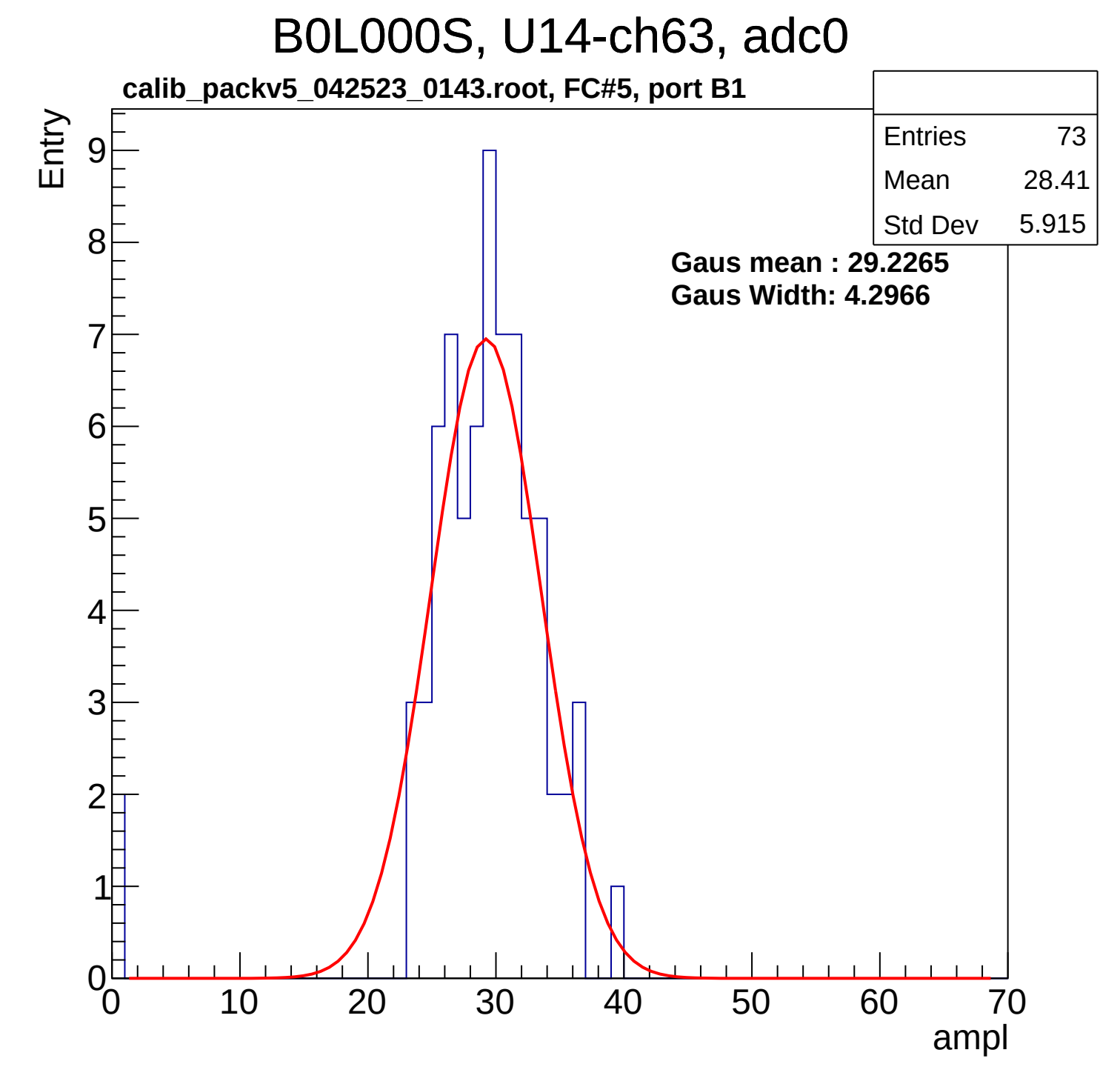
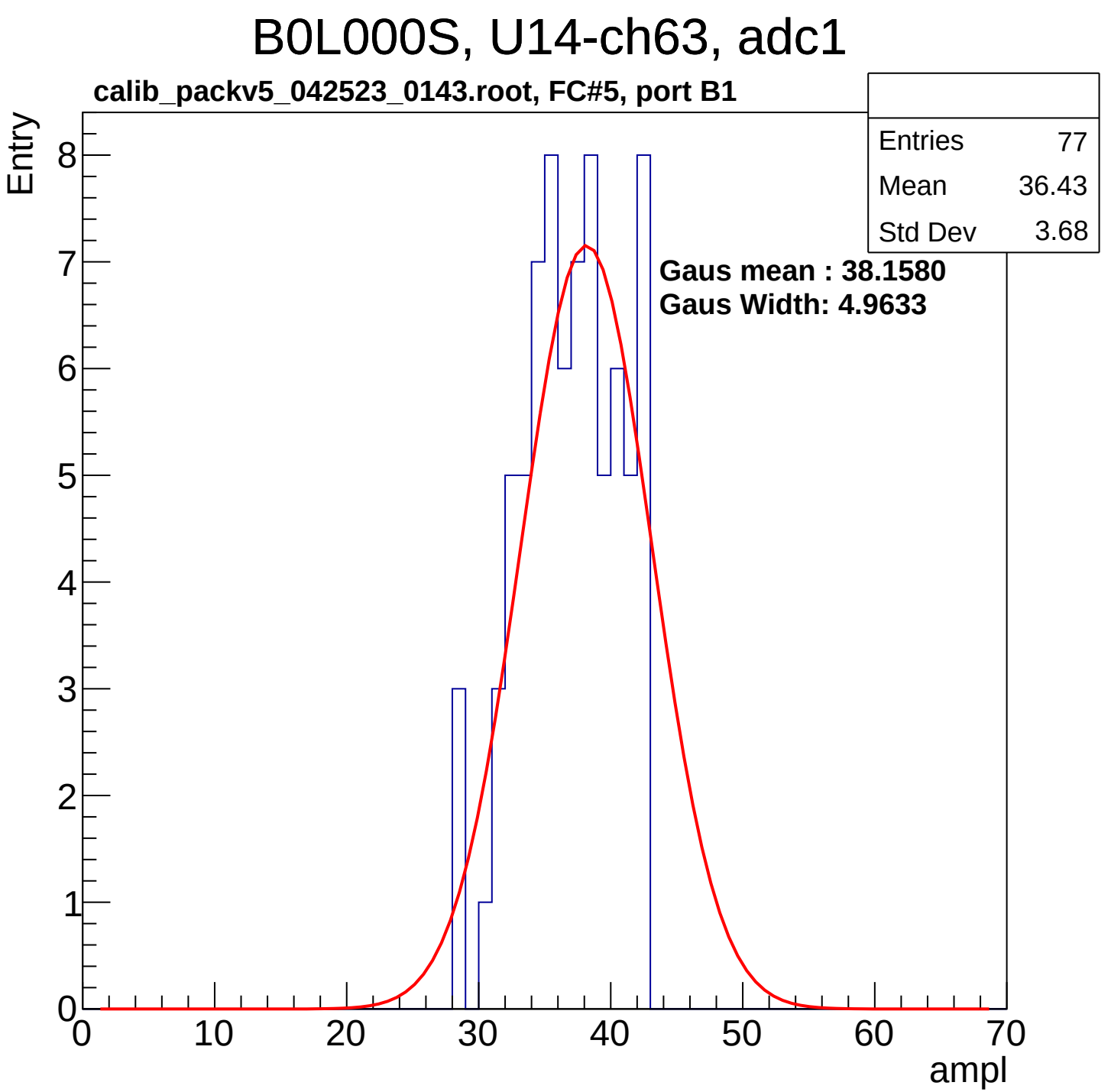

B0L000S, U14-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 53.87
Std Dev 3.625
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

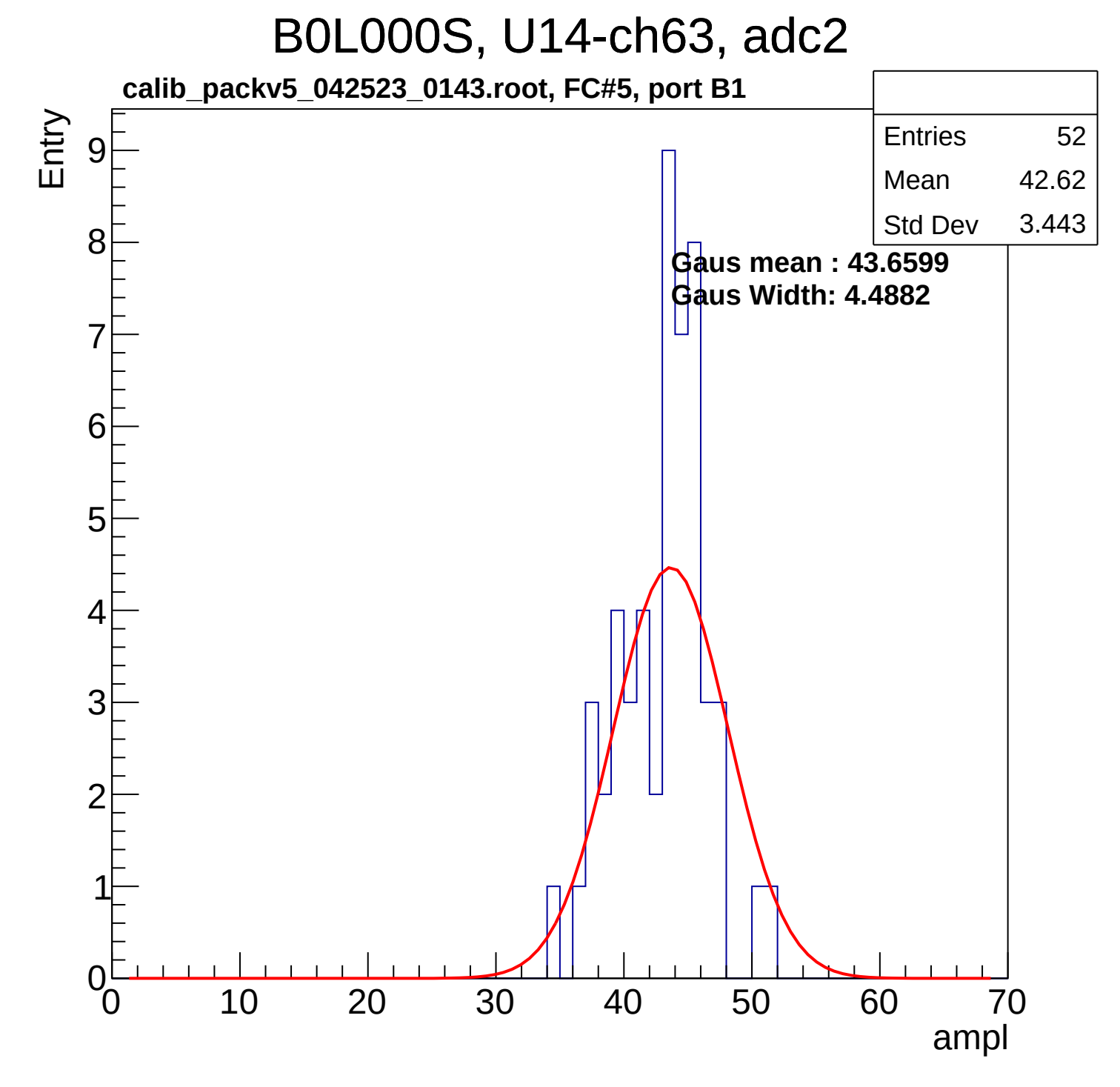

B0L000S, U14-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 57.5
Std Dev 9.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

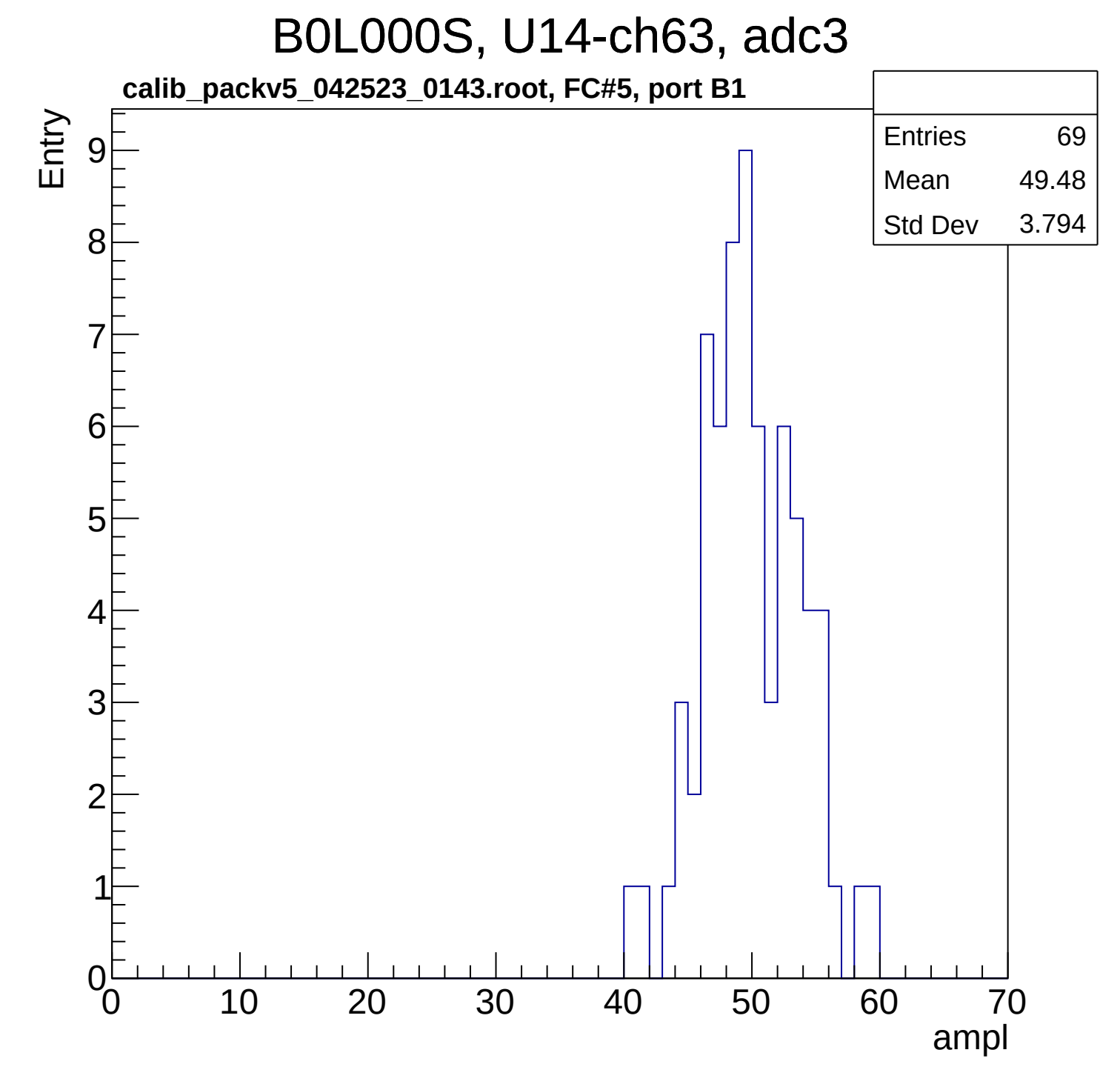

B0L000S, U14-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 61.25
Std Dev 2.095
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

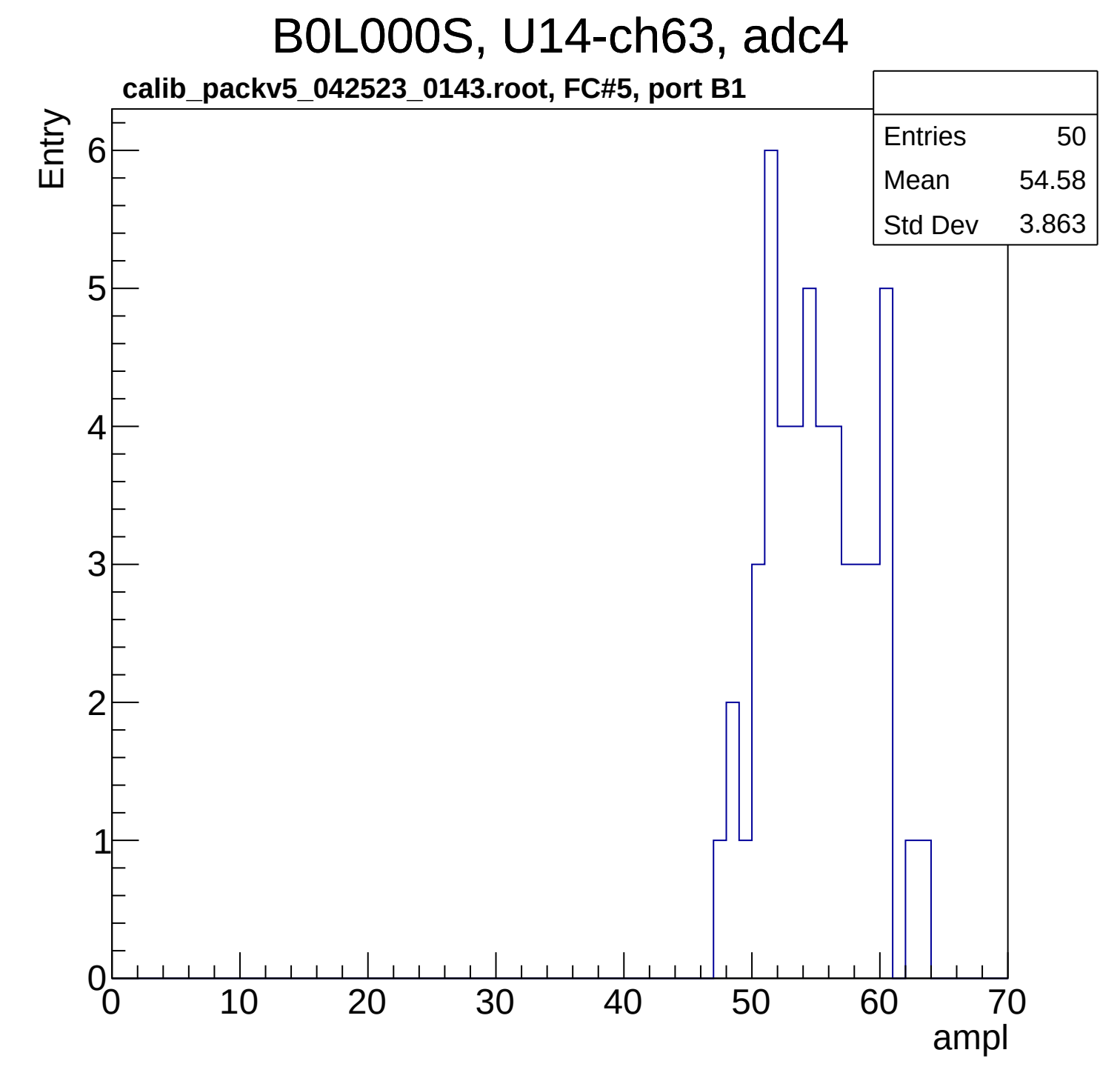

B0L000S, U14-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

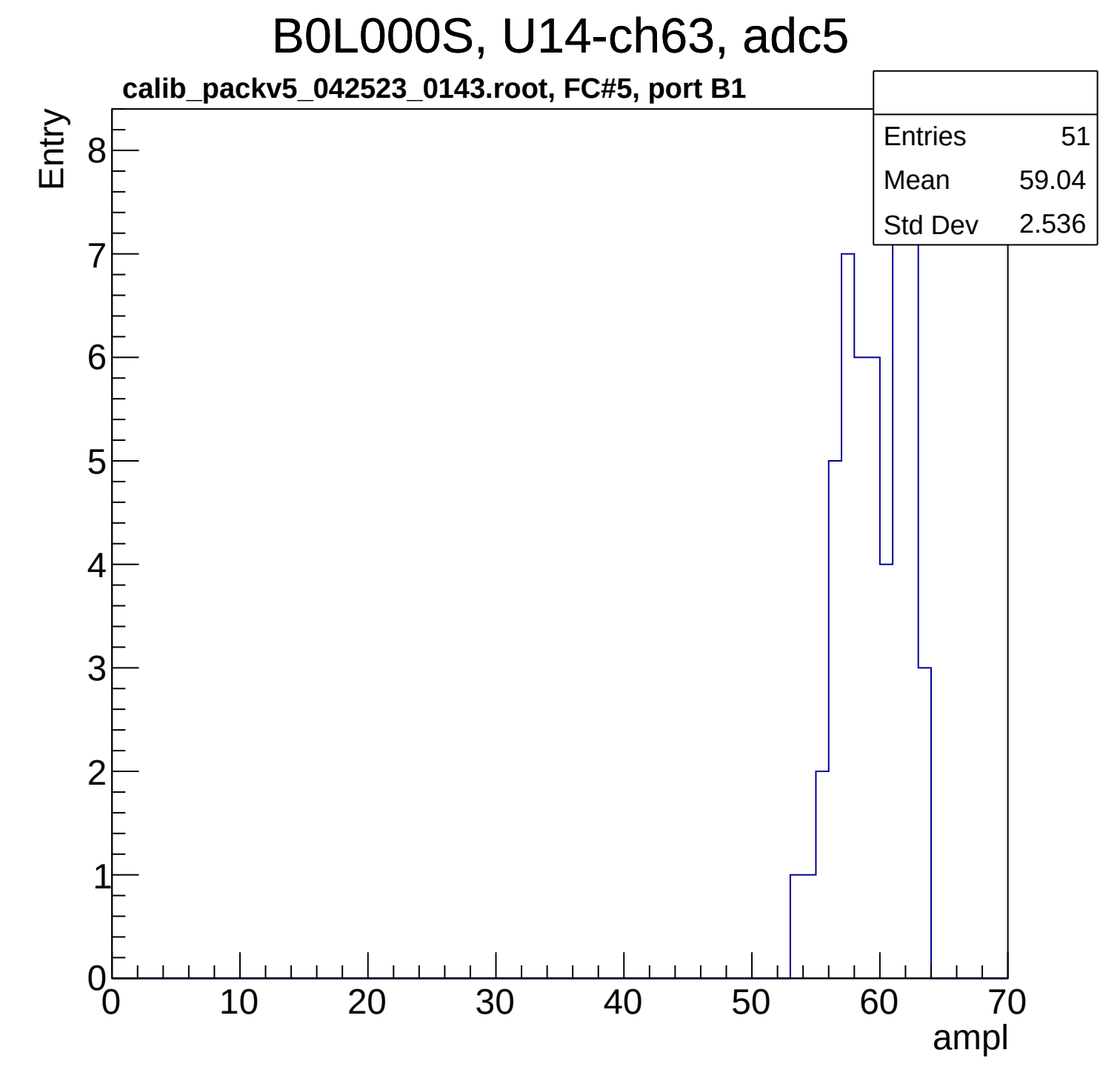

B0L000S, U14-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.41
Std Dev 5.915
Gaus mean : 29.2265
Gaus Width: 4.2966
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.43
Std Dev 3.68
Gaus mean : 38.1580
Gaus Width: 4.9633
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.62
Std Dev 3.443
Gaus mean : 43.6599
Gaus Width: 4.4882
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.48
Std Dev 3.794
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.58
Std Dev 3.863
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.04
Std Dev 2.536
Entry
ampl
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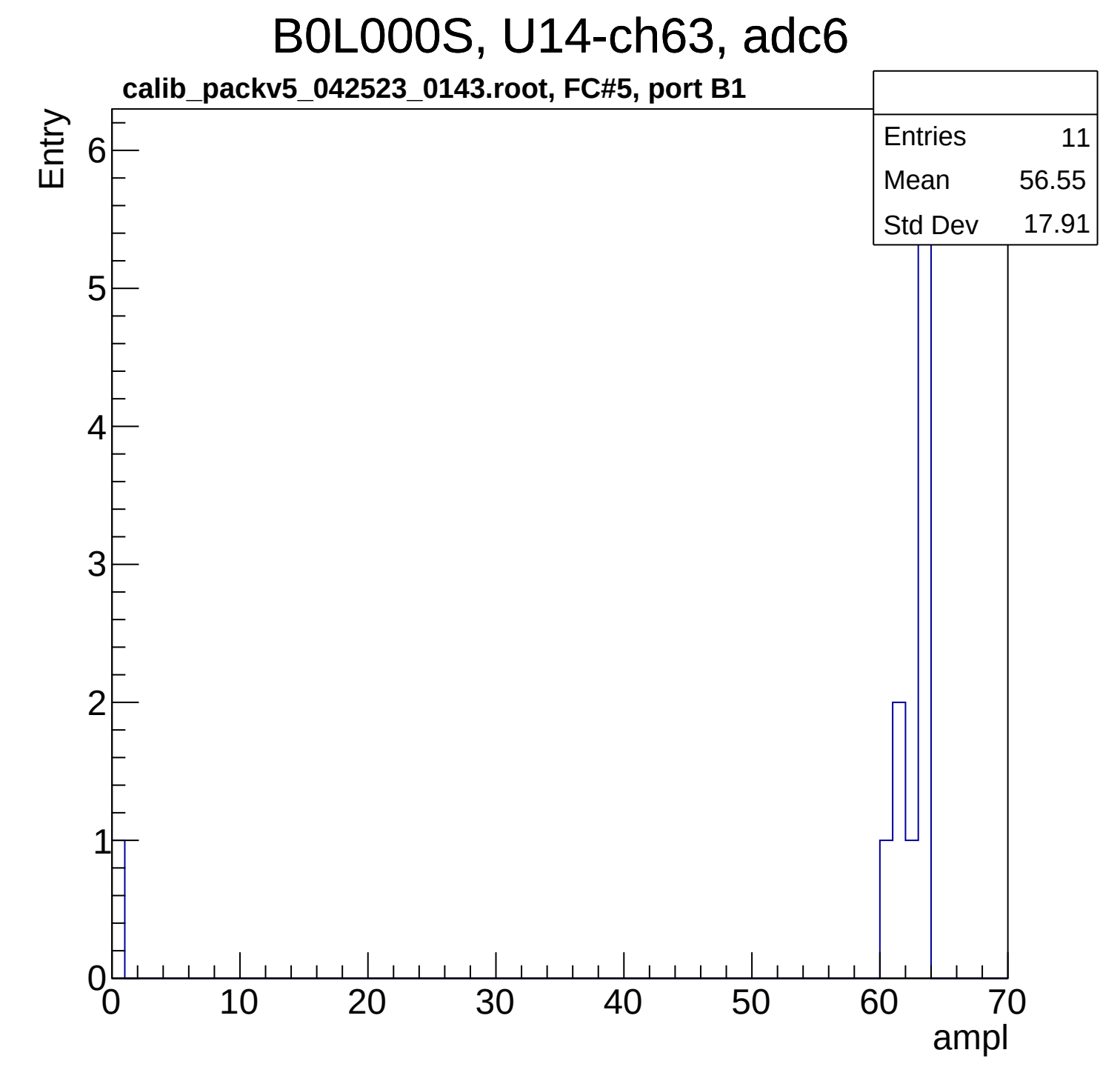
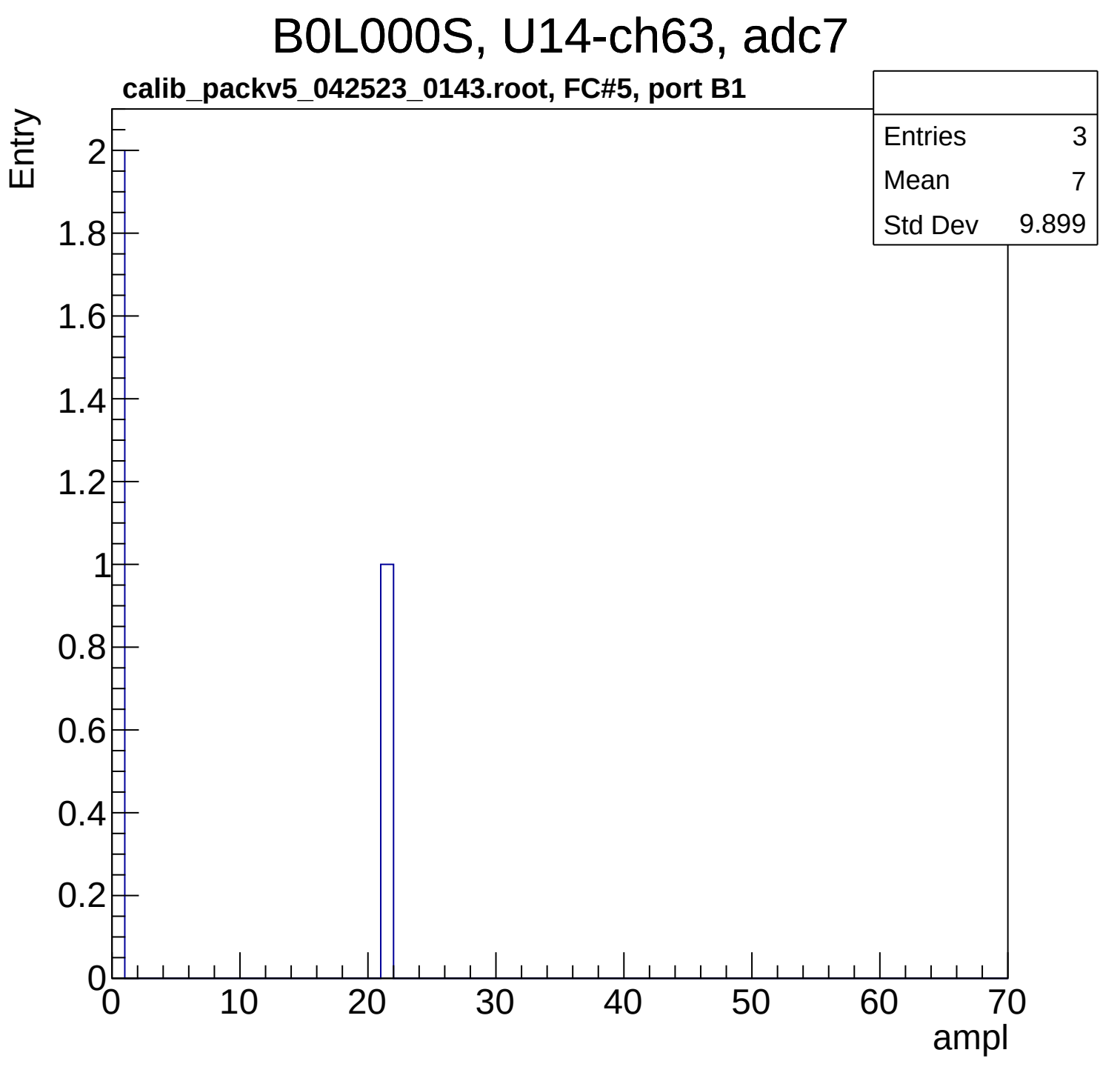
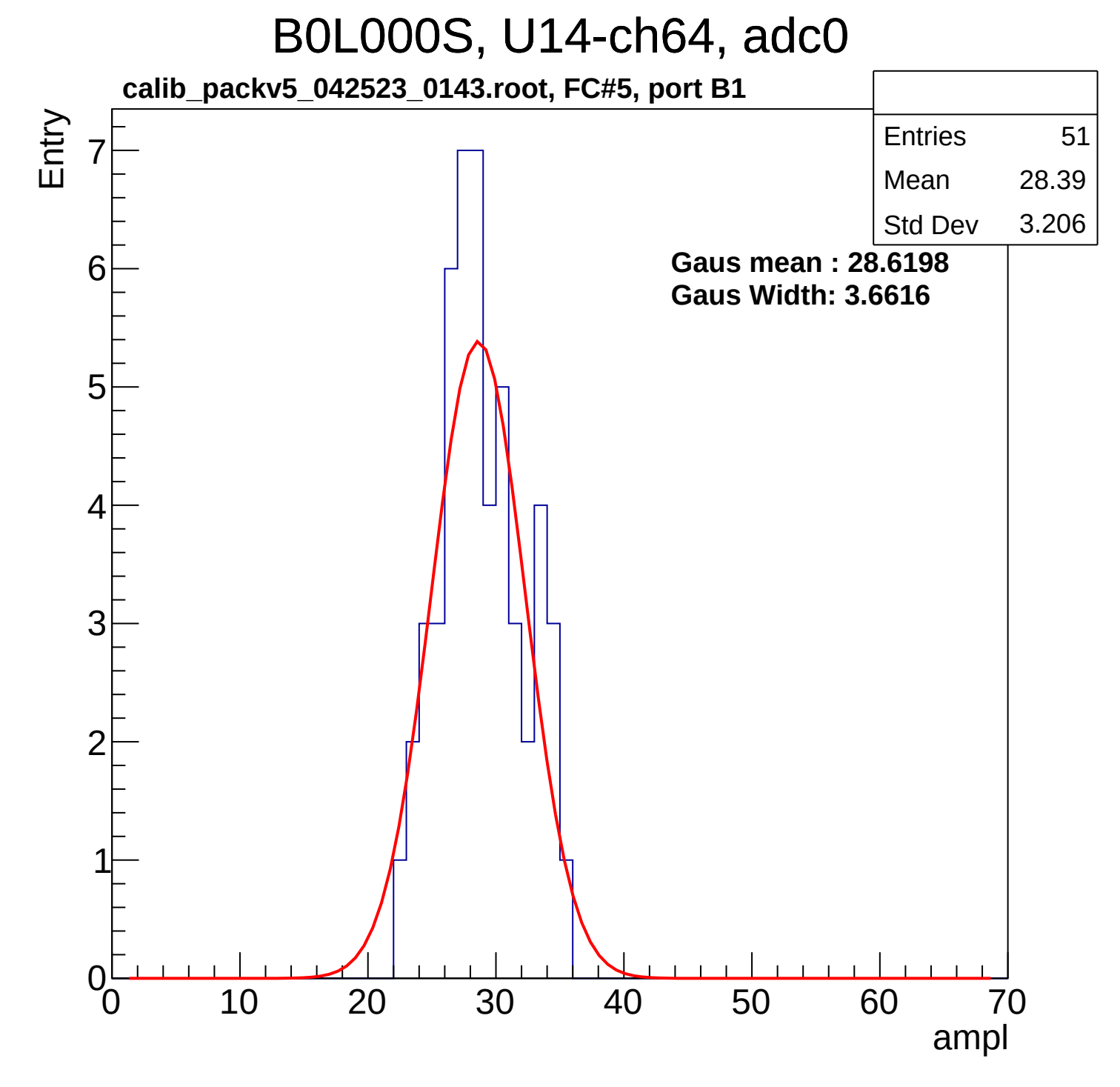
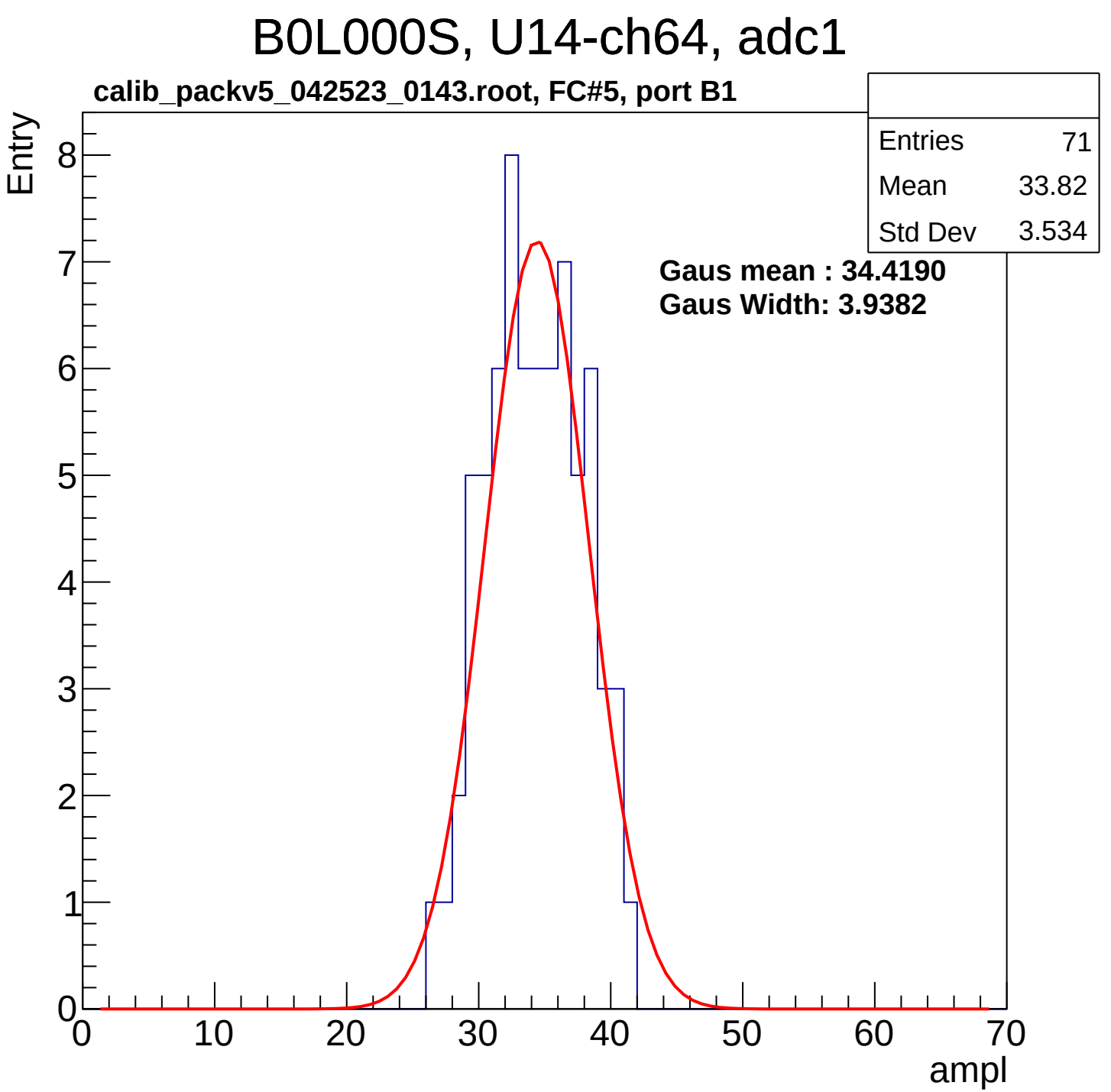
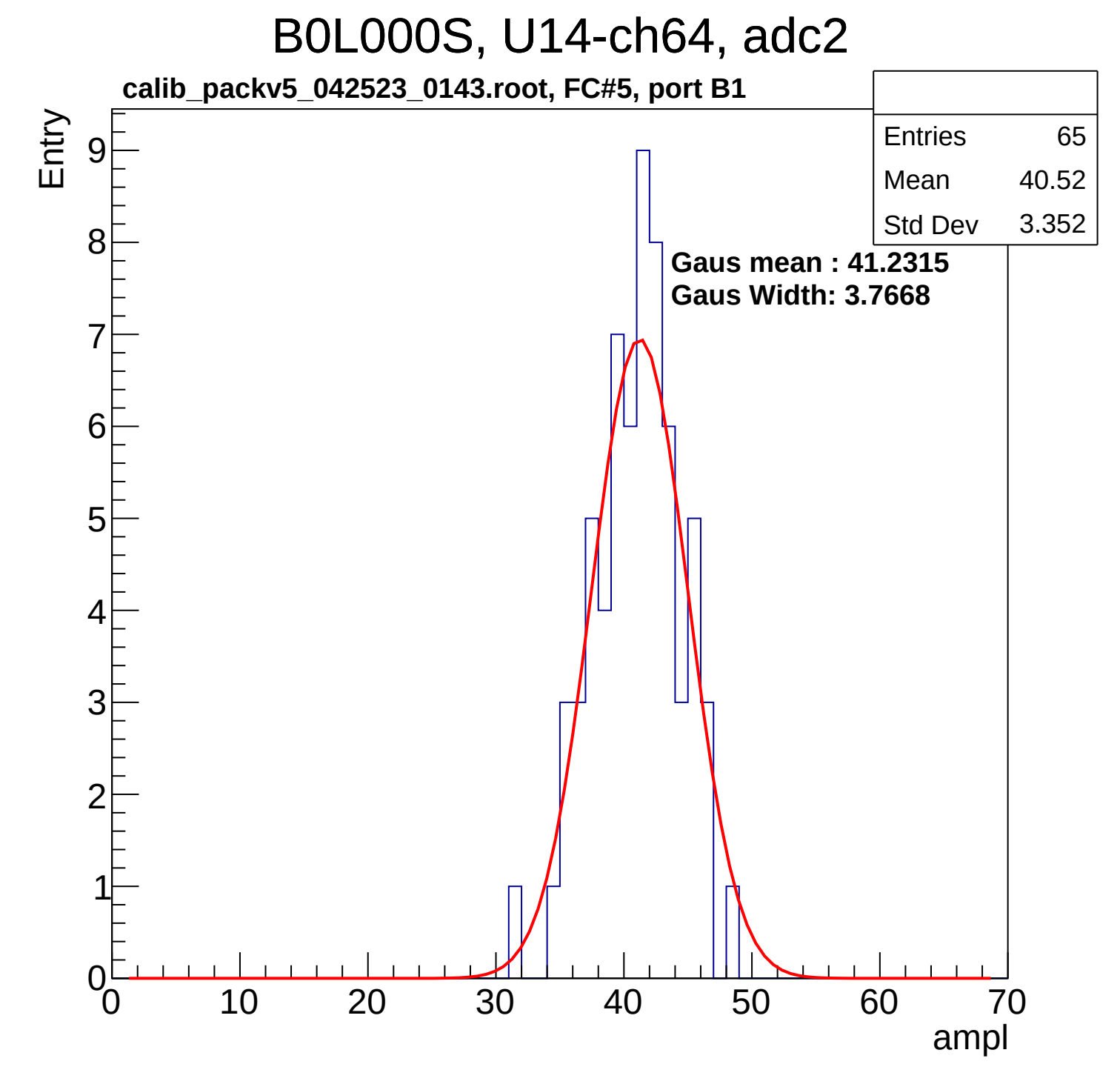
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

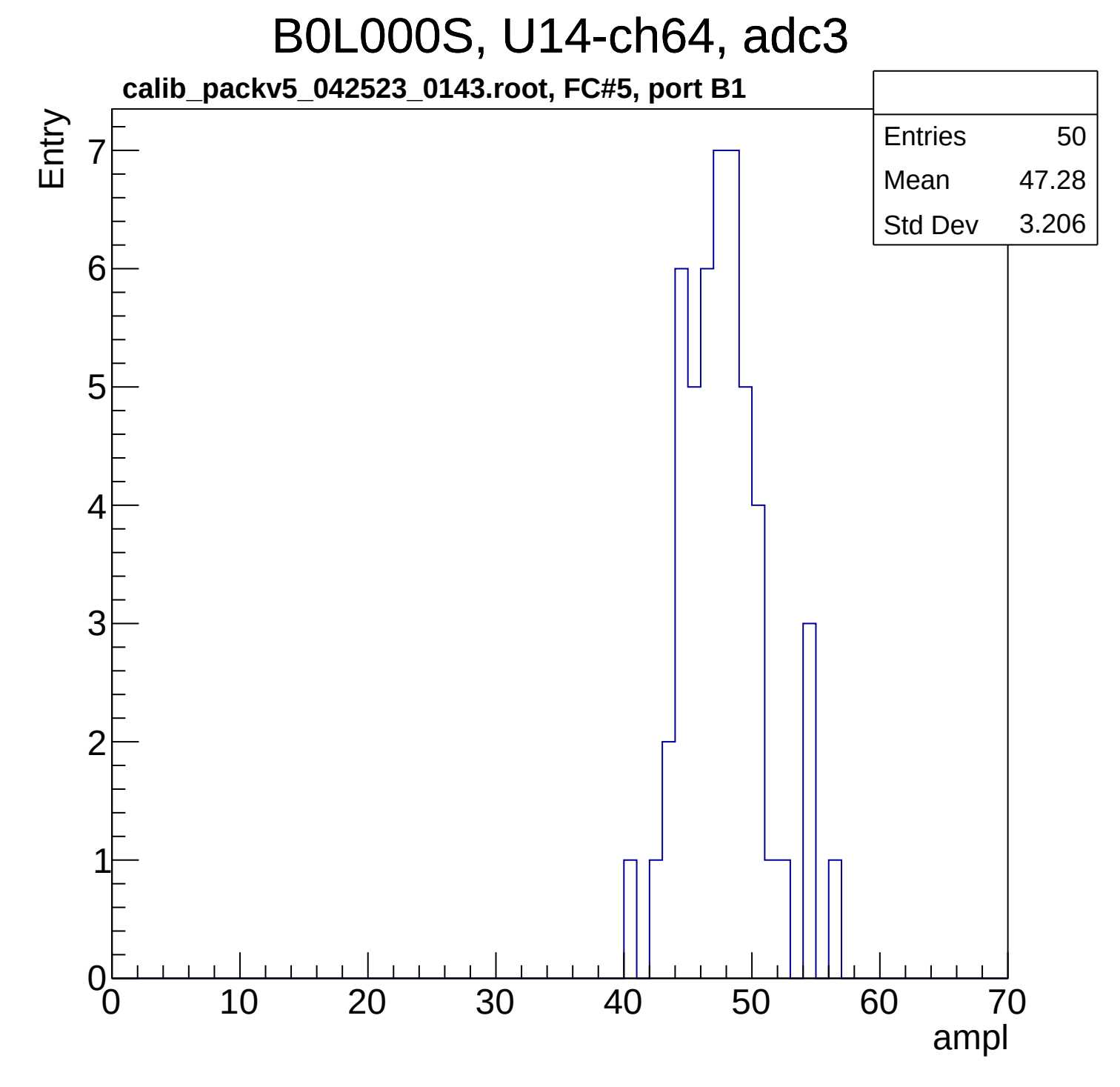

B0L000S, U14-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 56.55
Std Dev 17.91
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

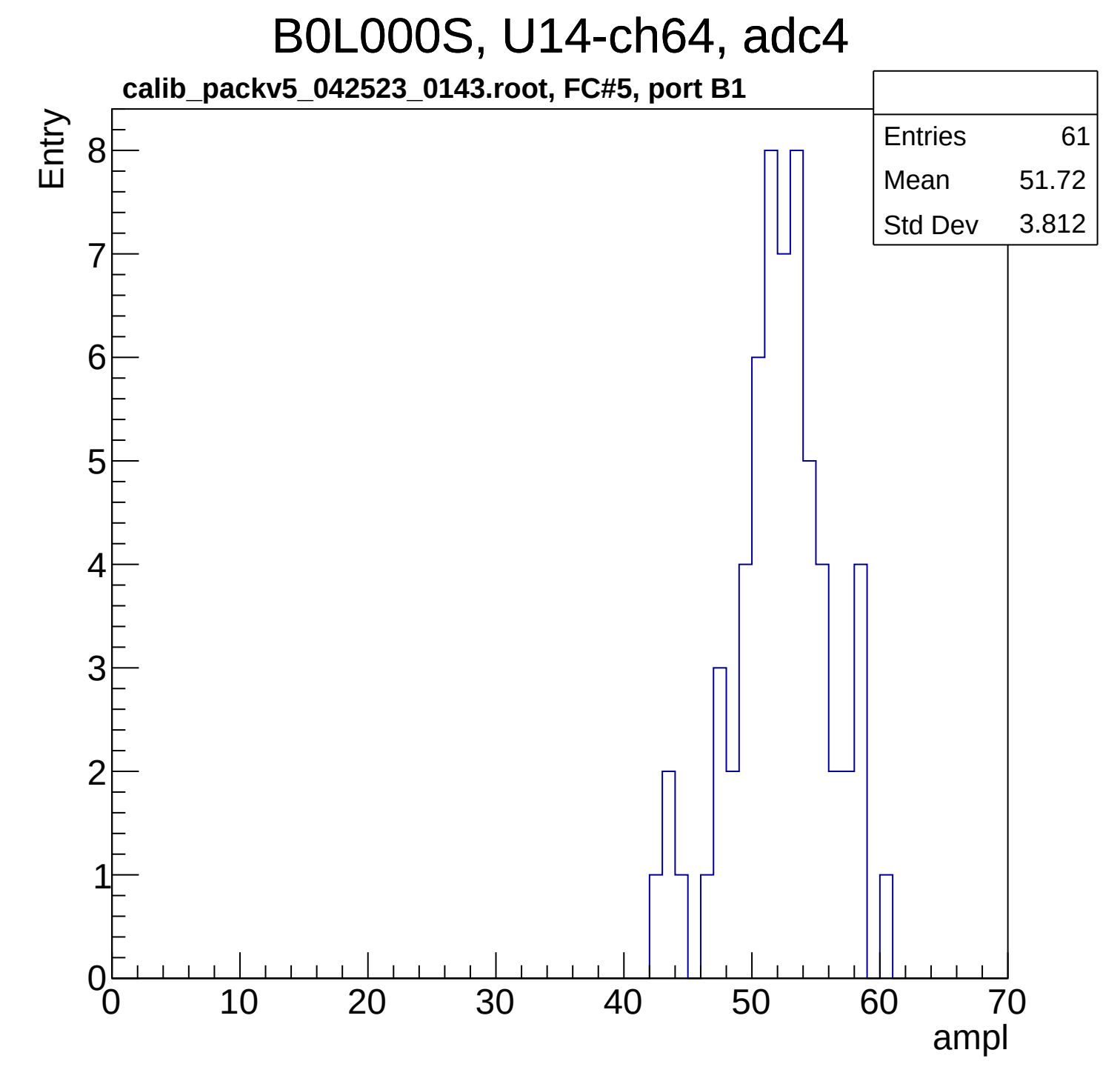

B0L000S, U14-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 28.39
Std Dev 3.206
Gaus mean : 28.6198
Gaus Width: 3.6616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 33.82
Std Dev 3.534
Gaus mean : 34.4190
Gaus Width: 3.9382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 40.52
Std Dev 3.352
Gaus mean : 41.2315
Gaus Width: 3.7668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 47.28
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 51.72
Std Dev 3.812
Entry
ampl
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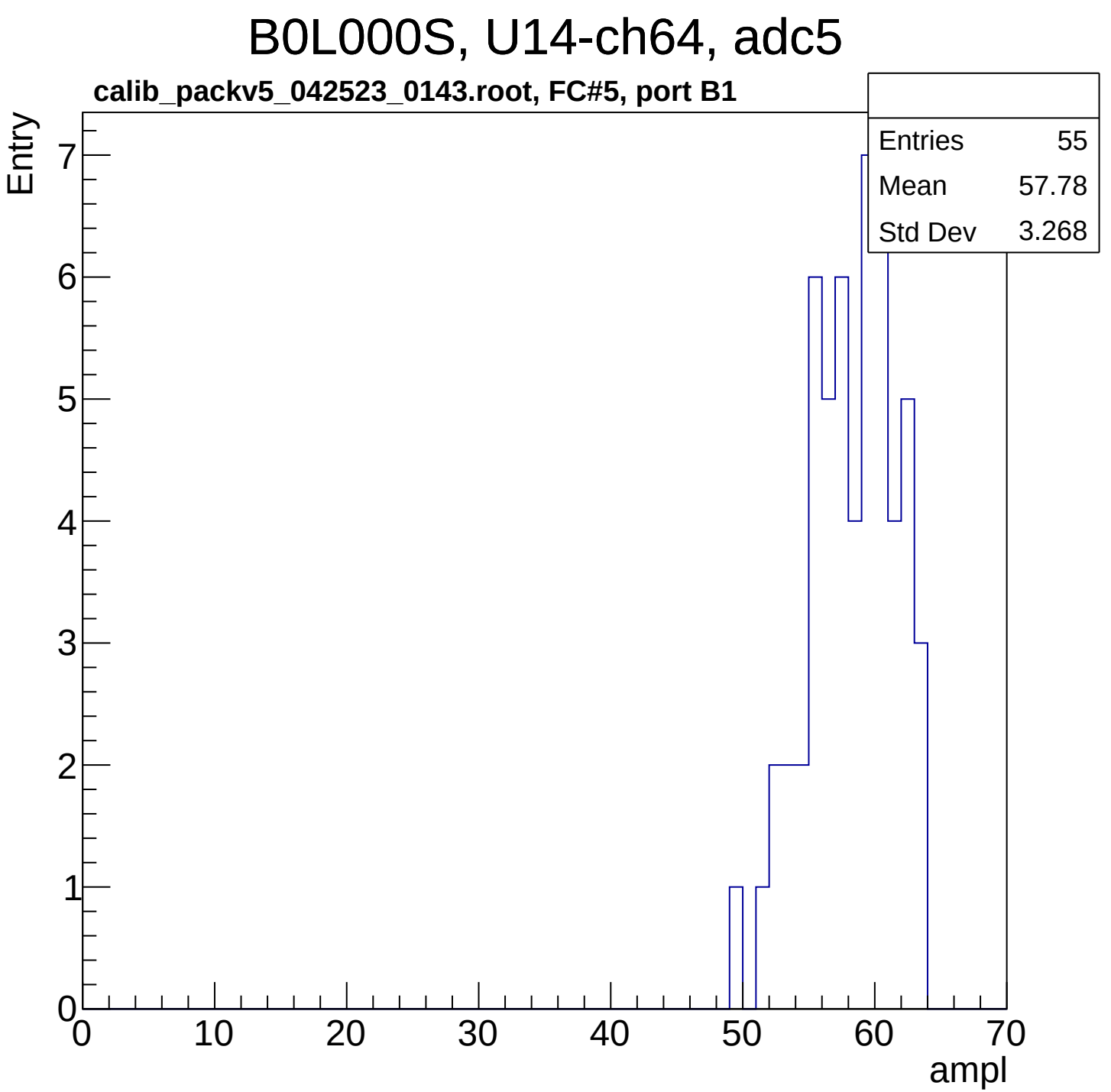
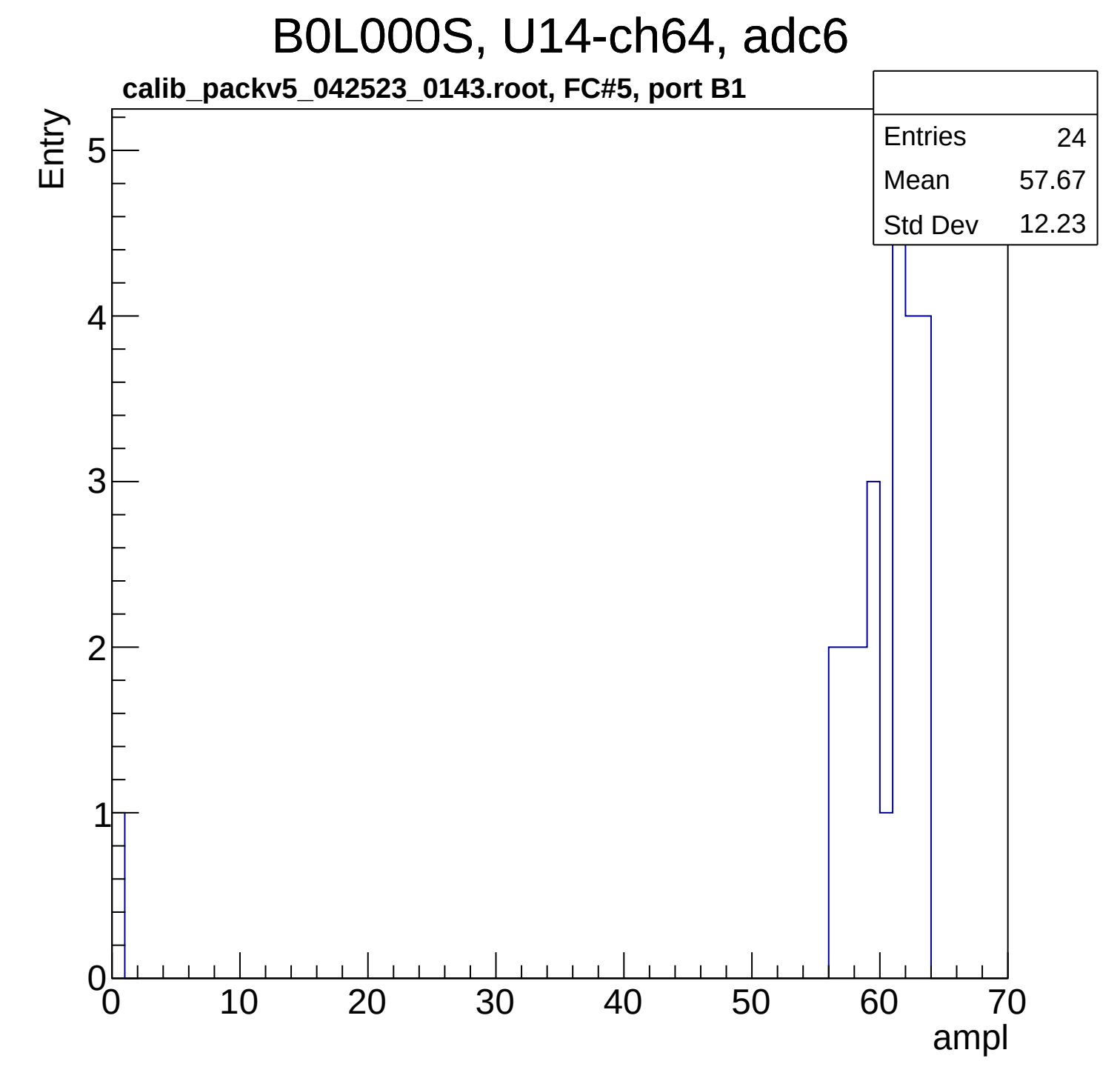
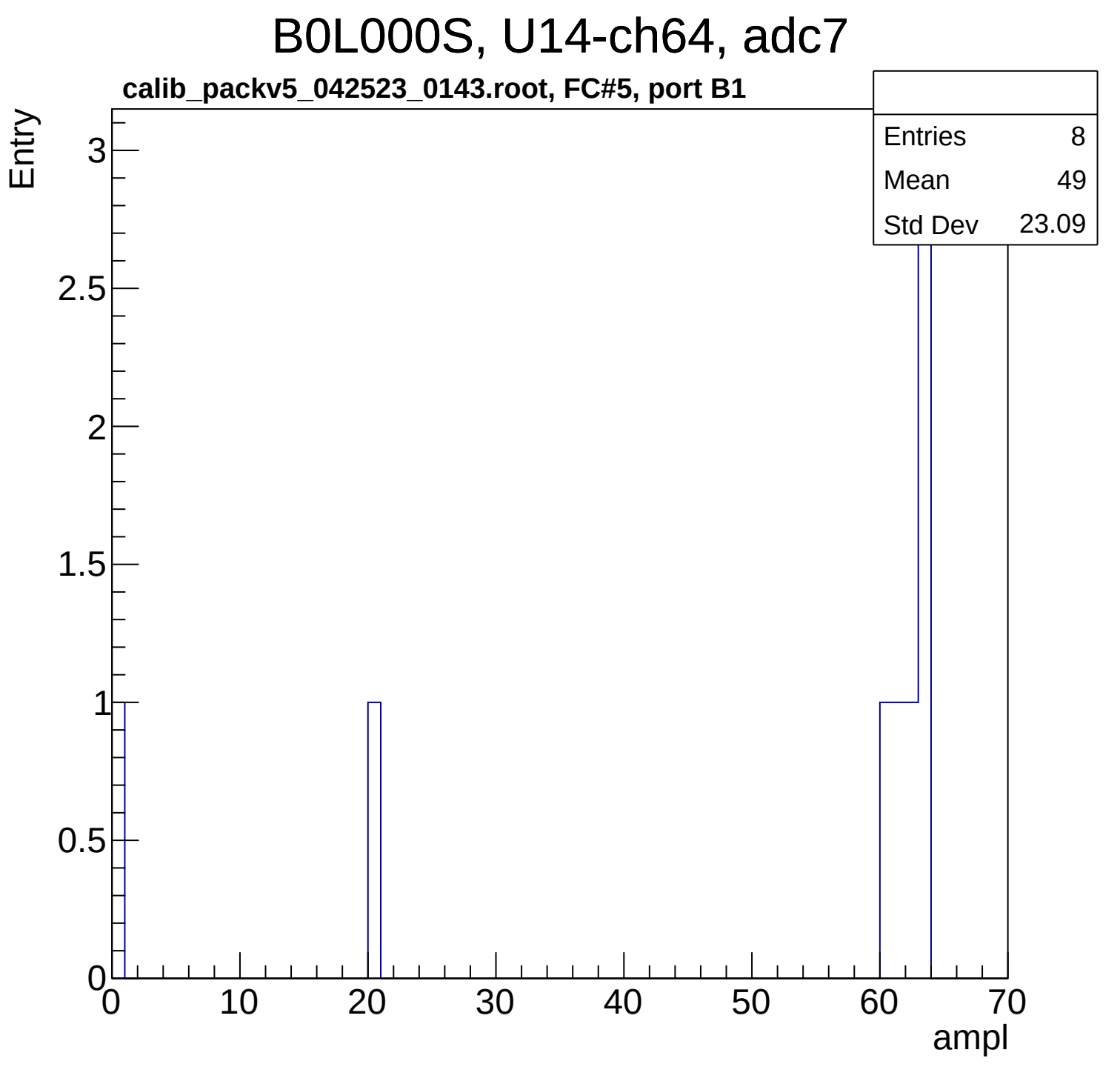
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.78
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 57.67
Std Dev 12.23
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 49
Std Dev 23.09
Entry
ampl
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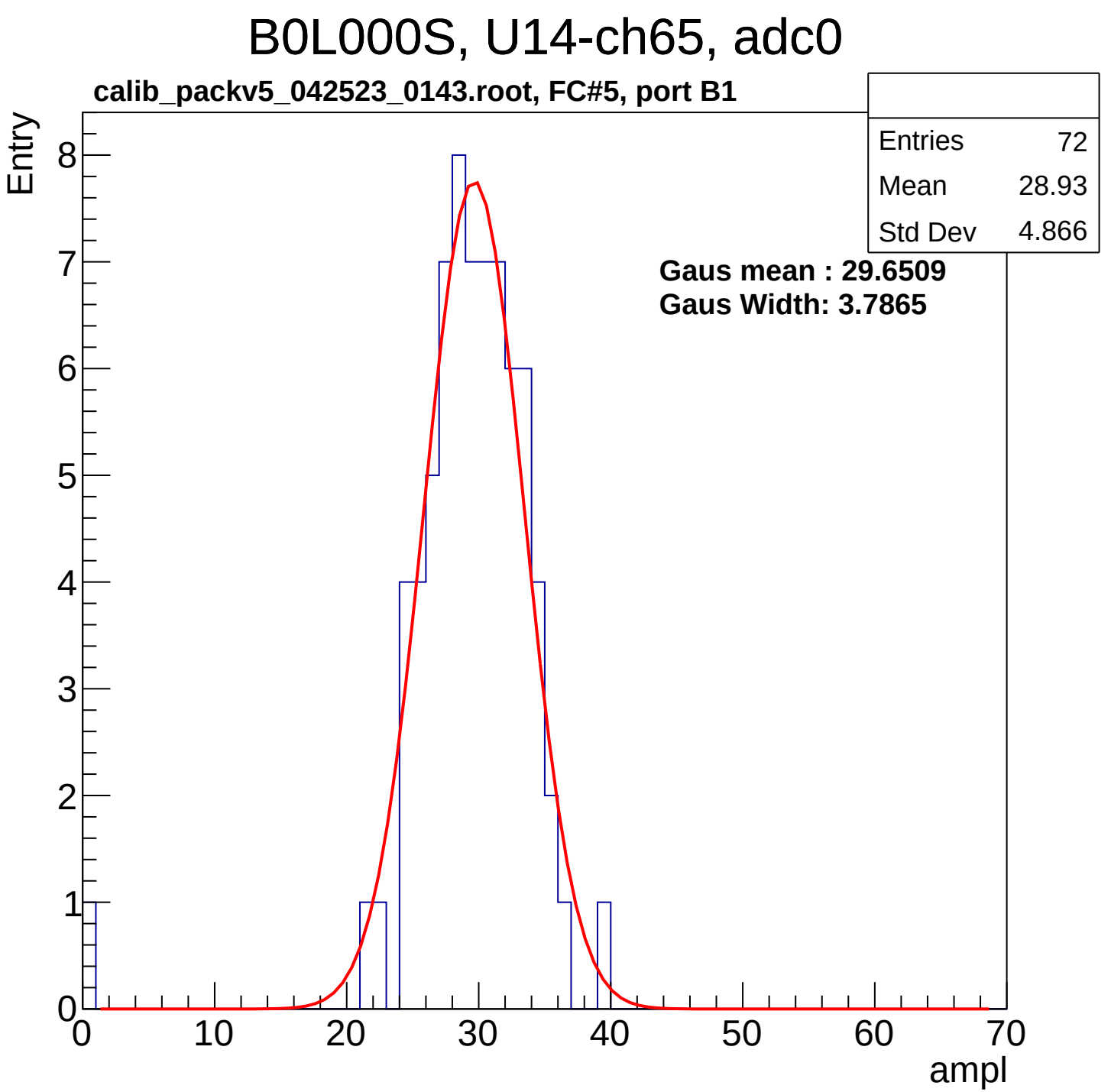
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.93
Std Dev 4.866
Gaus mean : 29.6509
Gaus Width: 3.7865
Entry
ampl
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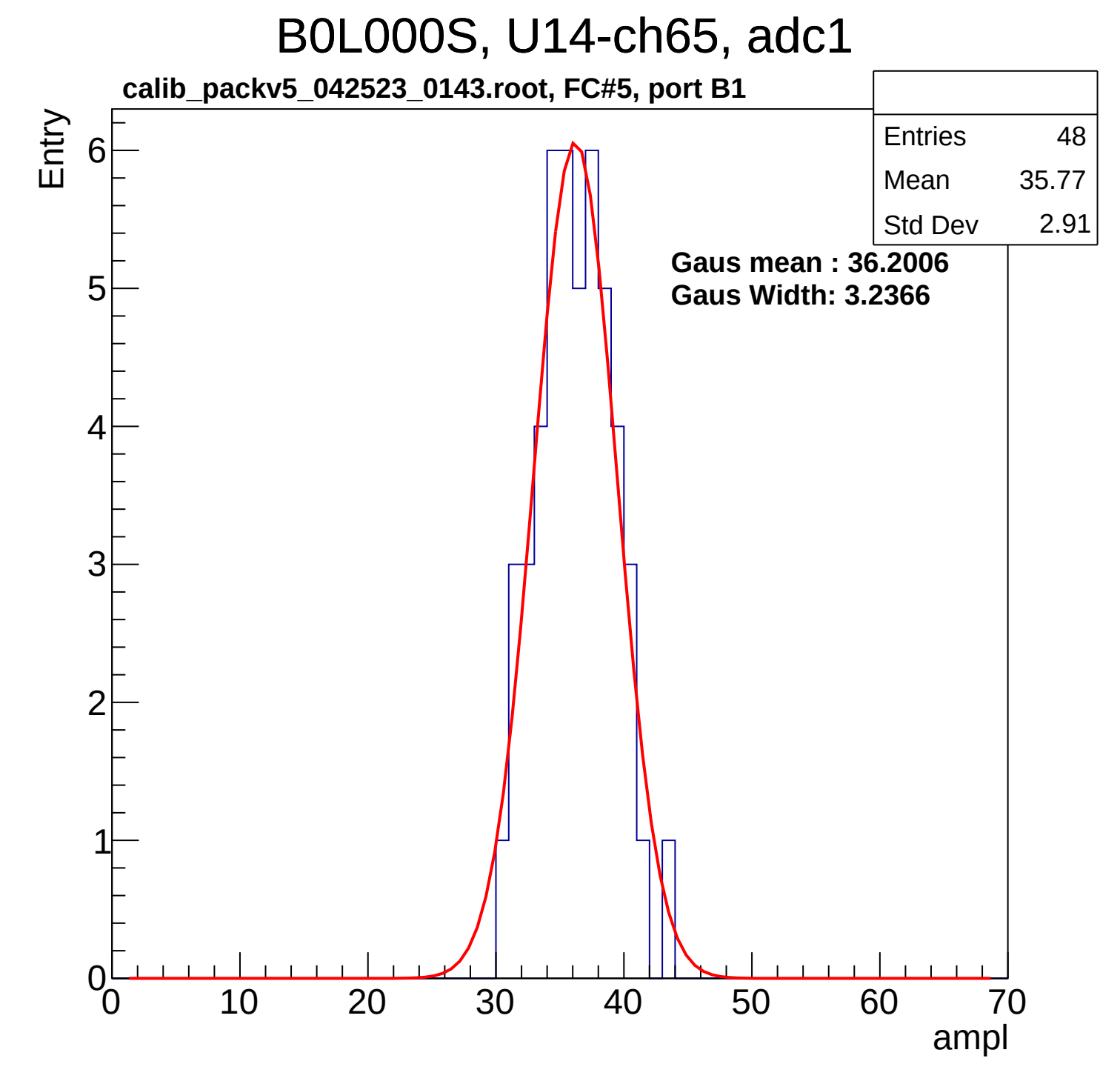
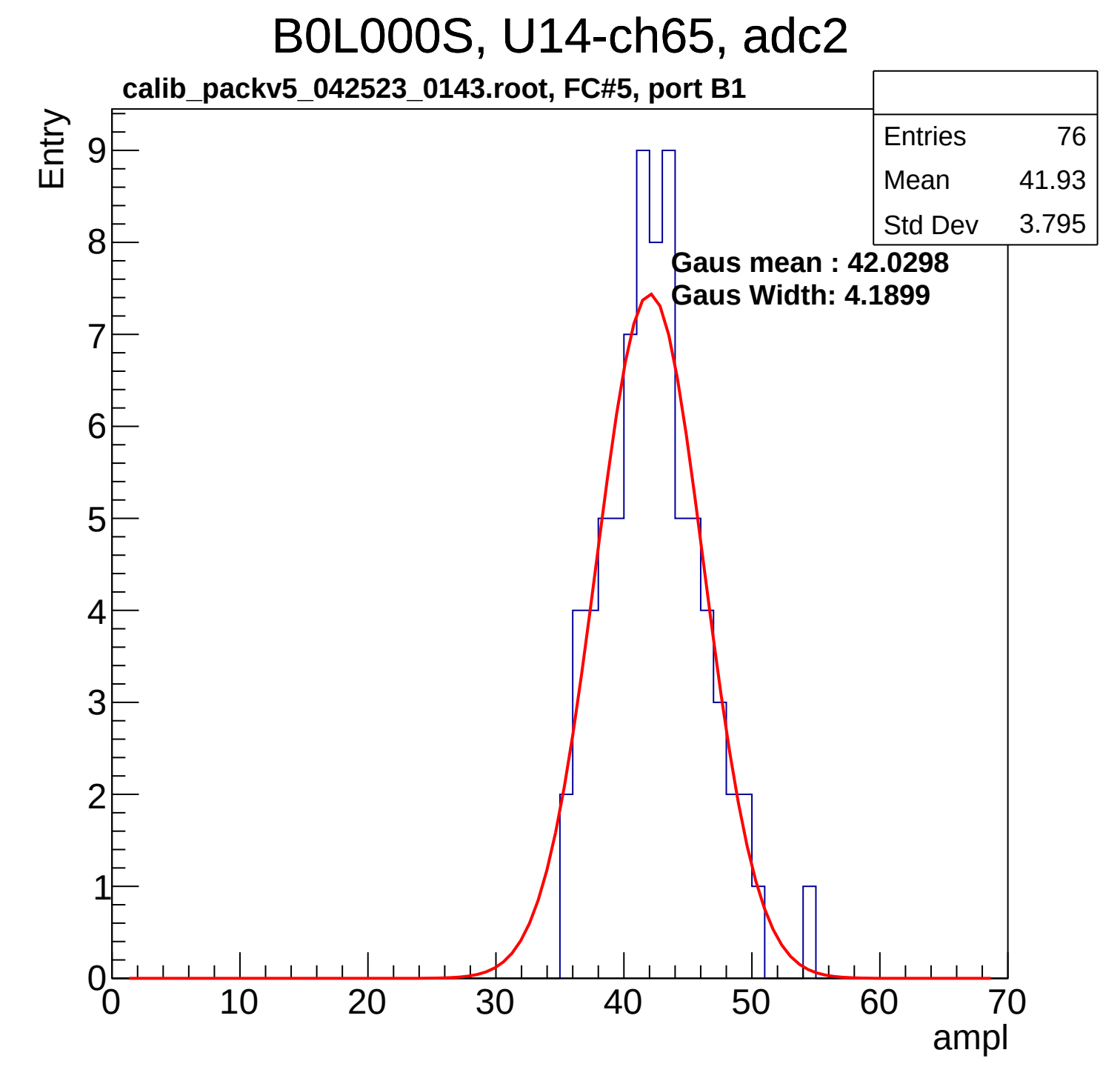
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 35.77
Std Dev 2.91
Gaus mean : 36.2006
Gaus Width: 3.2366
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 41.93
Std Dev 3.795
Gaus mean : 42.0298
Gaus Width: 4.1899
Entry
ampl
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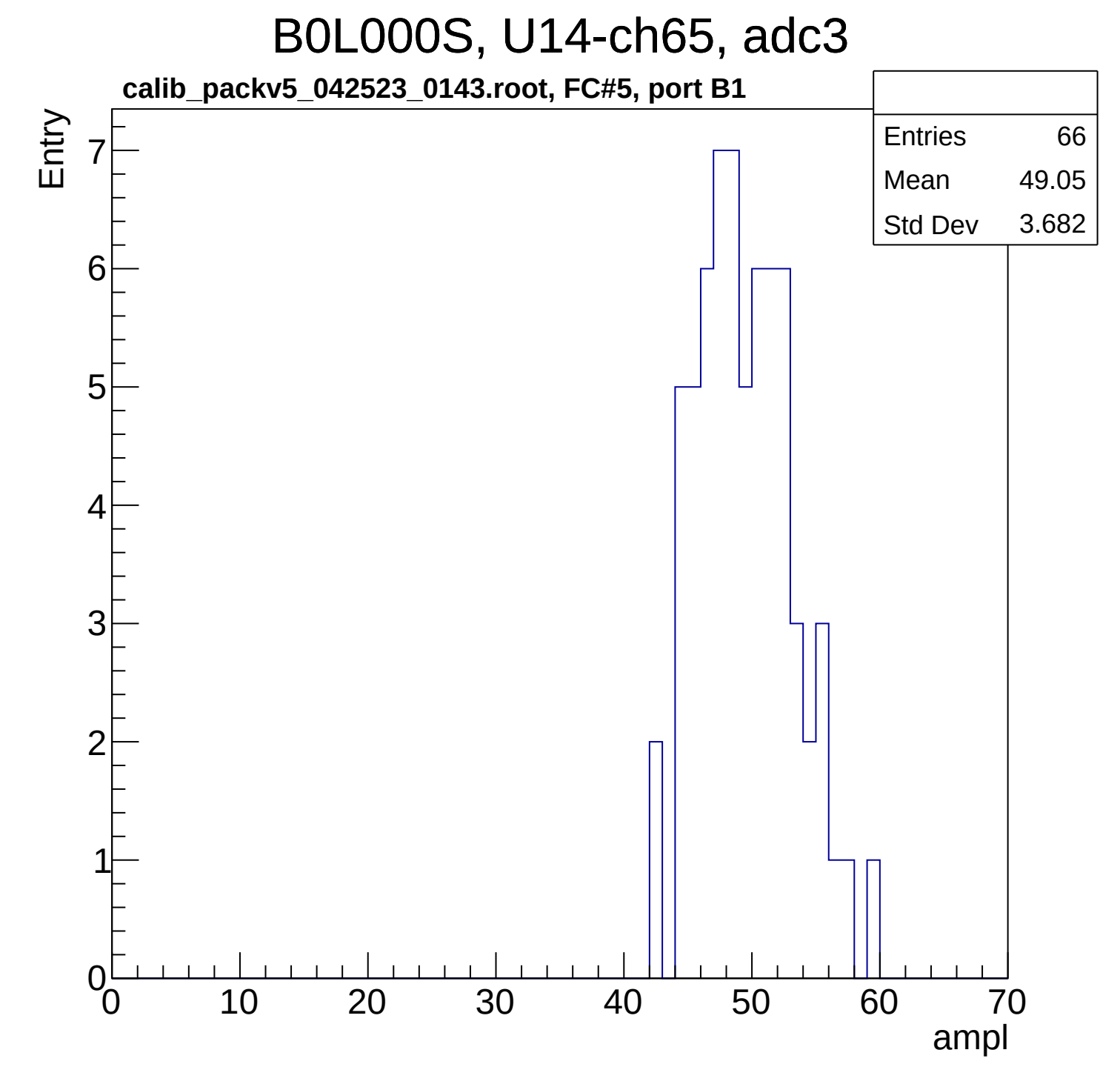
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.05
Std Dev 3.682
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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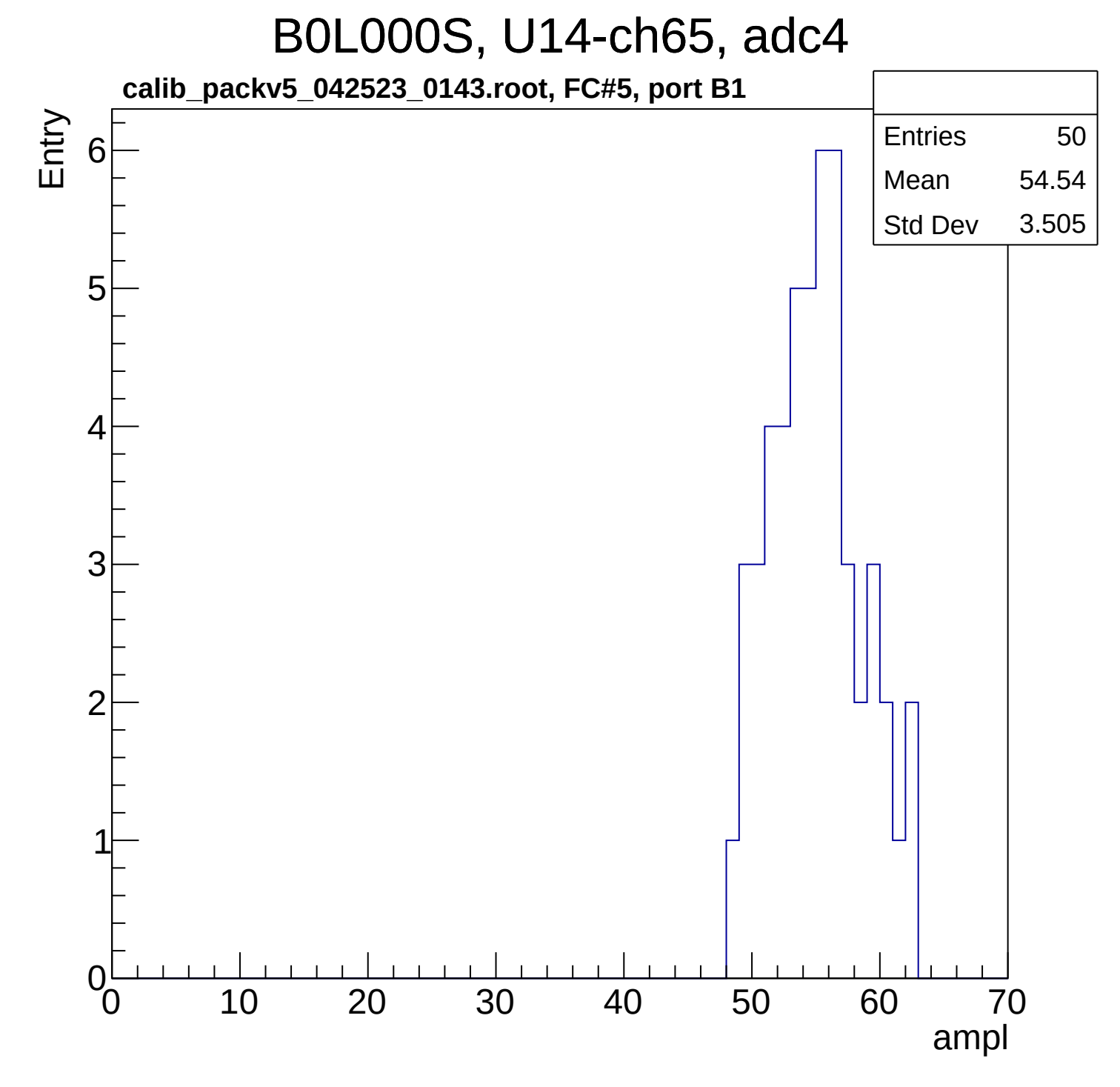
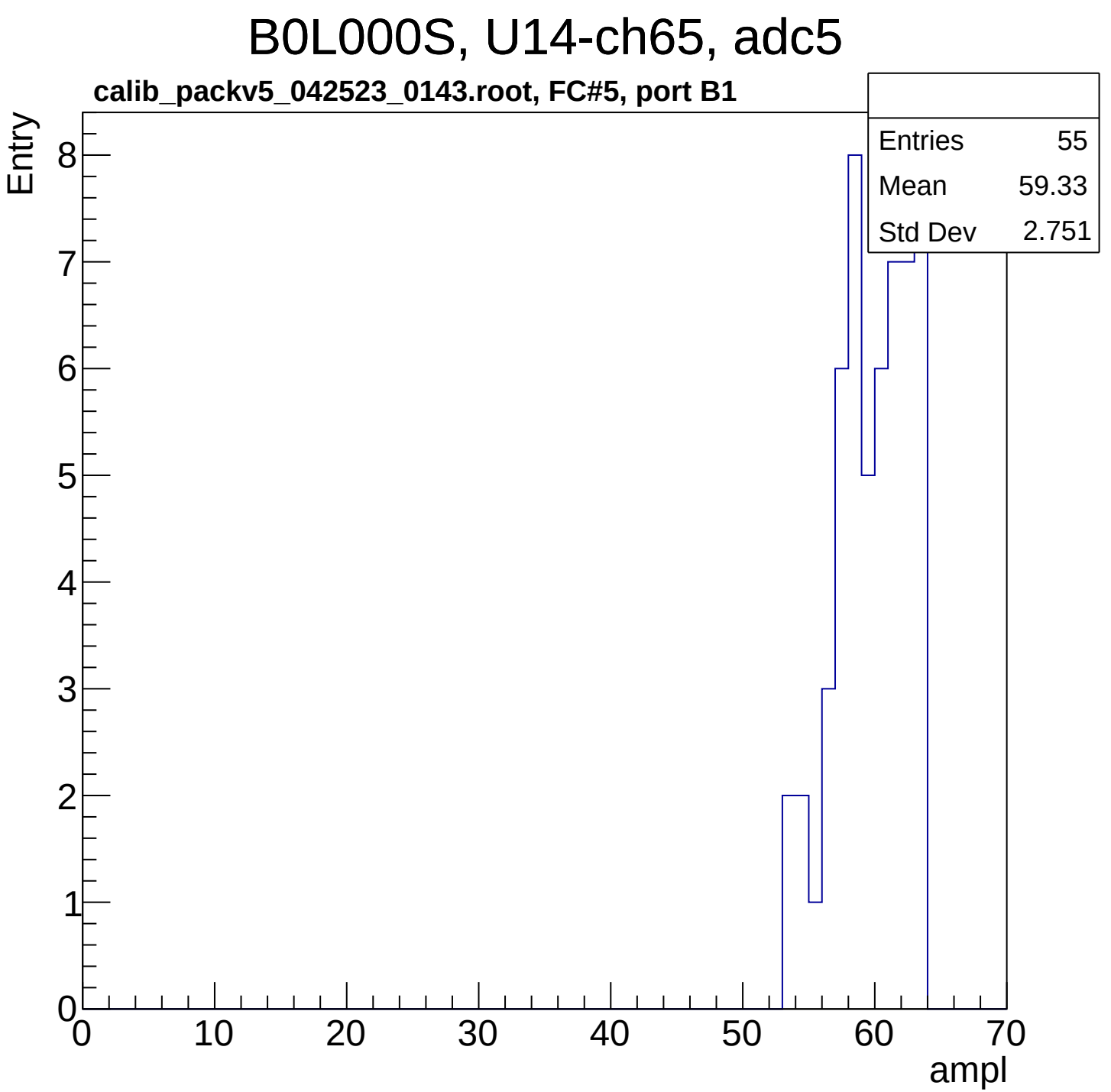

B0L000S, U14-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54.54
Std Dev 3.505
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 59.33
Std Dev 2.751
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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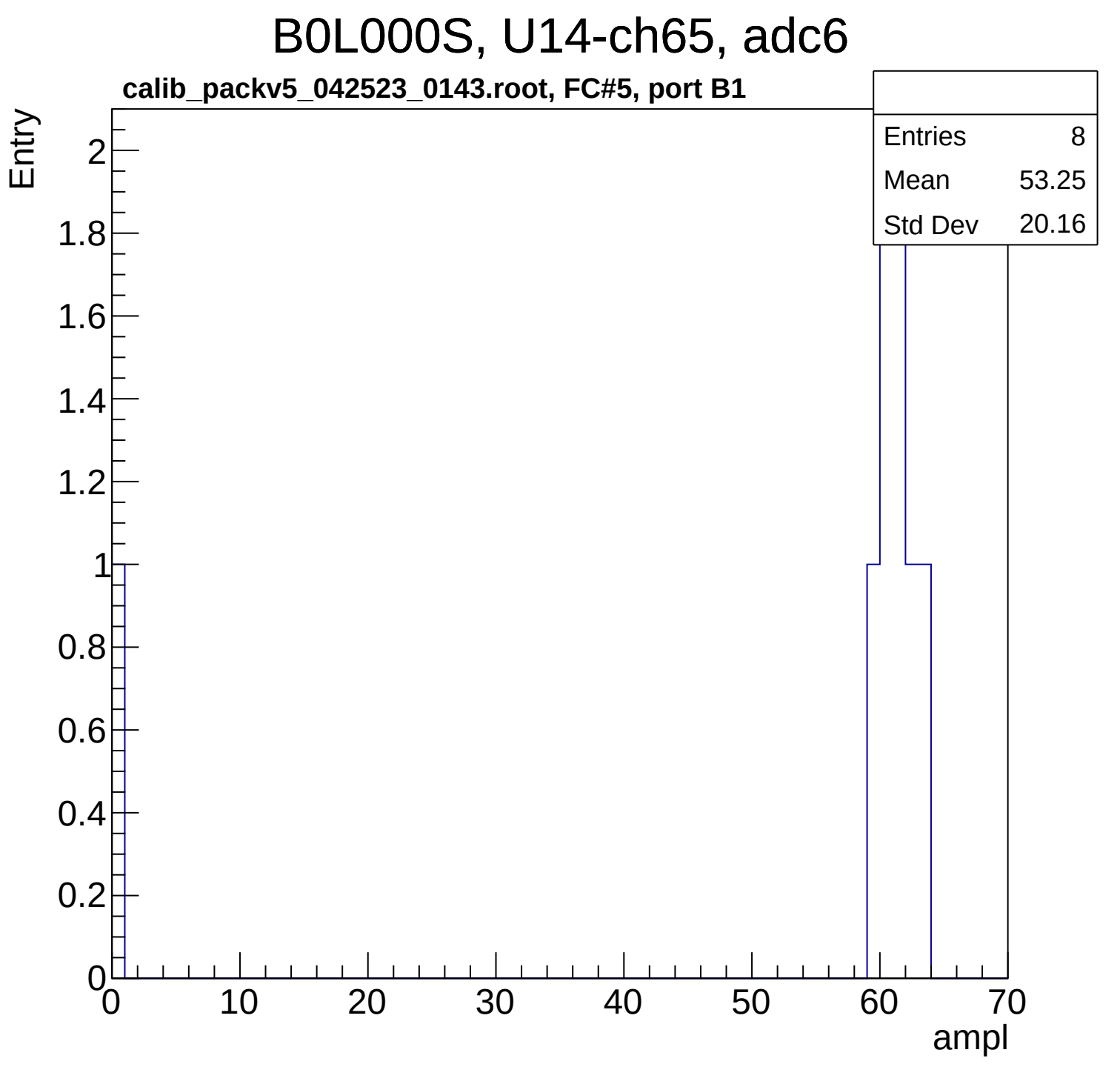
B0L000S, U14-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.25
Std Dev 20.16
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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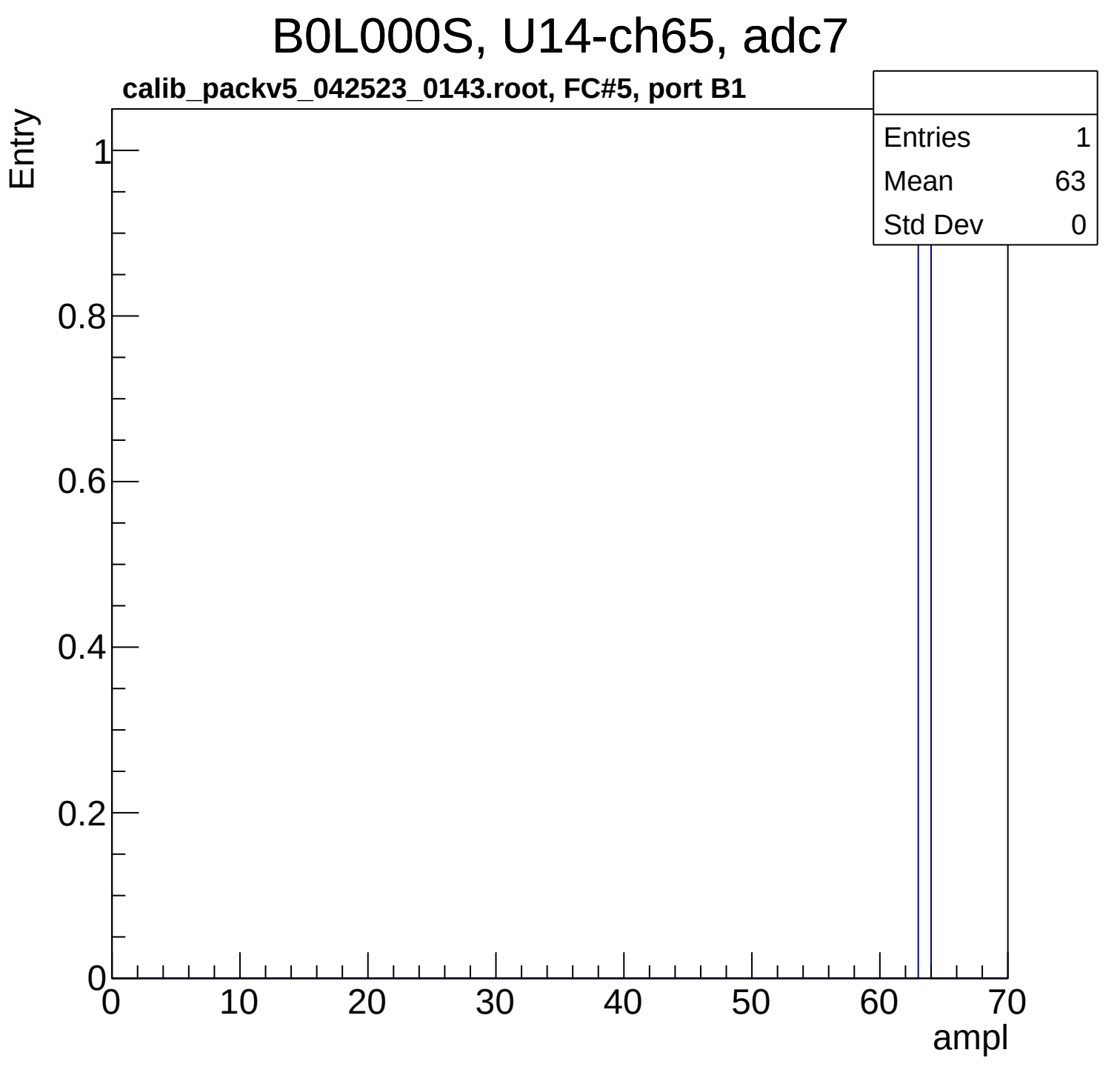
40
50
60
70


B0L000S, U14-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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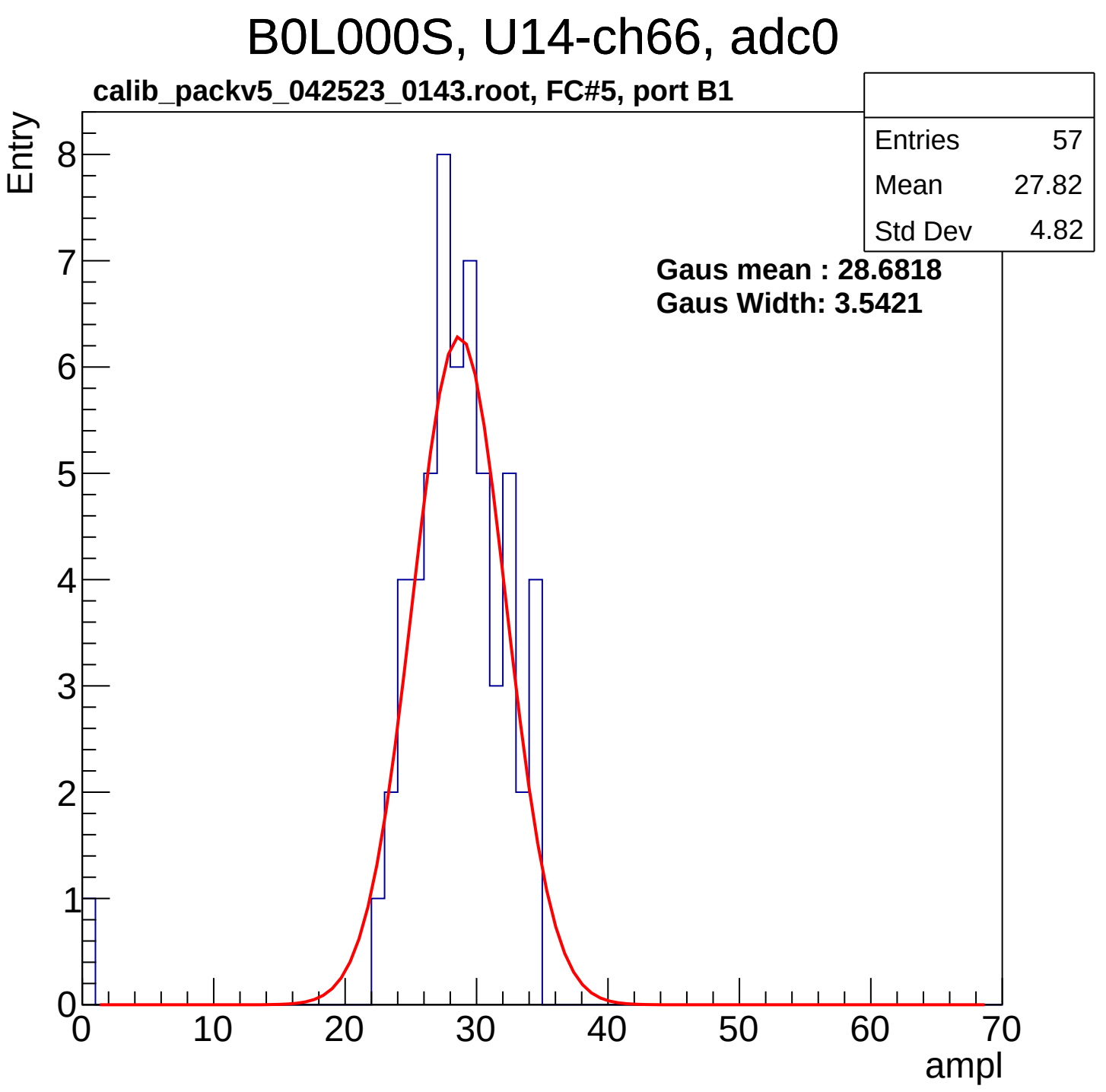
40
50
60
70
ampl


B0L000S, U14-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 27.82
Std Dev 4.82
Gaus mean : 28.6818
Gaus Width: 3.5421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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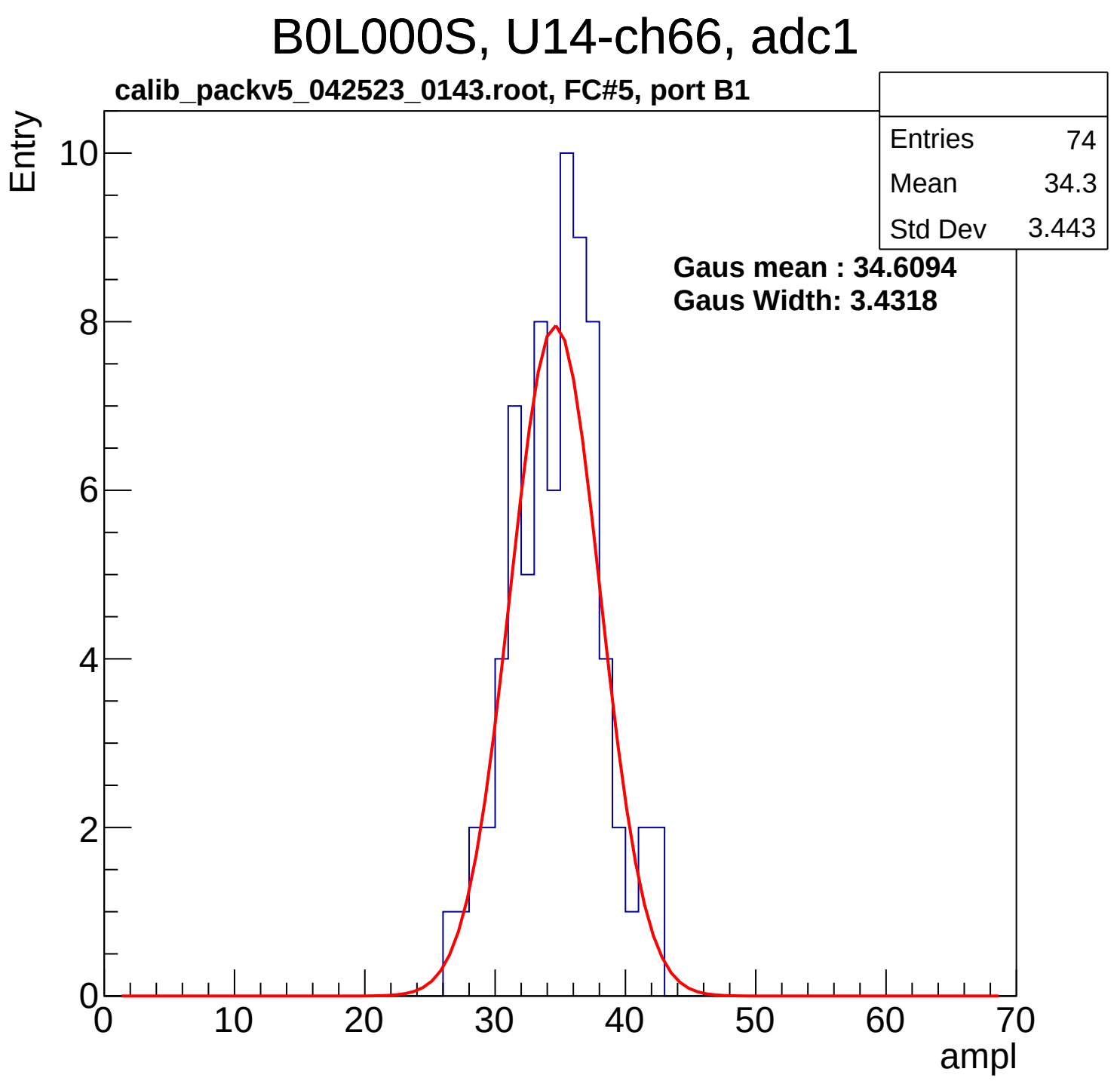
B0L000S, U14-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 34.3
Std Dev 3.443
Gaus mean : 34.6094
Gaus Width: 3.4318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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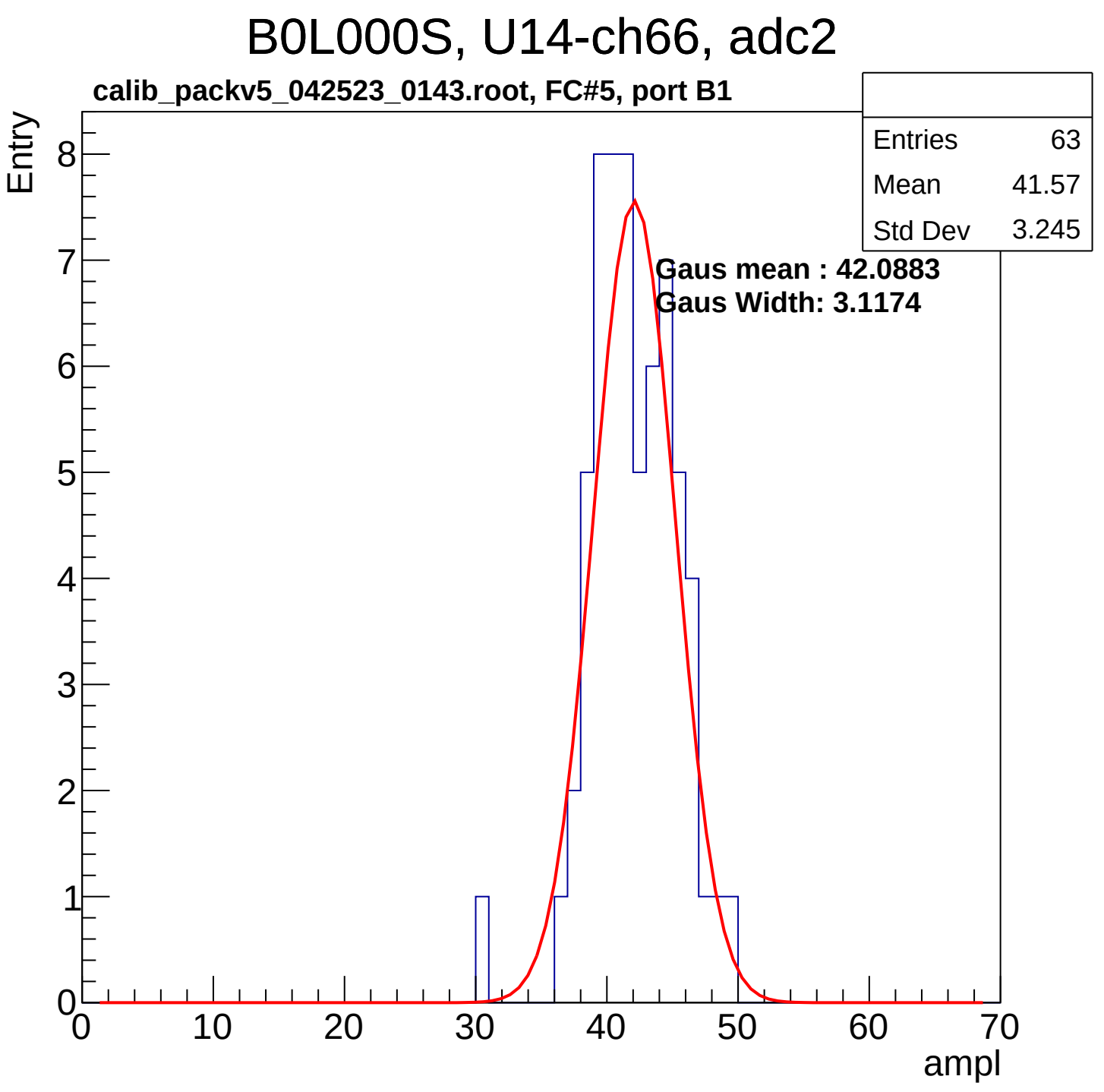
B0L000S, U14-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.57
Std Dev 3.245
Gaus mean : 42.0883
Gaus Width: 3.1174
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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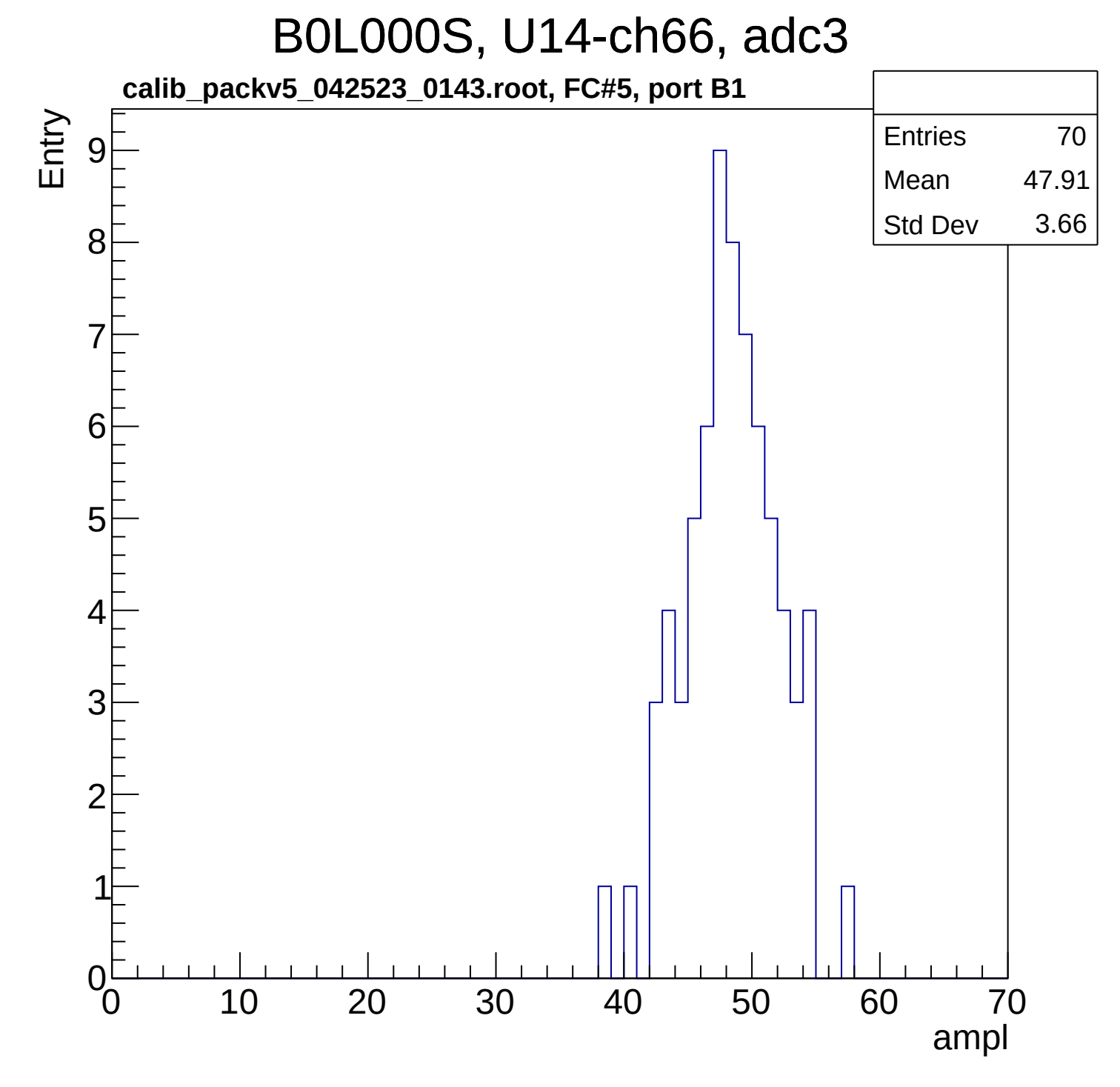
B0L000S, U14-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 47.91
Std Dev 3.66
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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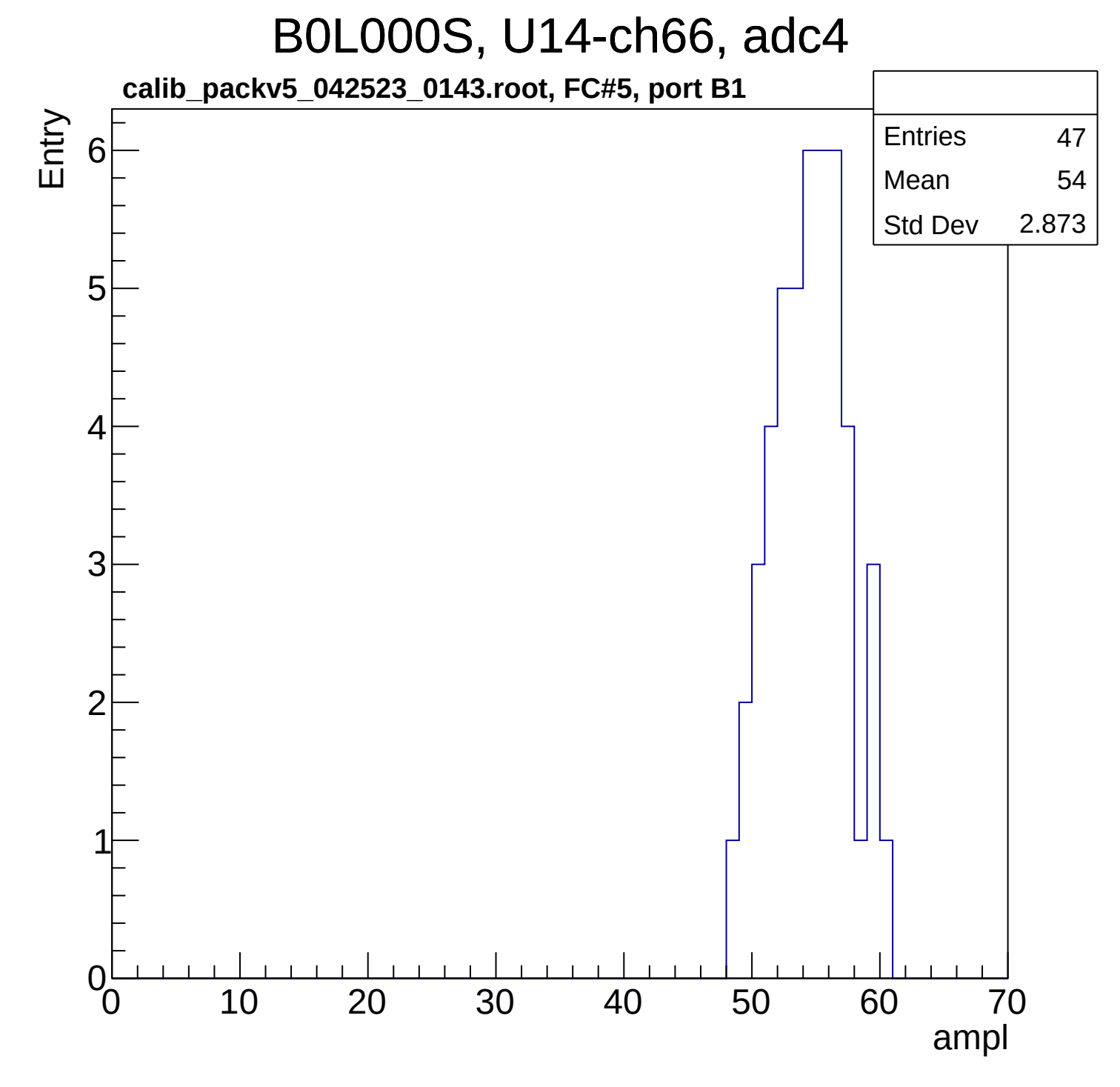
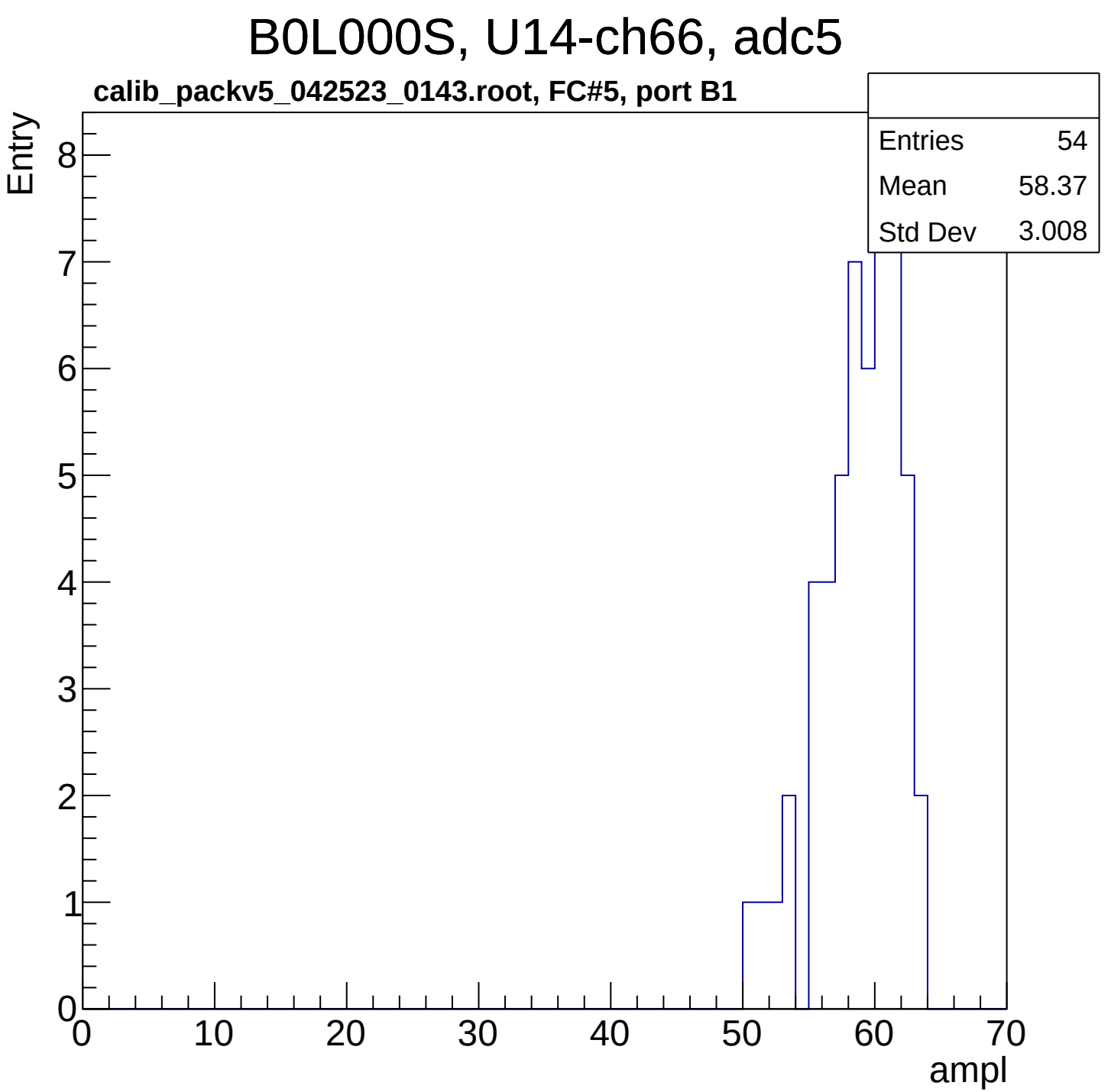

B0L000S, U14-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 54
Std Dev 2.873
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.37
Std Dev 3.008
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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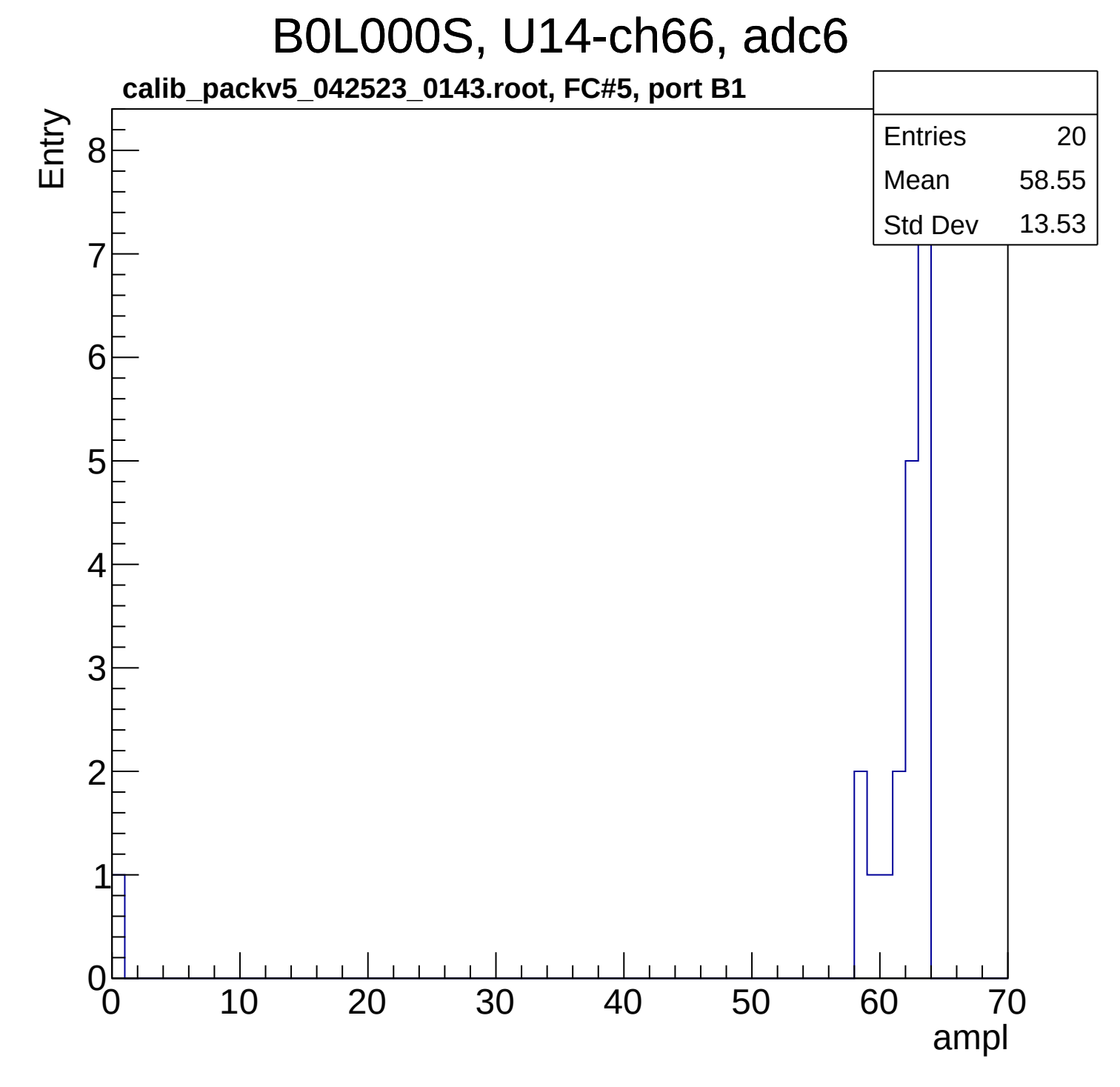
B0L000S, U14-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.55
Std Dev 13.53
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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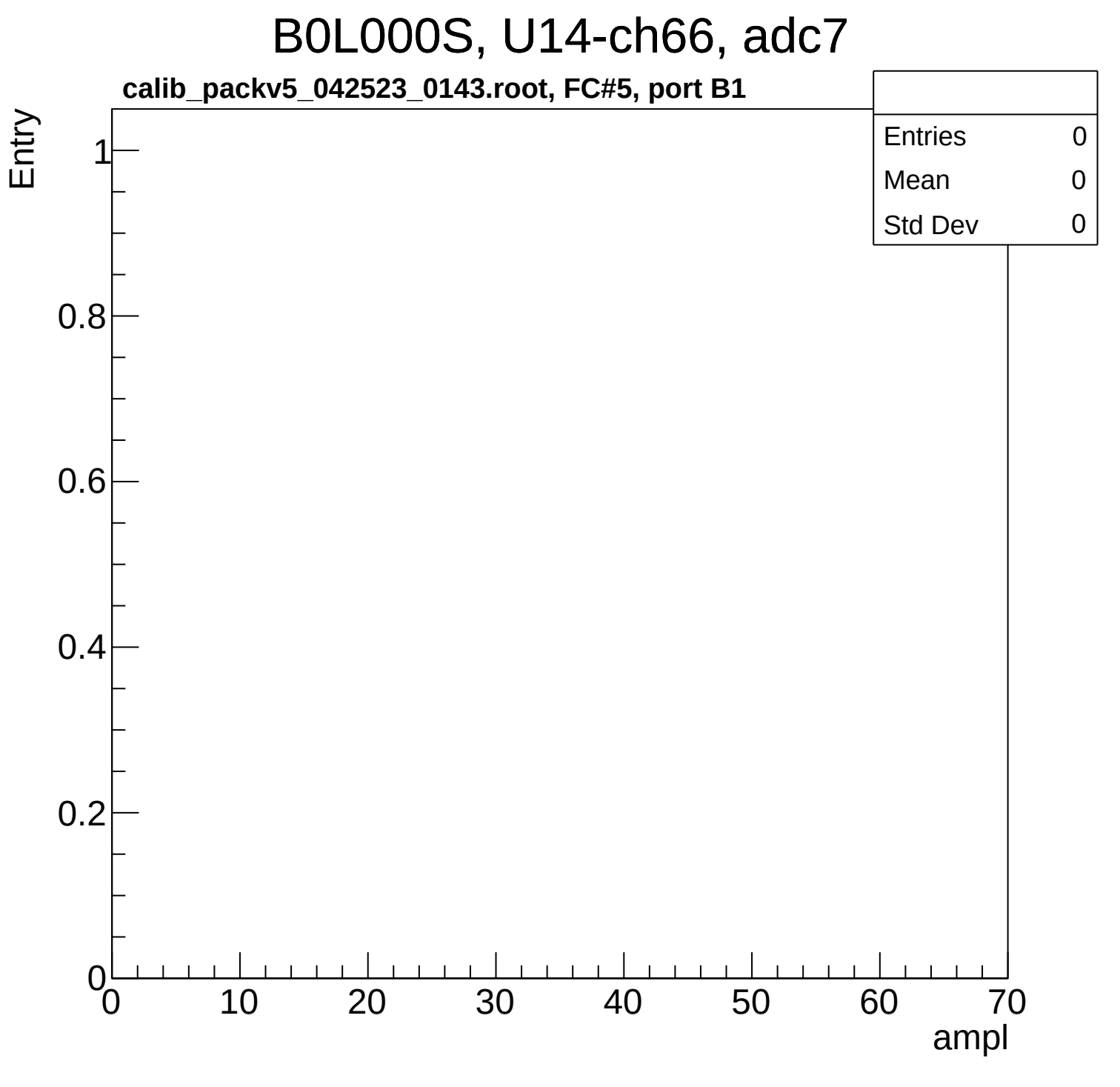
B0L000S, U14-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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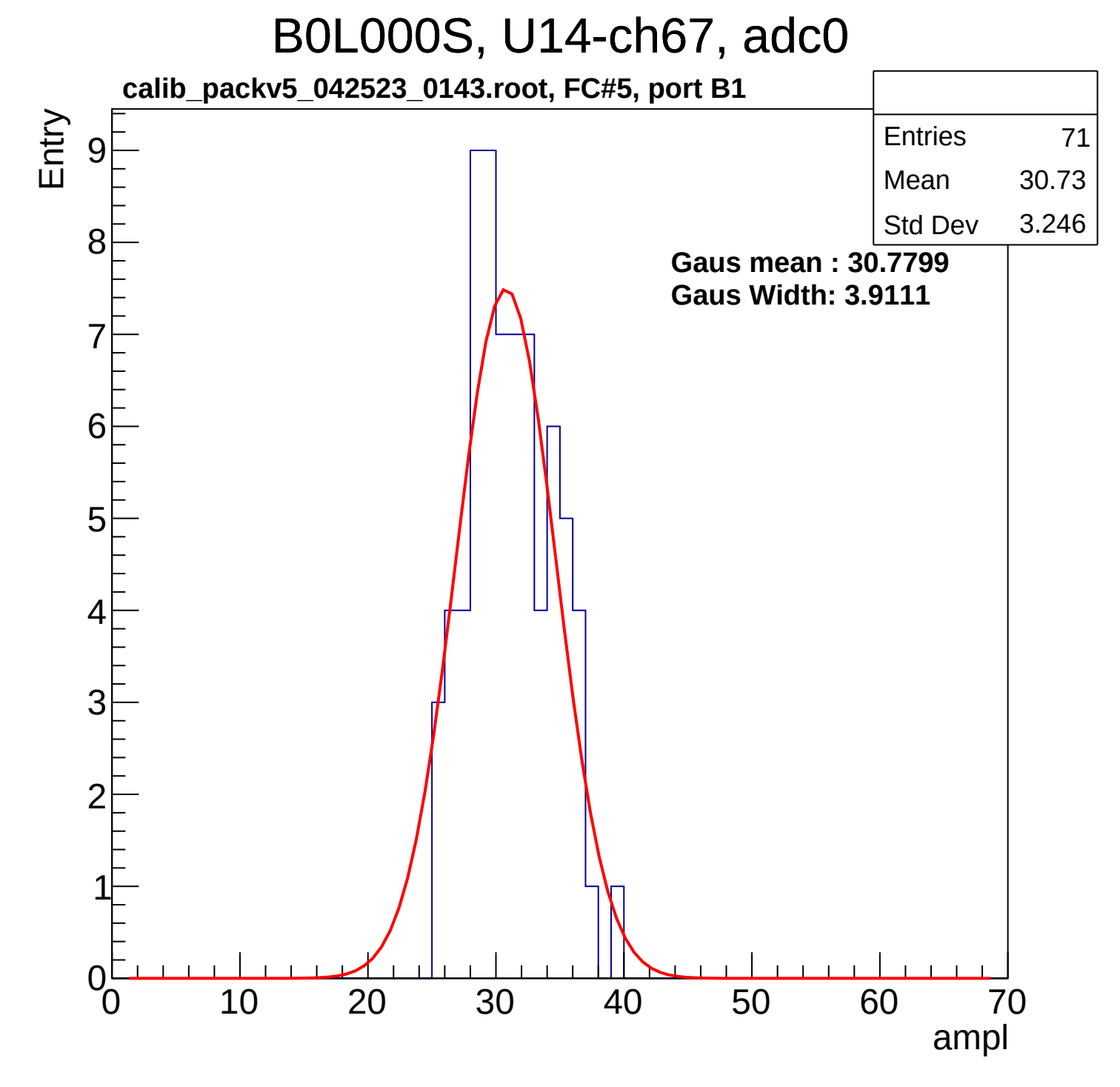
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.73
Std Dev 3.246
Gaus mean : 30.7799
Gaus Width: 3.9111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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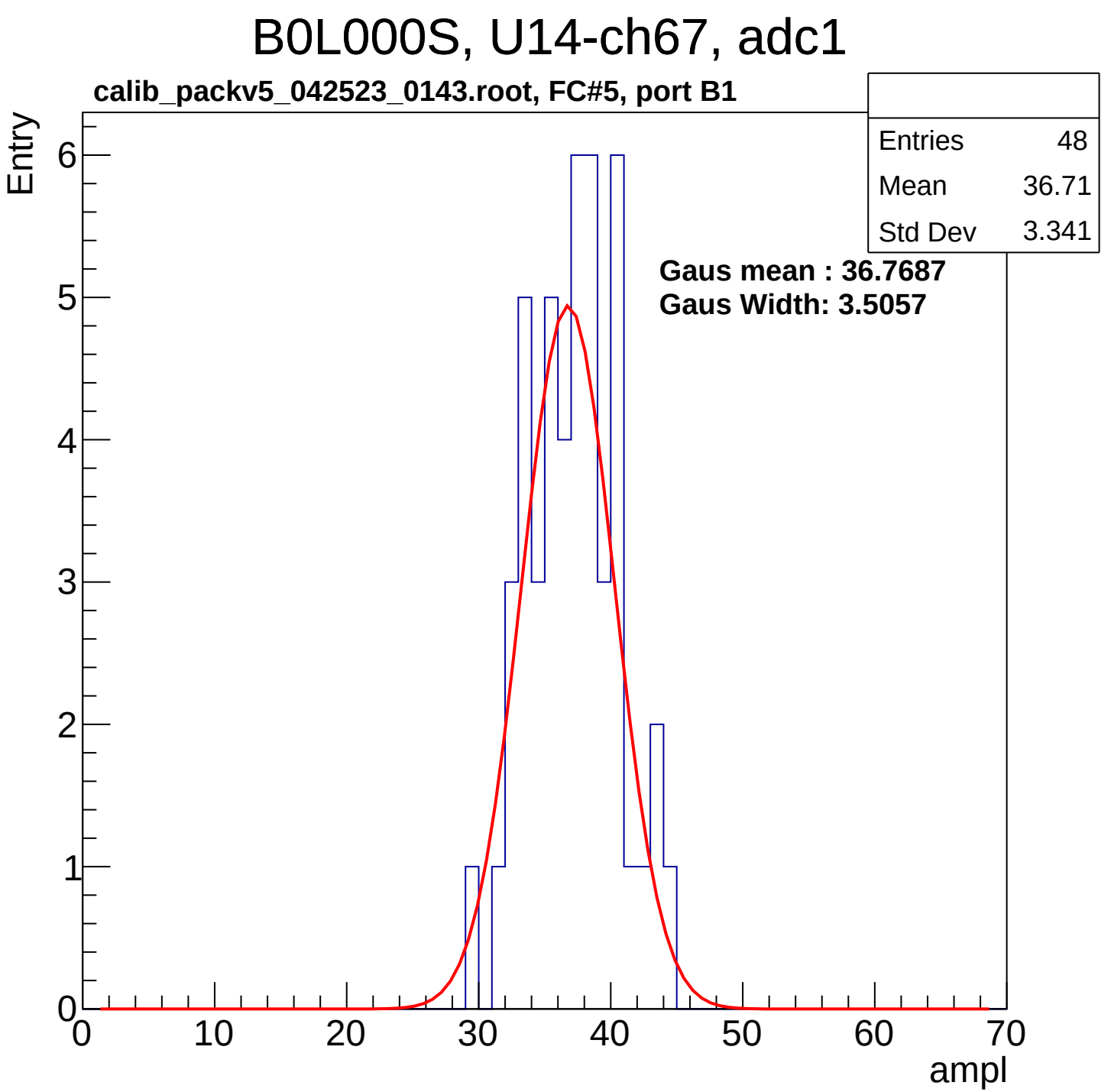
B0L000S, U14-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 36.71
Std Dev 3.341
Gaus mean : 36.7687
Gaus Width: 3.5057
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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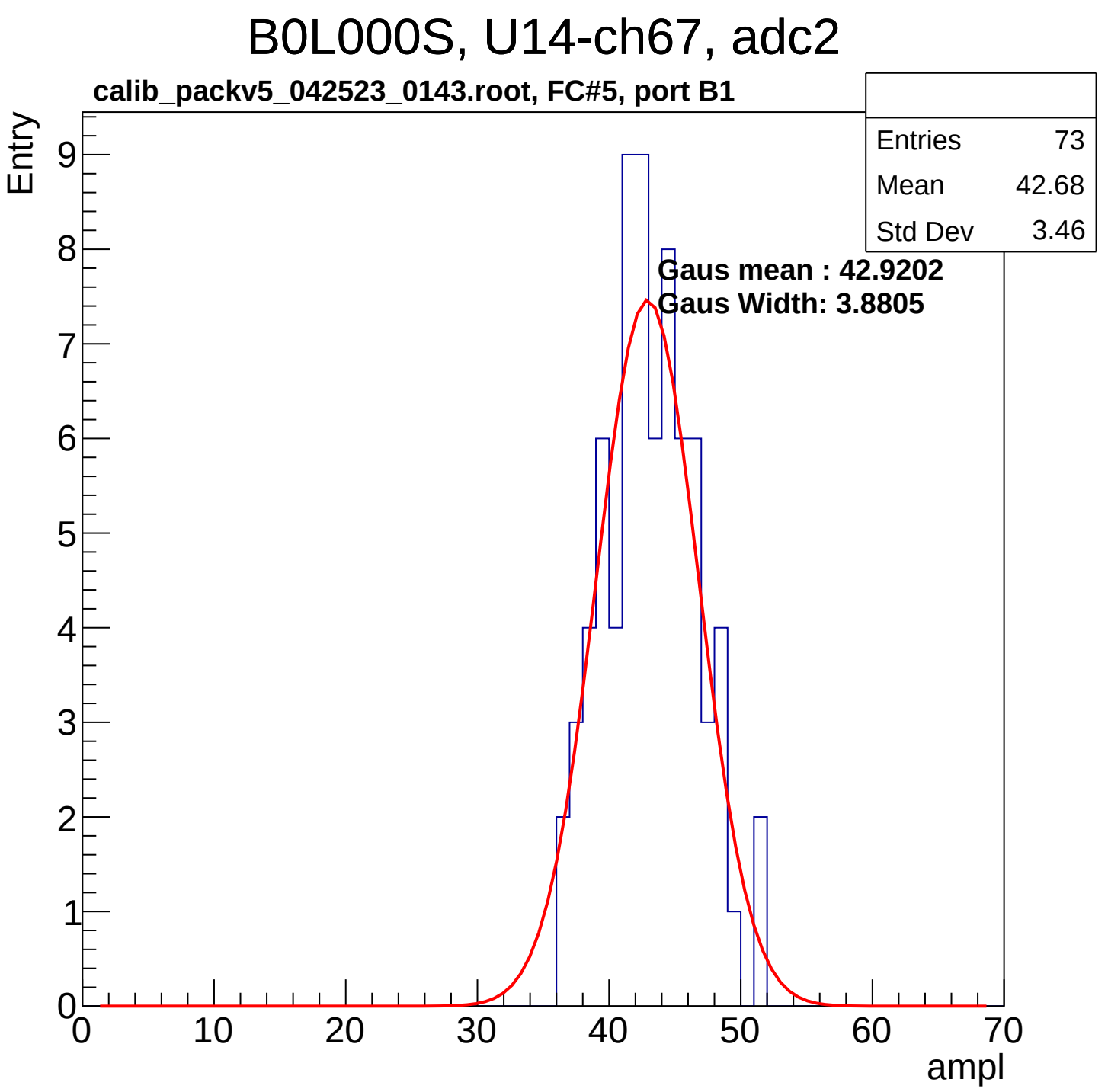
B0L000S, U14-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.68
Std Dev 3.46
Gaus mean : 42.9202
Gaus Width: 3.8805
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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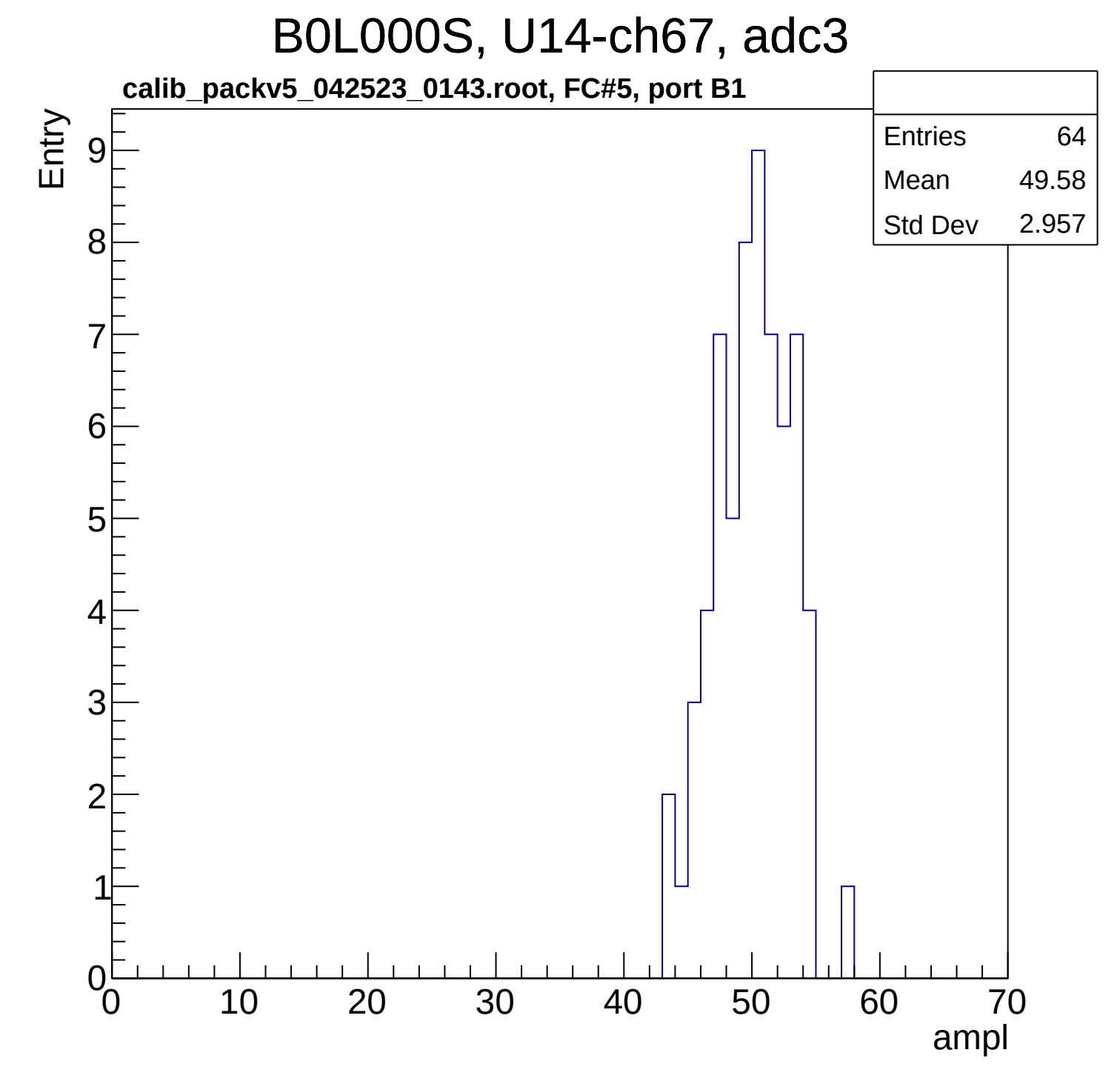
B0L000S, U14-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.58
Std Dev 2.957
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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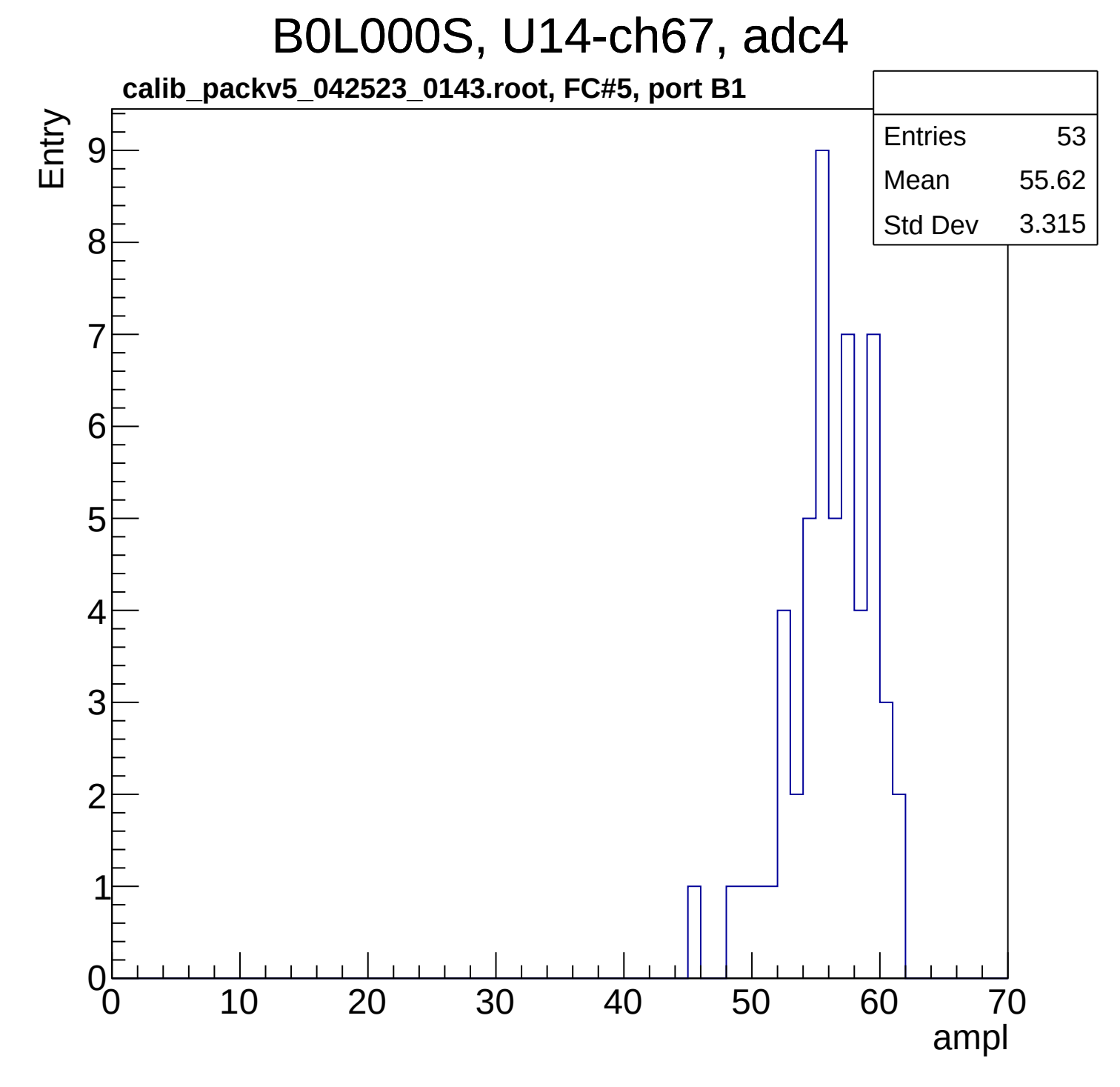
B0L000S, U14-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 55.62
Std Dev 3.315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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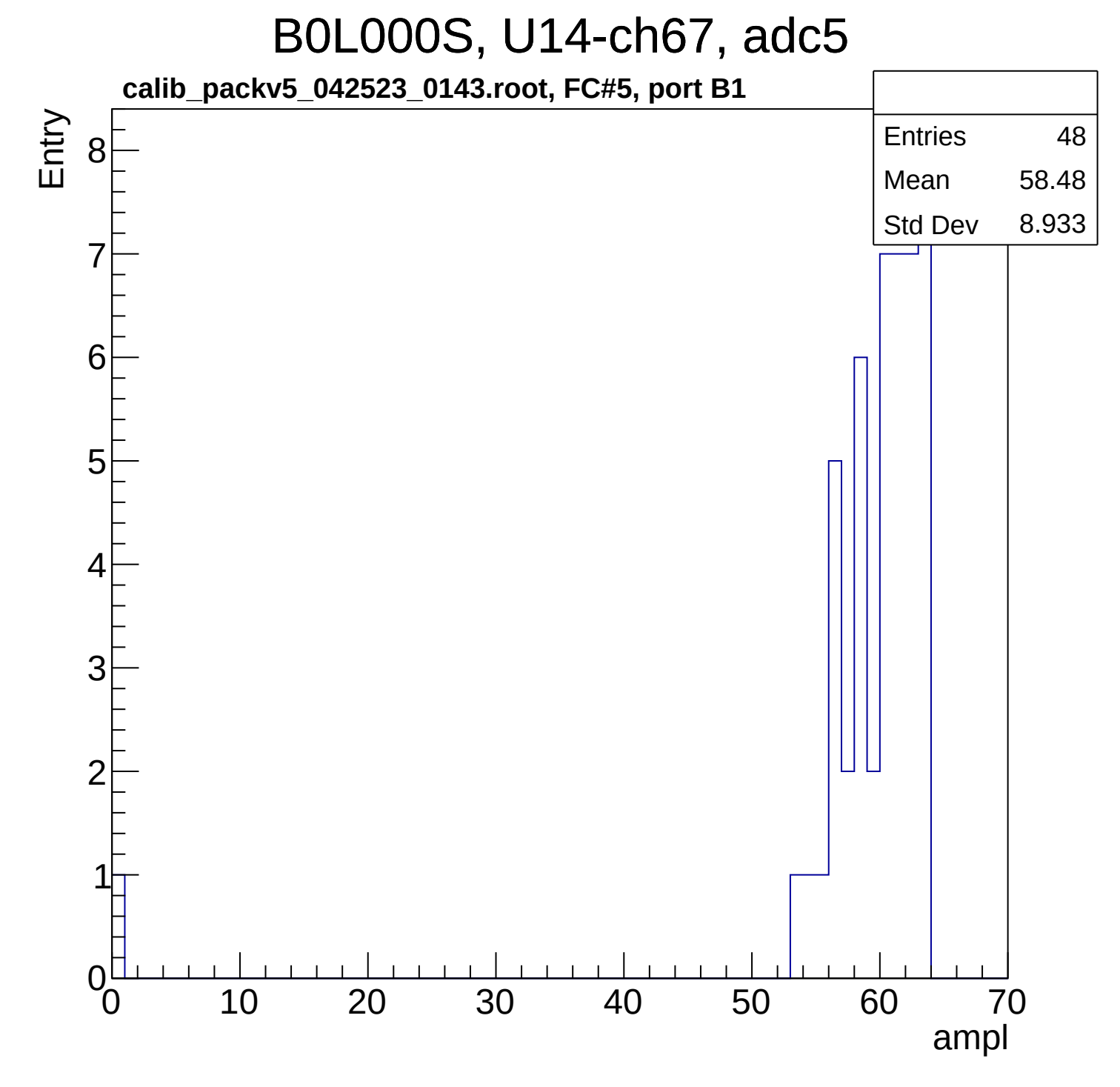
B0L000S, U14-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.48
Std Dev 8.933
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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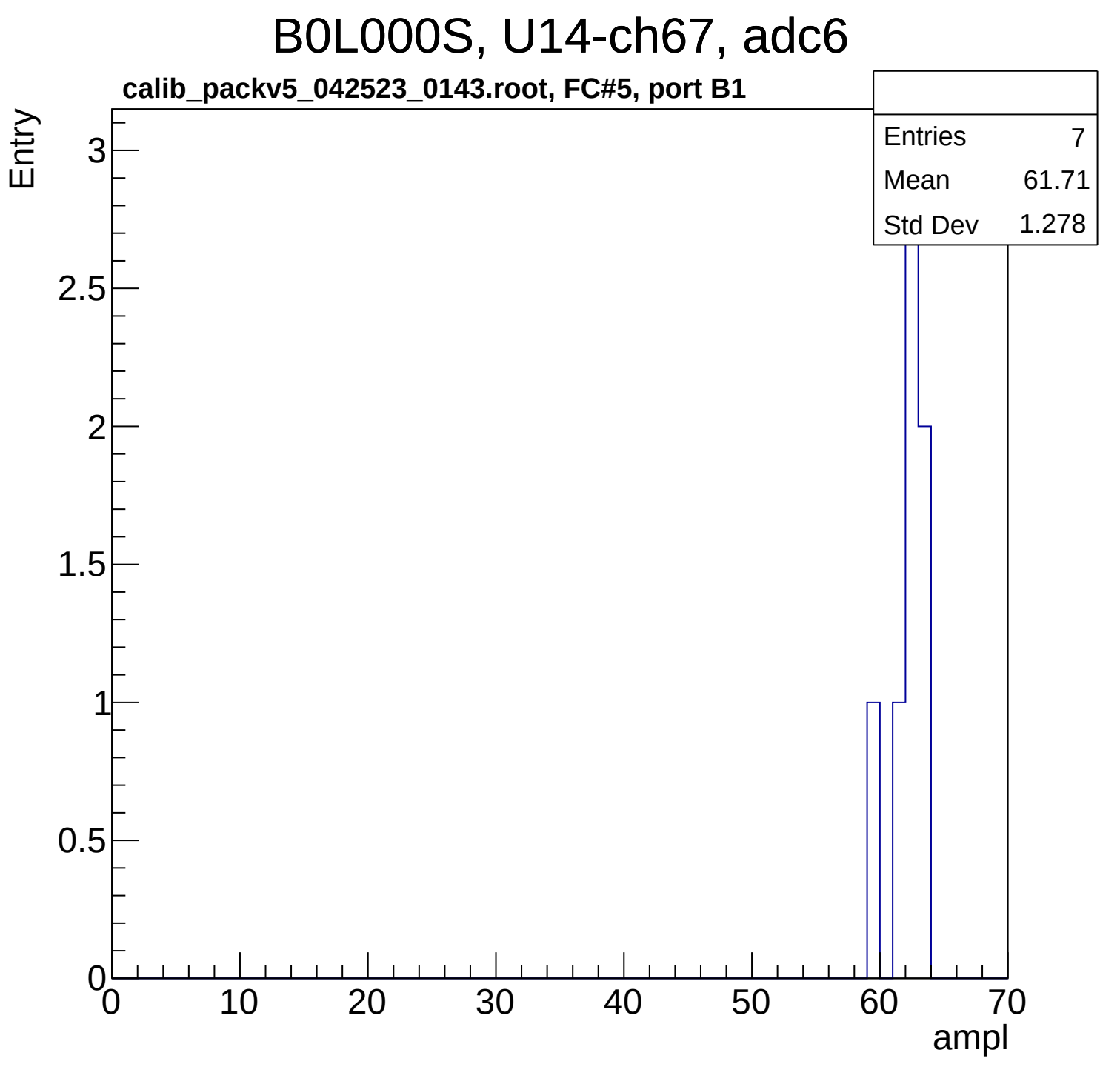
B0L000S, U14-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 1.278
Entry
ampl
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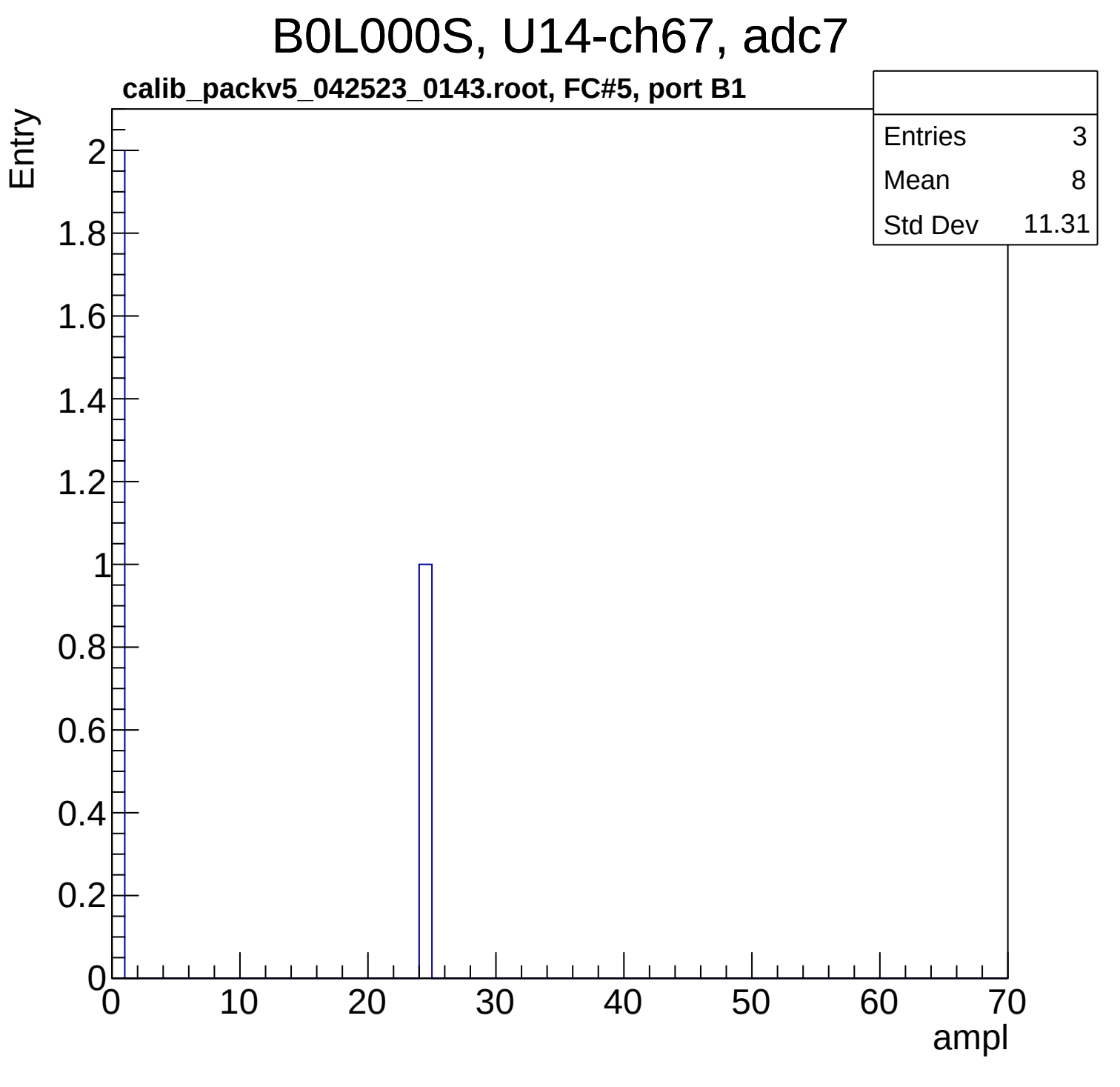
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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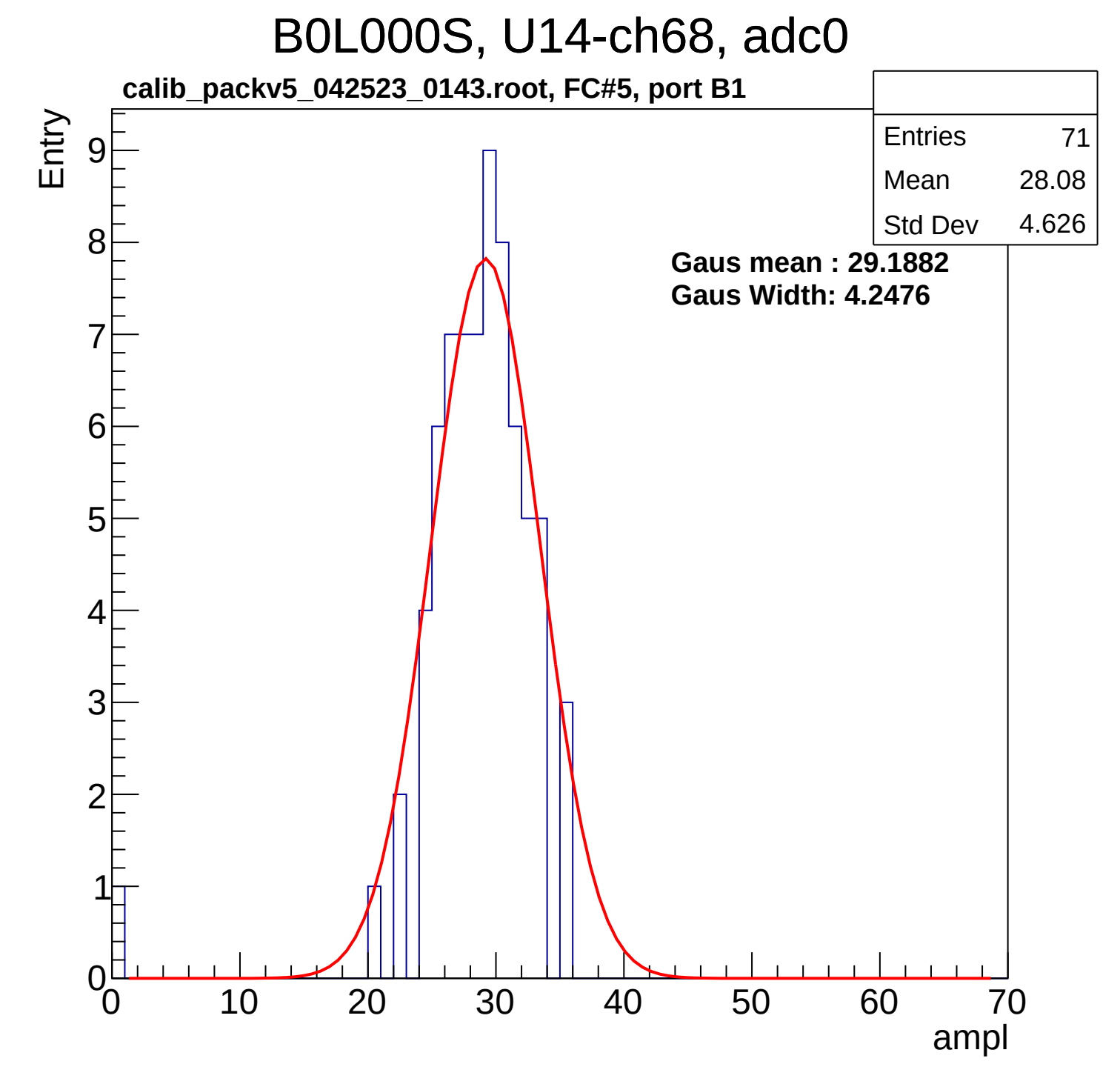
B0L000S, U14-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.08
Std Dev 4.626
Gaus mean : 29.1882
Gaus Width: 4.2476
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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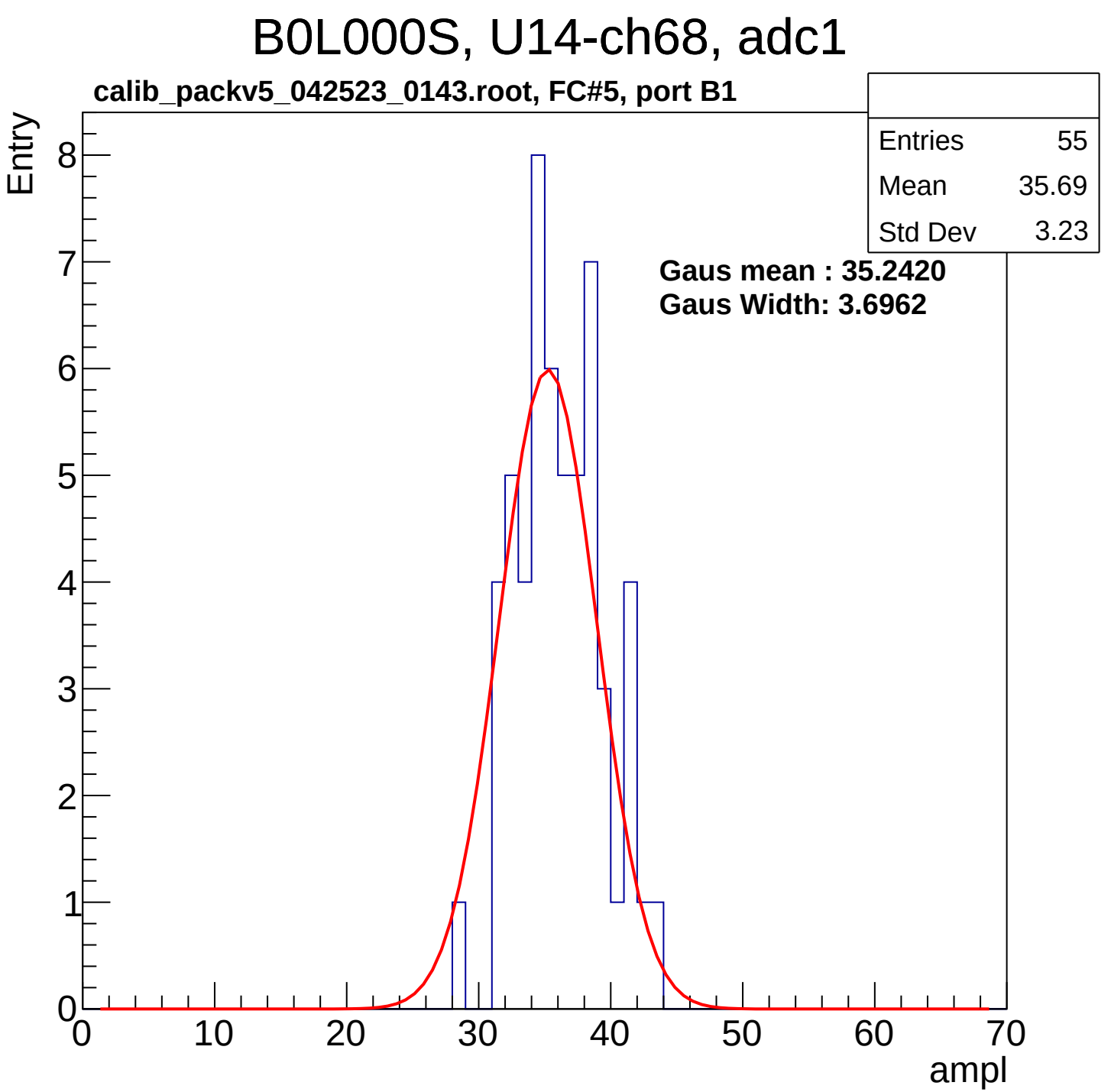
B0L000S, U14-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 35.69
Std Dev 3.23
Gaus mean : 35.2420
Gaus Width: 3.6962
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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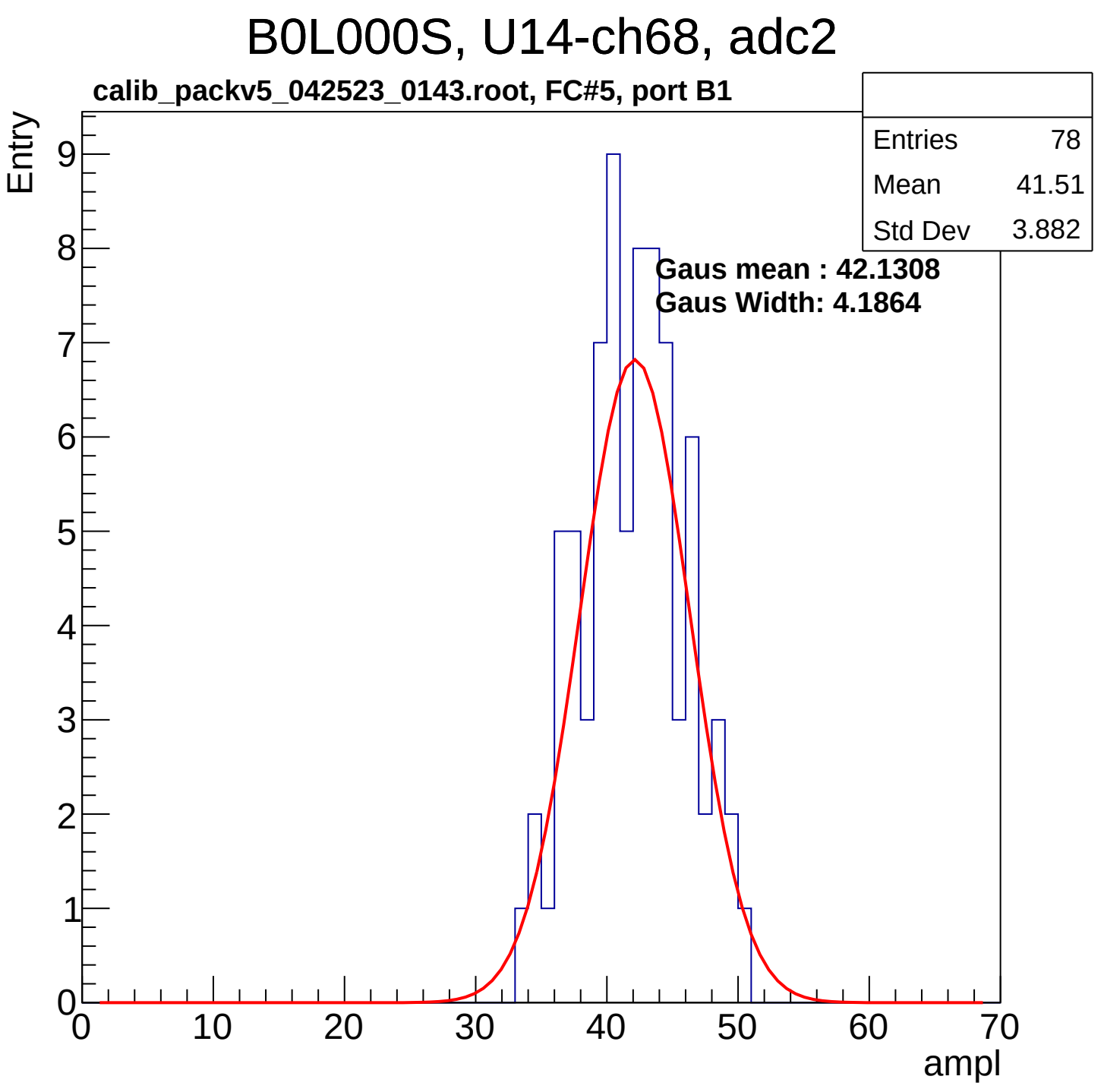
B0L000S, U14-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 41.51
Std Dev 3.882
Gaus mean : 42.1308
Gaus Width: 4.1864
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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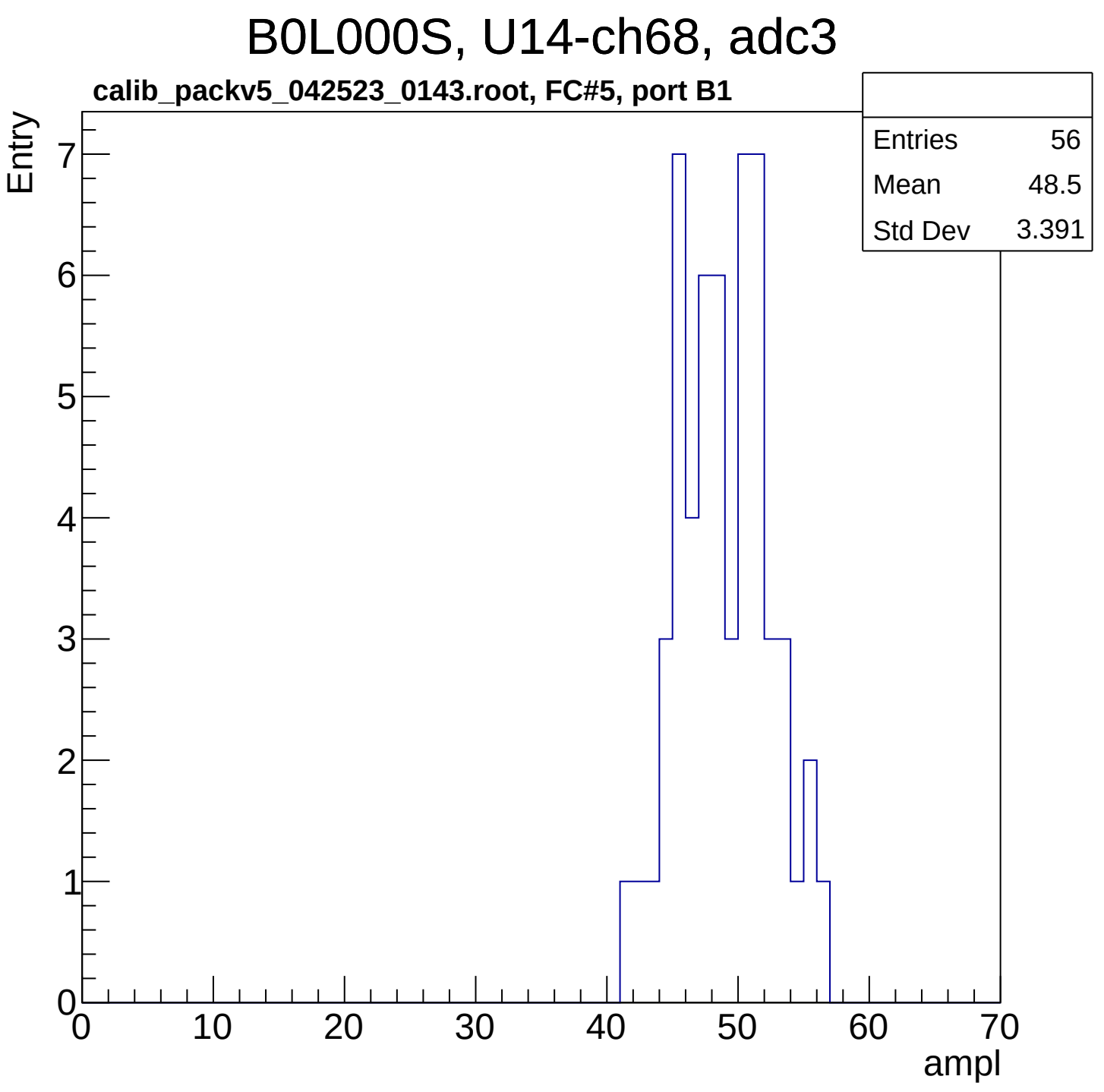
B0L000S, U14-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.5
Std Dev 3.391
Entry
ampl
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1
2
3
4
5
6
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0
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40
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60
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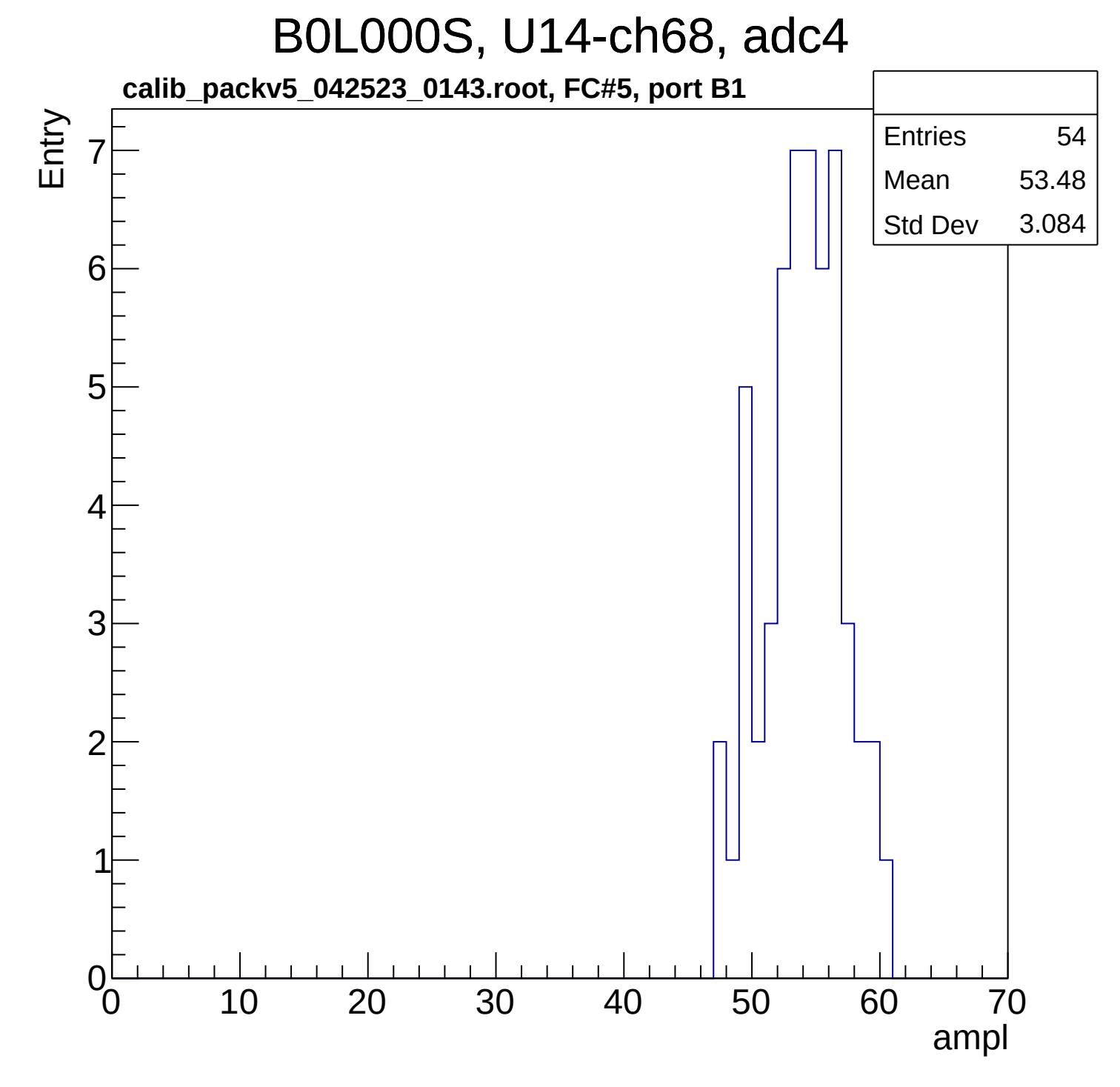
B0L000S, U14-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.48
Std Dev 3.084
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
50
60
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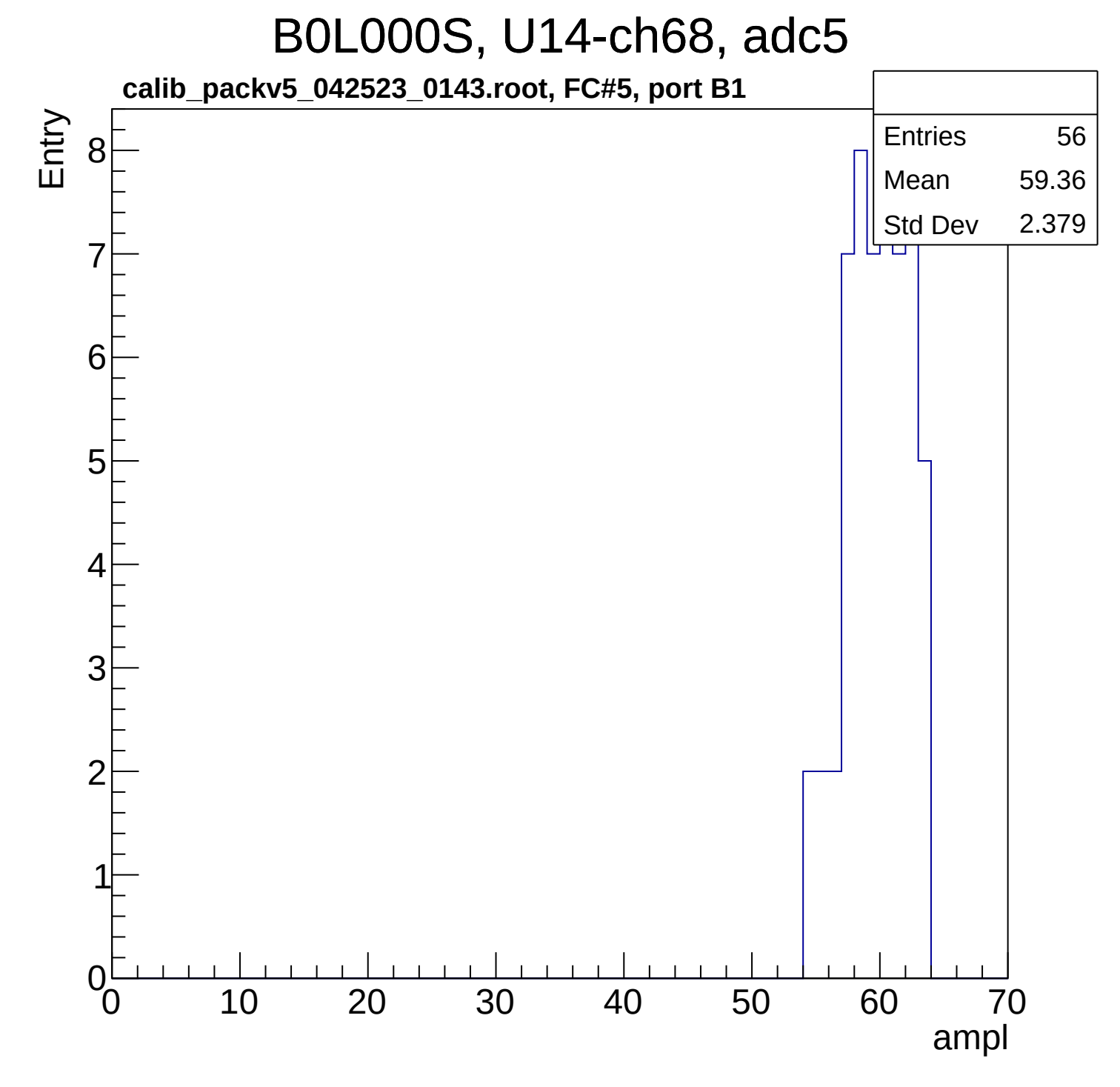
B0L000S, U14-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.36
Std Dev 2.379
Entry
ampl
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3
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5
6
7
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0
10
20
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40
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60
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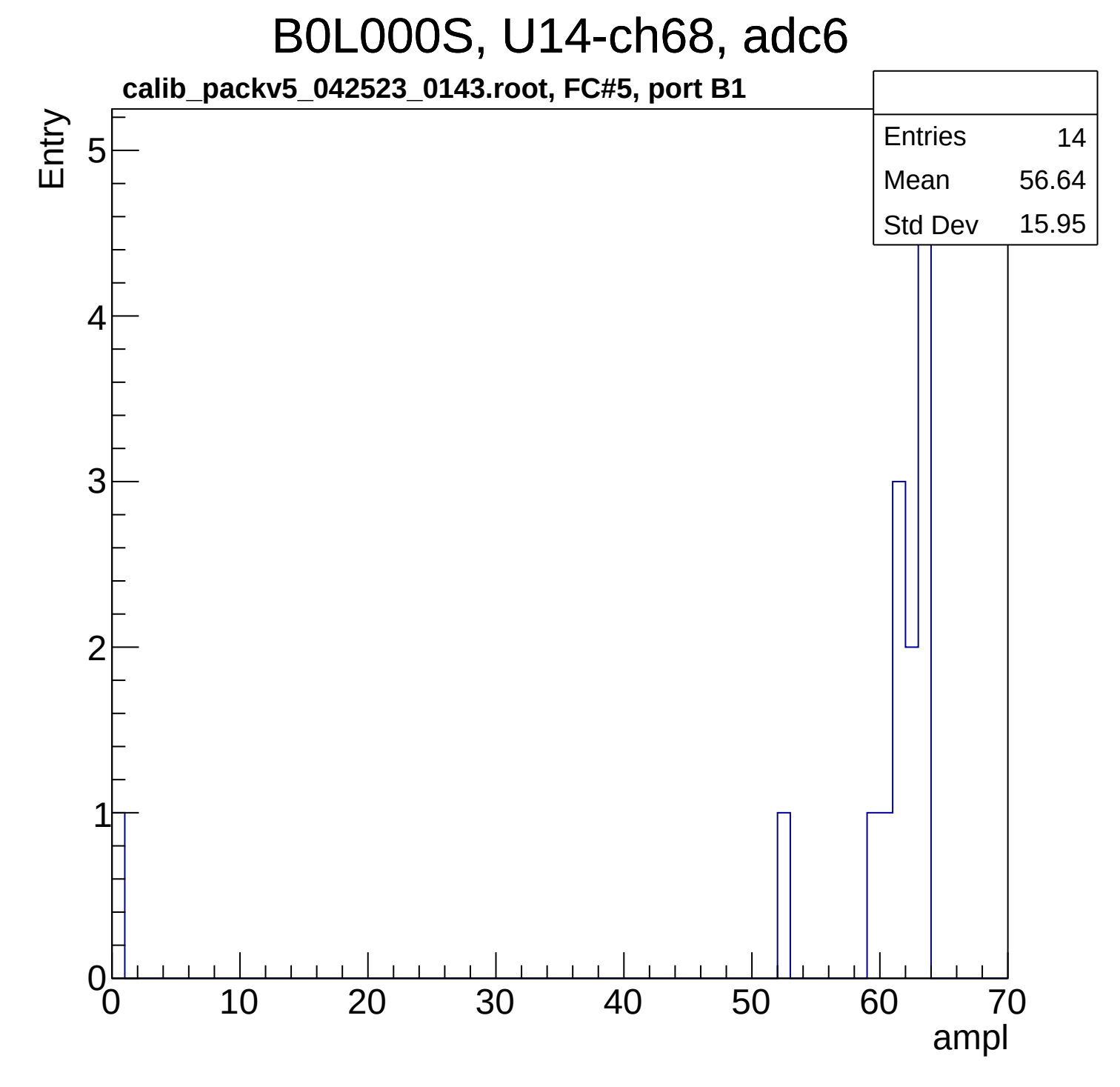
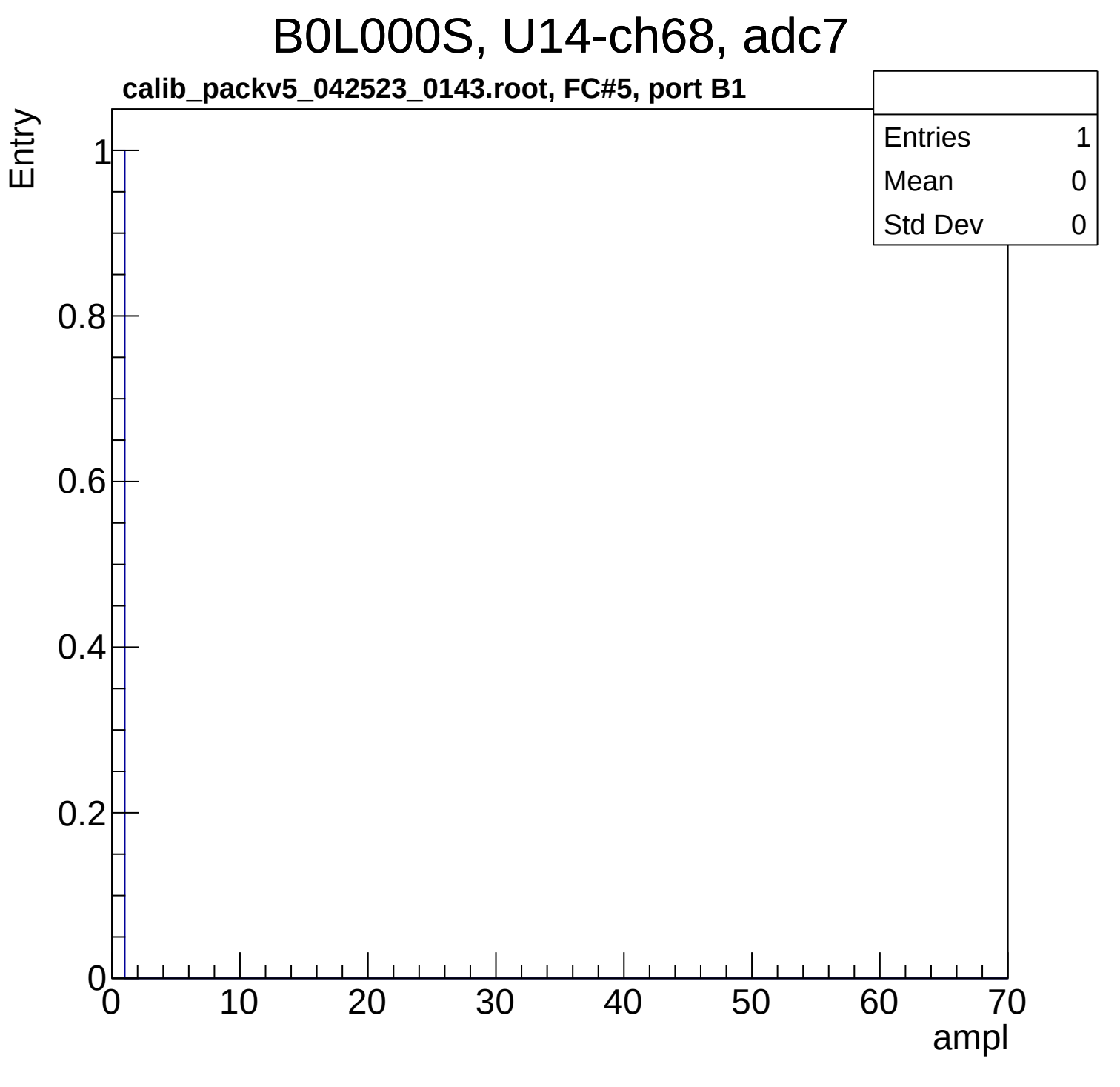

B0L000S, U14-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 56.64
Std Dev 15.95
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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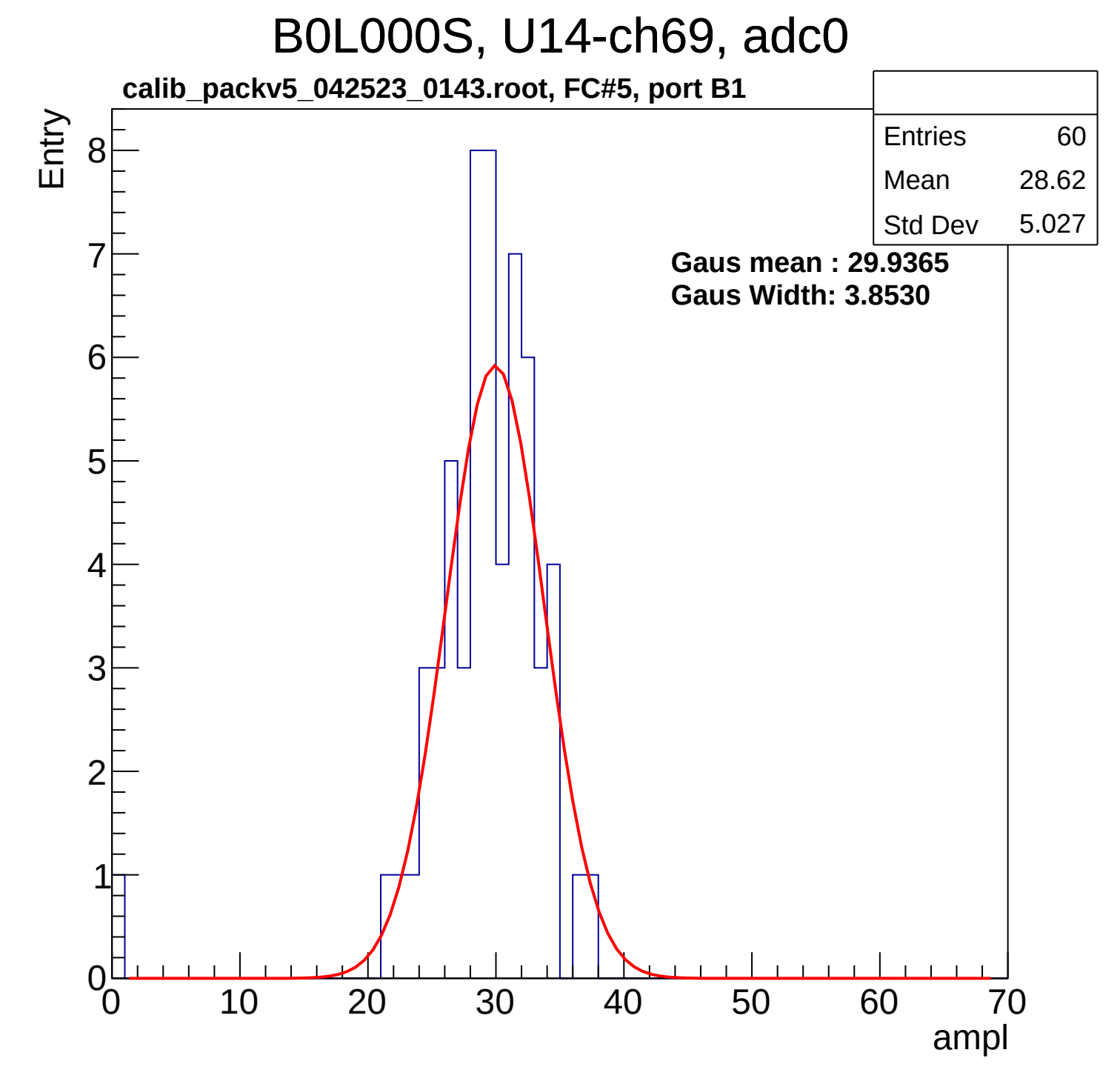
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.62
Std Dev 5.027
Gaus mean : 29.9365
Gaus Width: 3.8530
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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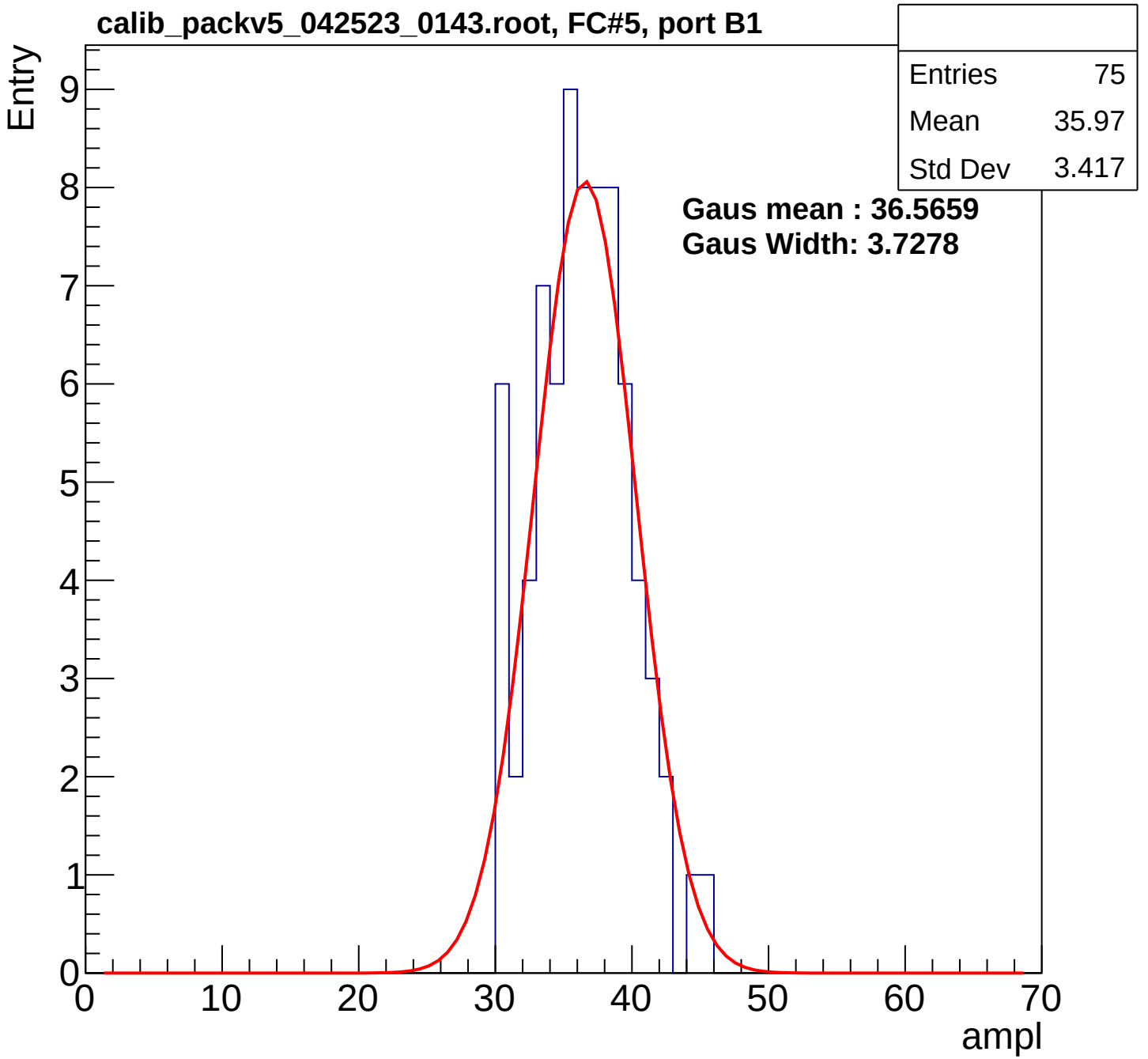
B0L000S, U14-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.97
Std Dev 3.417
Gaus mean : 36.5659
Gaus Width: 3.7278
Entry
ampl
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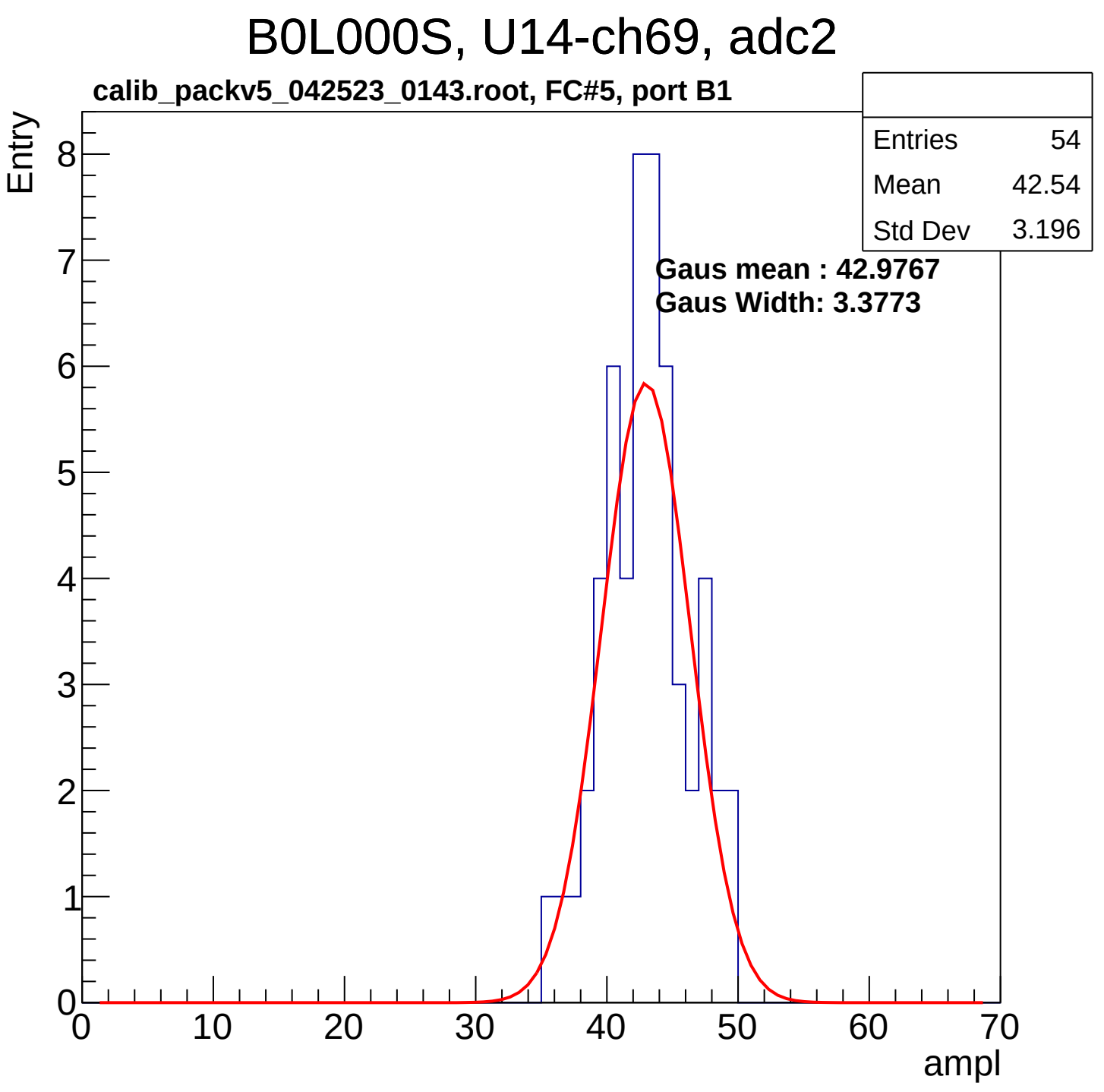
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 42.54
Std Dev 3.196
Gaus mean : 42.9767
Gaus Width: 3.3773
Entry
ampl
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60
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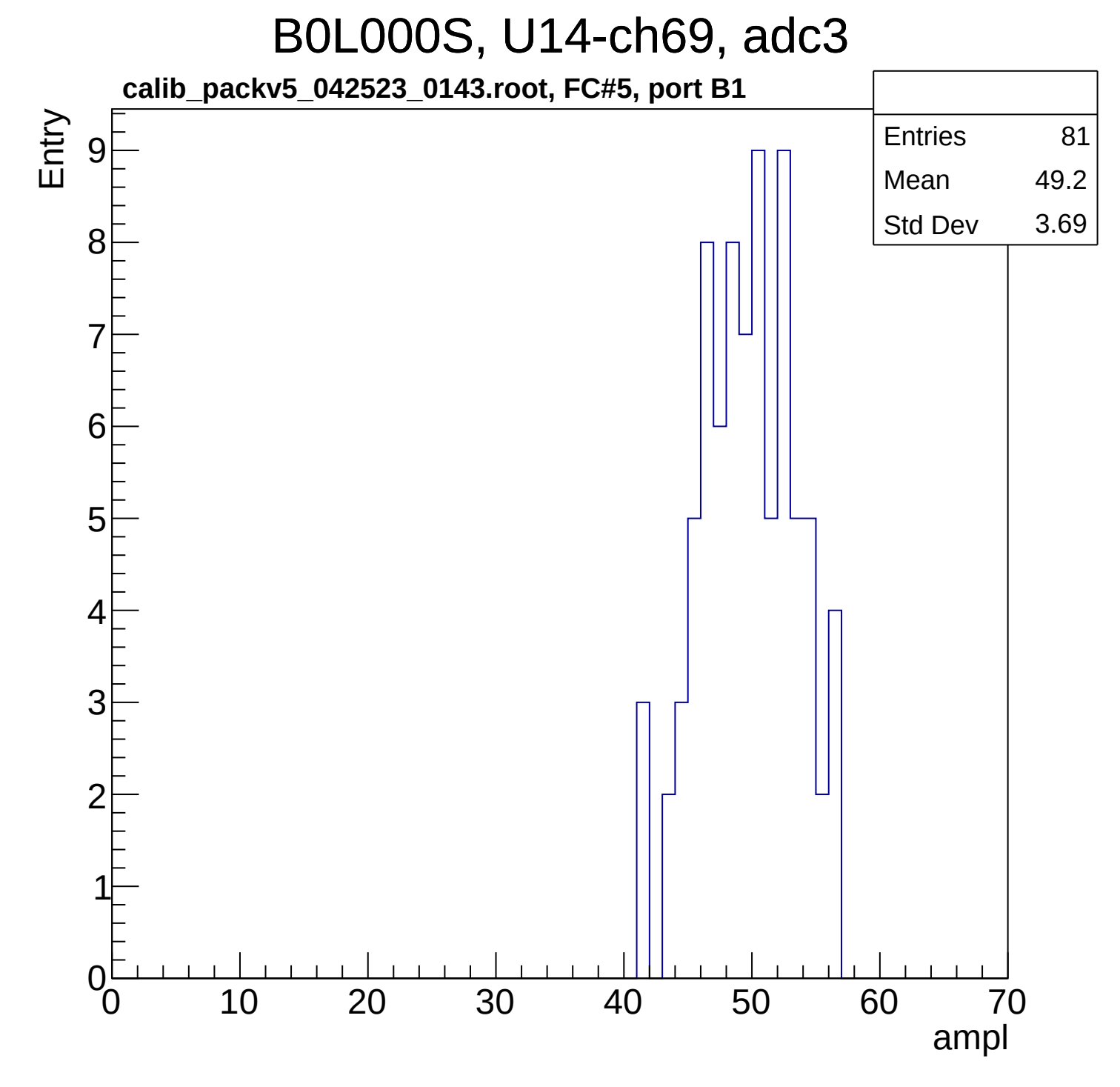
B0L000S, U14-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 49.2
Std Dev 3.69
Entry
ampl
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4
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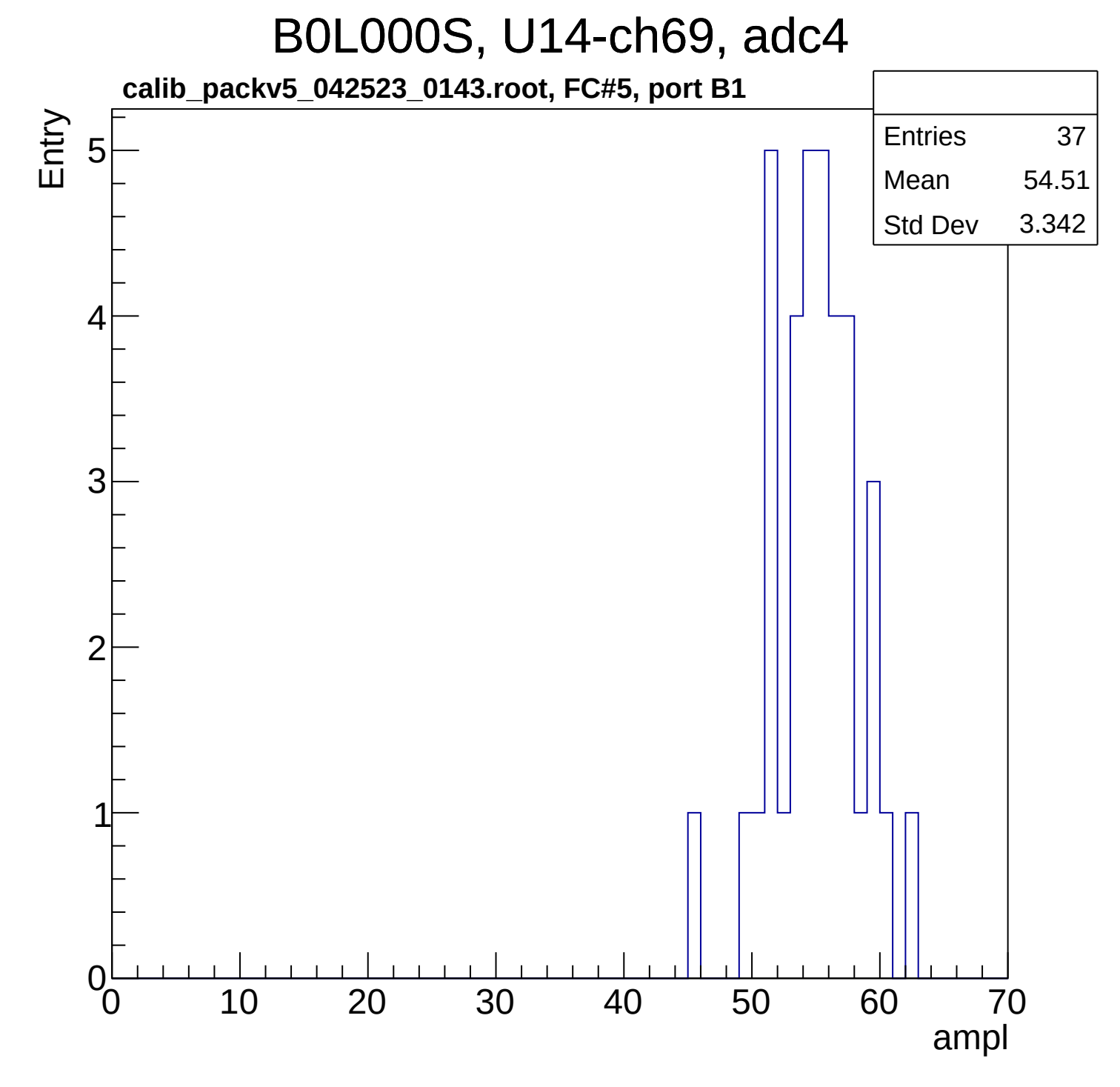
B0L000S, U14-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 54.51
Std Dev 3.342
Entry
ampl
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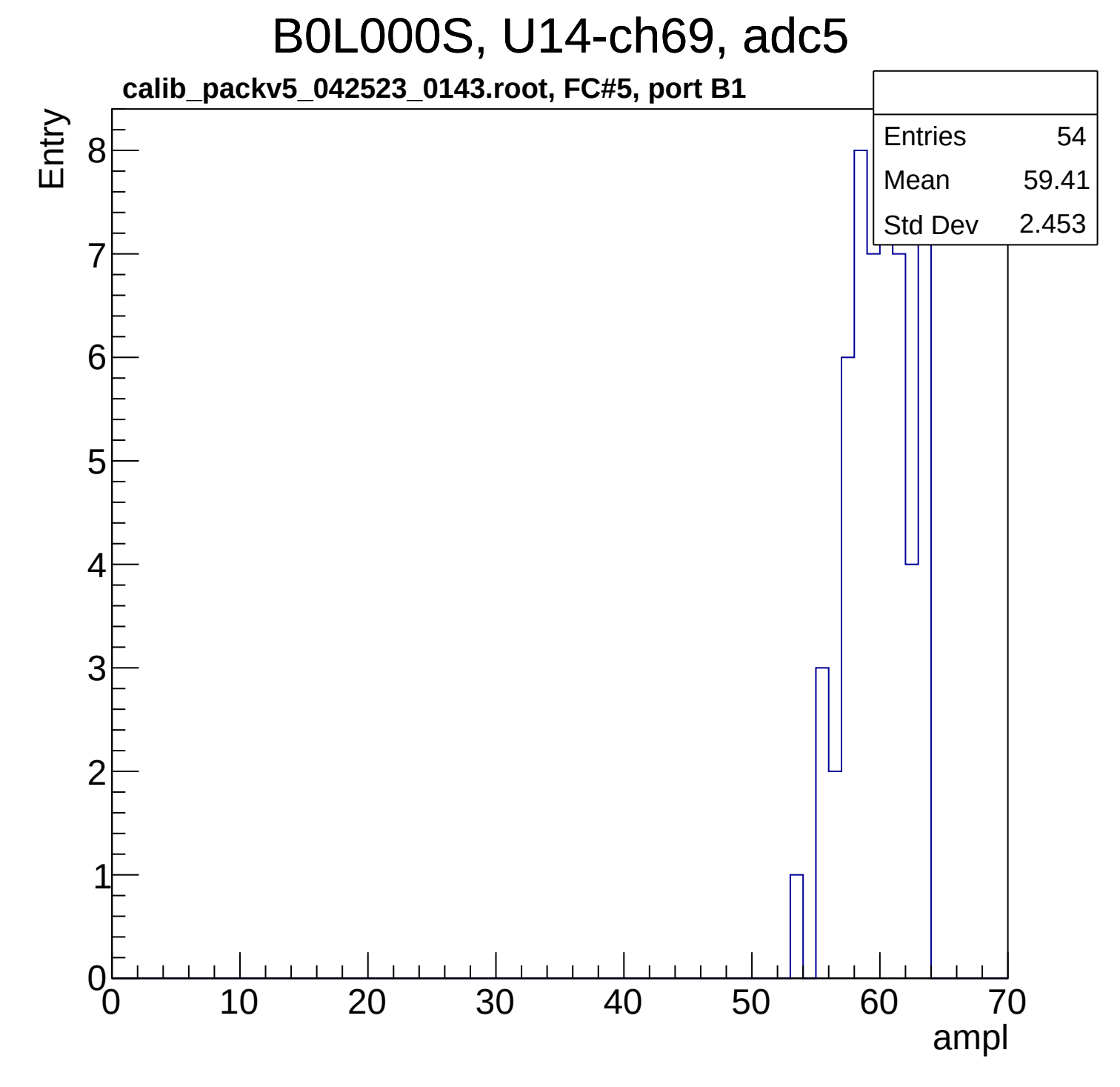
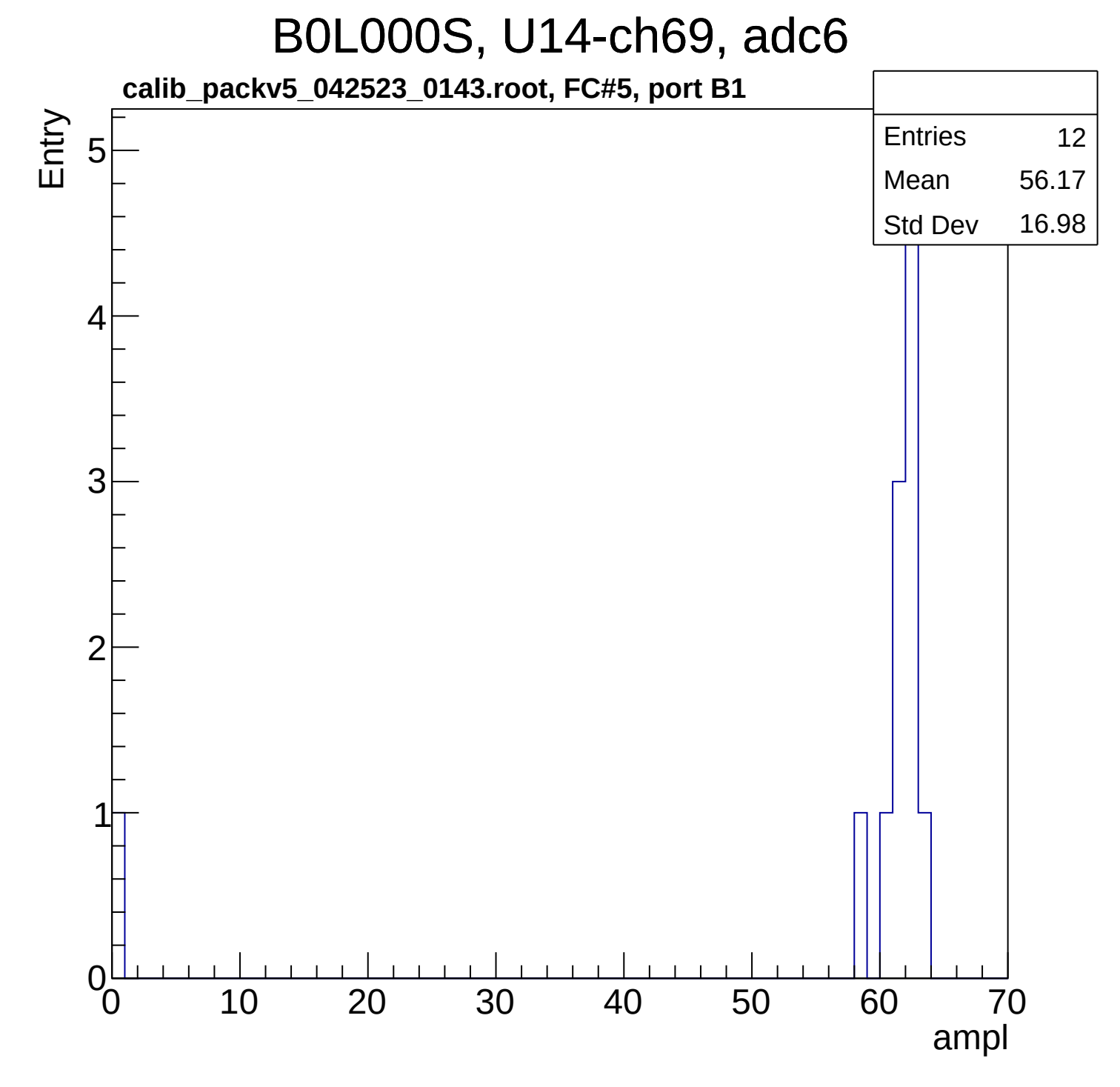
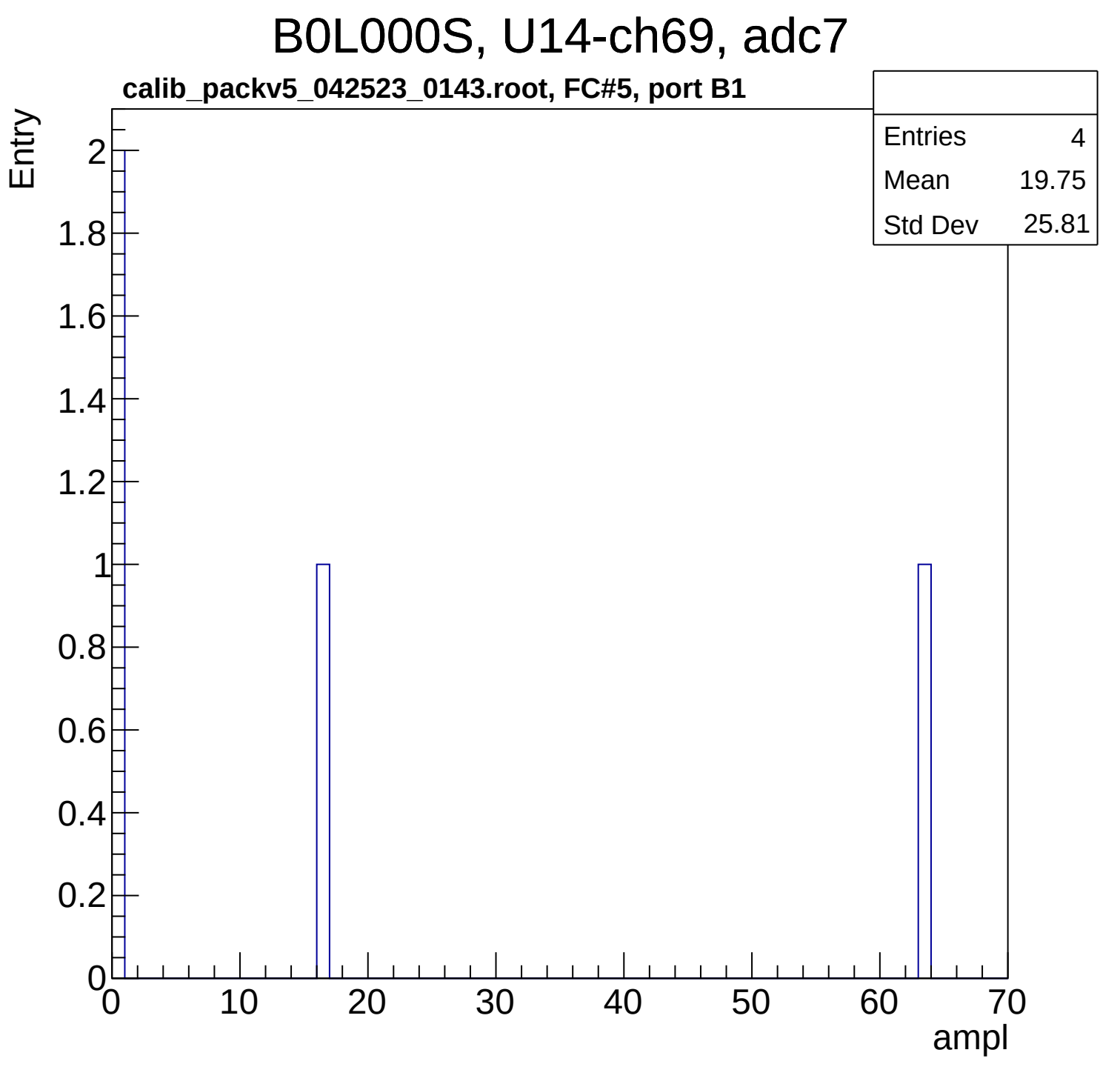

B0L000S, U14-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.41
Std Dev 2.453
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 56.17
Std Dev 16.98
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 19.75
Std Dev 25.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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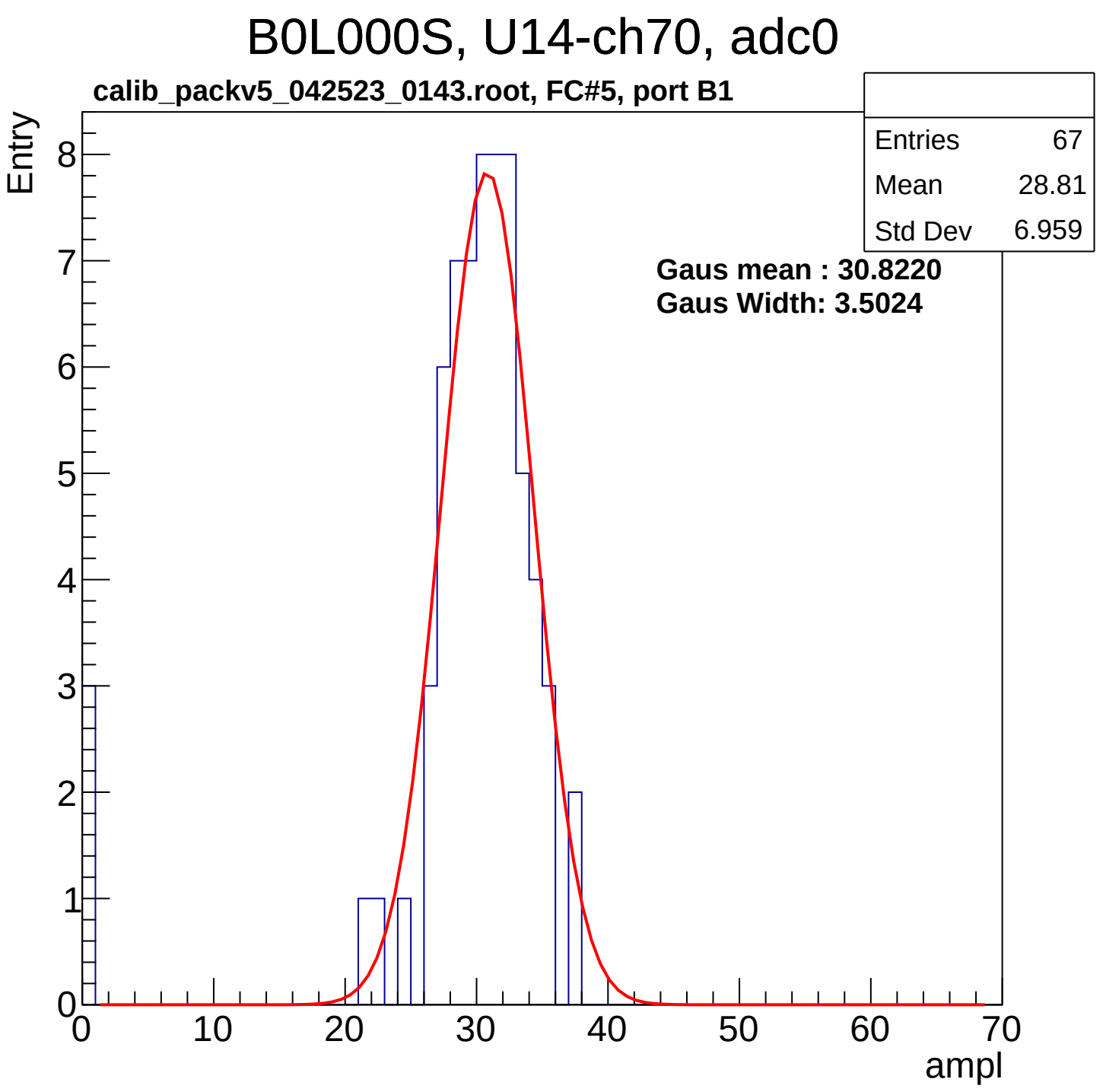
B0L000S, U14-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 28.81
Std Dev 6.959
Gaus mean : 30.8220
Gaus Width: 3.5024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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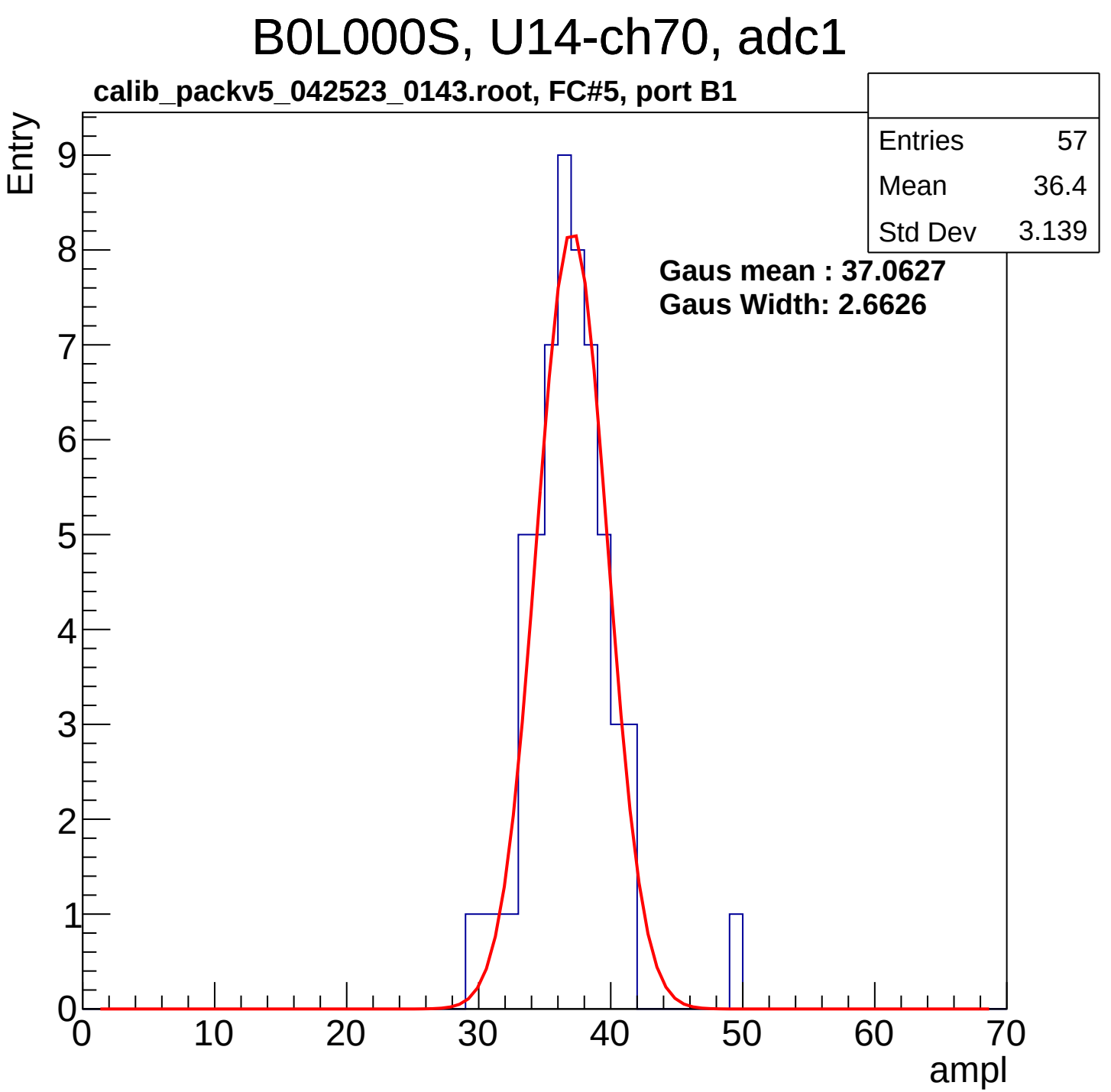
B0L000S, U14-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.4
Std Dev 3.139
Gaus mean : 37.0627
Gaus Width: 2.6626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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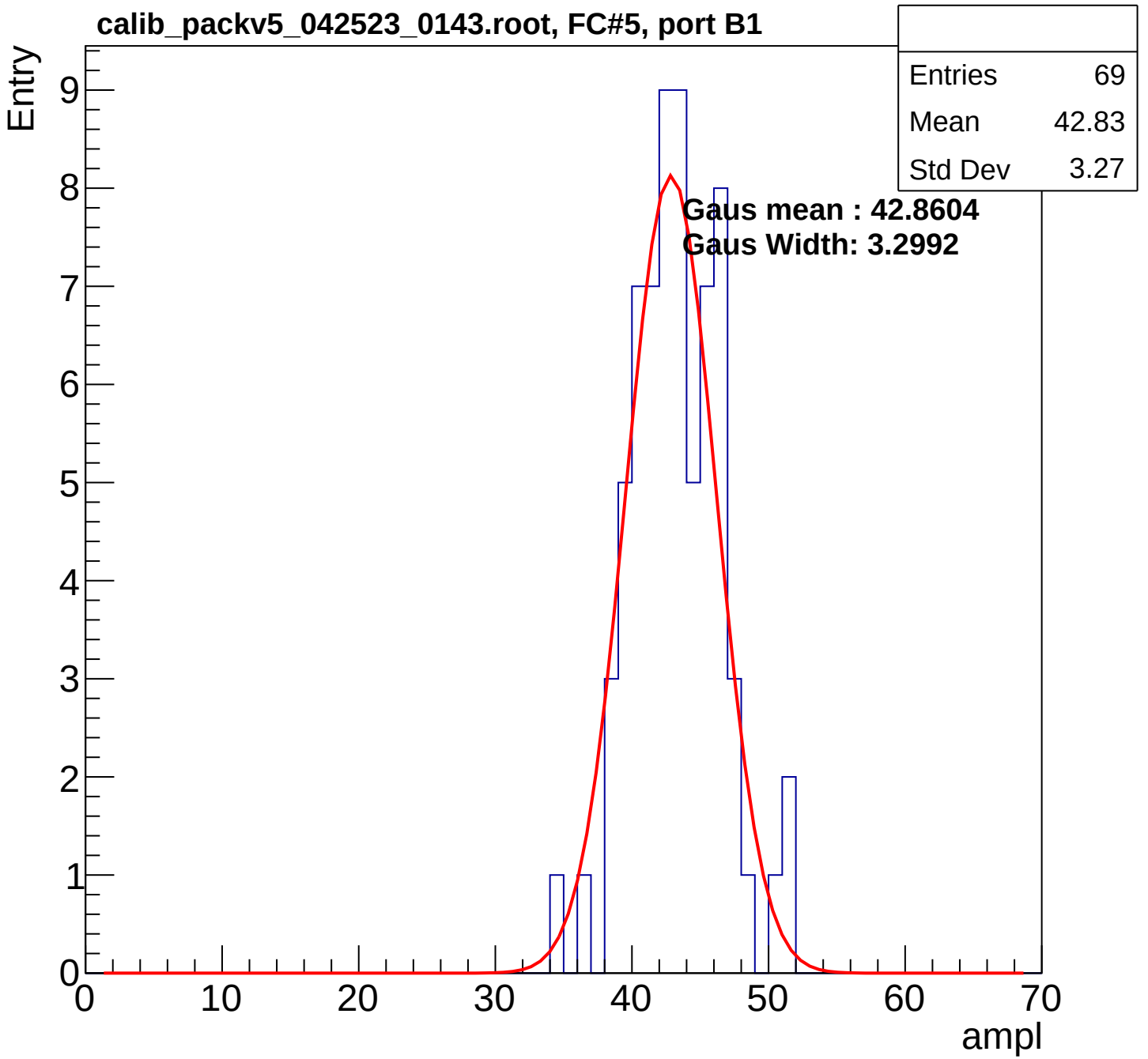
B0L000S, U14-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.83
Std Dev 3.27
Gaus mean : 42.8604
Gaus Width: 3.2992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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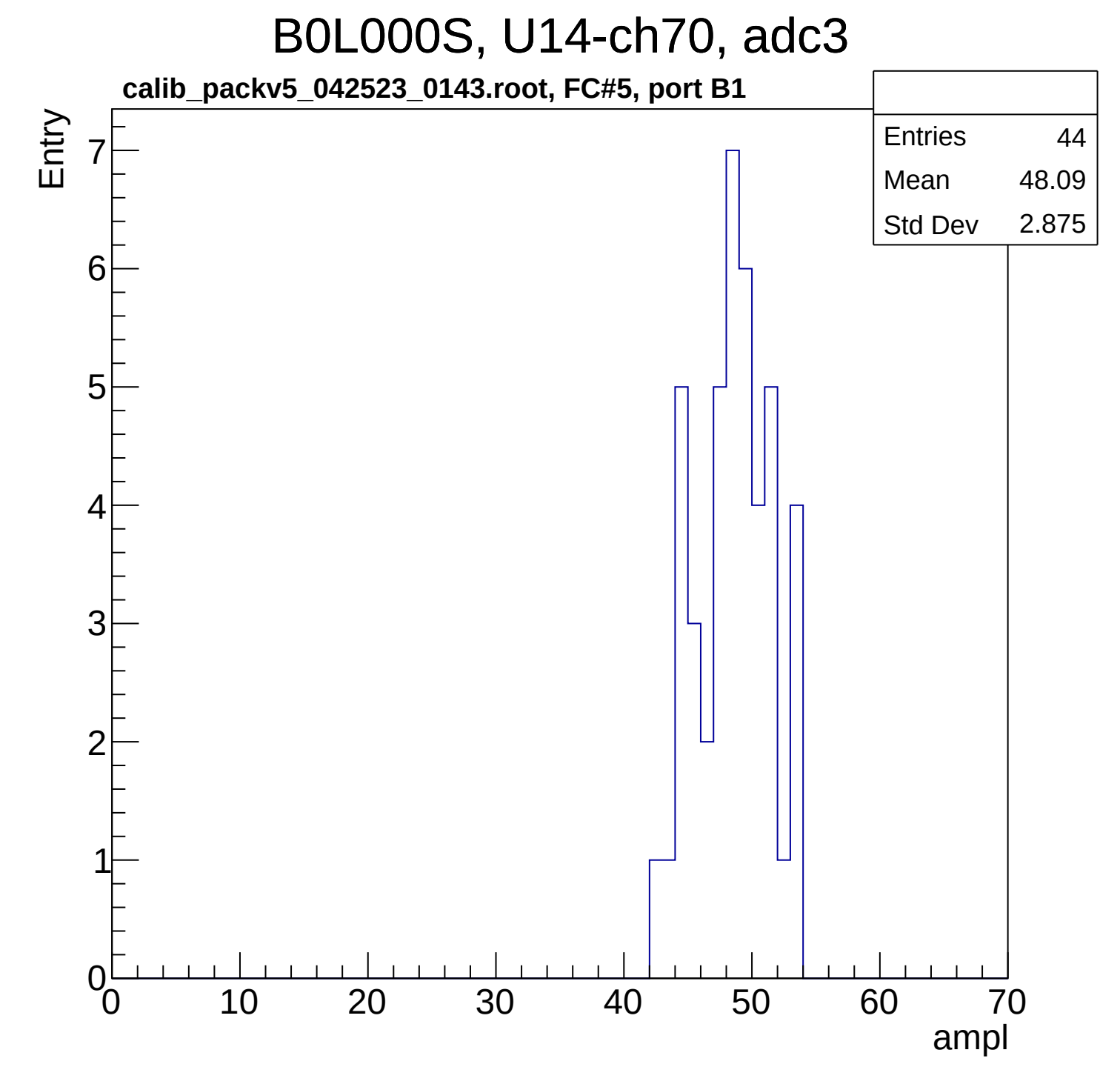
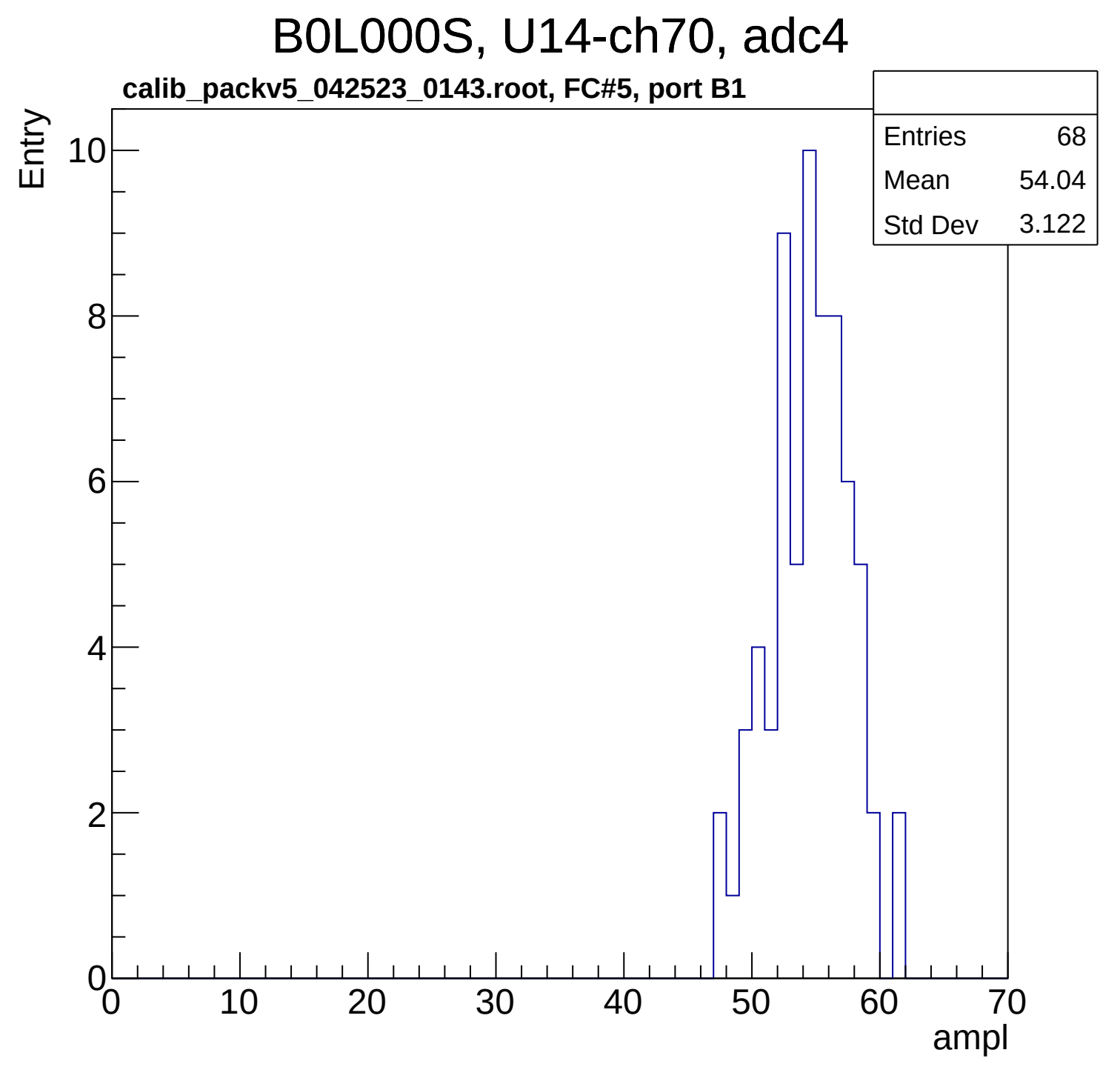

B0L000S, U14-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 48.09
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 54.04
Std Dev 3.122
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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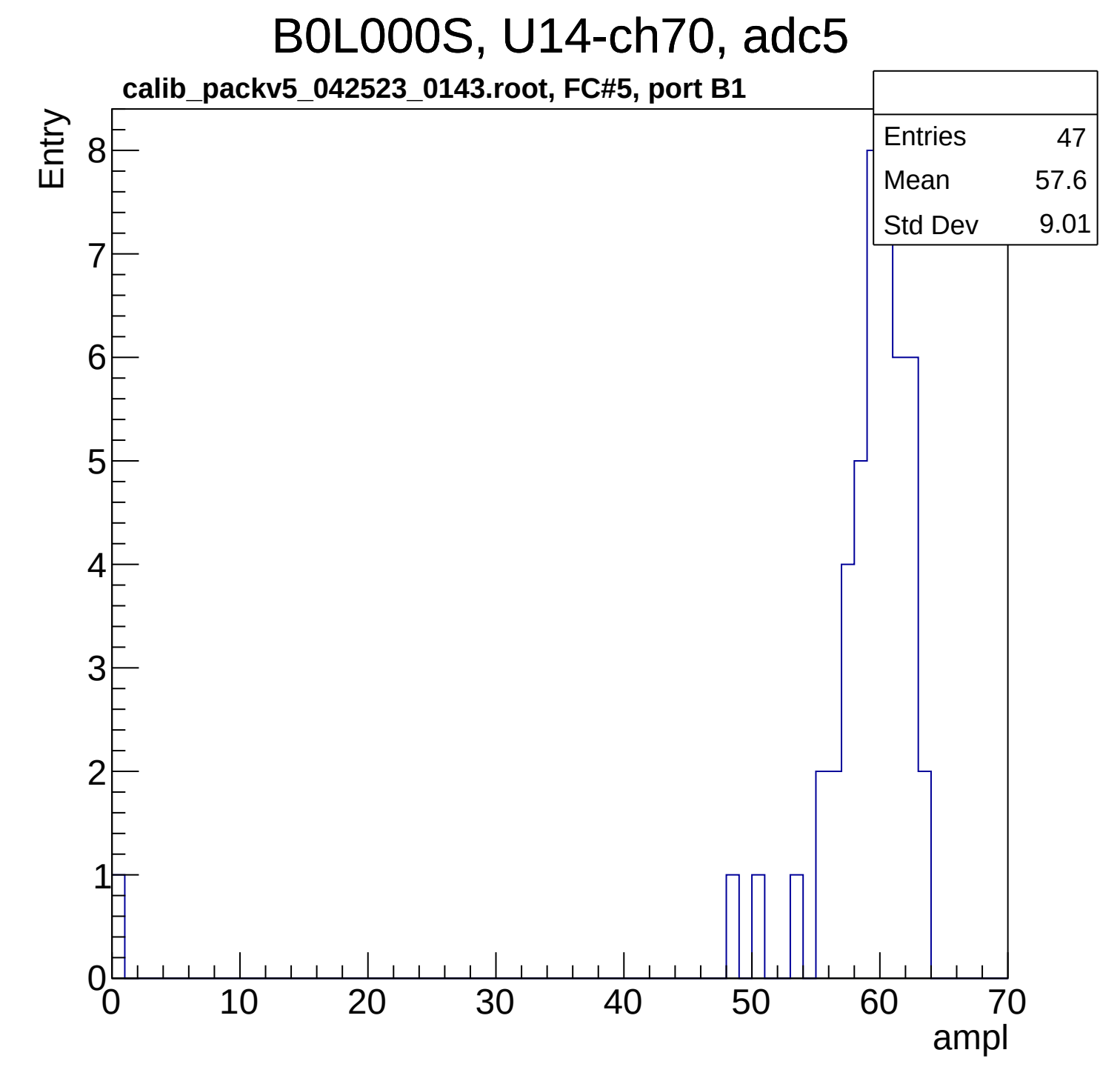
B0L000S, U14-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.6
Std Dev 9.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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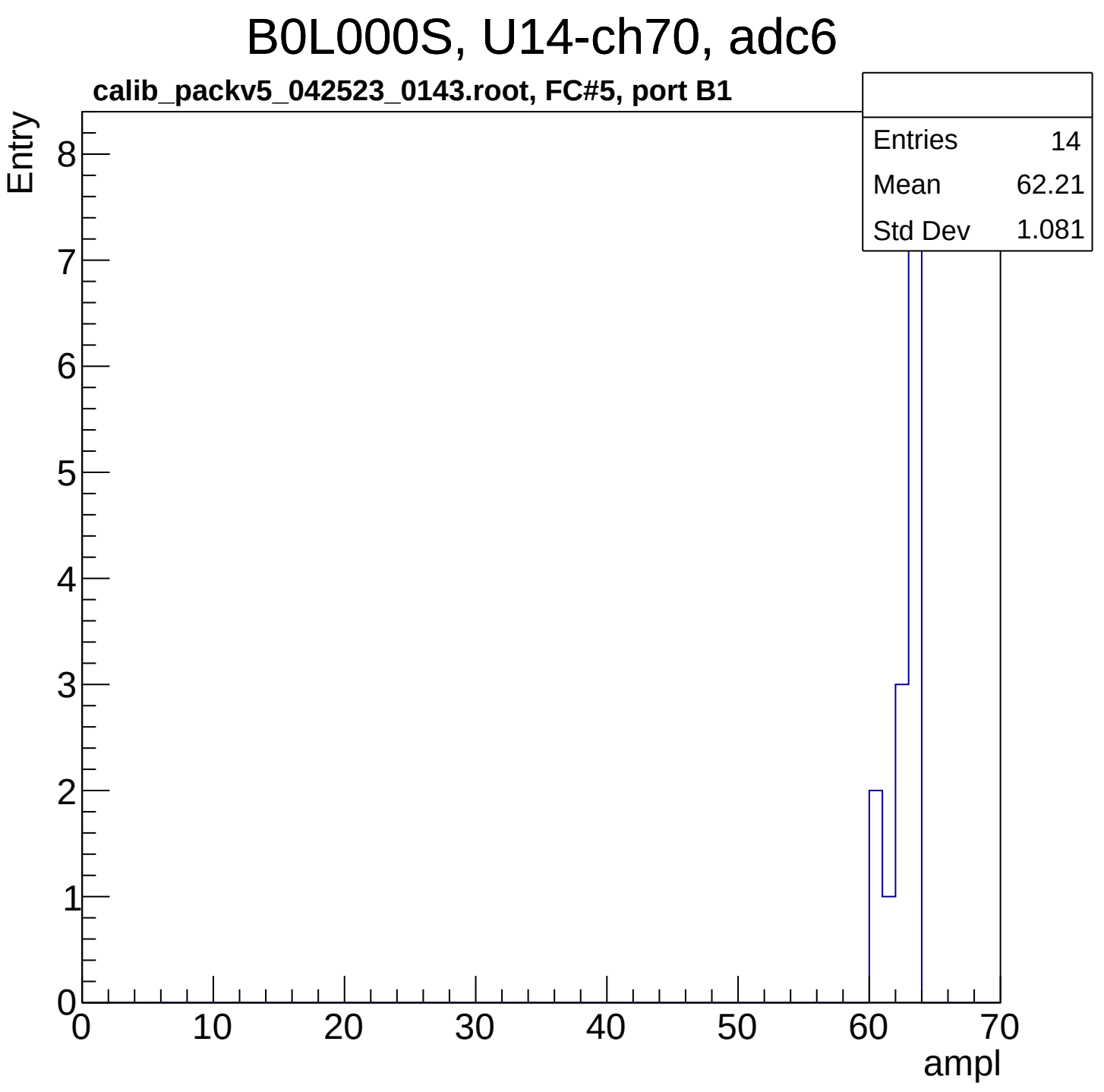
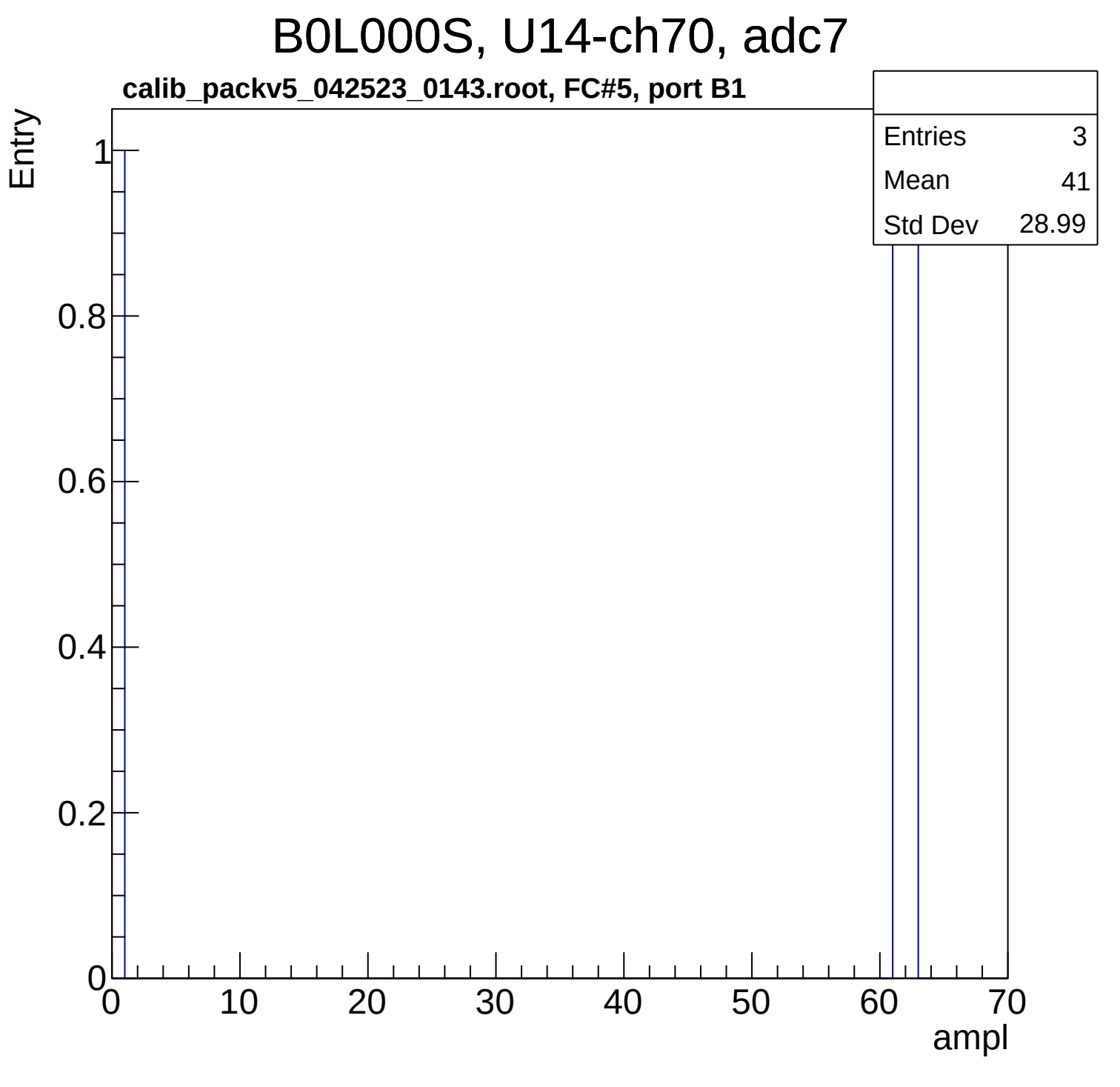
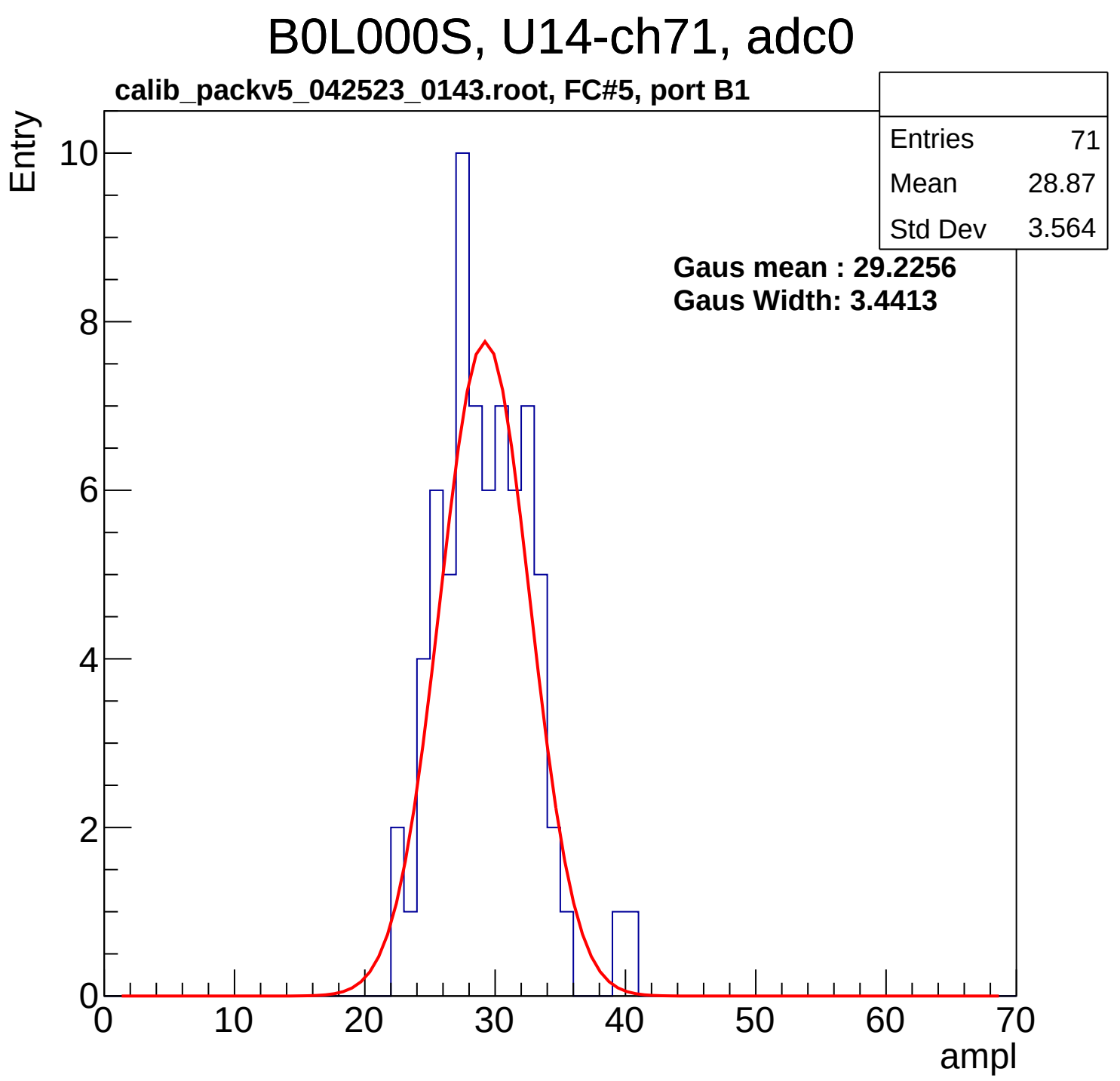
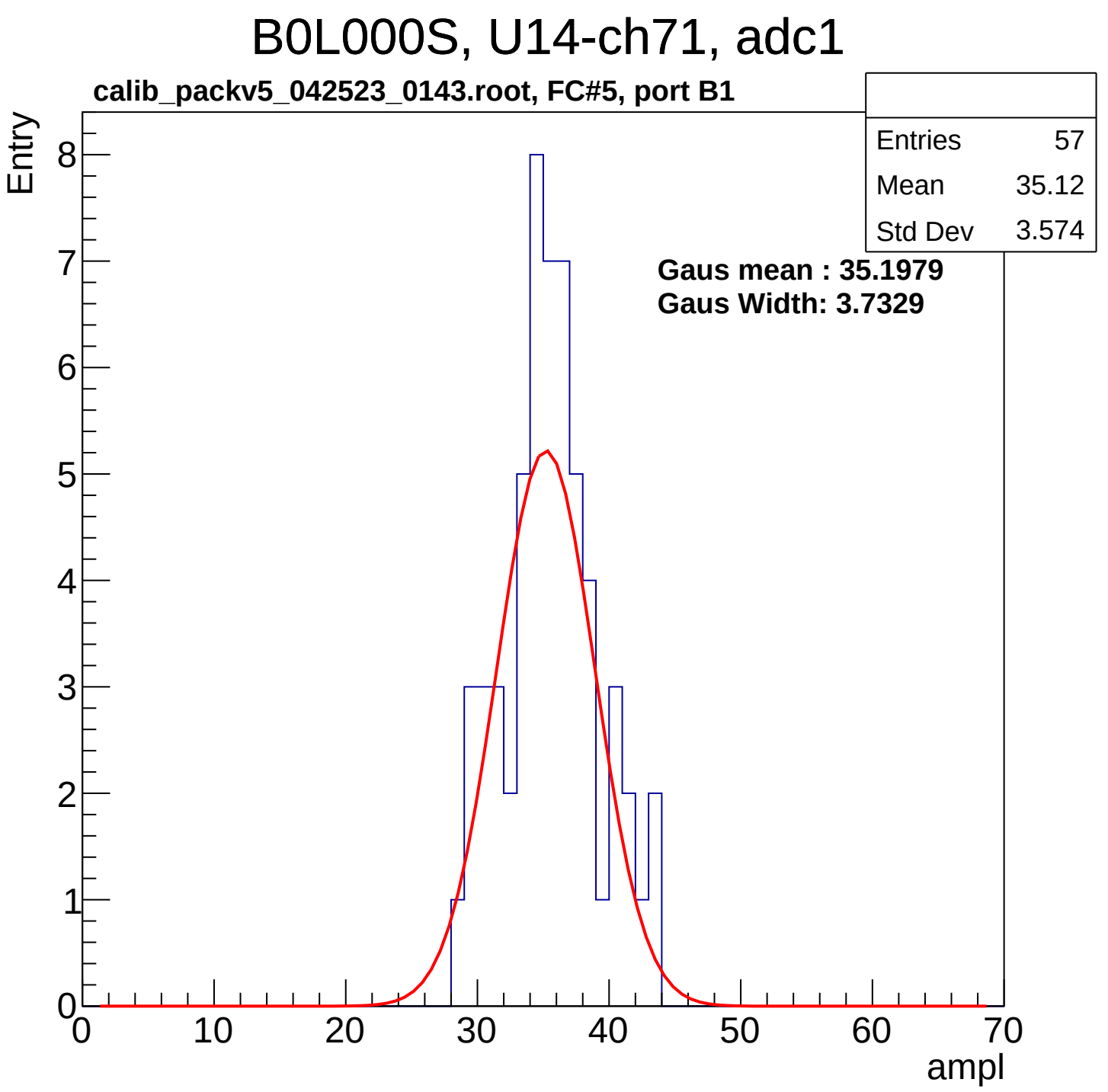

B0L000S, U14-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 62.21
Std Dev 1.081
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.87
Std Dev 3.564
Gaus mean : 29.2256
Gaus Width: 3.4413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.12
Std Dev 3.574
Gaus mean : 35.1979
Gaus Width: 3.7329
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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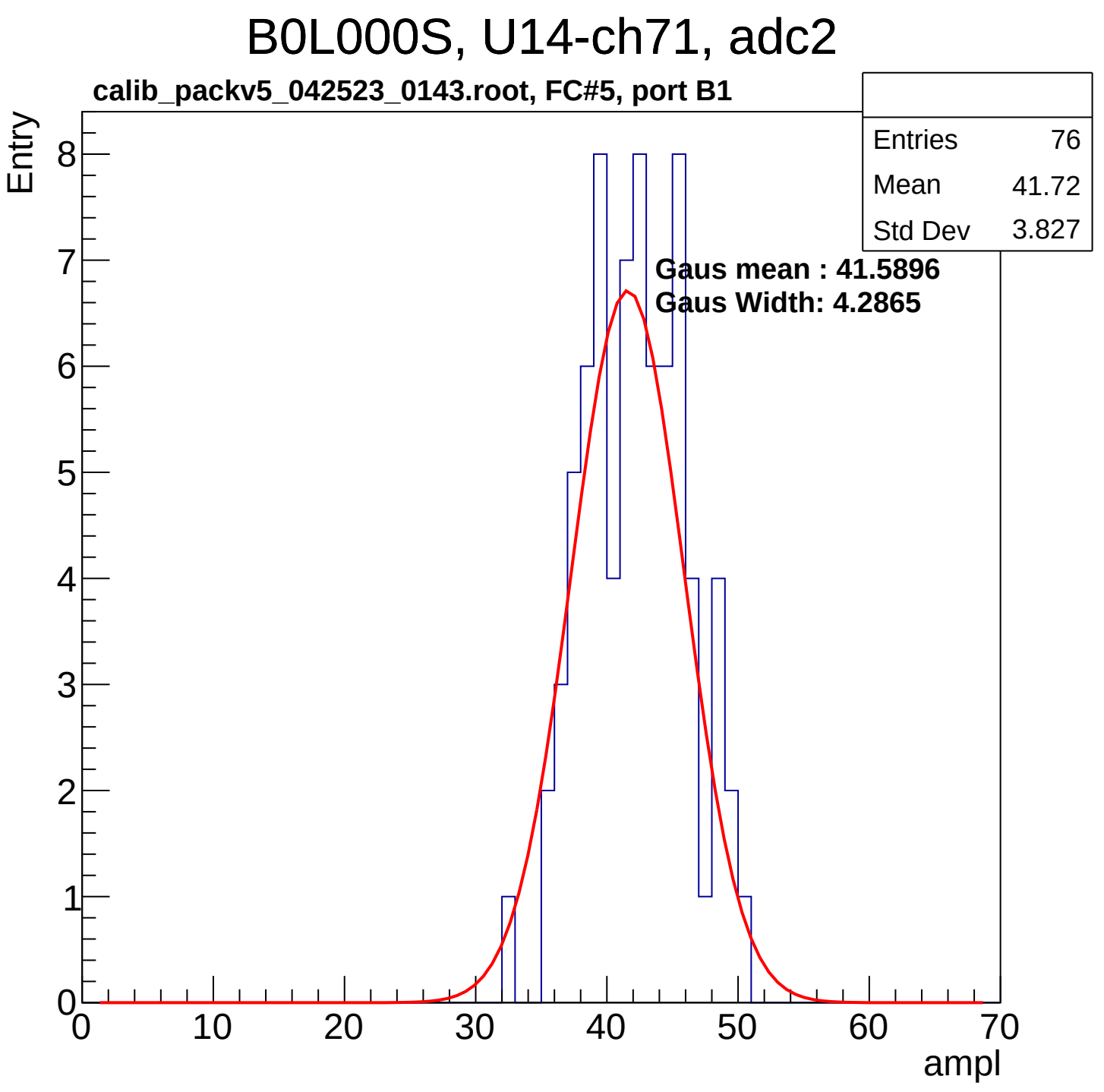
B0L000S, U14-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 41.72
Std Dev 3.827
Gaus mean : 41.5896
Gaus Width: 4.2865
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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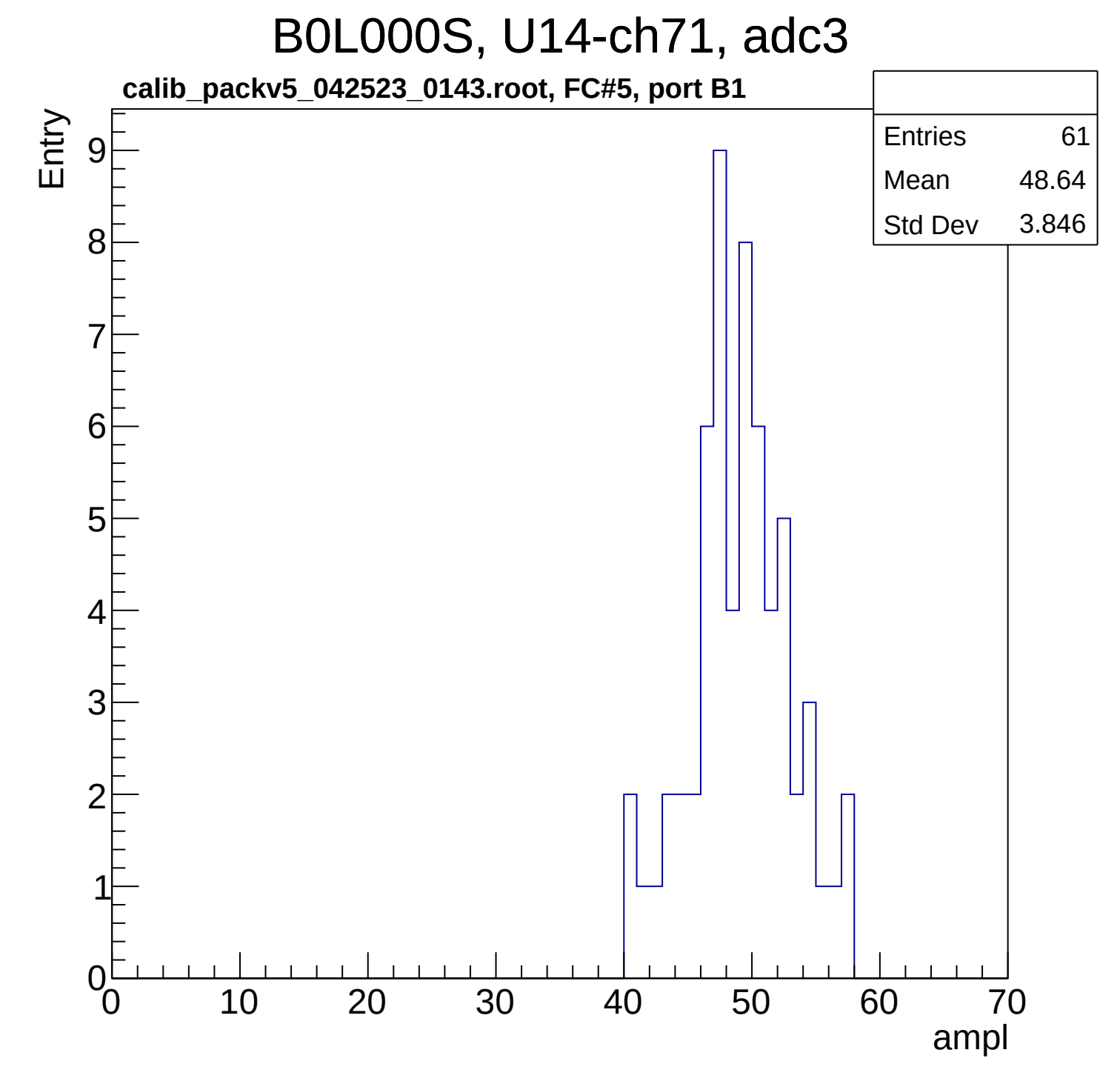
B0L000S, U14-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.64
Std Dev 3.846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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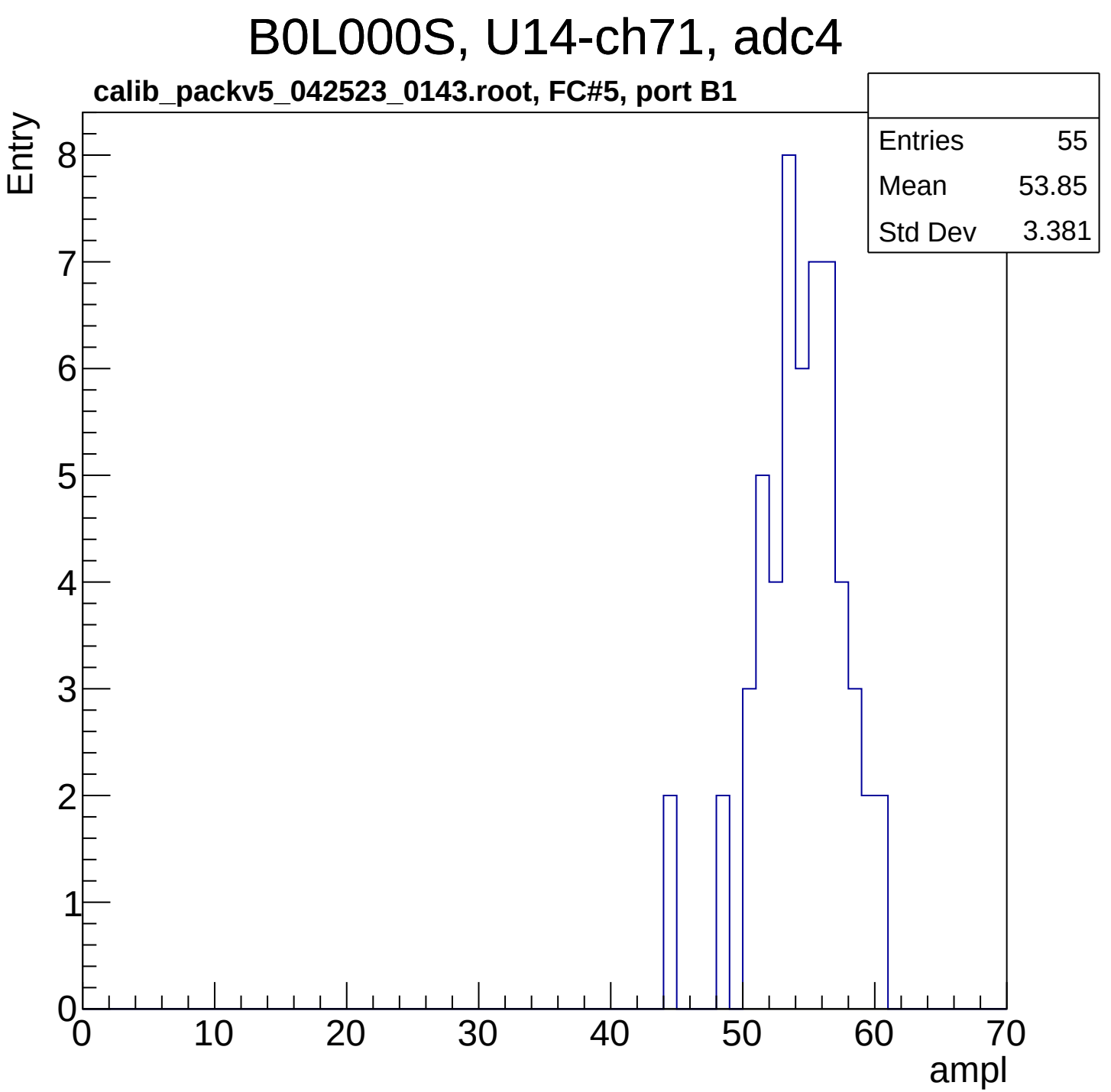
B0L000S, U14-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 53.85
Std Dev 3.381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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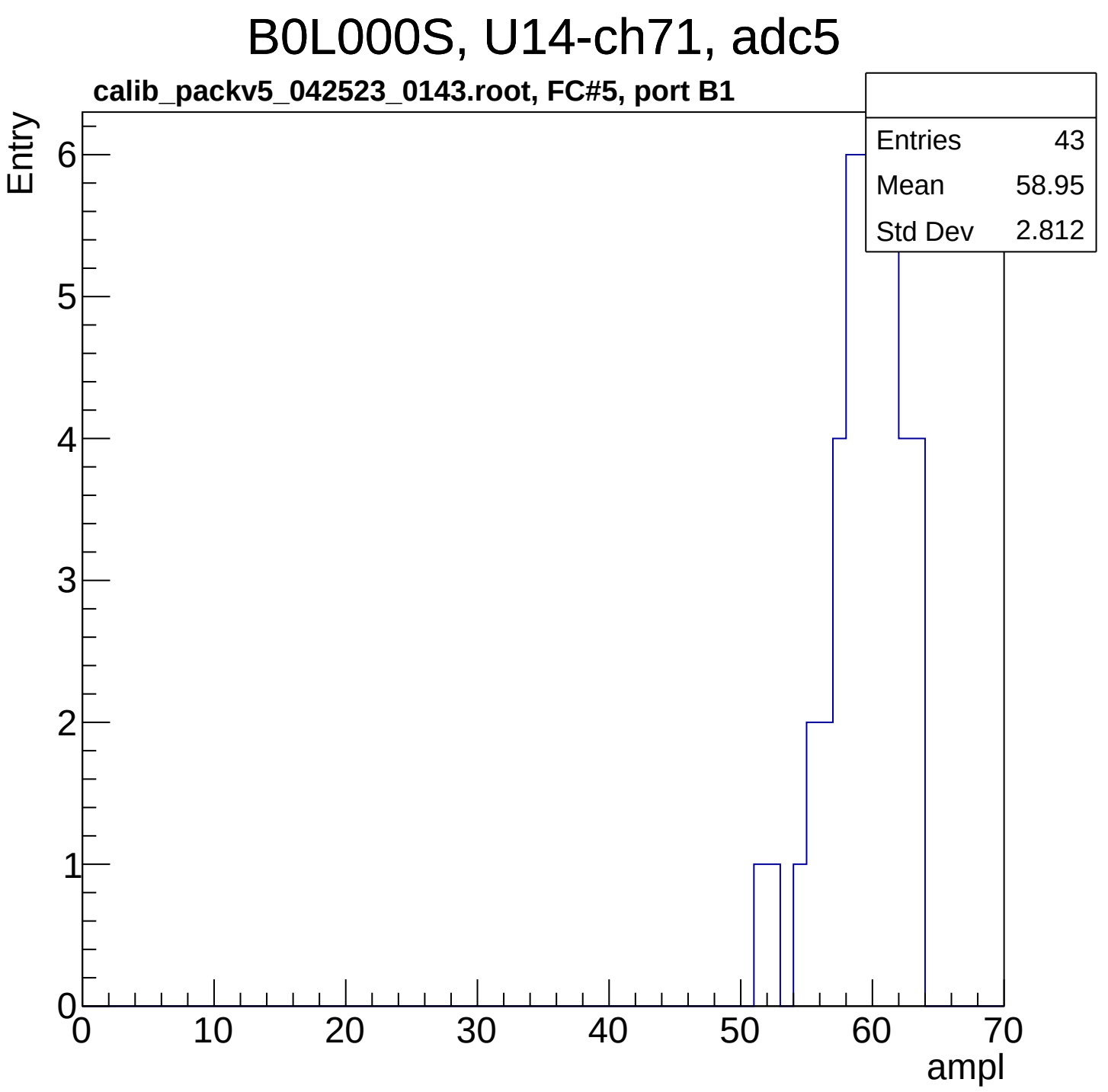
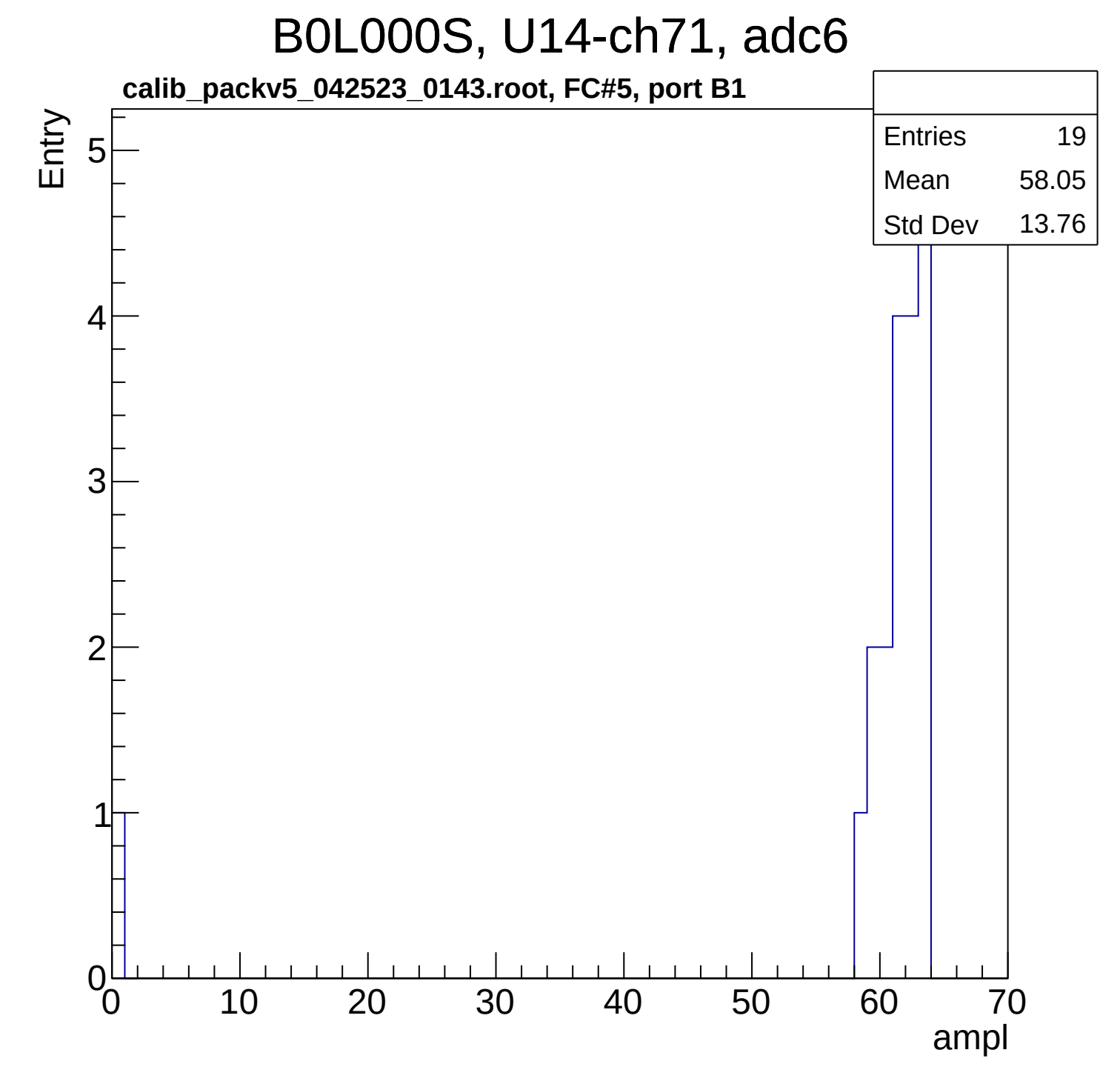
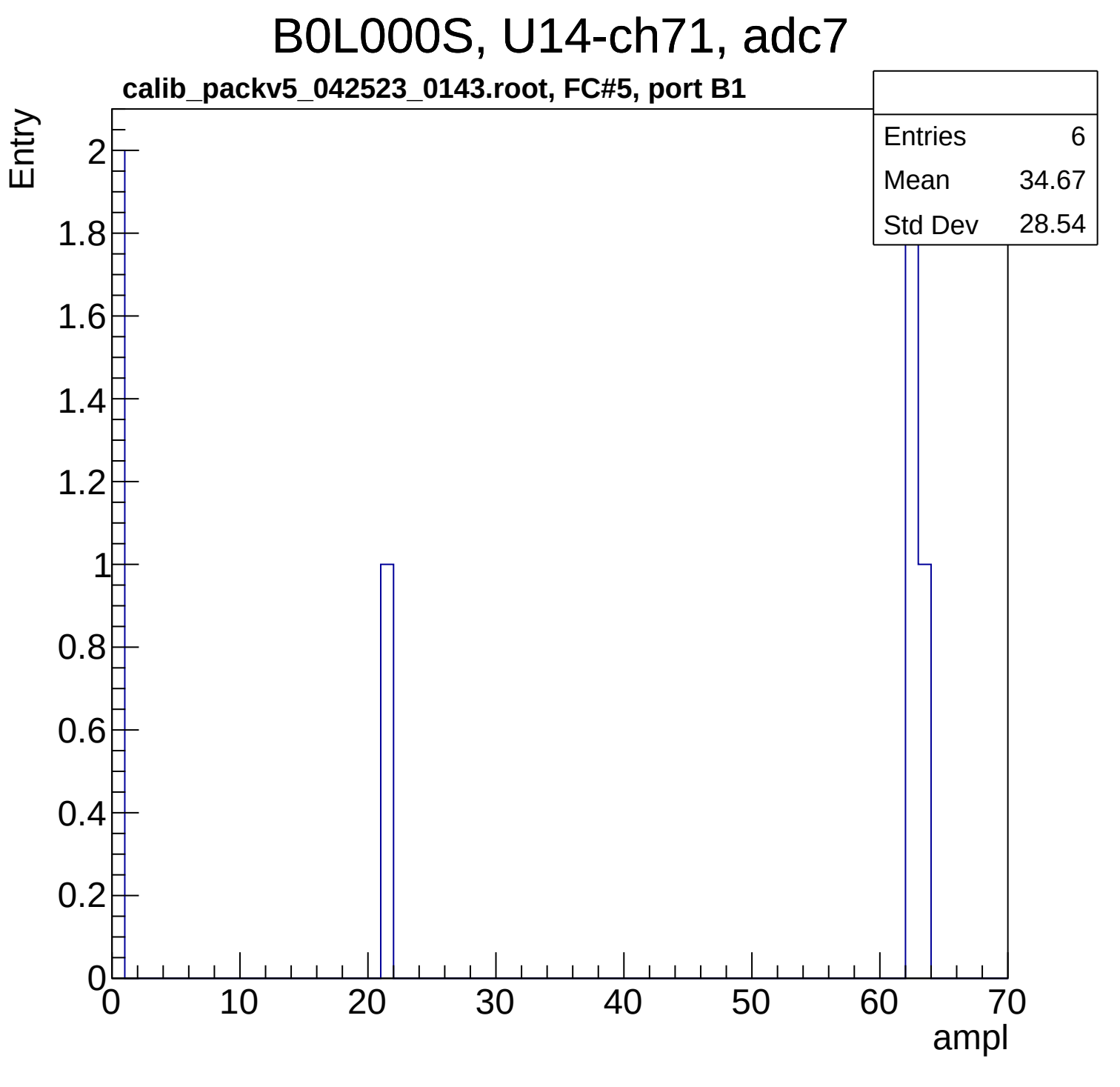
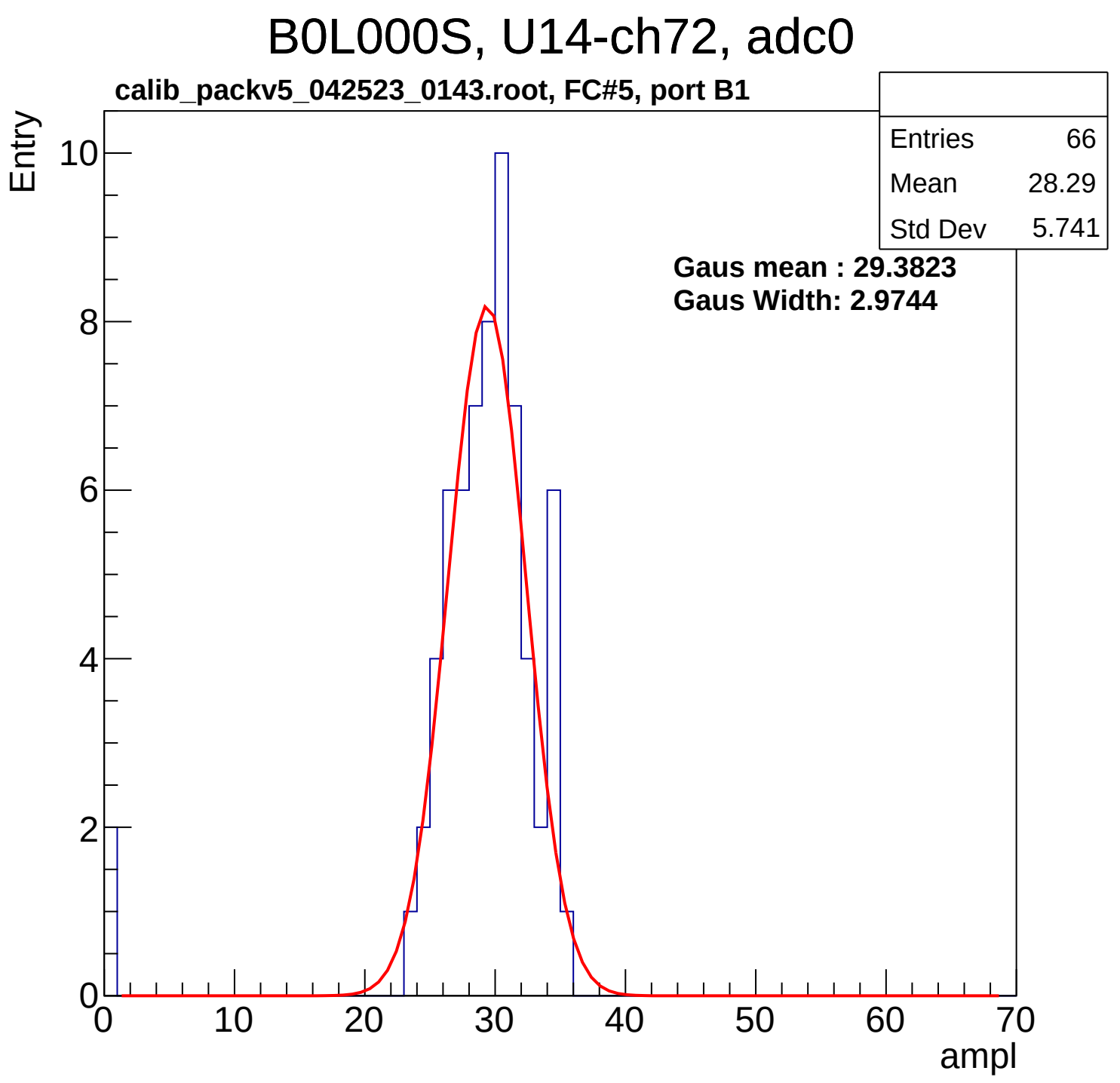
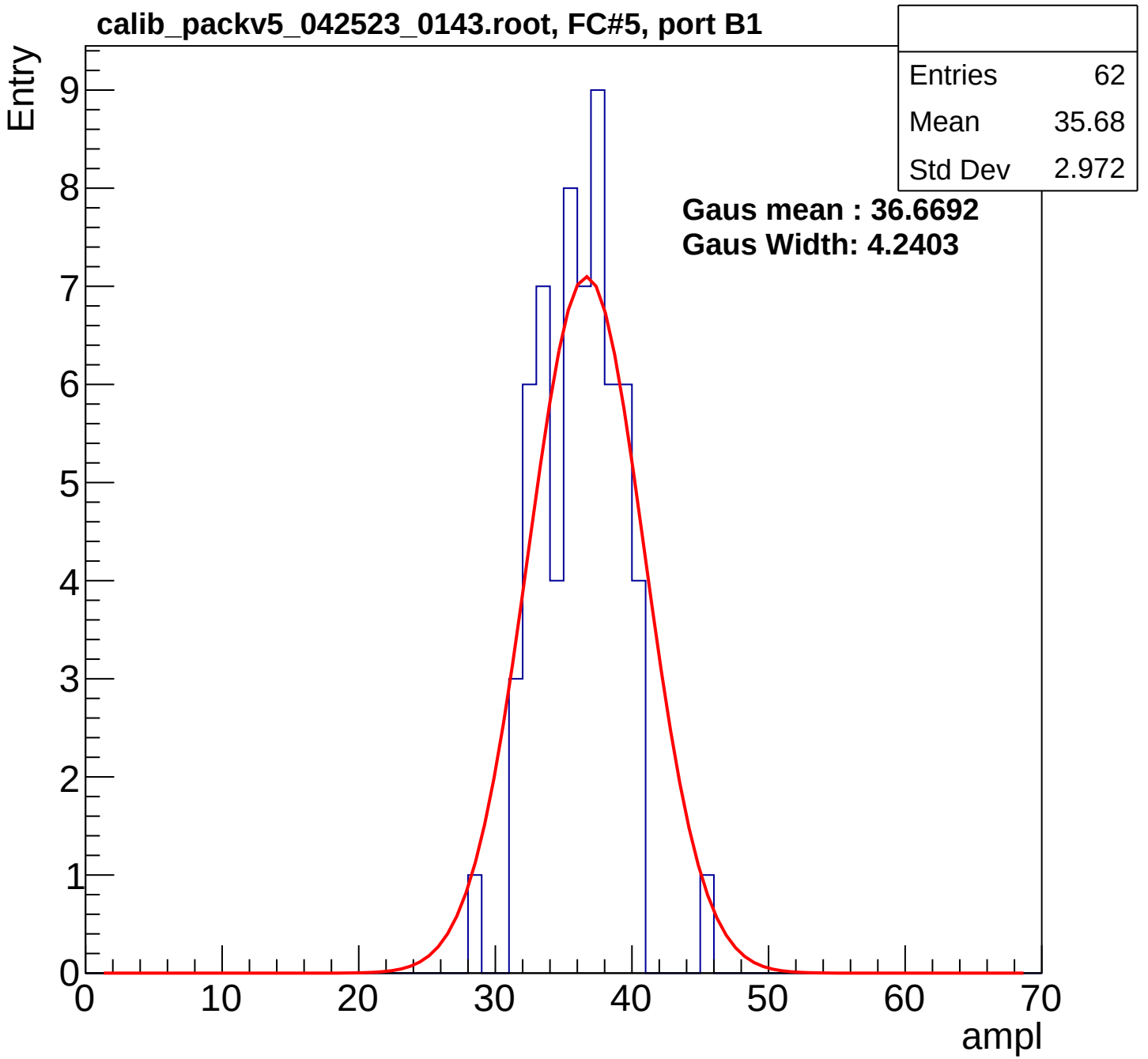
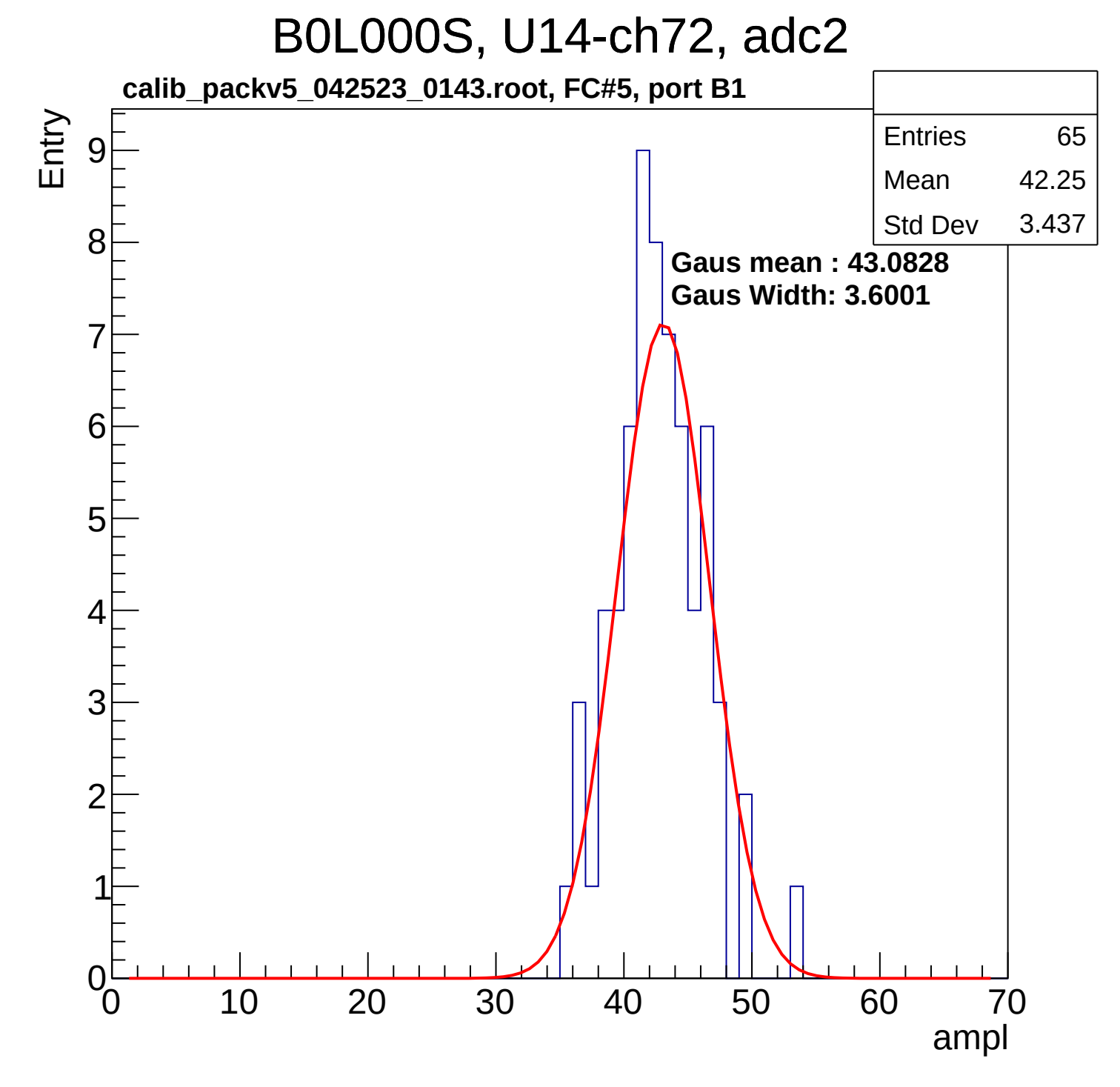

B0L000S, U14-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.95
Std Dev 2.812
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 58.05
Std Dev 13.76
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 34.67
Std Dev 28.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.29
Std Dev 5.741
Gaus mean : 29.3823
Gaus Width: 2.9744
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.68
Std Dev 2.972
Gaus mean : 36.6692
Gaus Width: 4.2403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.25
Std Dev 3.437
Gaus mean : 43.0828
Gaus Width: 3.6001
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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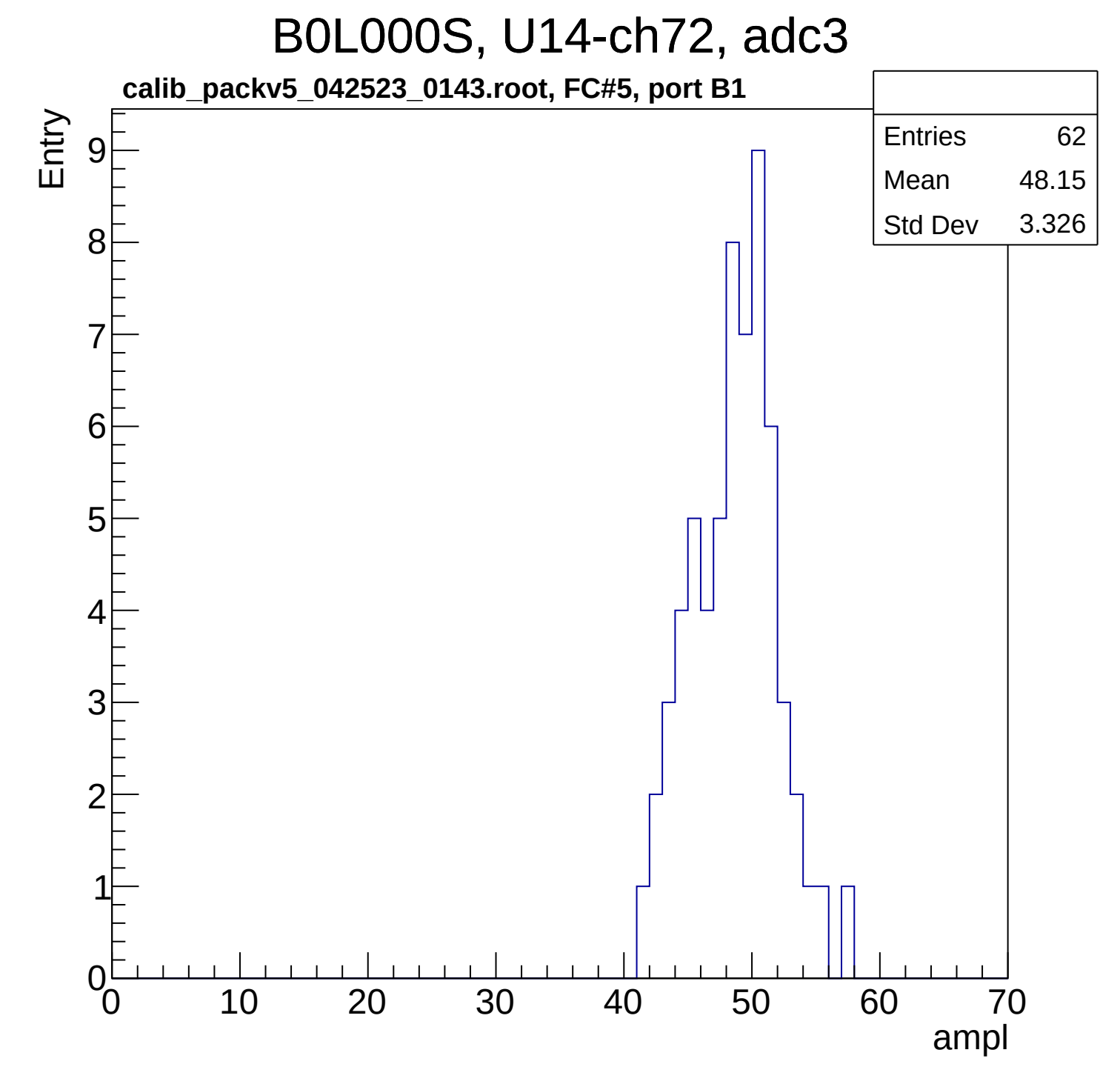
B0L000S, U14-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 48.15
Std Dev 3.326
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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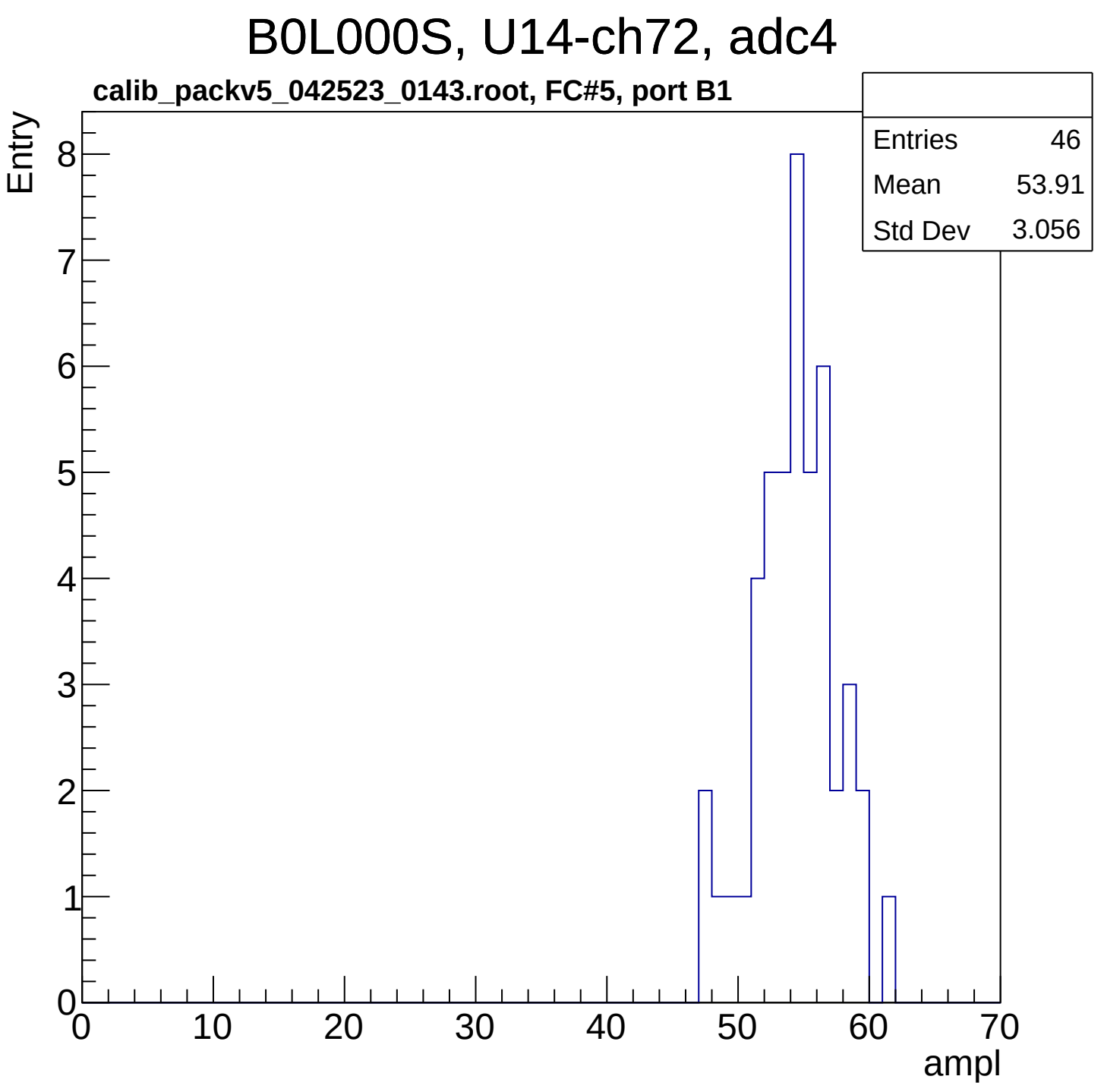
B0L000S, U14-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 53.91
Std Dev 3.056
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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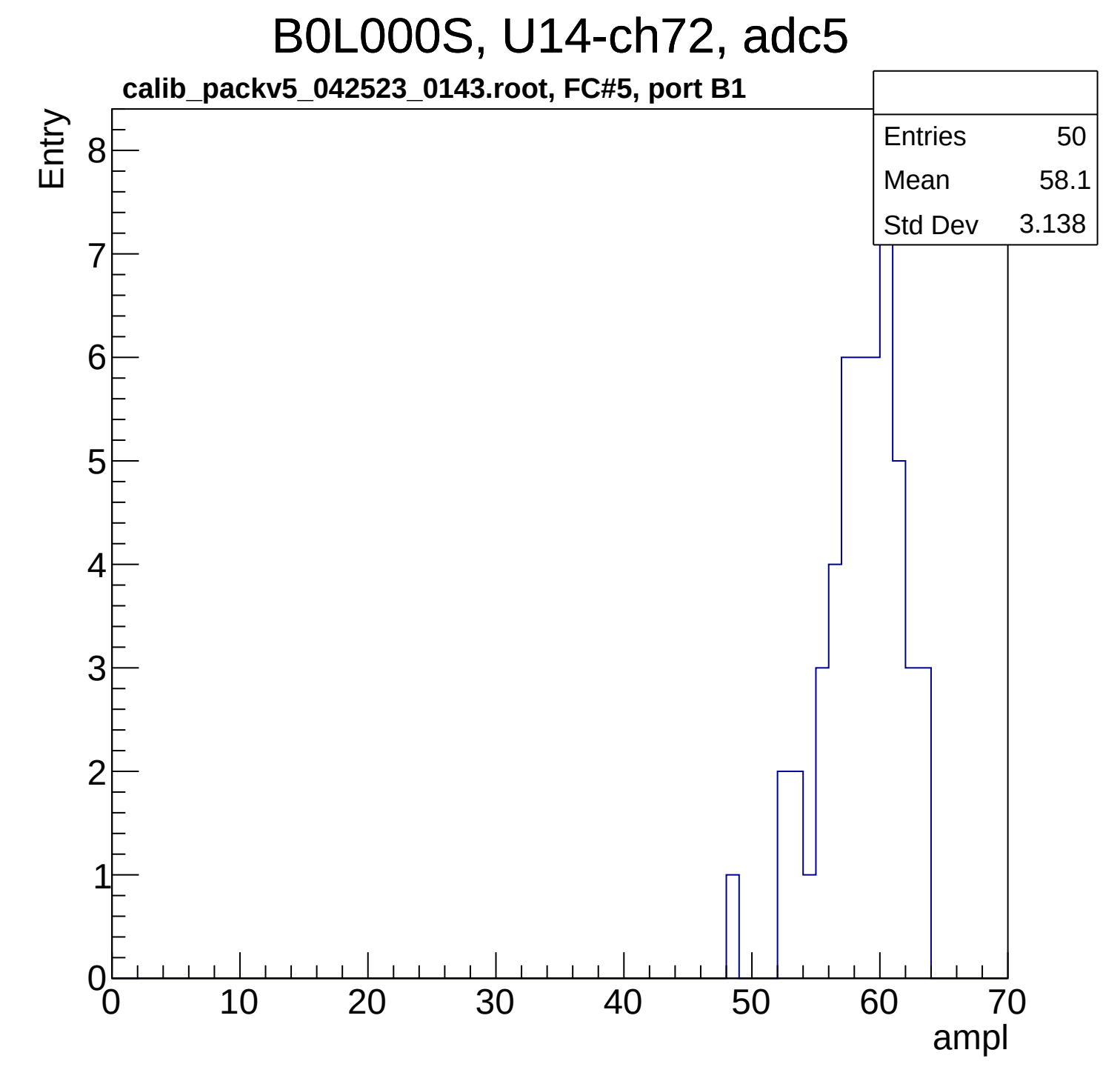
B0L000S, U14-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.1
Std Dev 3.138
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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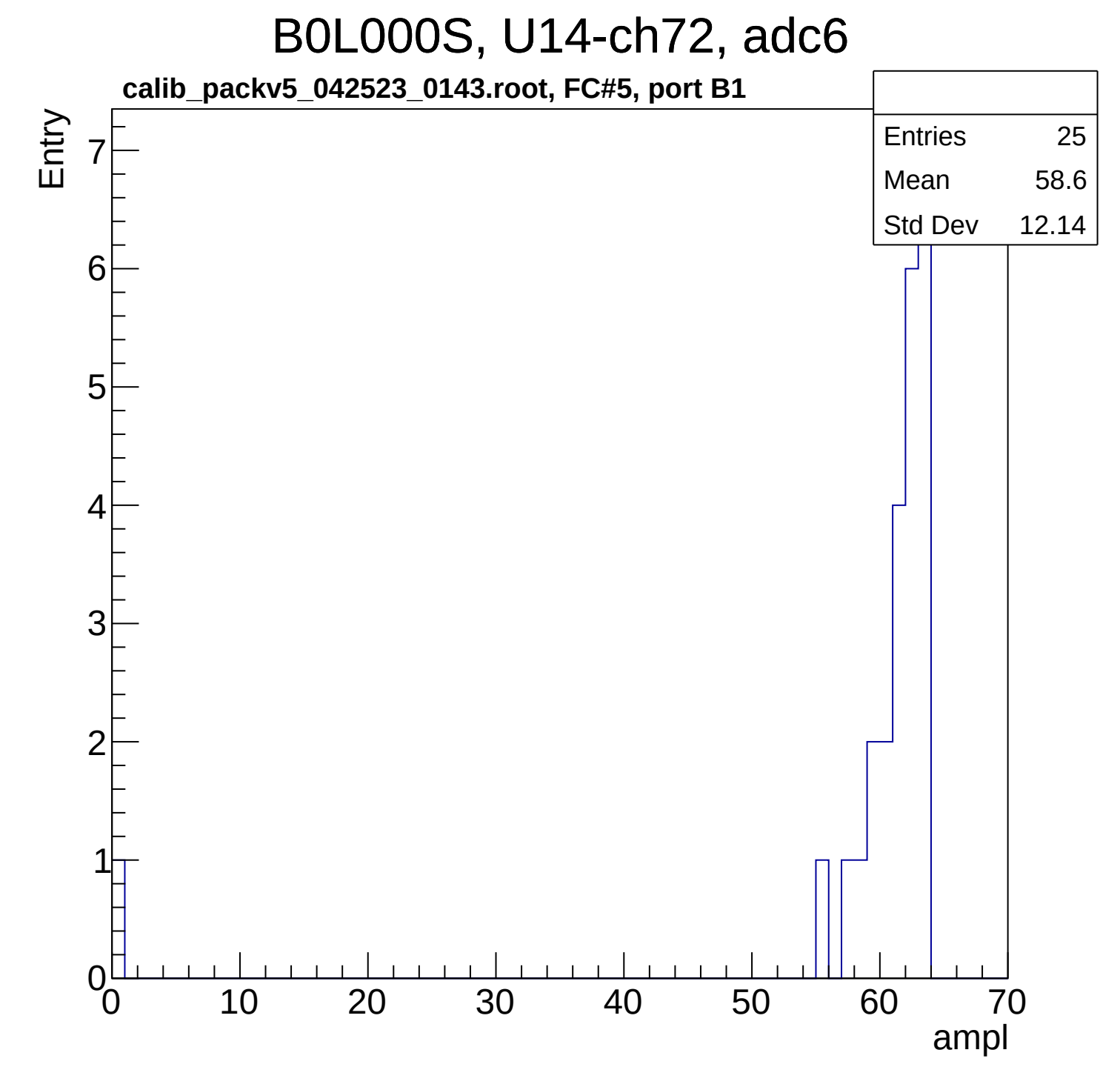
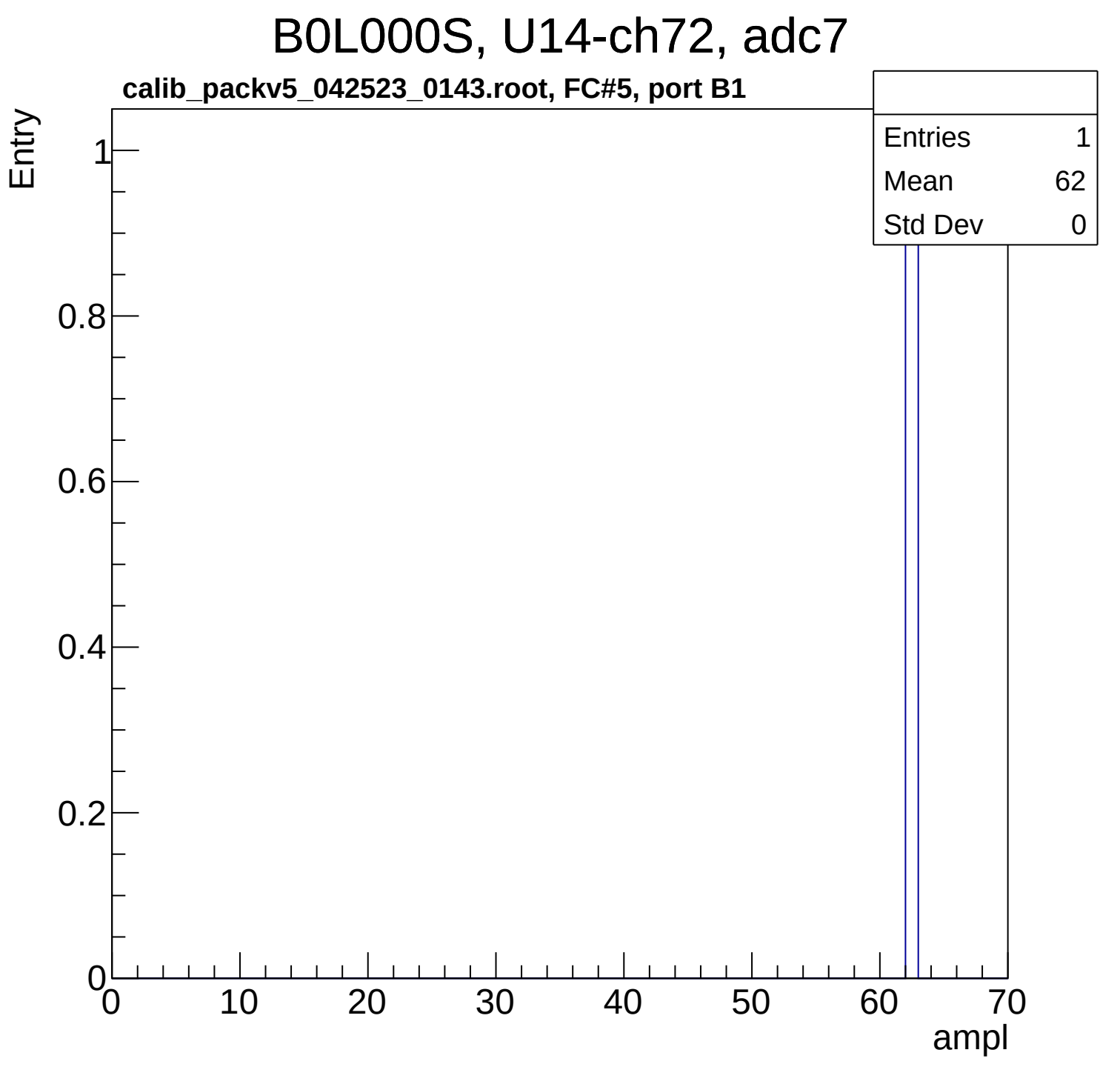

B0L000S, U14-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 58.6
Std Dev 12.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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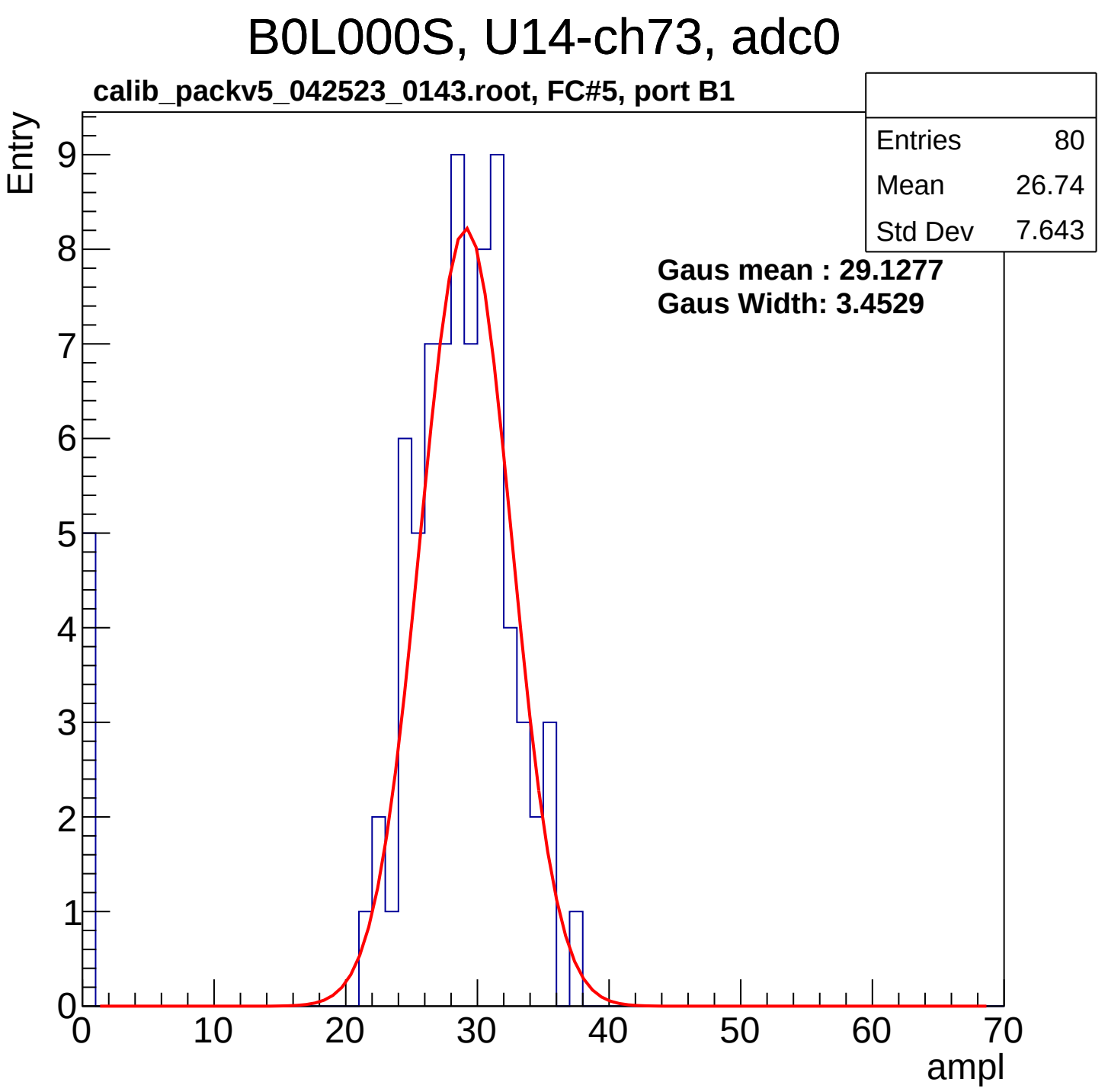
40
50
60
70
ampl


B0L000S, U14-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 26.74
Std Dev 7.643
Gaus mean : 29.1277
Gaus Width: 3.4529
Entry
ampl
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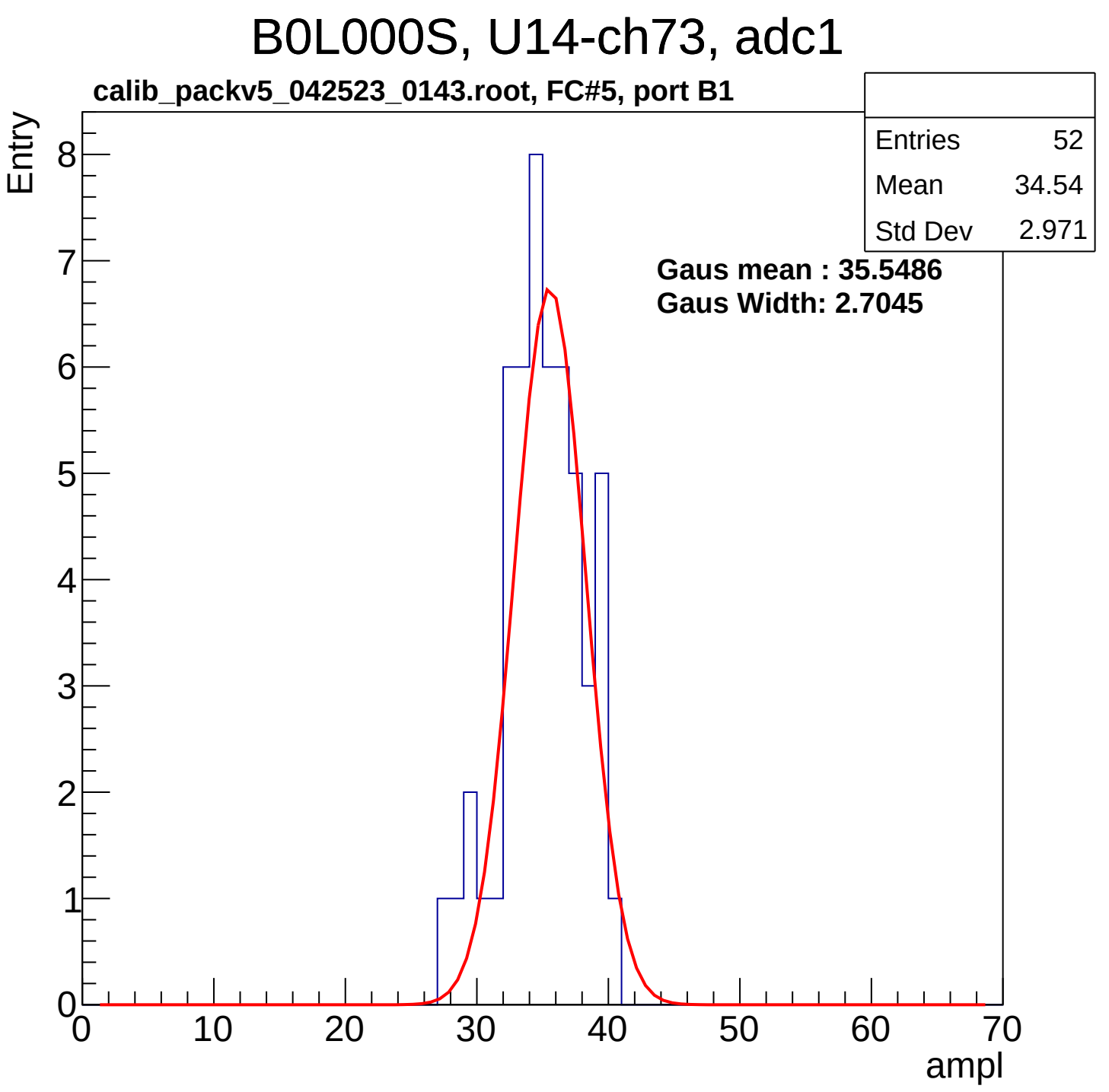
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 34.54
Std Dev 2.971
Gaus mean : 35.5486
Gaus Width: 2.7045
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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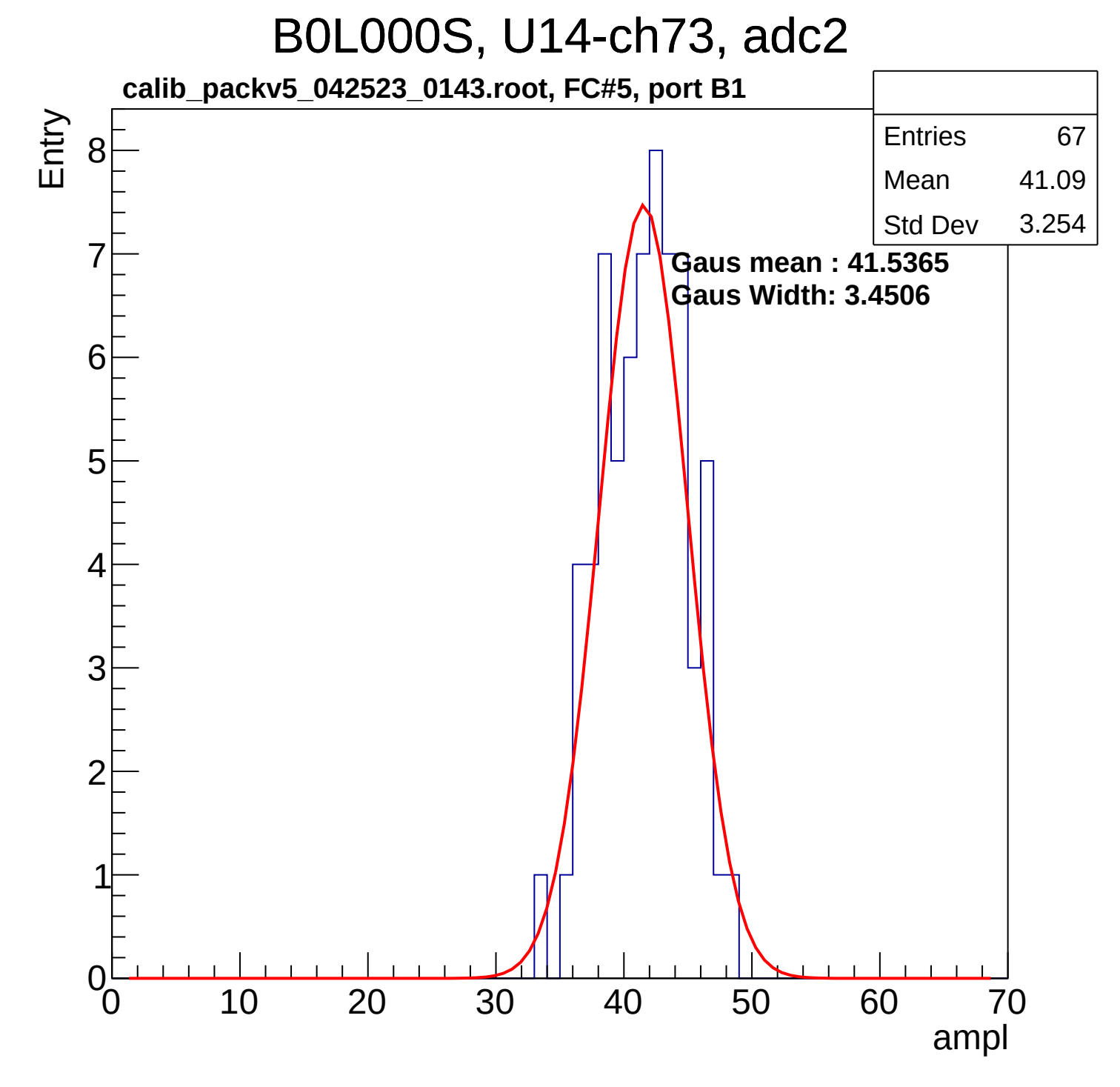
B0L000S, U14-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.09
Std Dev 3.254
Gaus mean : 41.5365
Gaus Width: 3.4506
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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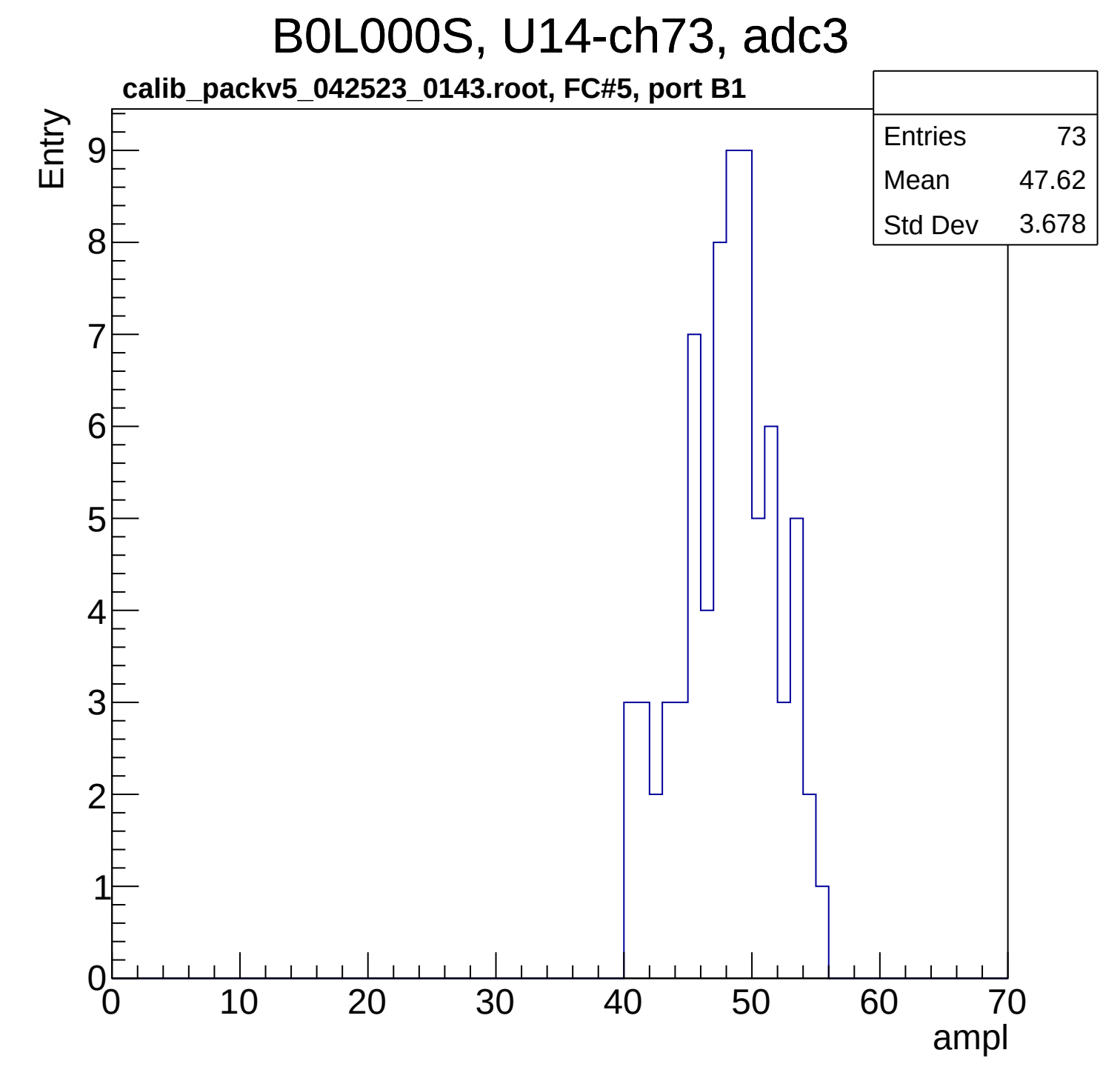
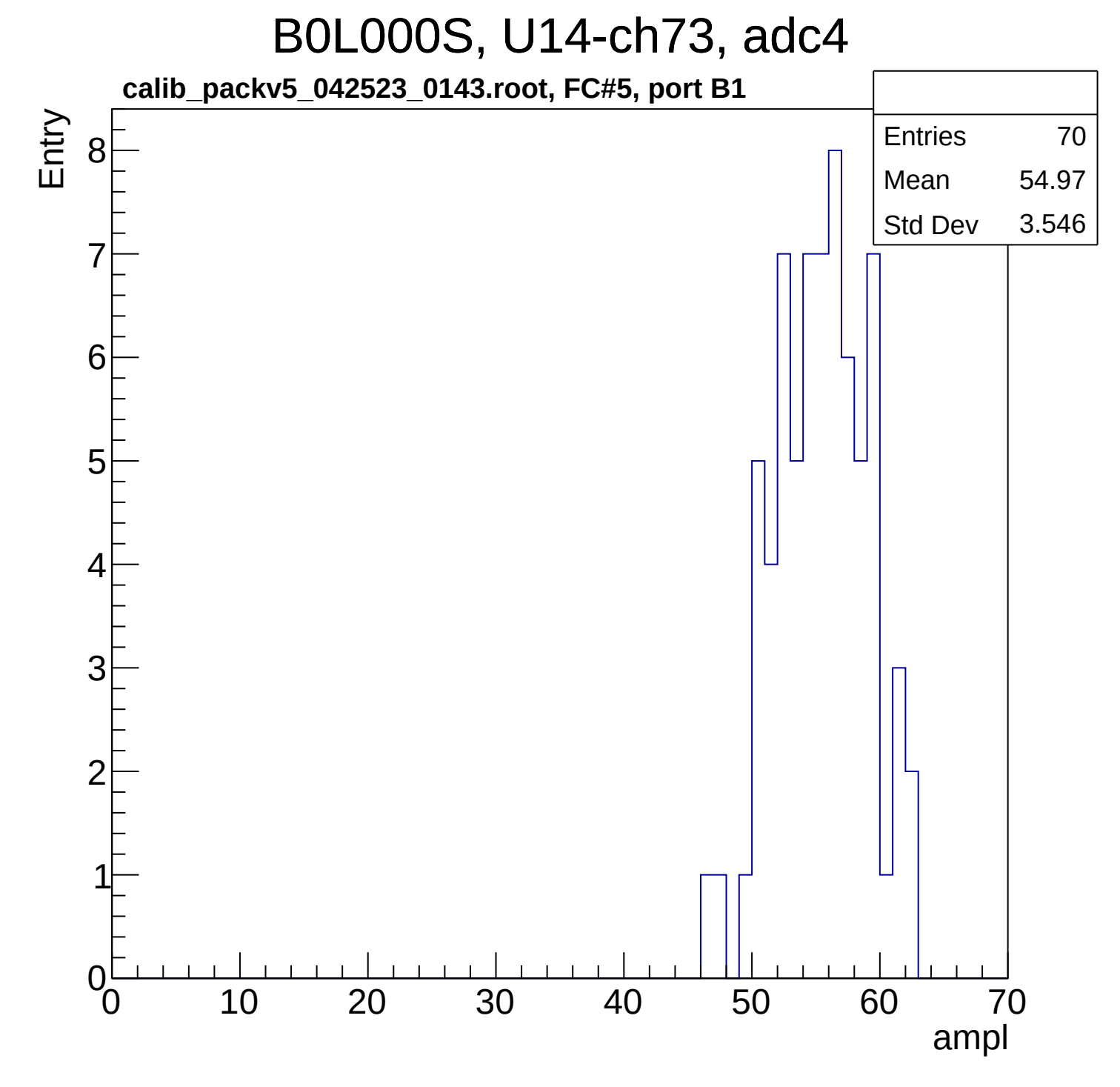

B0L000S, U14-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 47.62
Std Dev 3.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 54.97
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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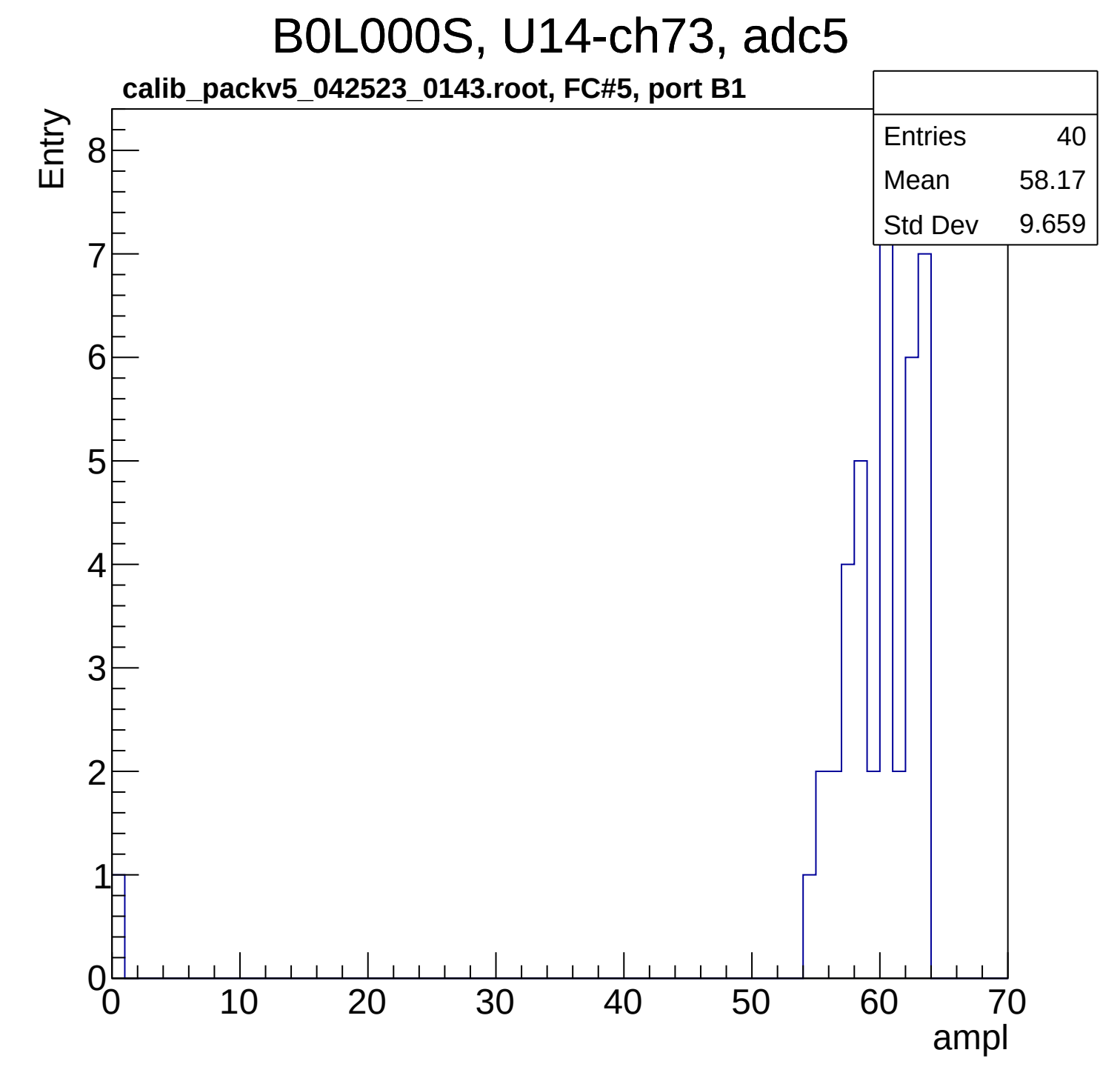
40
50
60
70


B0L000S, U14-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.17
Std Dev 9.659
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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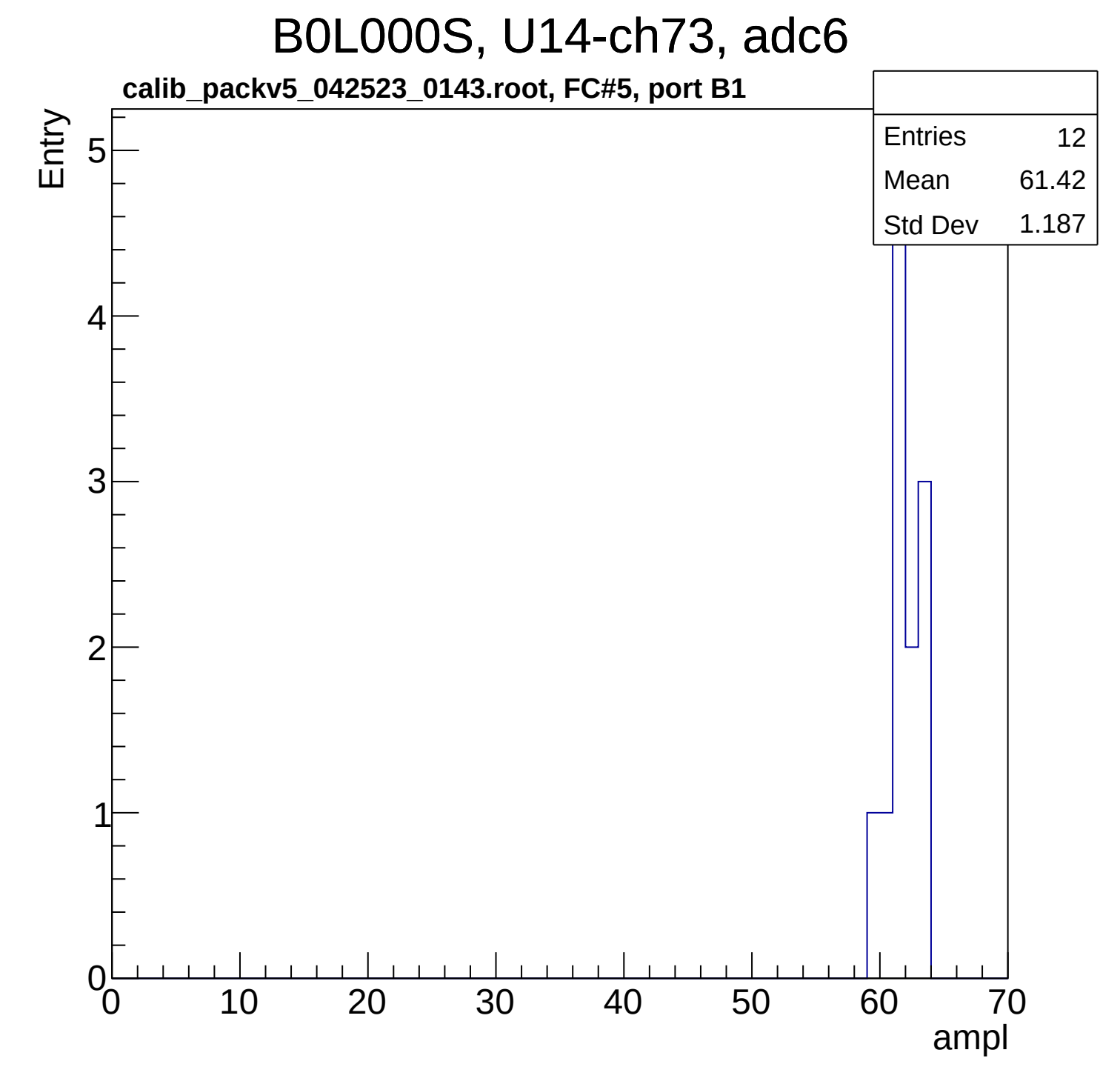
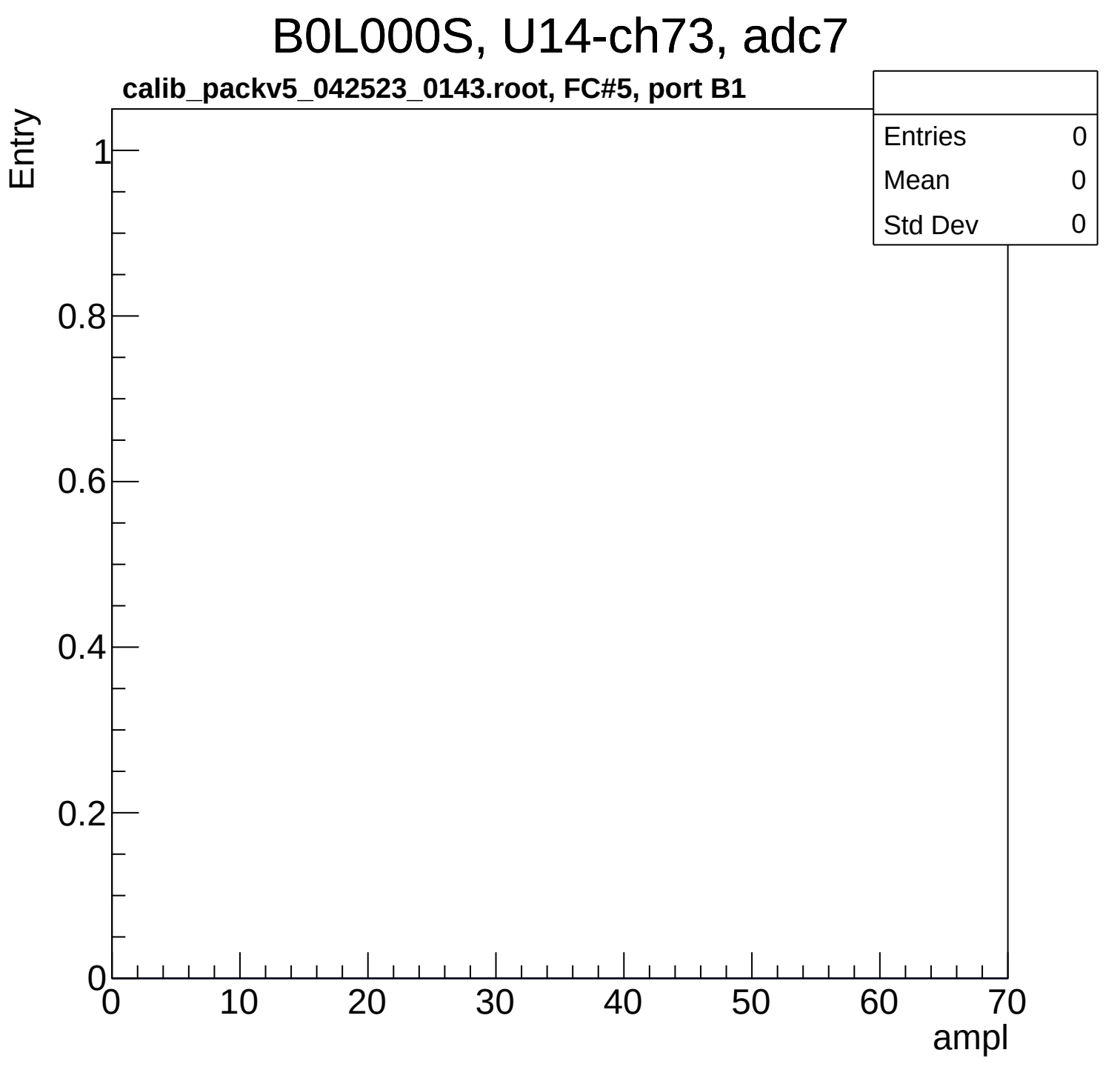
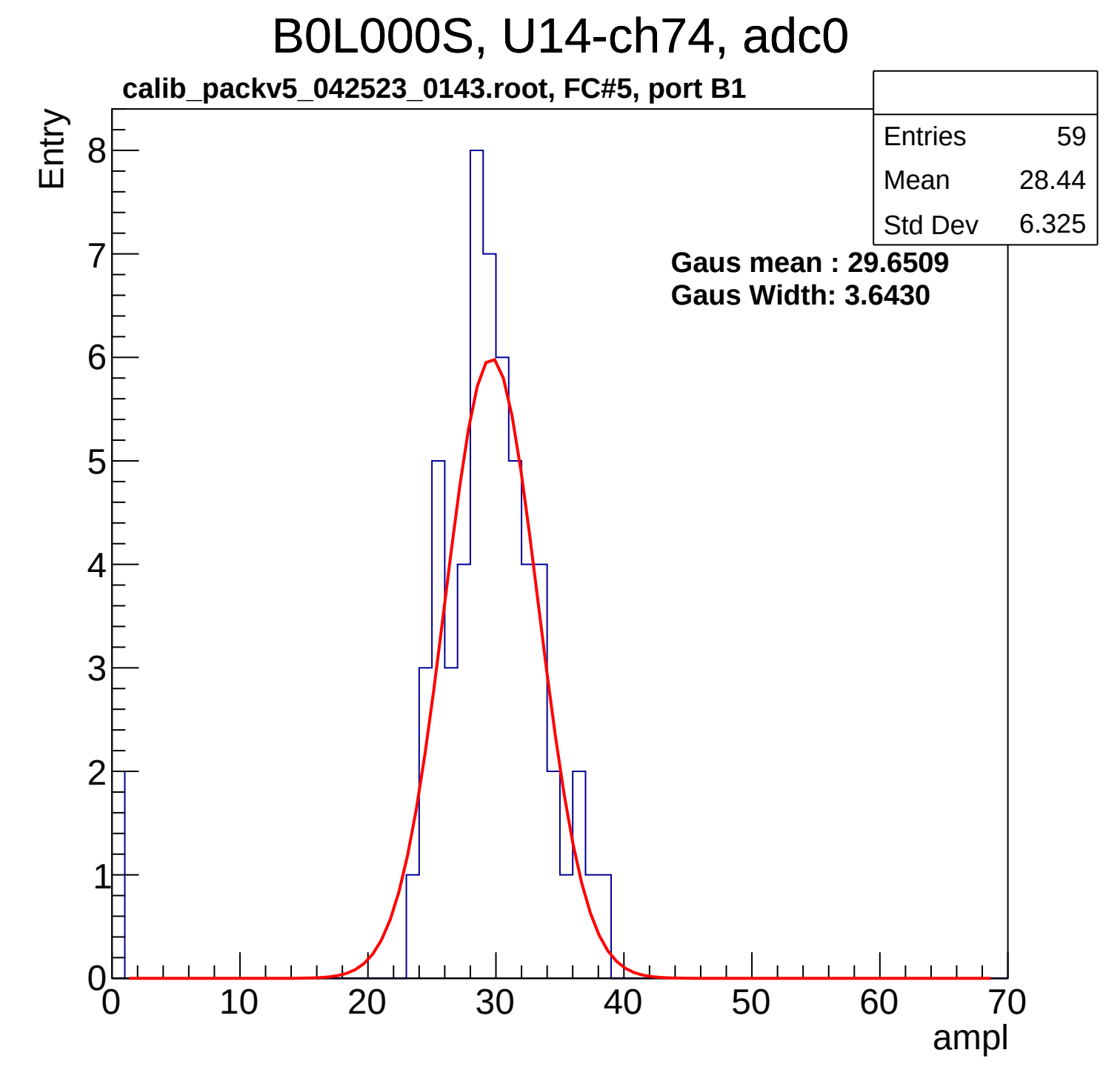

B0L000S, U14-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.42
Std Dev 1.187
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 28.44
Std Dev 6.325
Gaus mean : 29.6509
Gaus Width: 3.6430
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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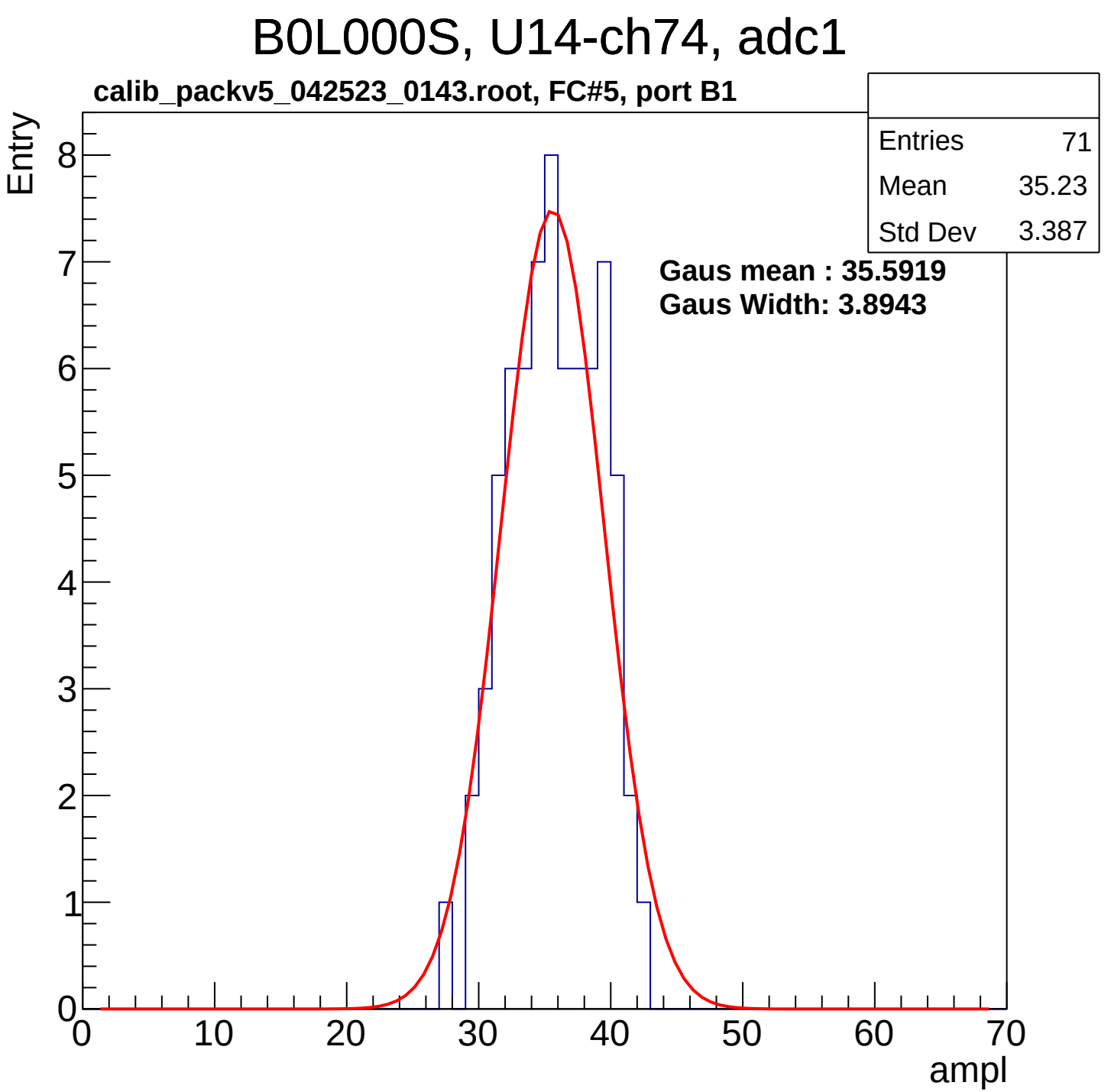
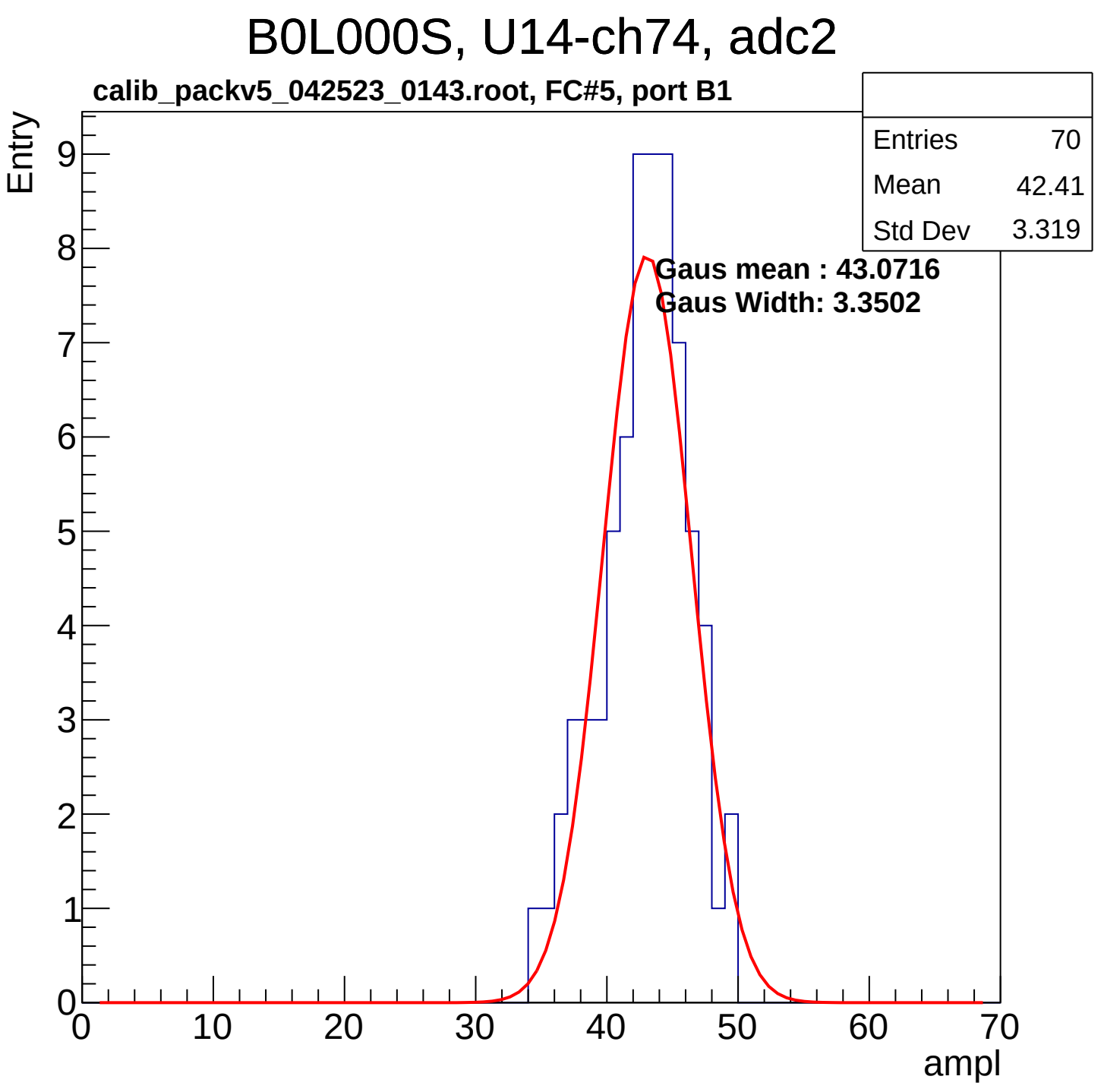
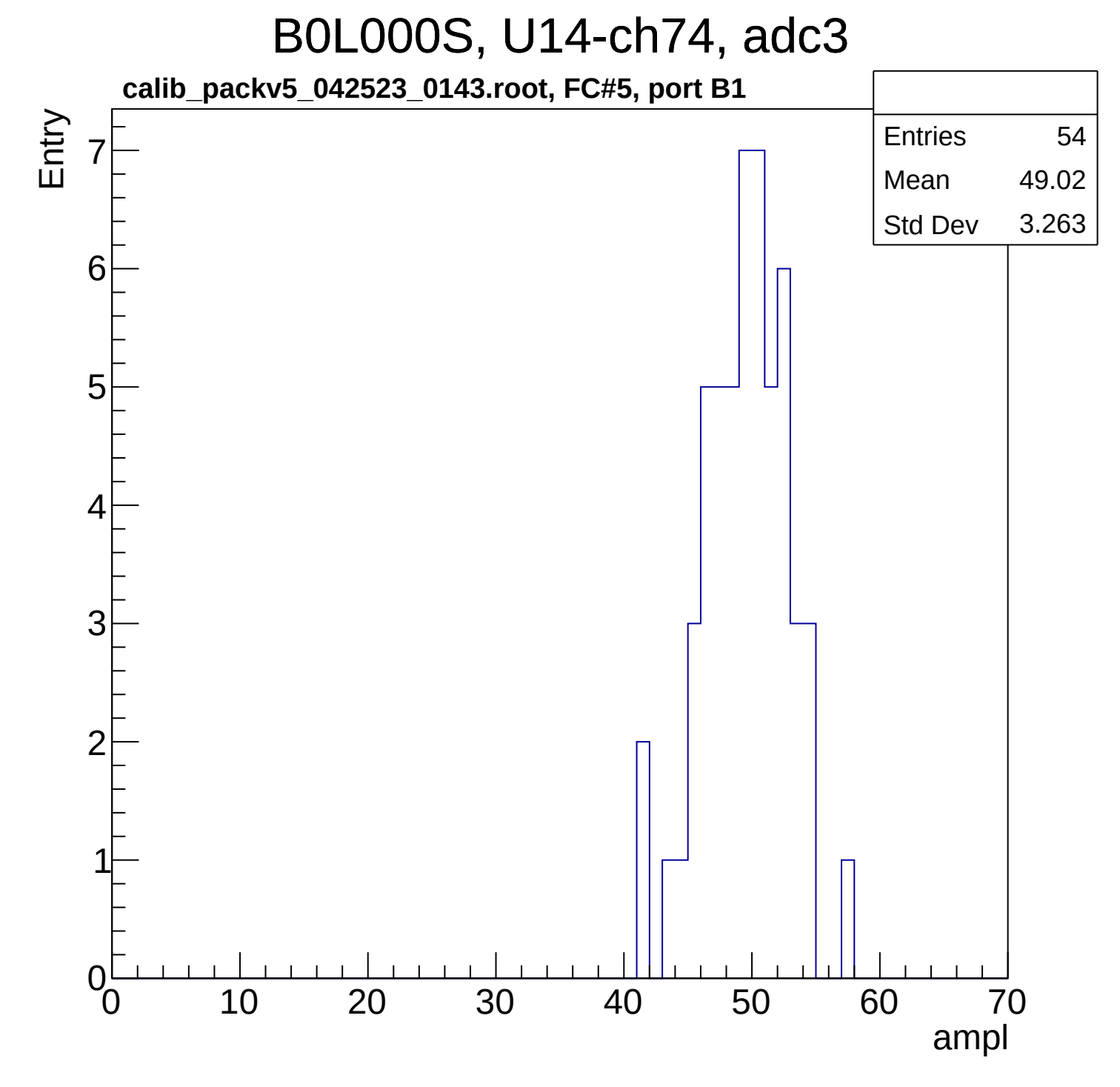
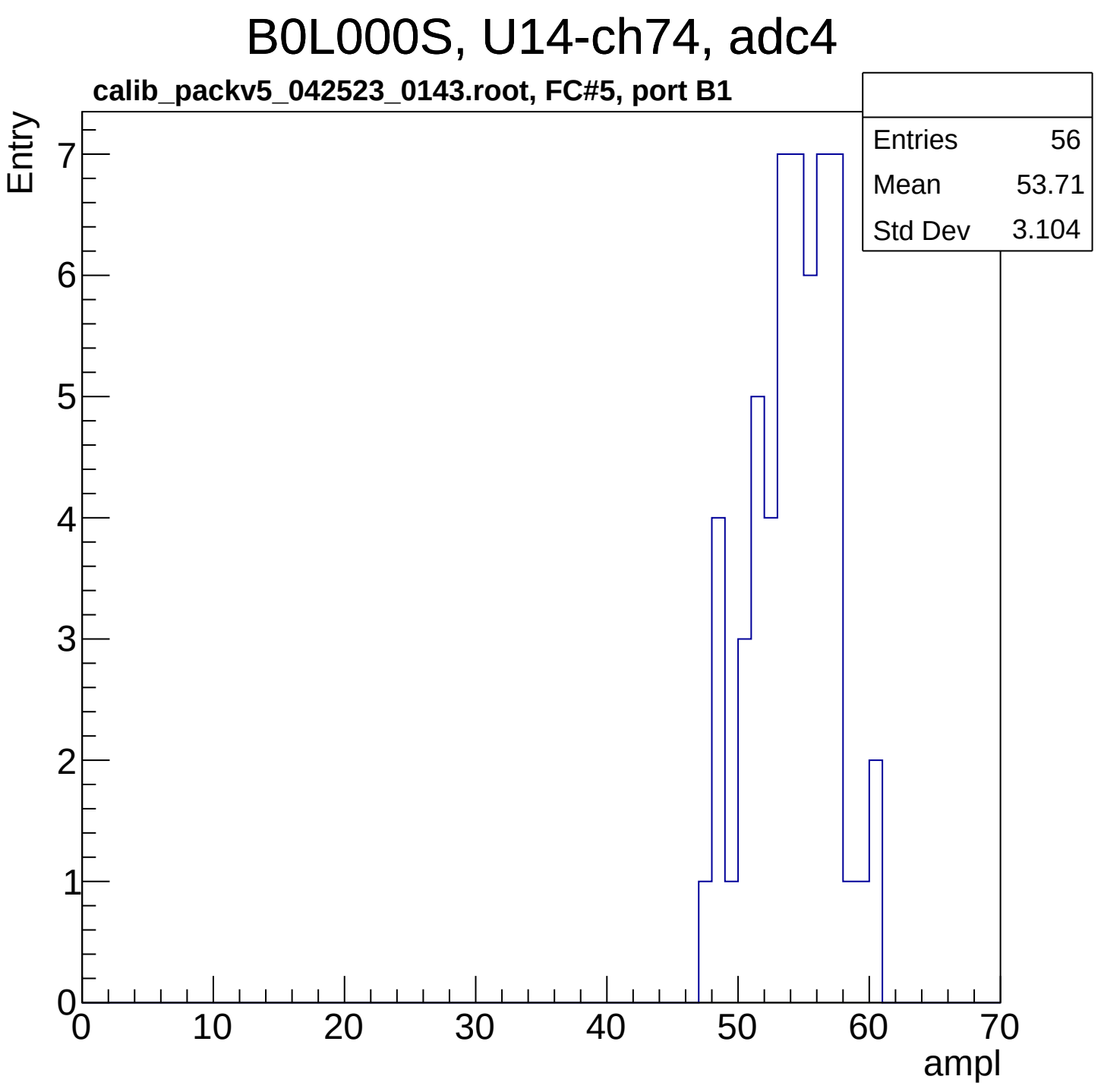
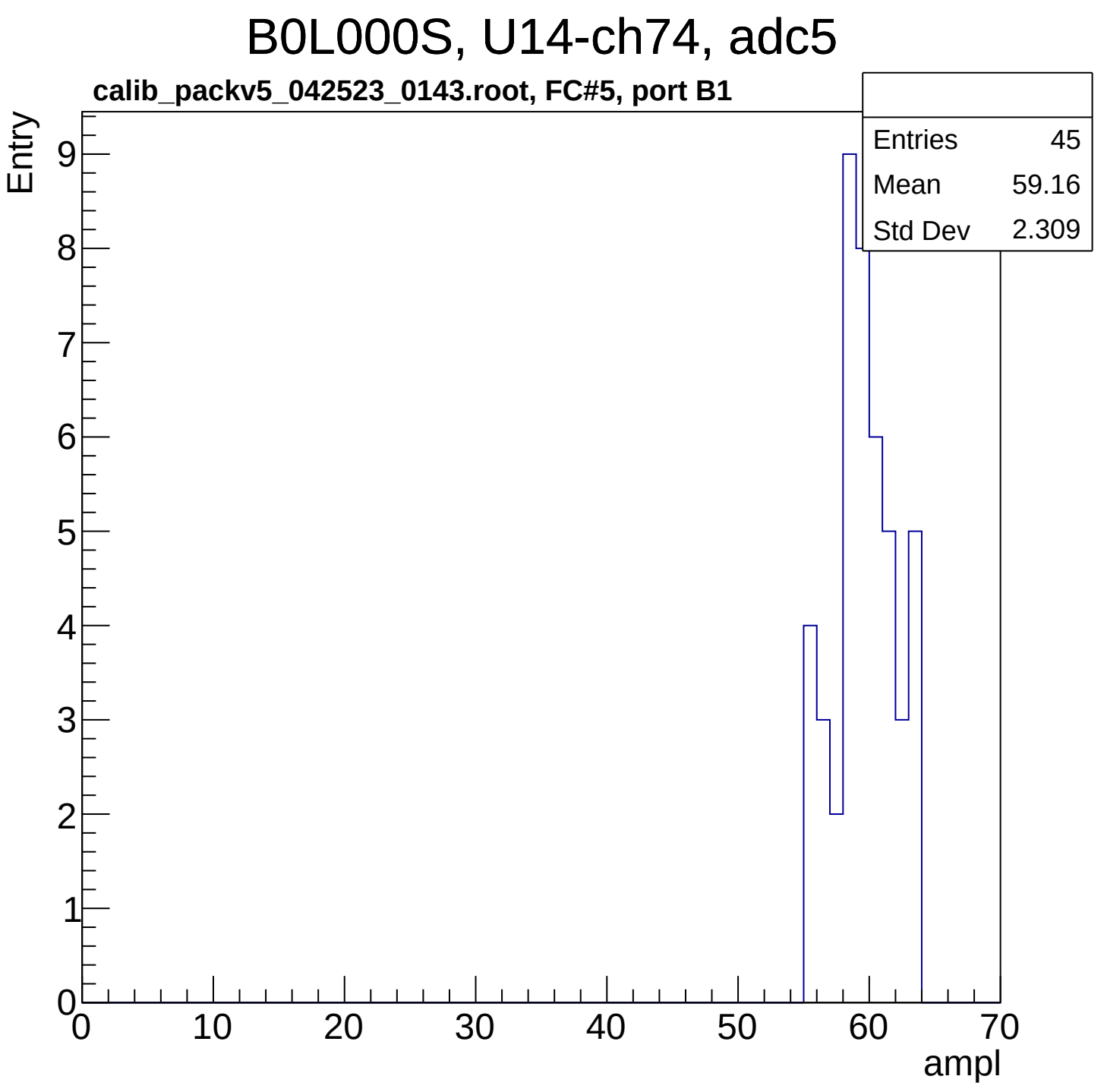

B0L000S, U14-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 35.23
Std Dev 3.387
Gaus mean : 35.5919
Gaus Width: 3.8943
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.41
Std Dev 3.319
Gaus mean : 43.0716
Gaus Width: 3.3502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.02
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 53.71
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.16
Std Dev 2.309
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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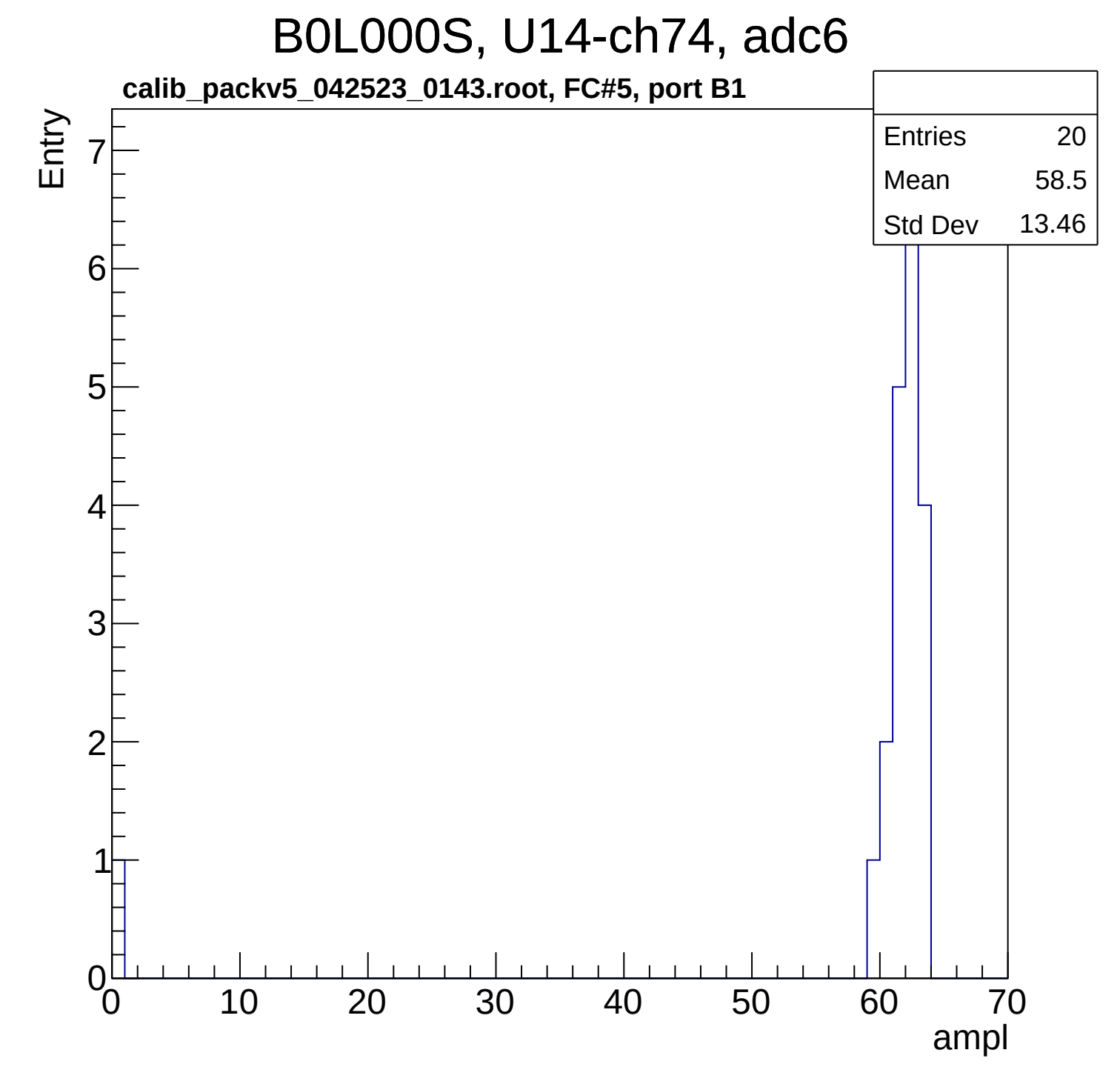
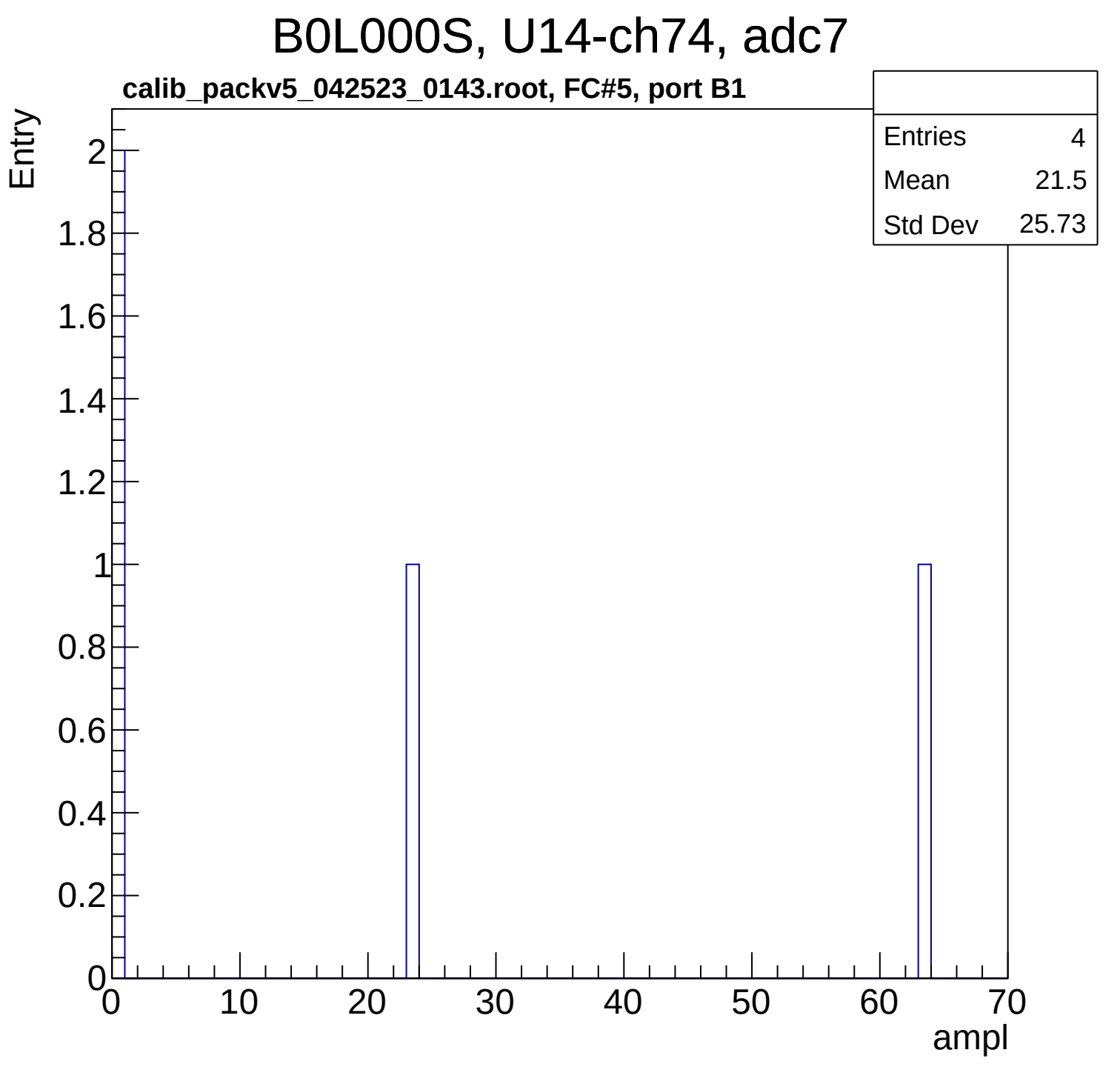
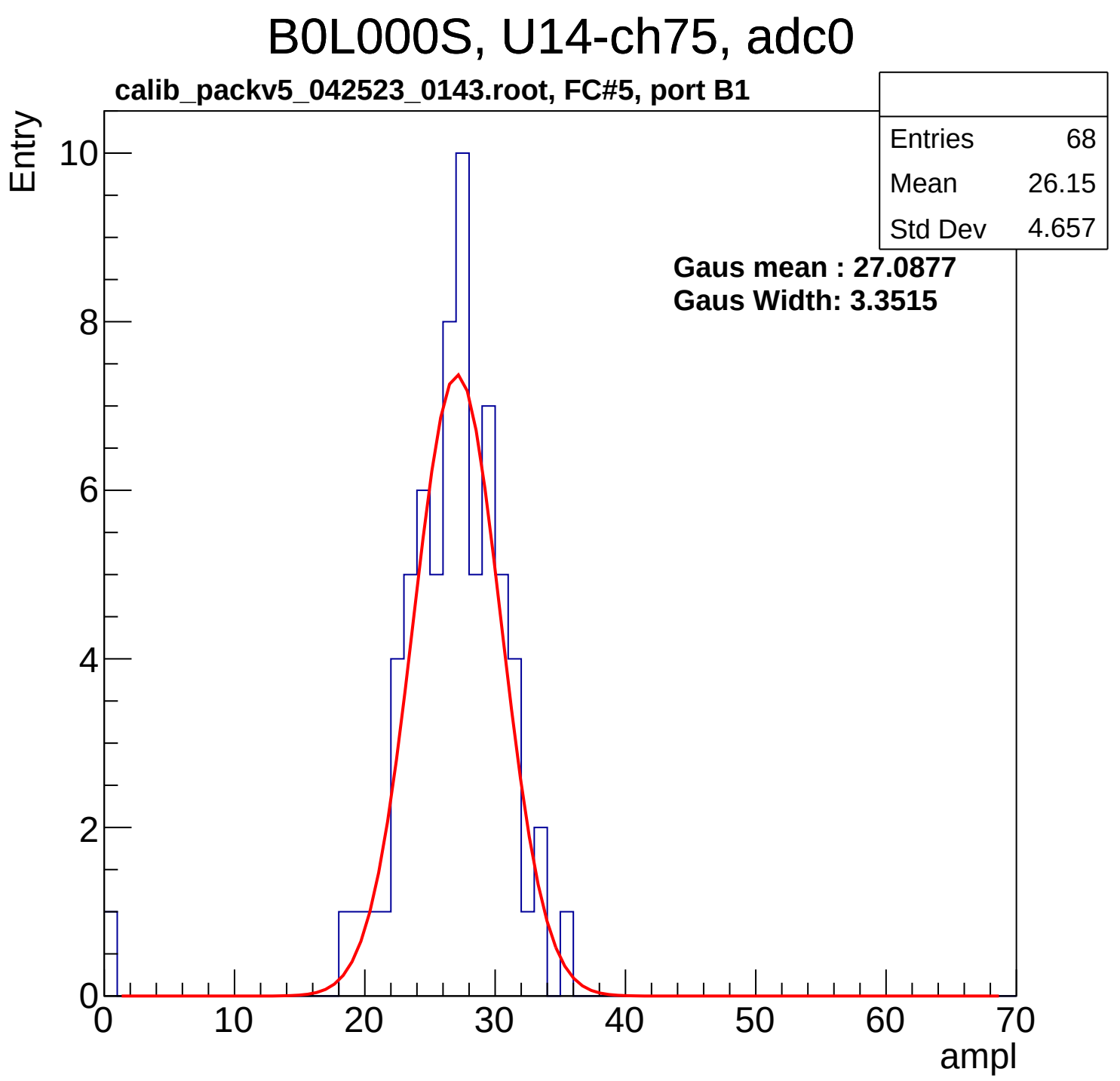
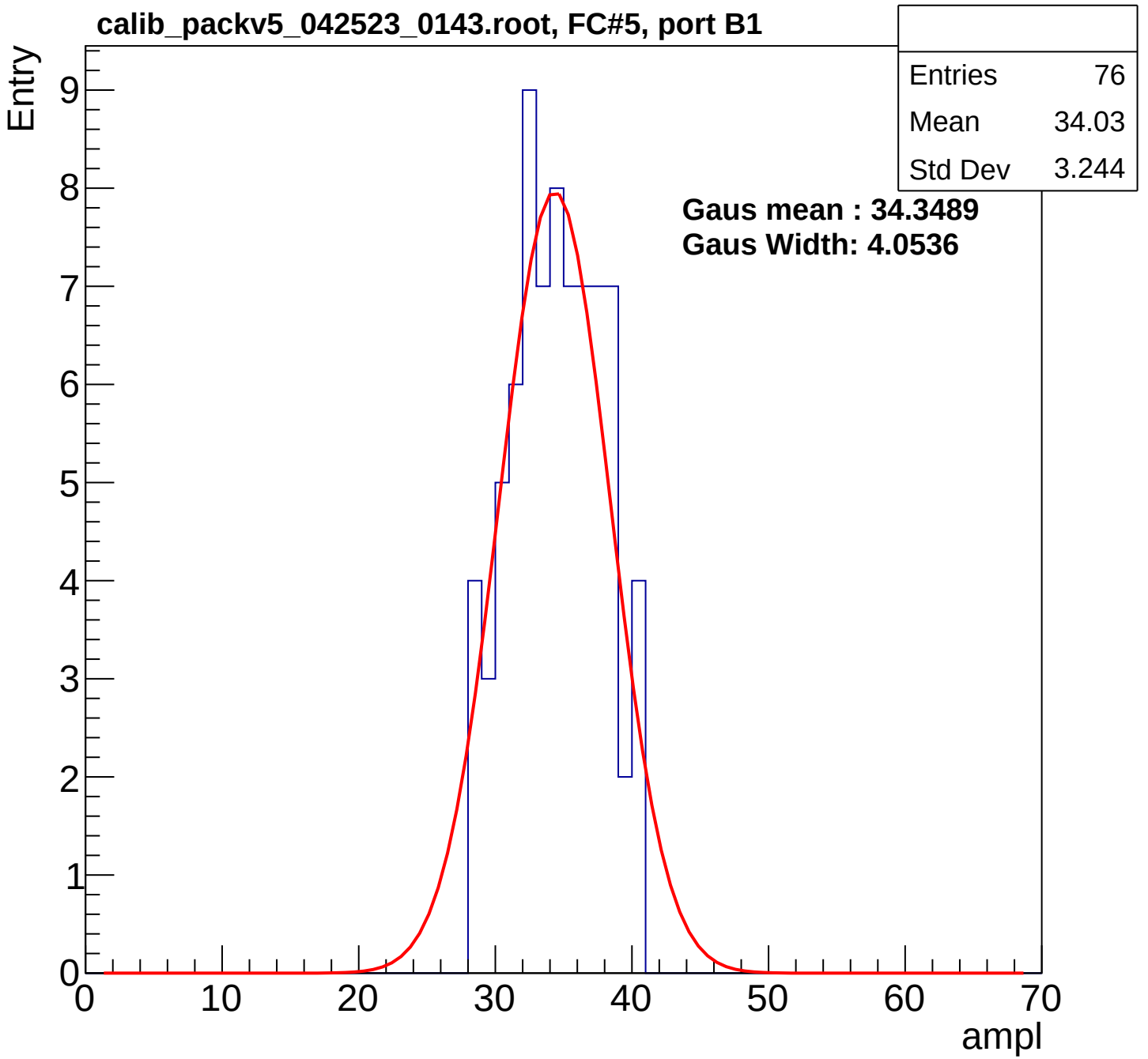
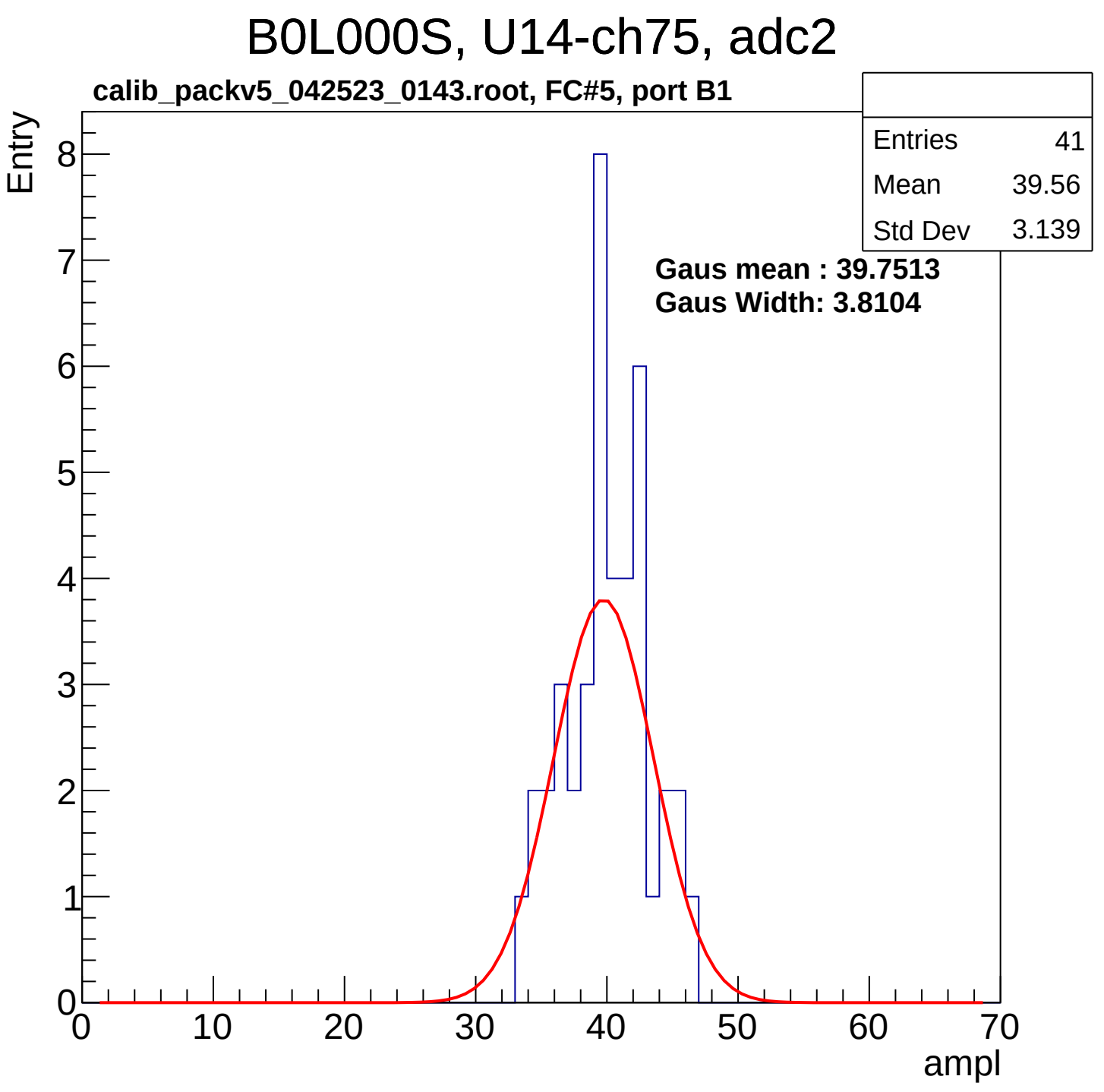
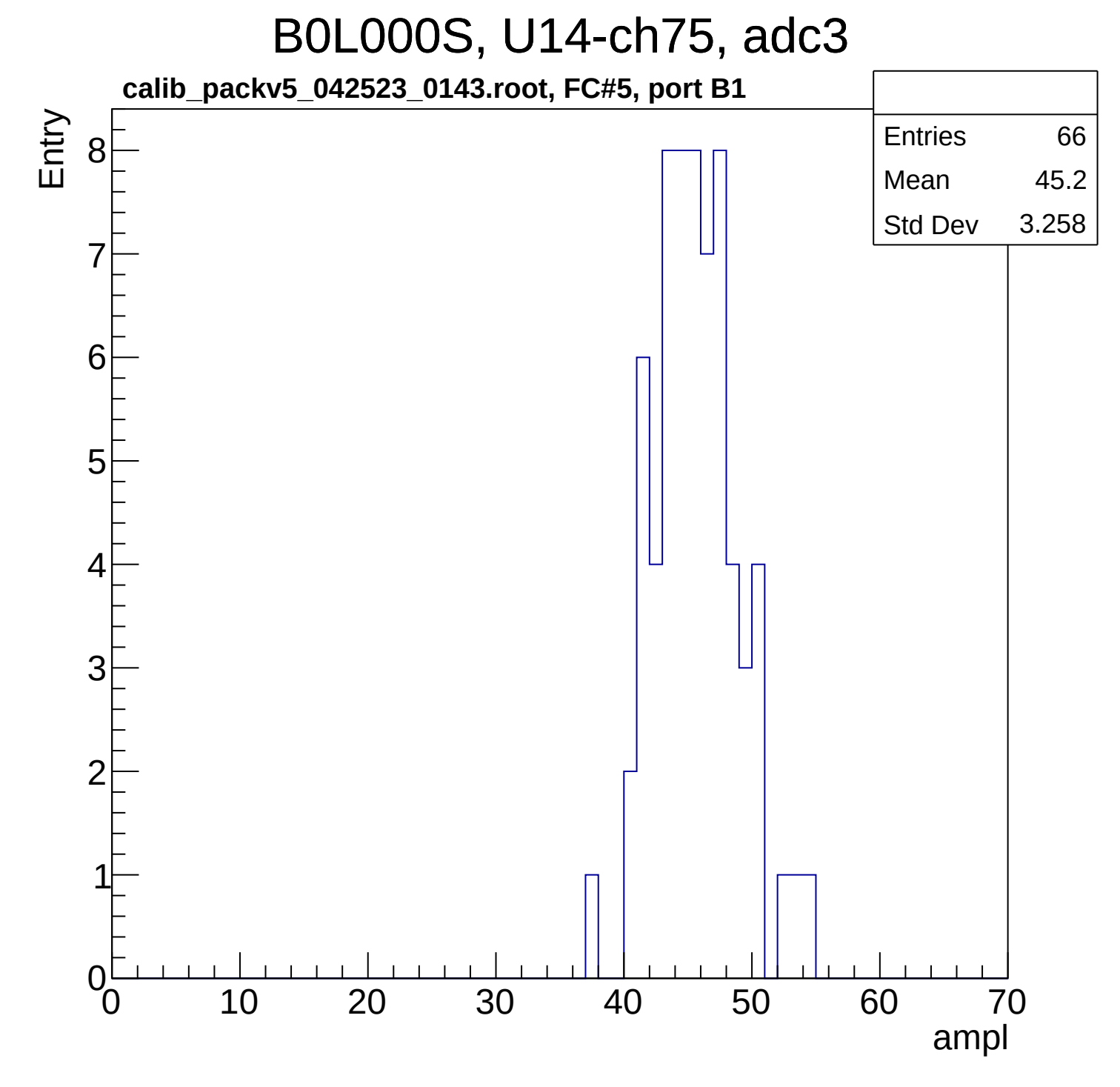

B0L000S, U14-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58.5
Std Dev 13.46
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

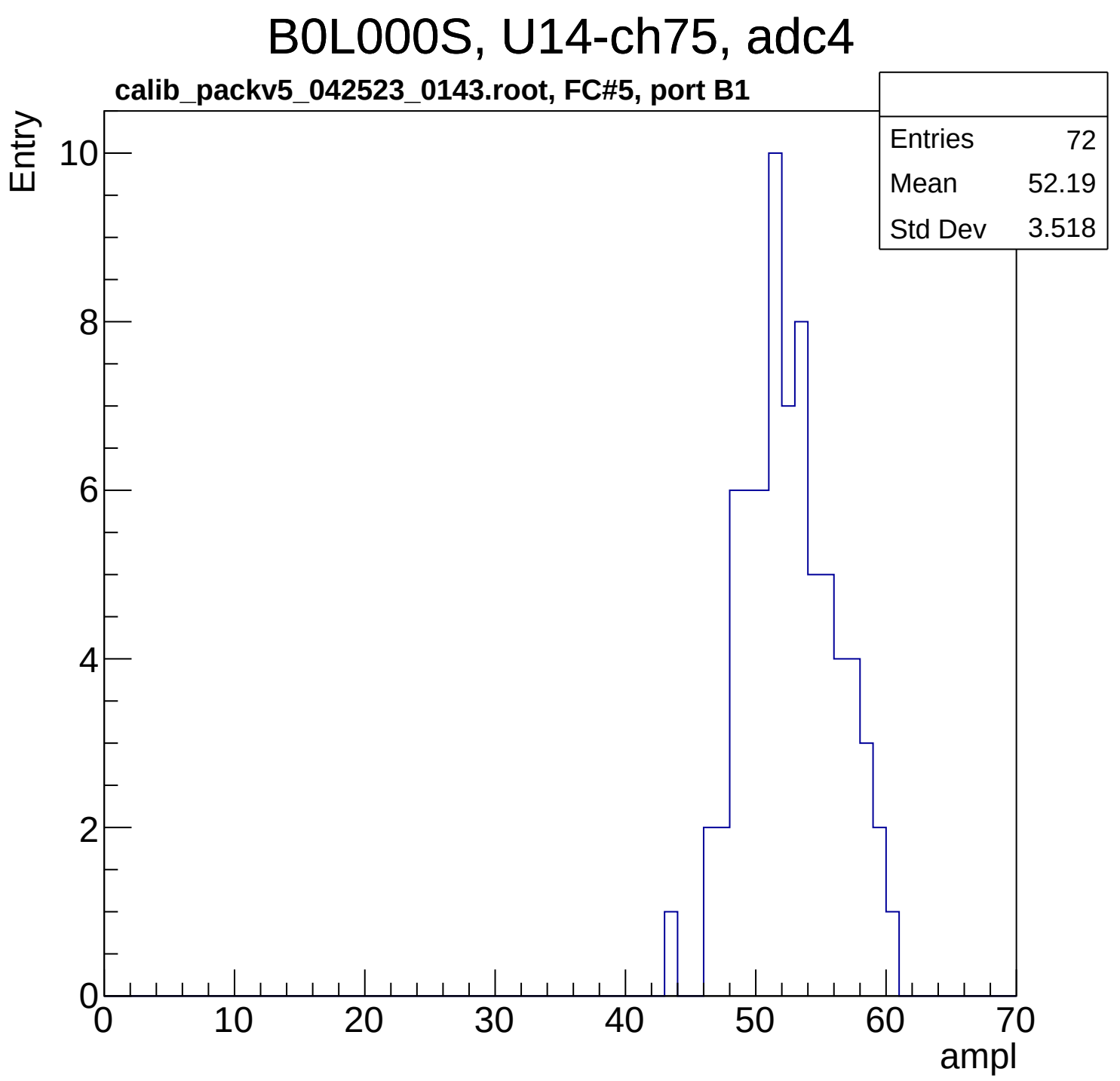

B0L000S, U14-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 21.5
Std Dev 25.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

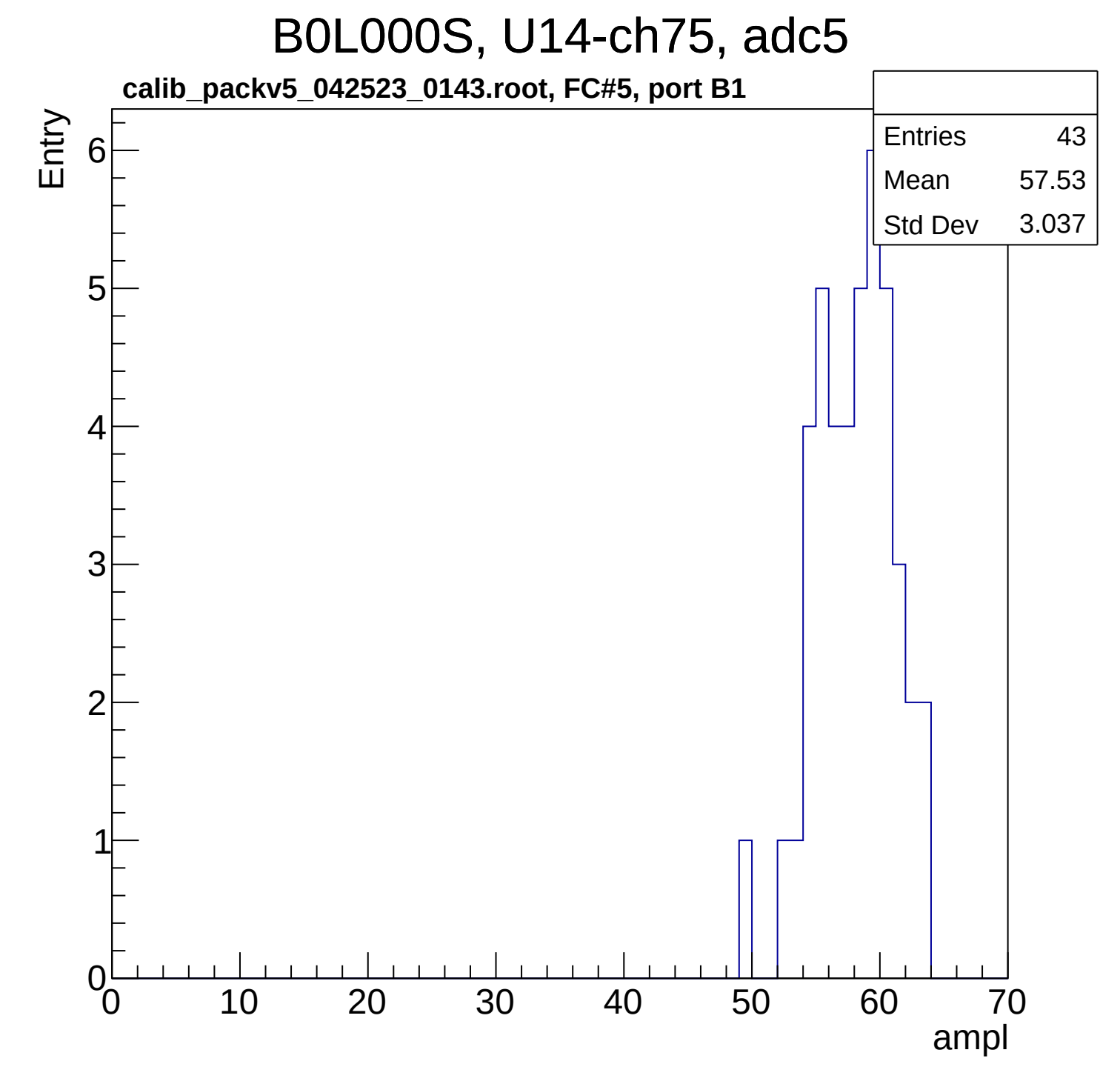

B0L000S, U14-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 26.15
Std Dev 4.657
Gaus mean : 27.0877
Gaus Width: 3.3515
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

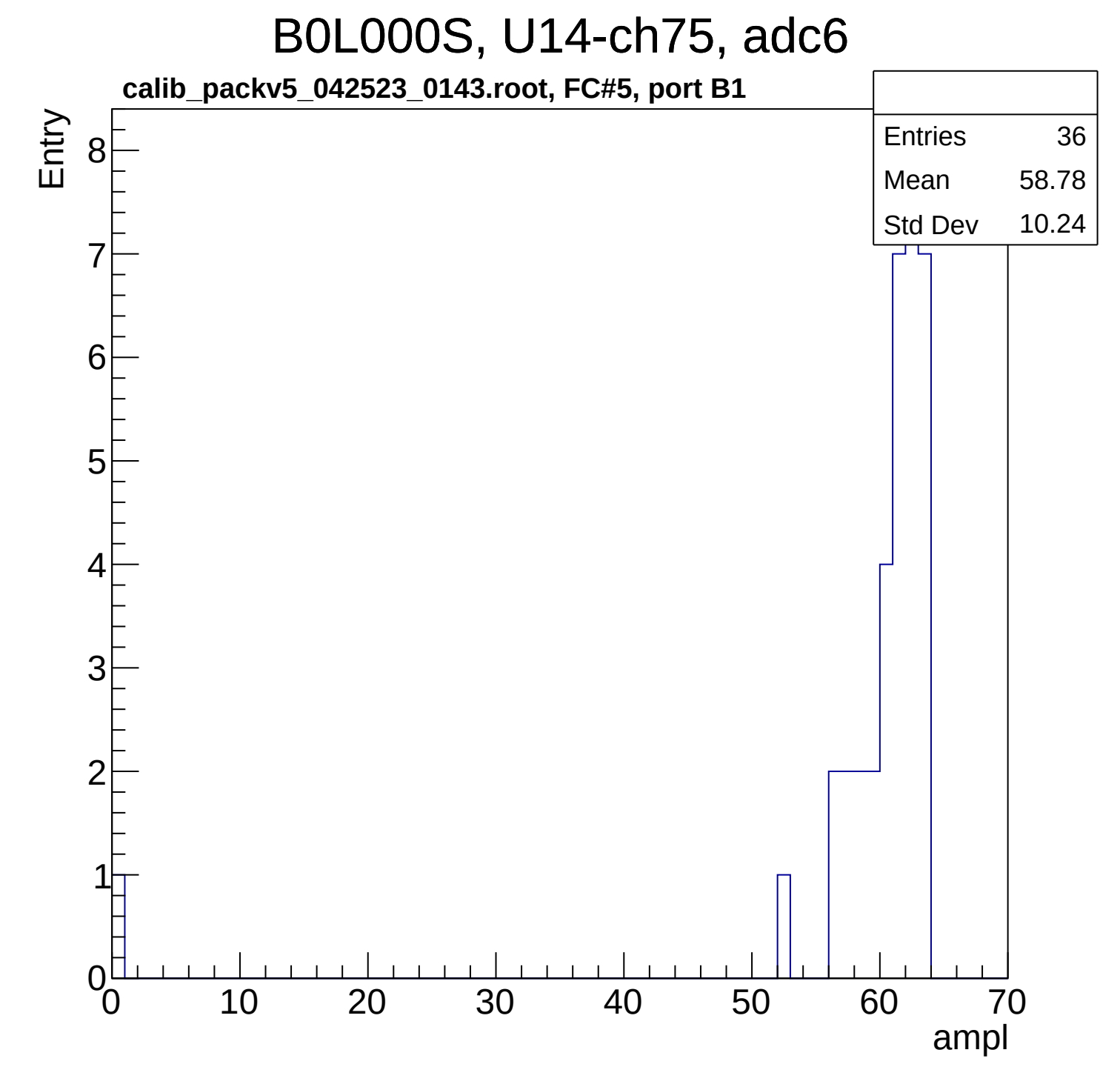

B0L000S, U14-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 34.03
Std Dev 3.244
Gaus mean : 34.3489
Gaus Width: 4.0536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

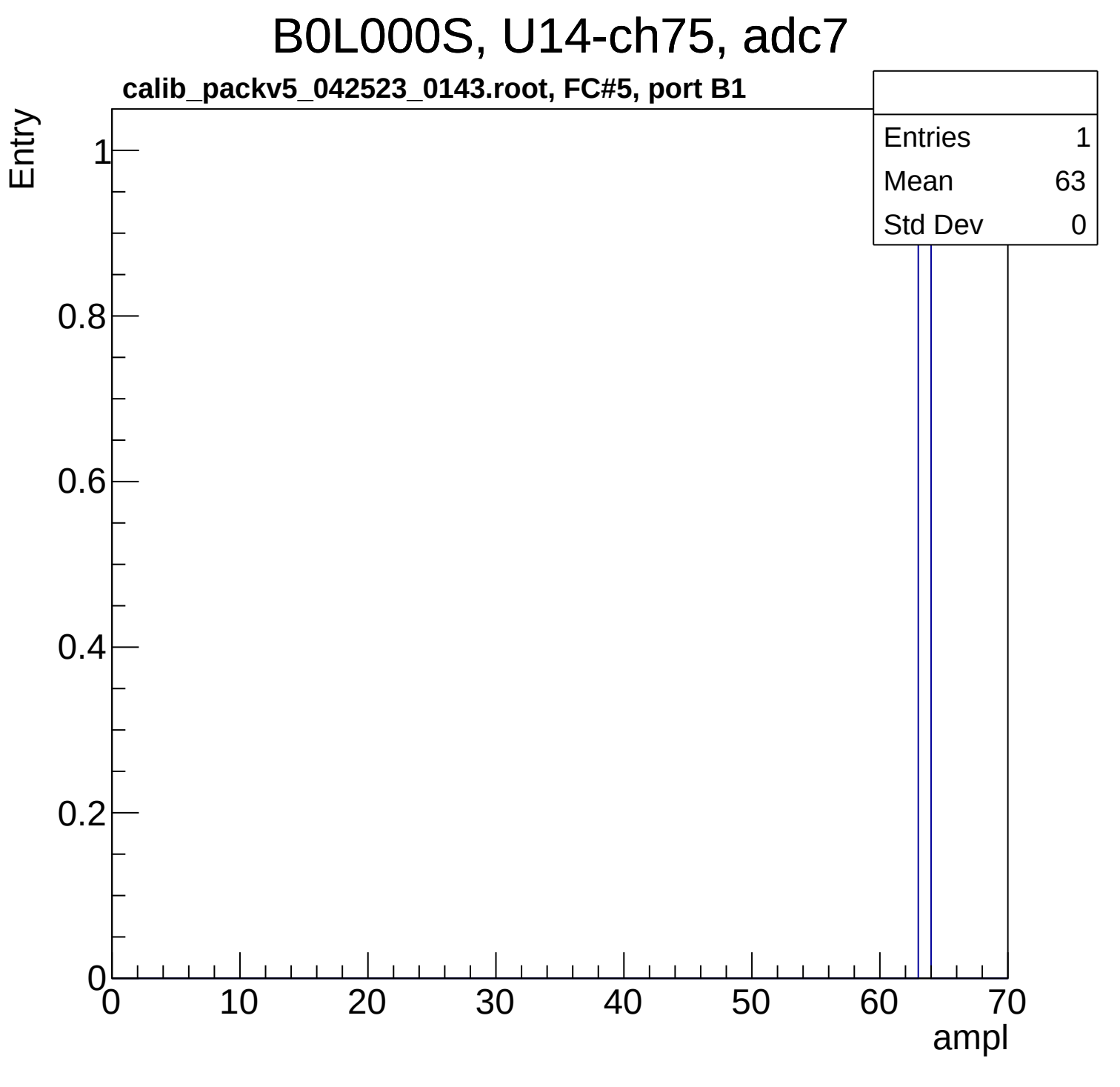

B0L000S, U14-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 39.56
Std Dev 3.139
Gaus mean : 39.7513
Gaus Width: 3.8104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

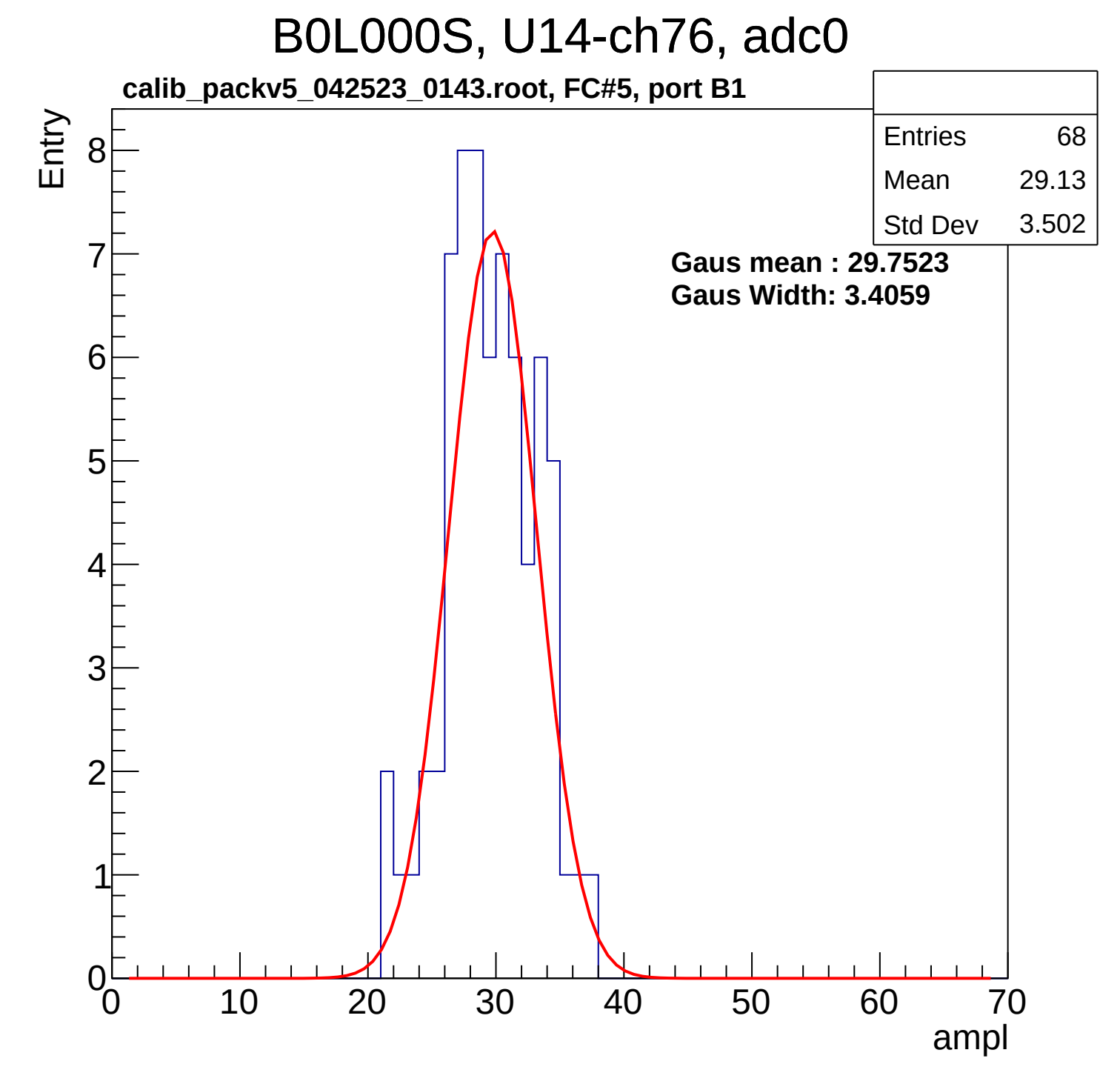

B0L000S, U14-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 45.2
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

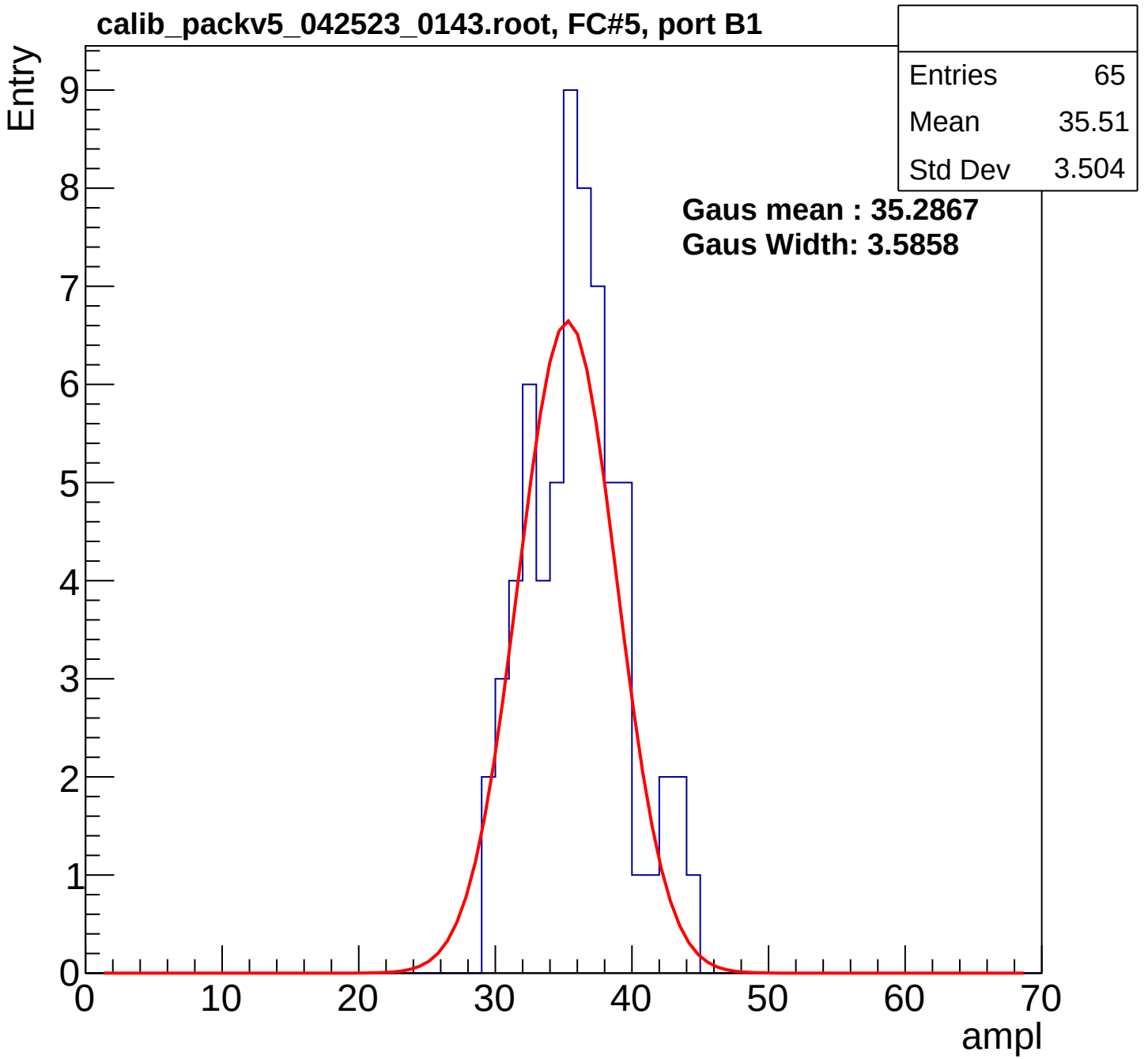

B0L000S, U14-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 52.19
Std Dev 3.518
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 57.53
Std Dev 3.037
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.78
Std Dev 10.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.13
Std Dev 3.502
Gaus mean : 29.7523
Gaus Width: 3.4059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 35.51
Std Dev 3.504
Gaus mean : 35.2867
Gaus Width: 3.5858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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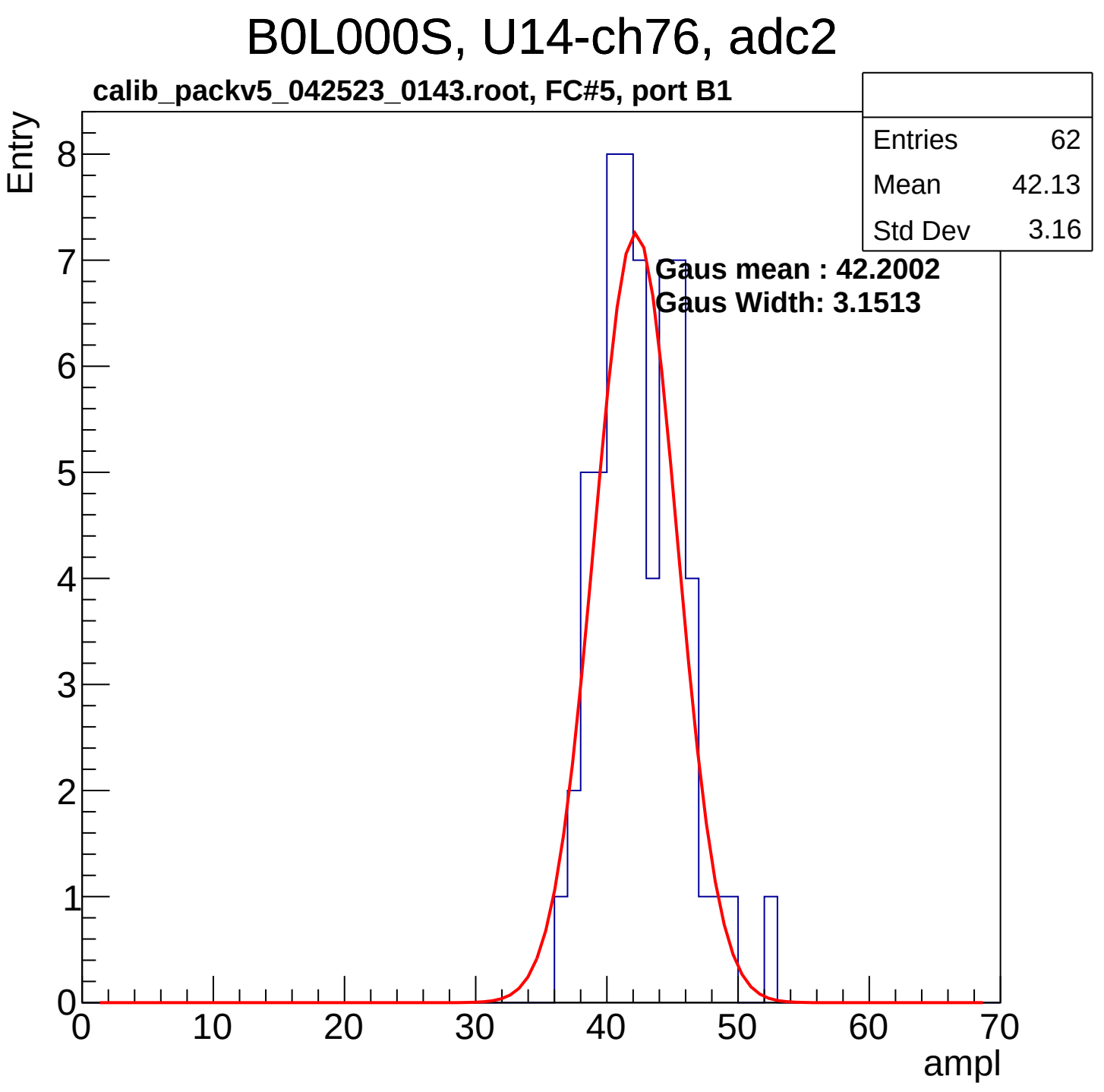
B0L000S, U14-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 42.13
Std Dev 3.16
Gaus mean : 42.2002
Gaus Width: 3.1513
Entry
ampl
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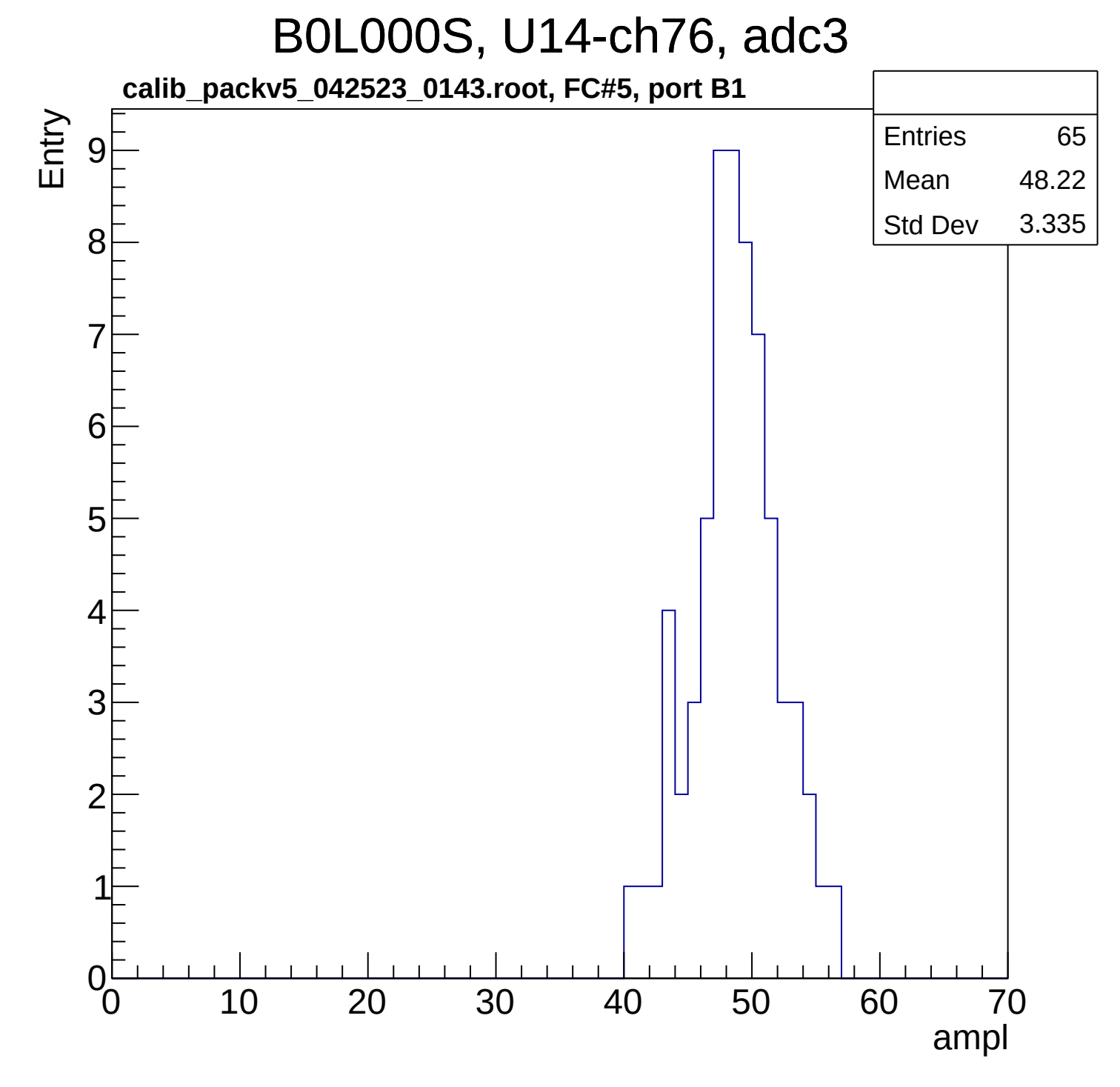
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 48.22
Std Dev 3.335
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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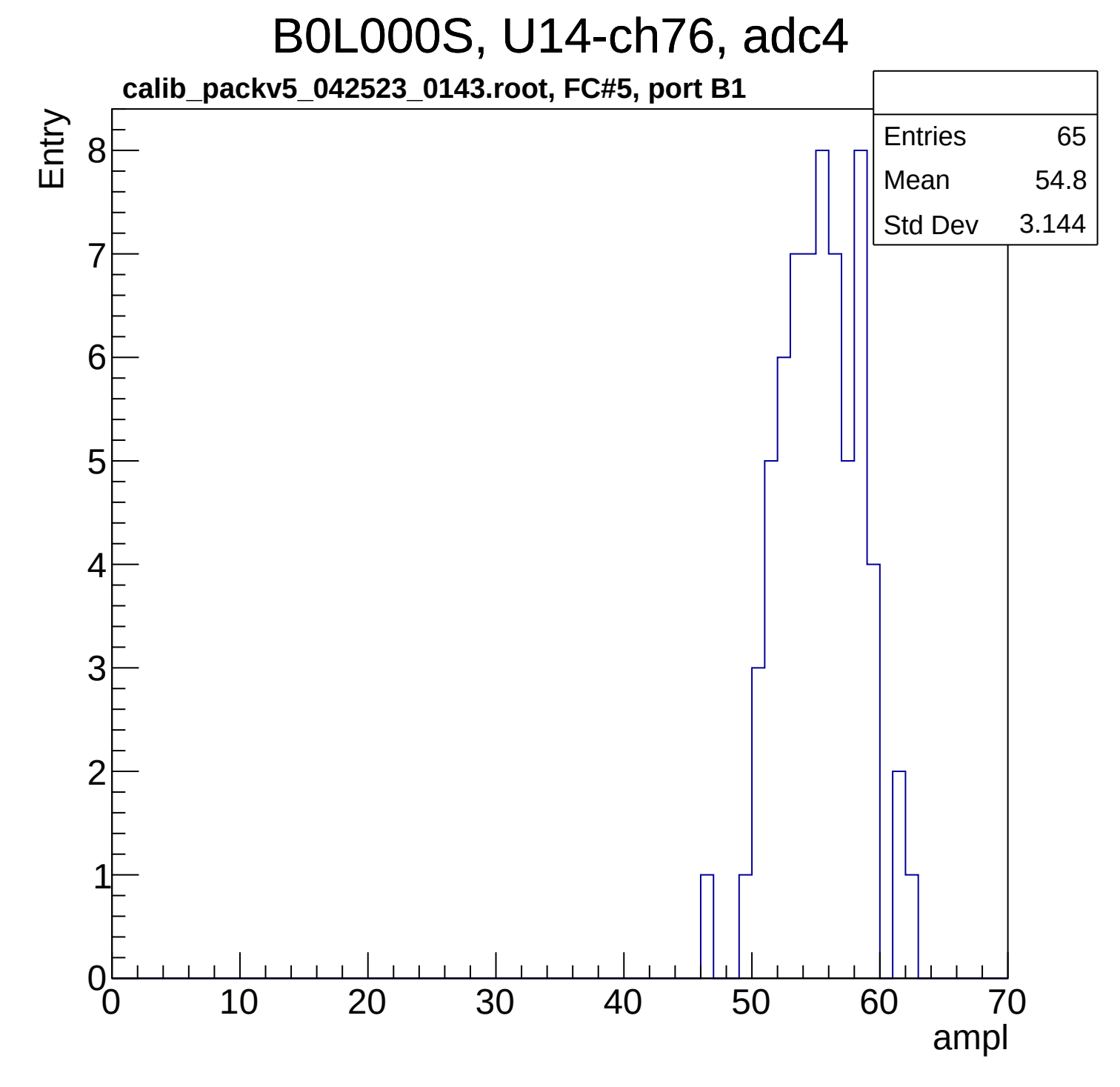
B0L000S, U14-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 54.8
Std Dev 3.144
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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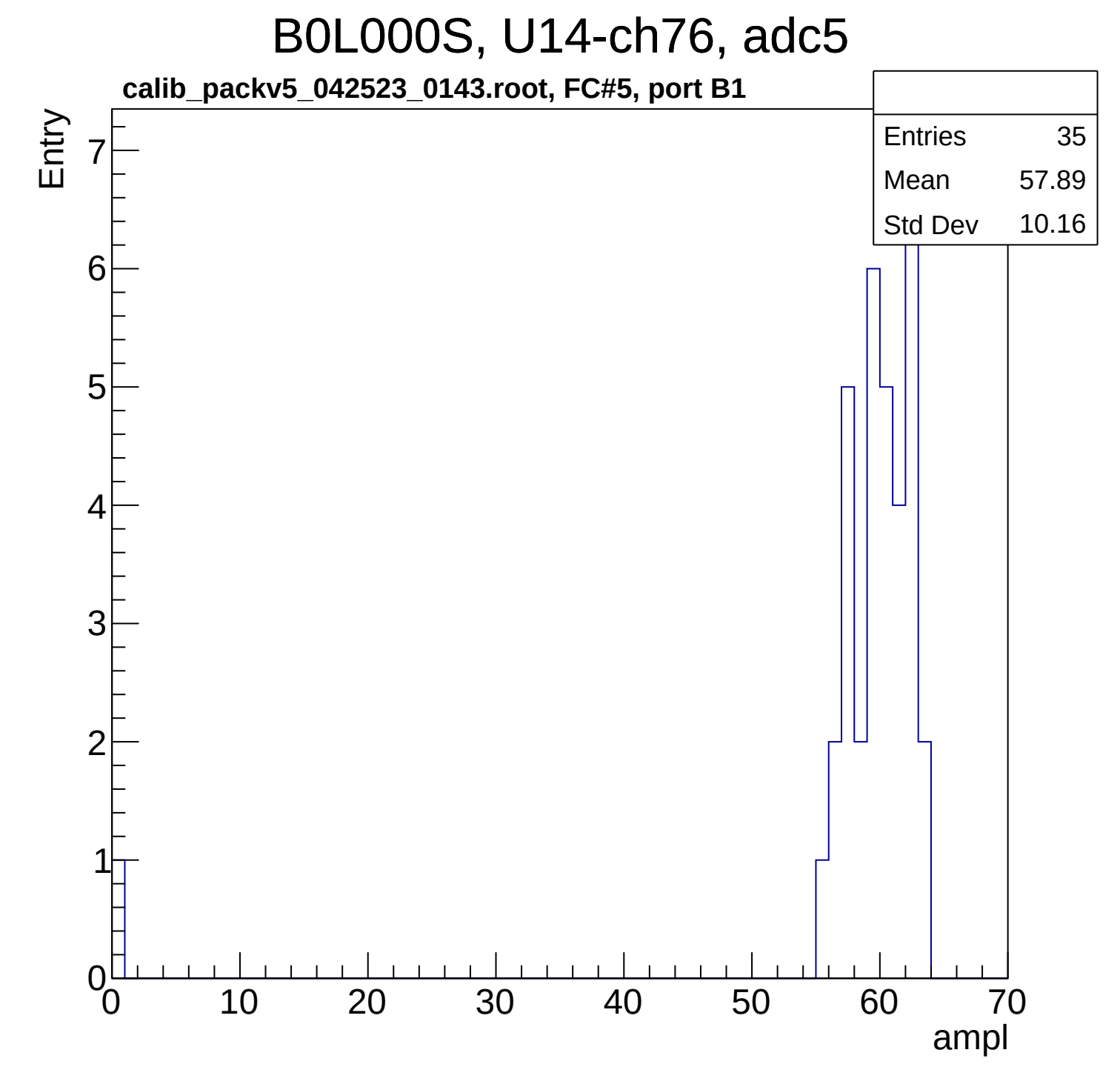
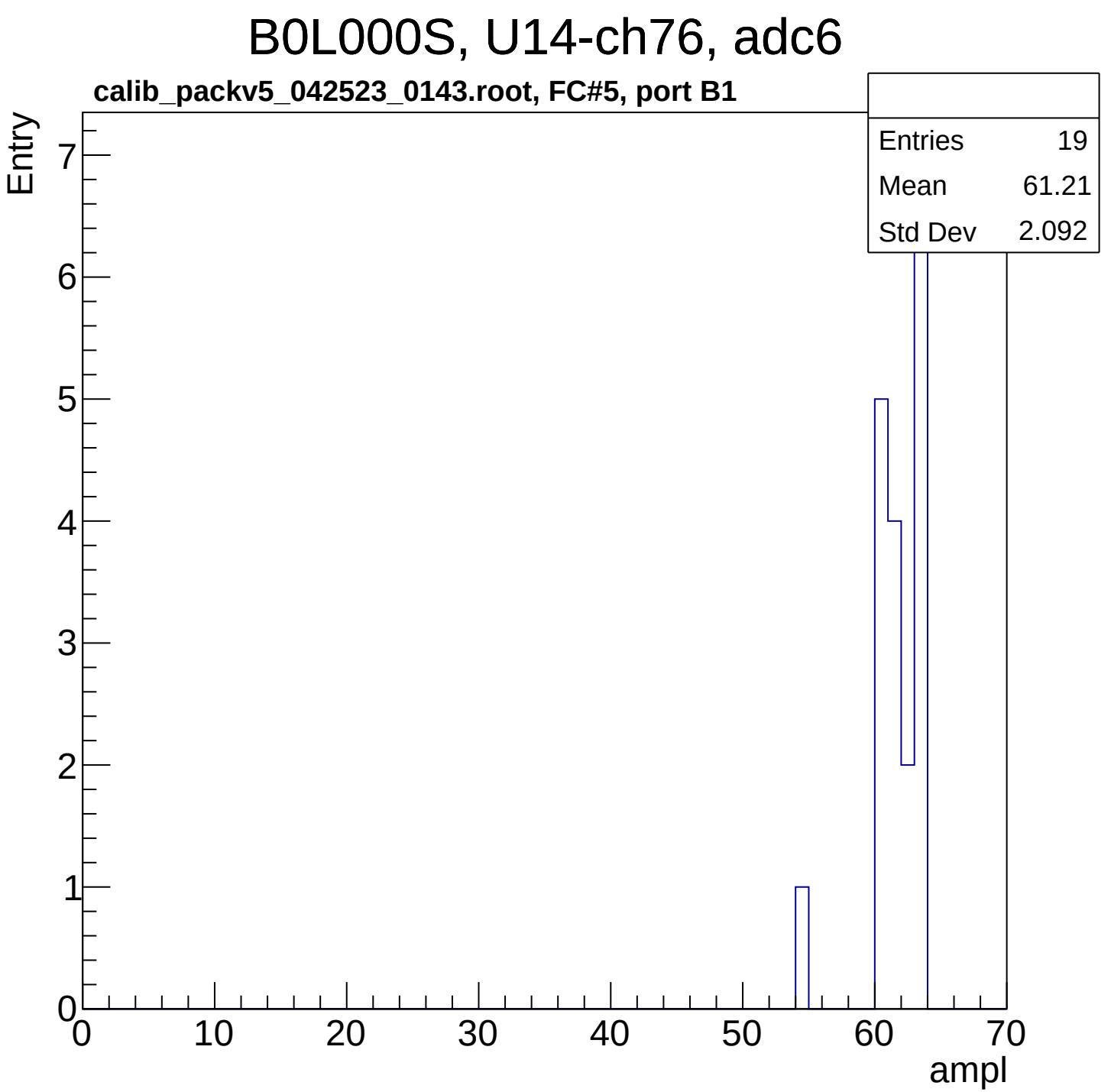
40
50
60
70


B0L000S, U14-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 57.89
Std Dev 10.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61.21
Std Dev 2.092
Entry
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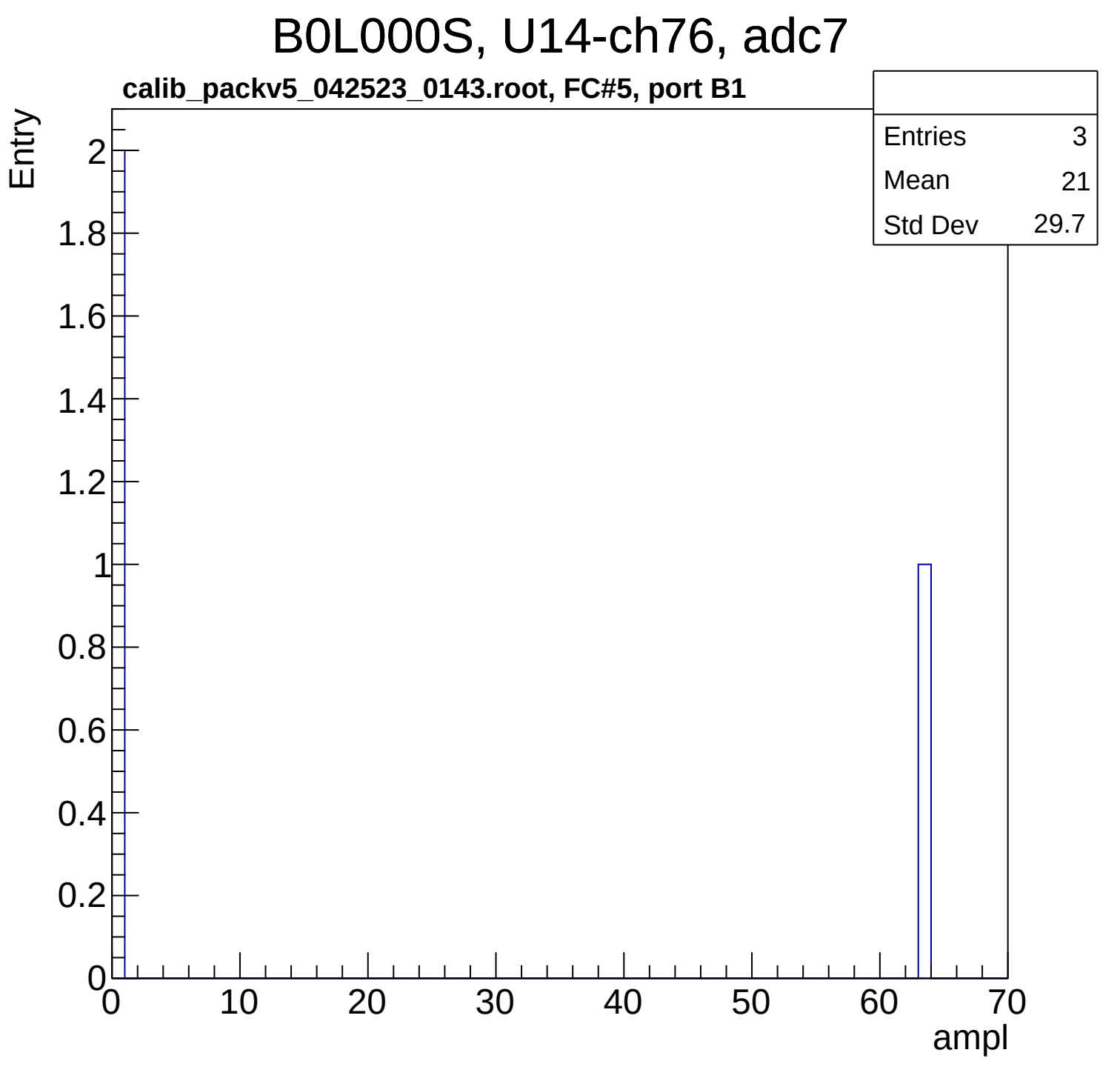
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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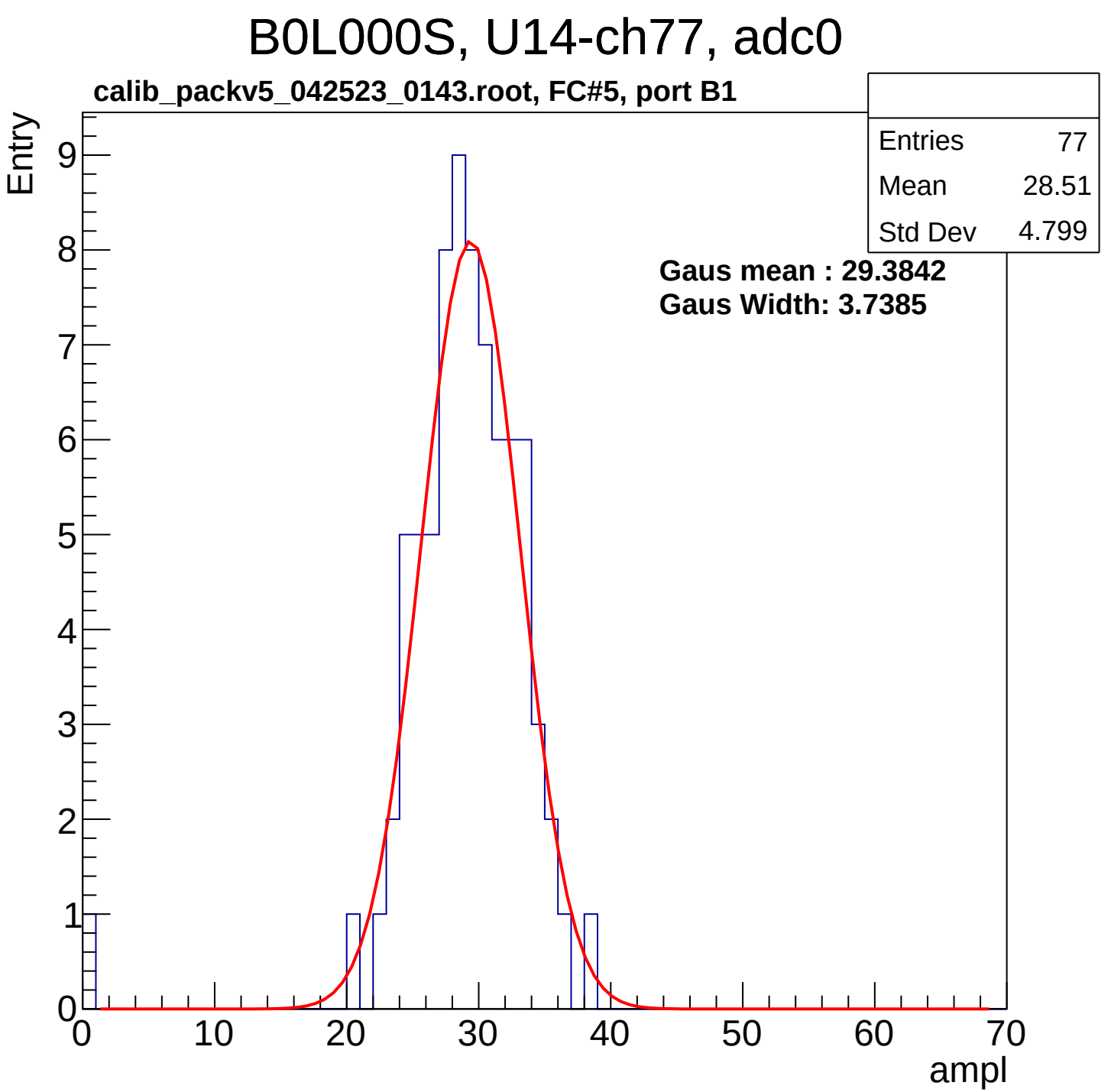
B0L000S, U14-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.51
Std Dev 4.799
Gaus mean : 29.3842
Gaus Width: 3.7385
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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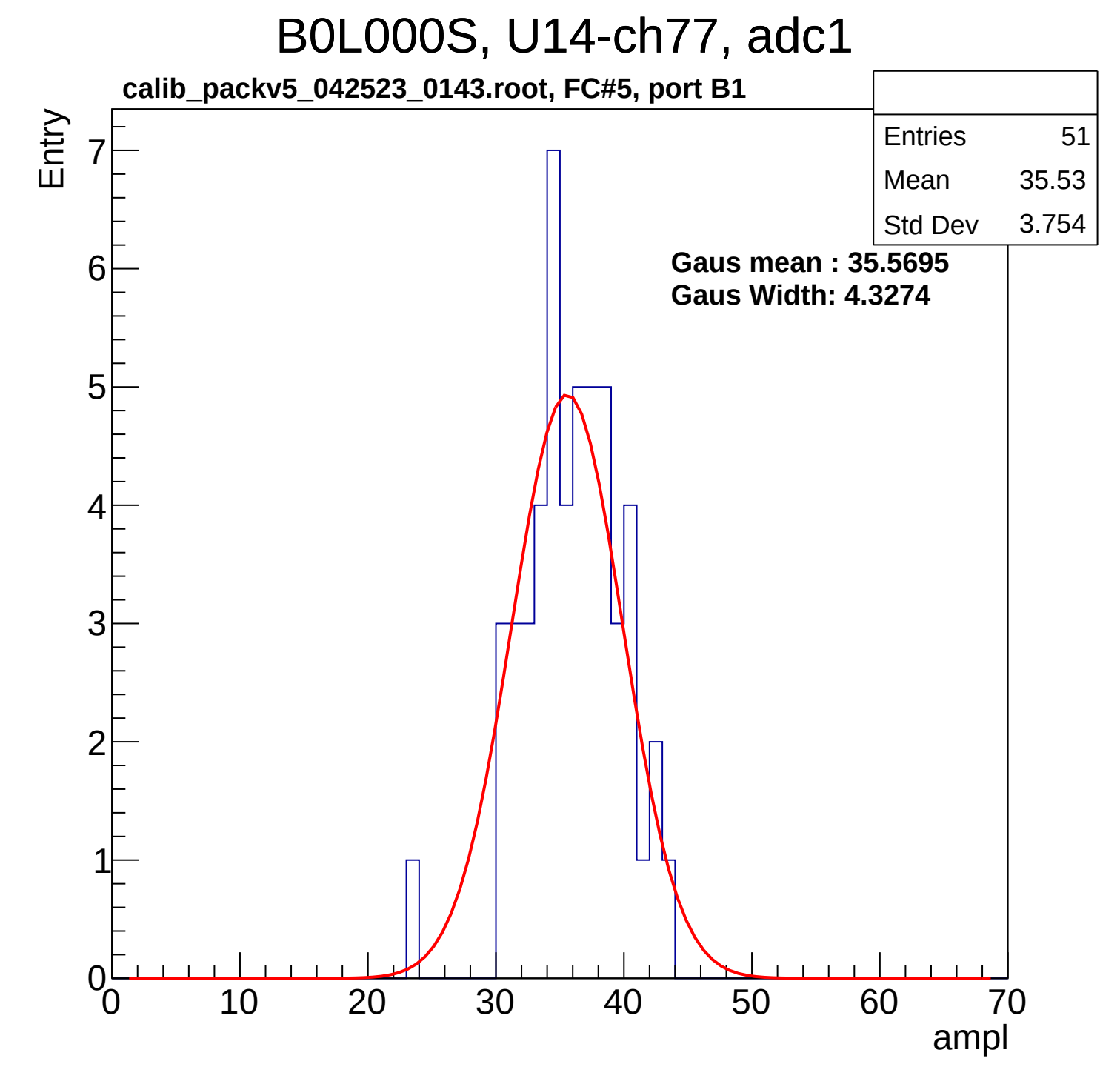
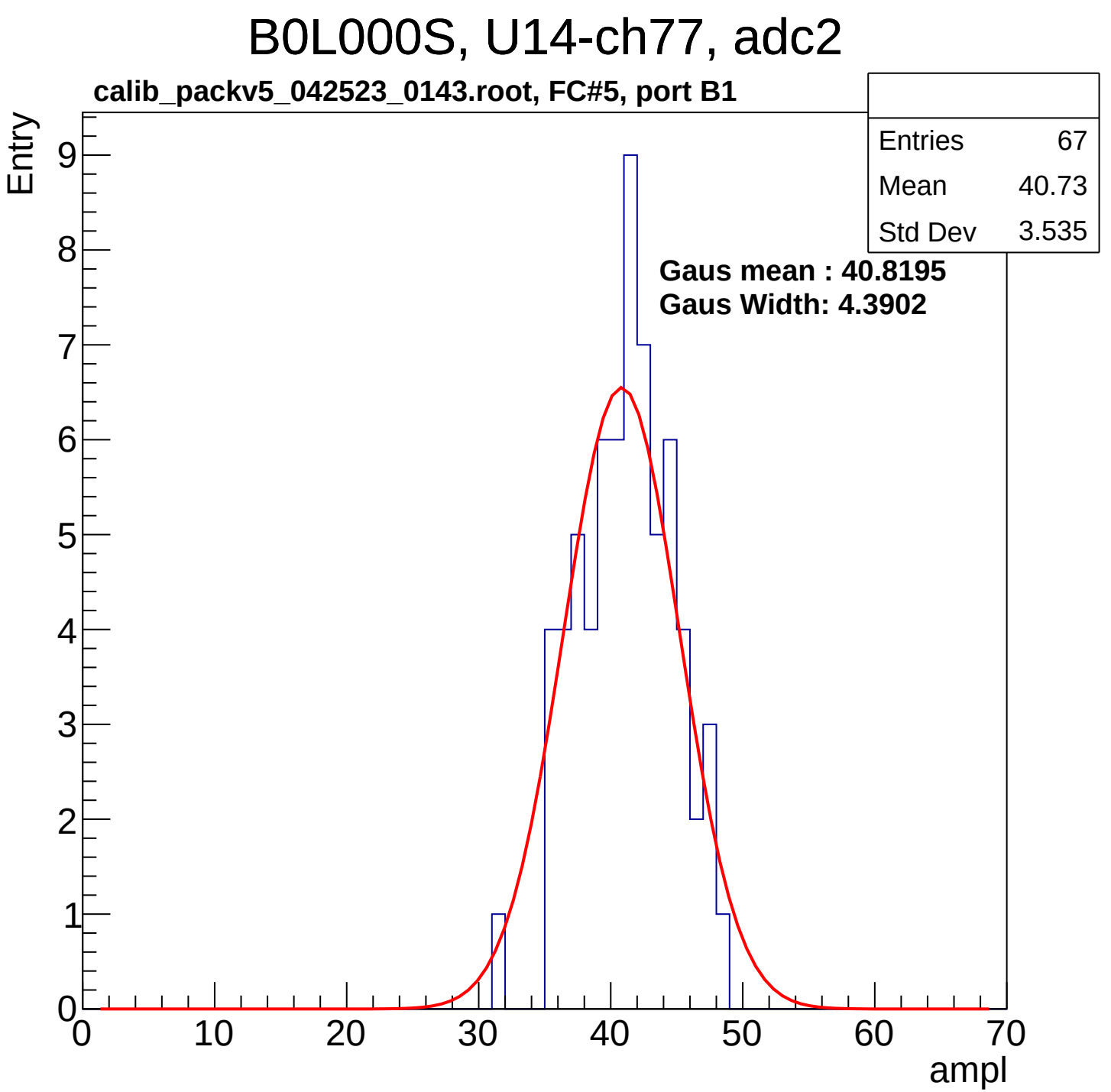

B0L000S, U14-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 35.53
Std Dev 3.754
Gaus mean : 35.5695
Gaus Width: 4.3274
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 40.73
Std Dev 3.535
Gaus mean : 40.8195
Gaus Width: 4.3902
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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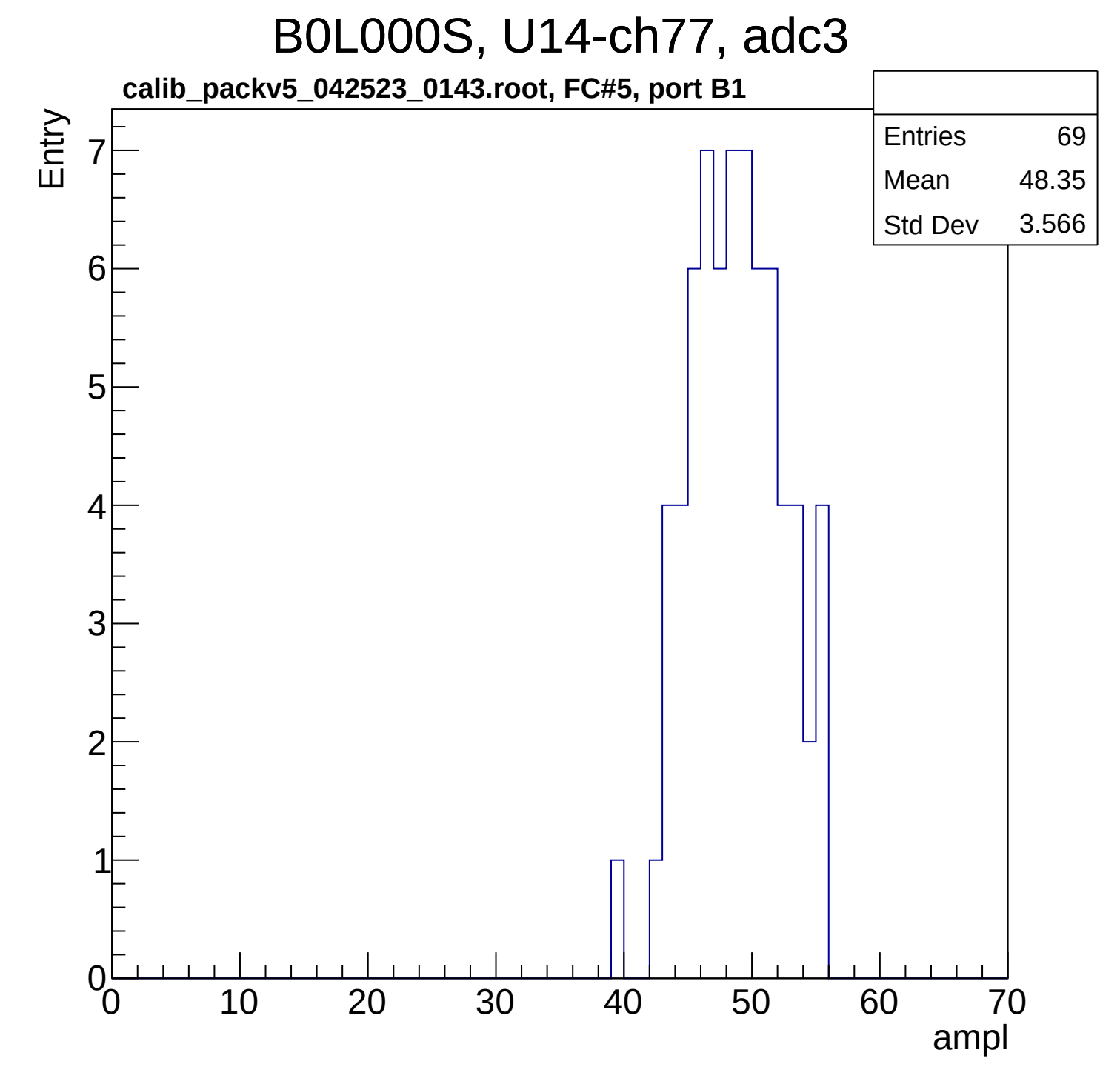
B0L000S, U14-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48.35
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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60
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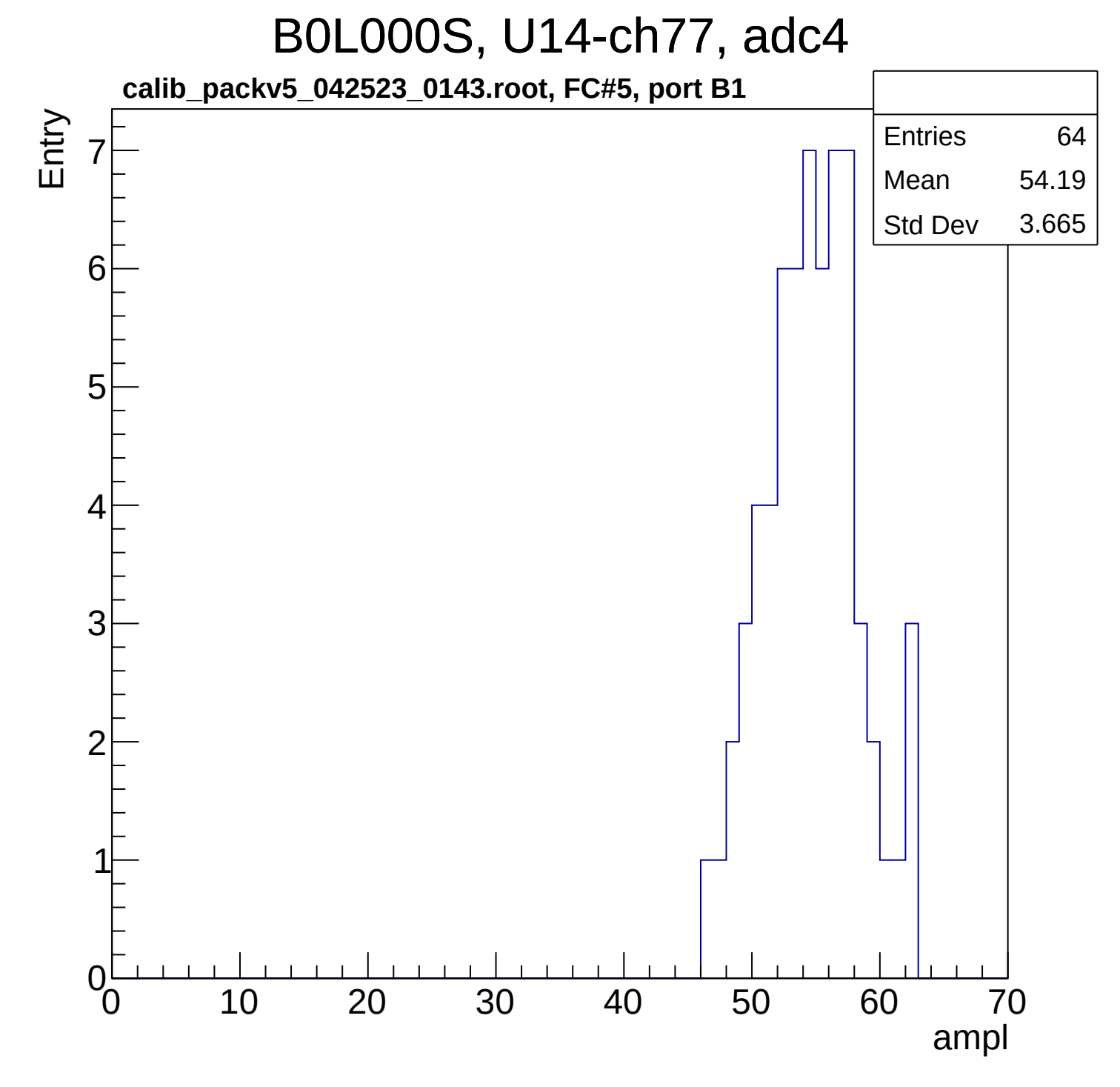
B0L000S, U14-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 54.19
Std Dev 3.665
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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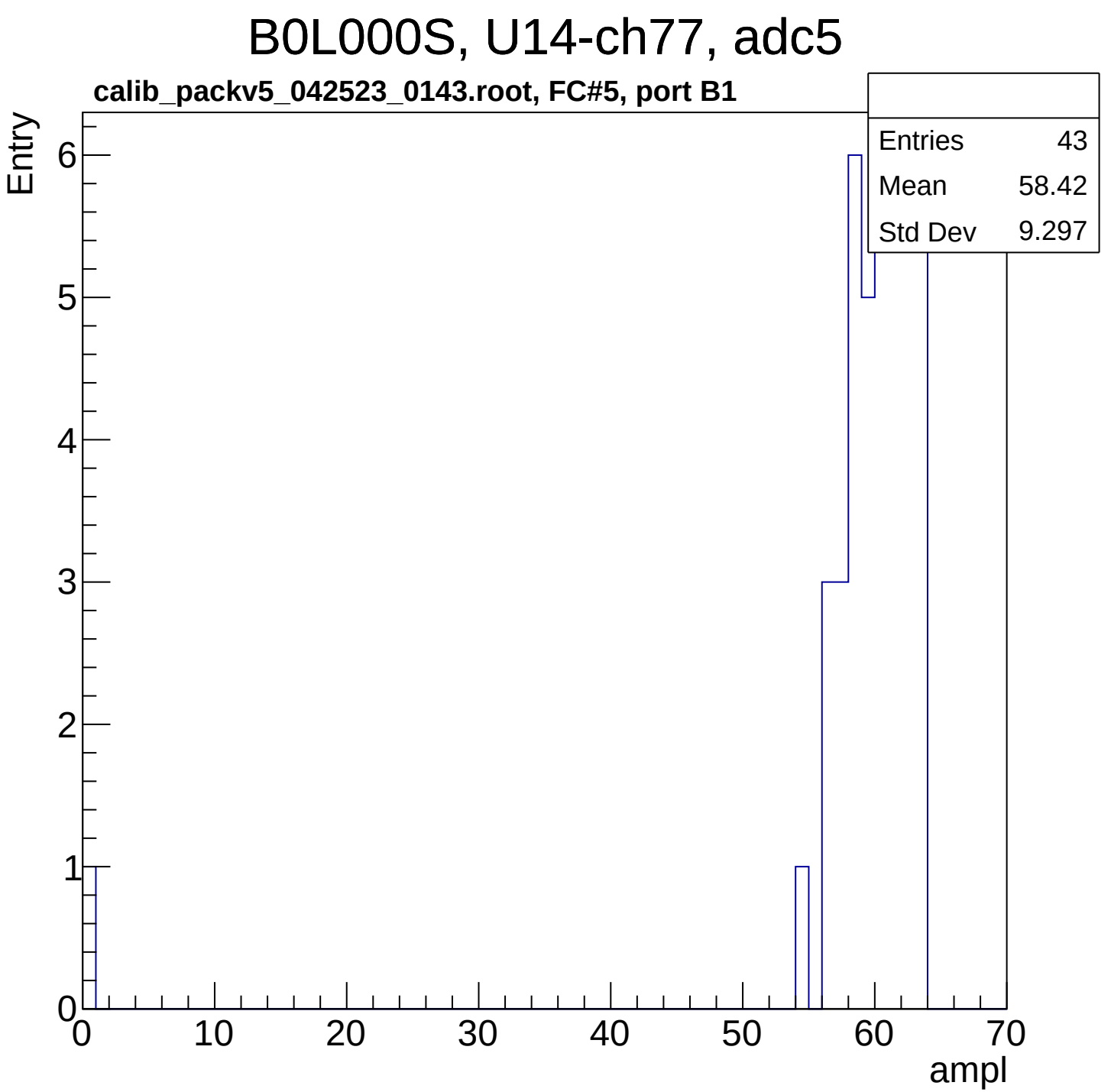
B0L000S, U14-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.42
Std Dev 9.297
Entry
0
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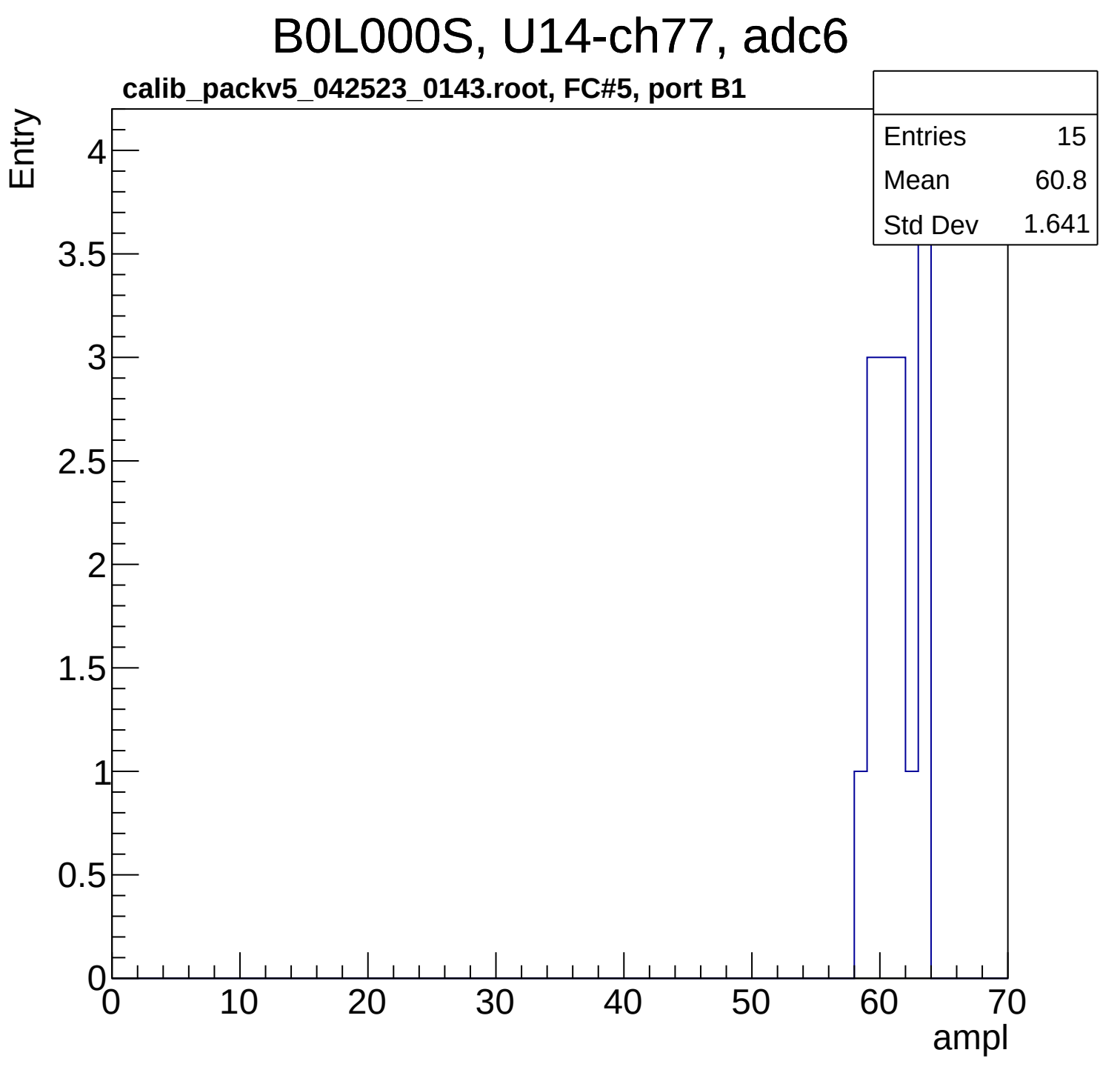
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 60.8
Std Dev 1.641
Entry
ampl
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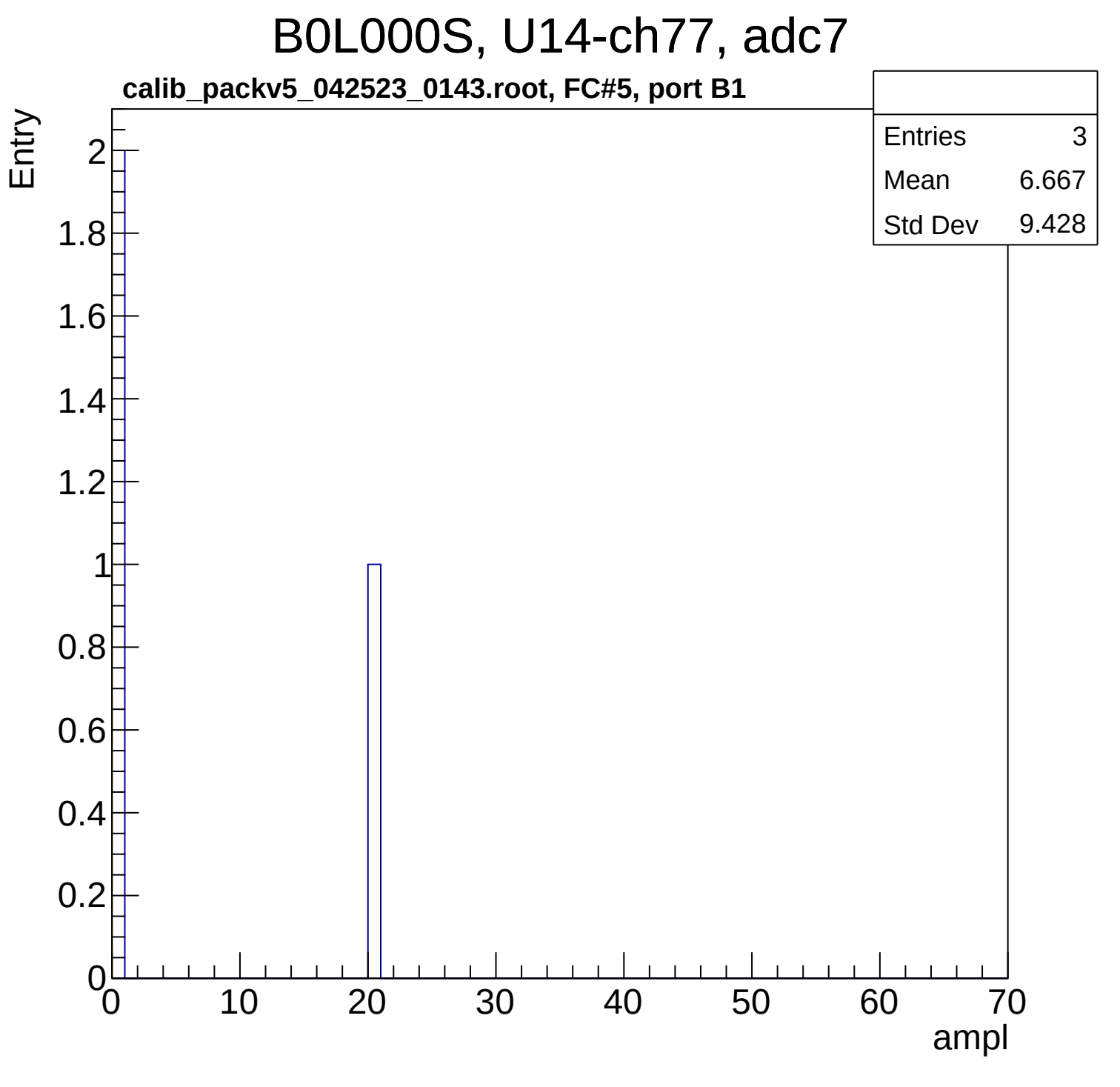
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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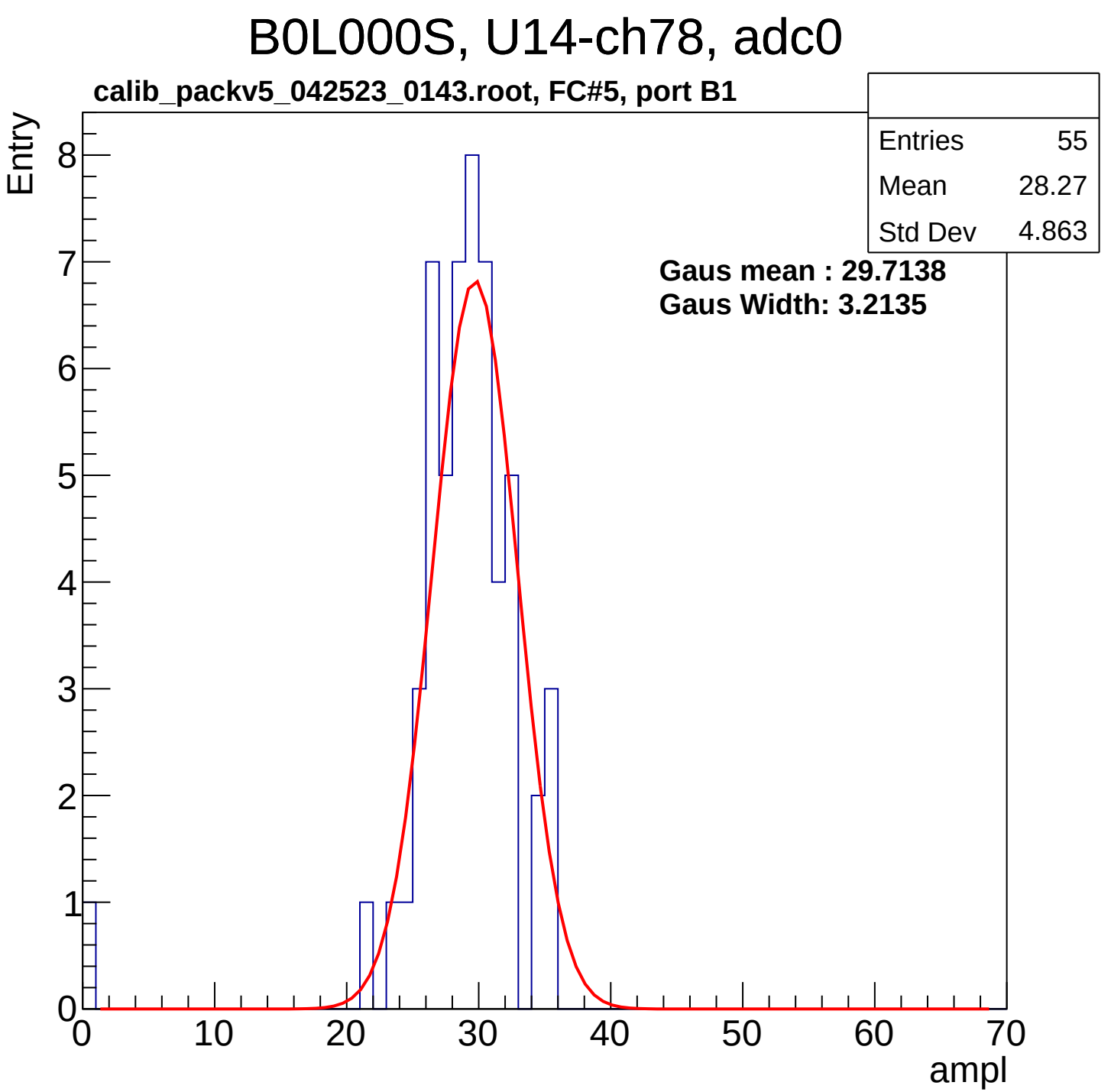
B0L000S, U14-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 28.27
Std Dev 4.863
Gaus mean : 29.7138
Gaus Width: 3.2135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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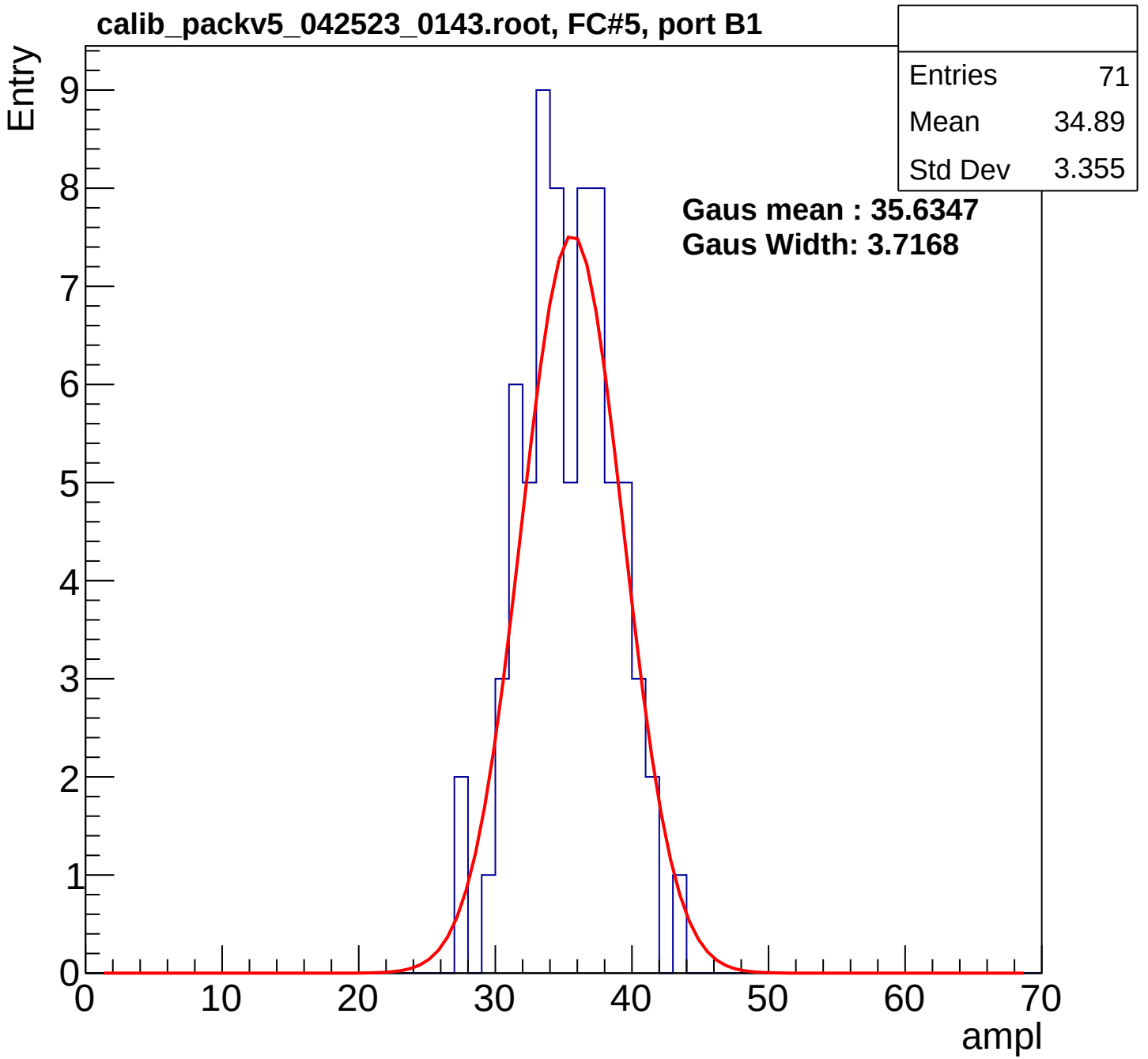
B0L000S, U14-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 34.89
Std Dev 3.355
Gaus mean : 35.6347
Gaus Width: 3.7168
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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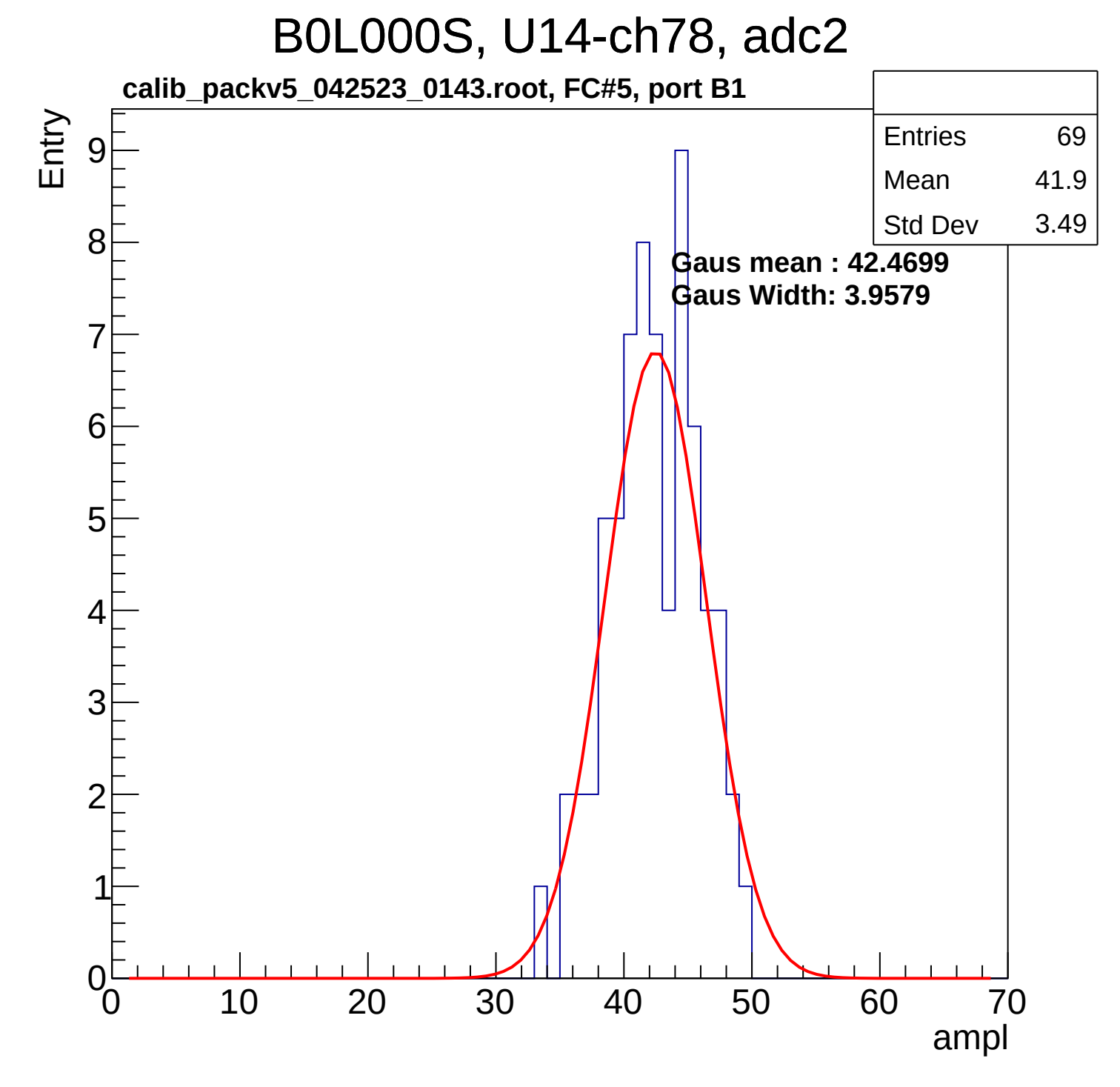
B0L000S, U14-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 41.9
Std Dev 3.49
Gaus mean : 42.4699
Gaus Width: 3.9579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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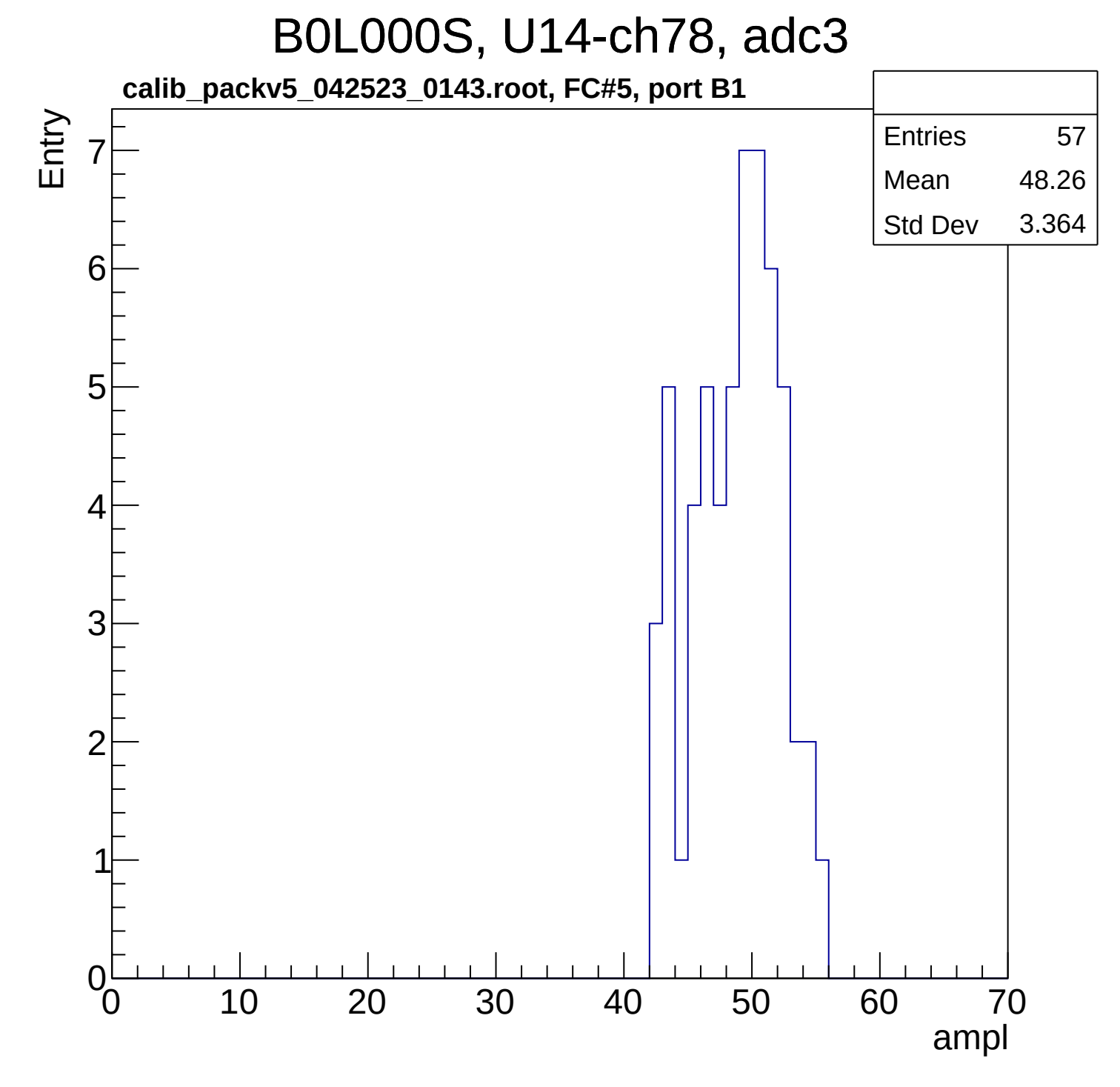
B0L000S, U14-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 48.26
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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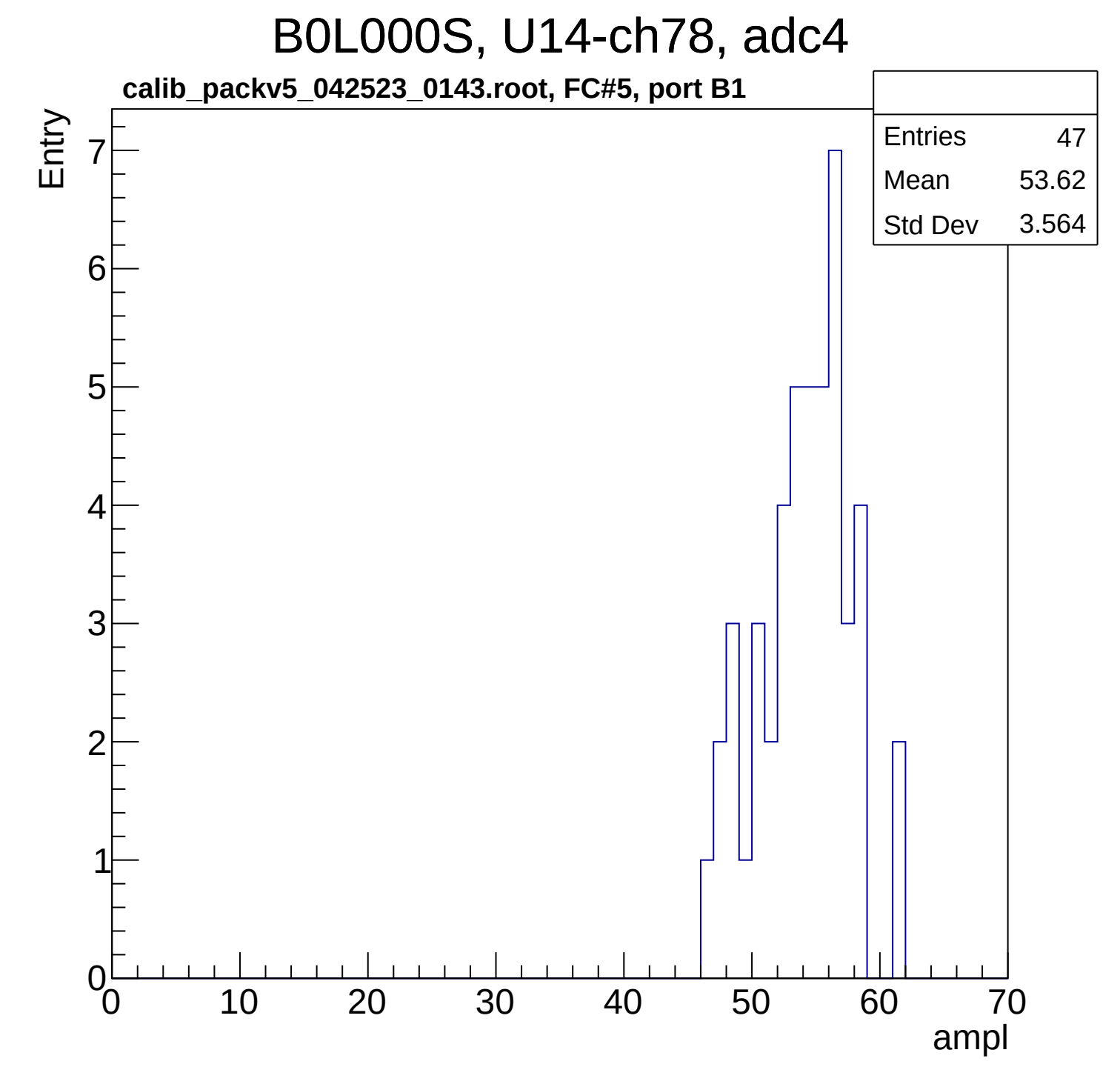
B0L000S, U14-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 53.62
Std Dev 3.564
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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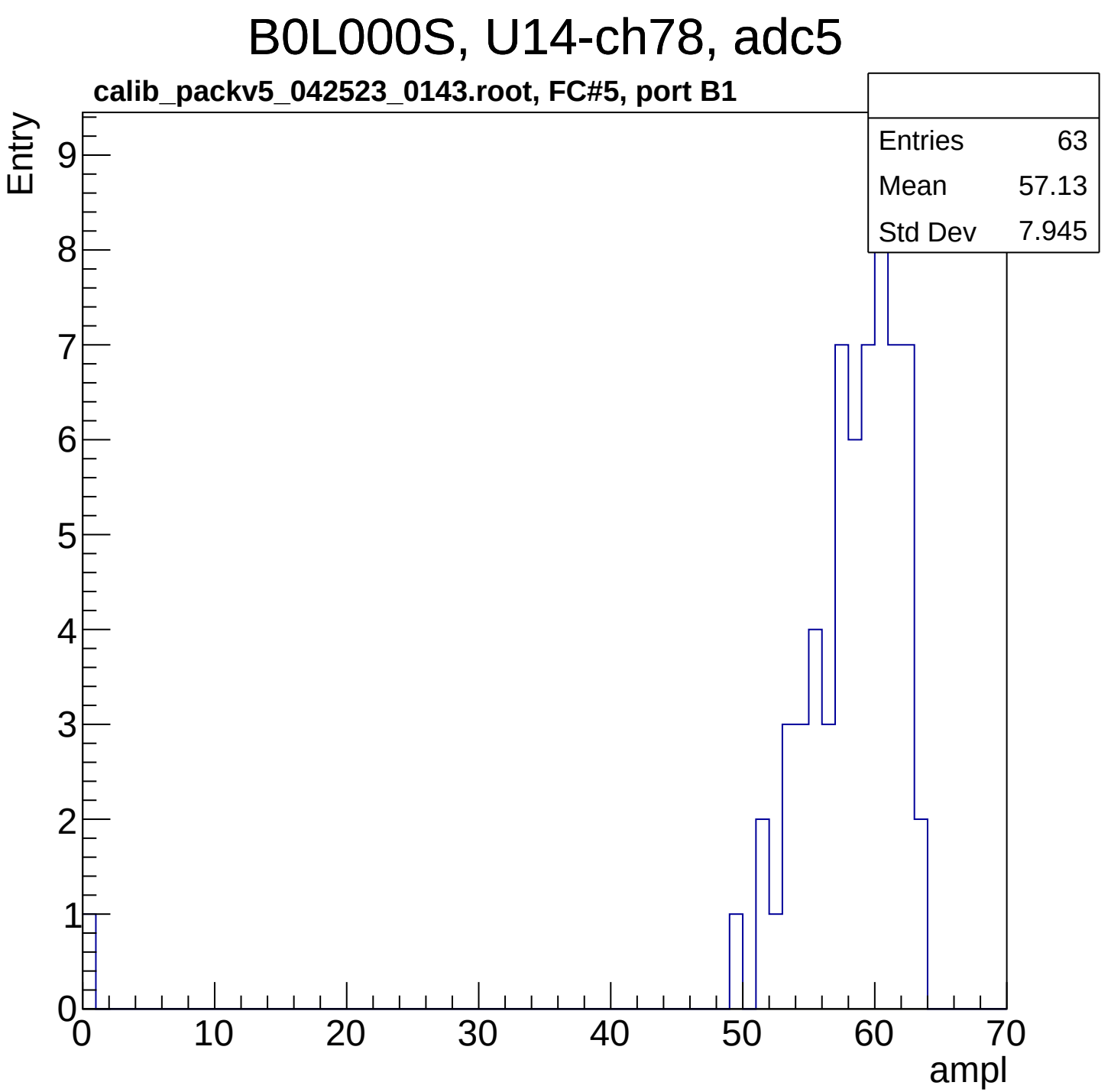
B0L000S, U14-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 57.13
Std Dev 7.945
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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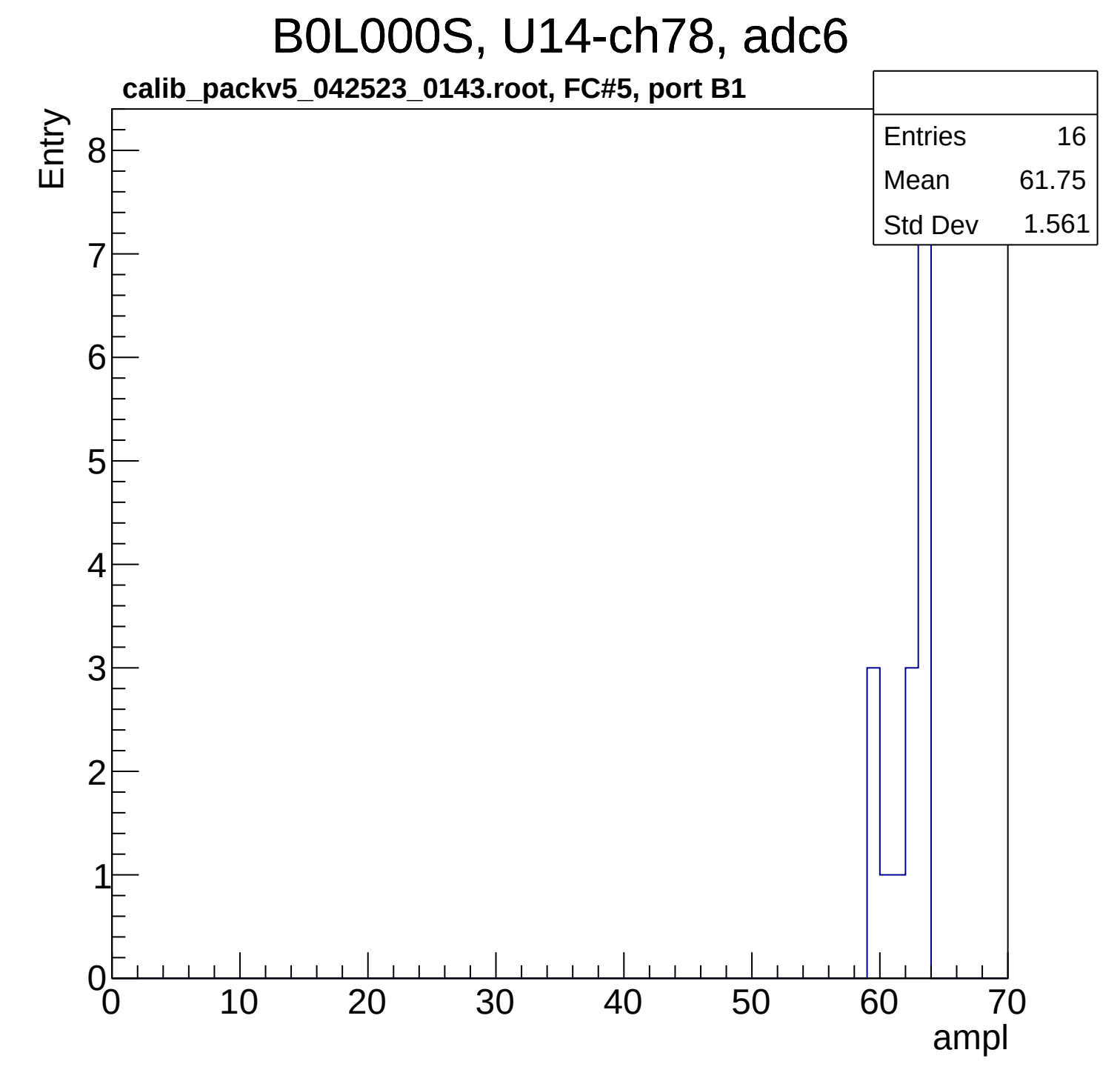
B0L000S, U14-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.75
Std Dev 1.561
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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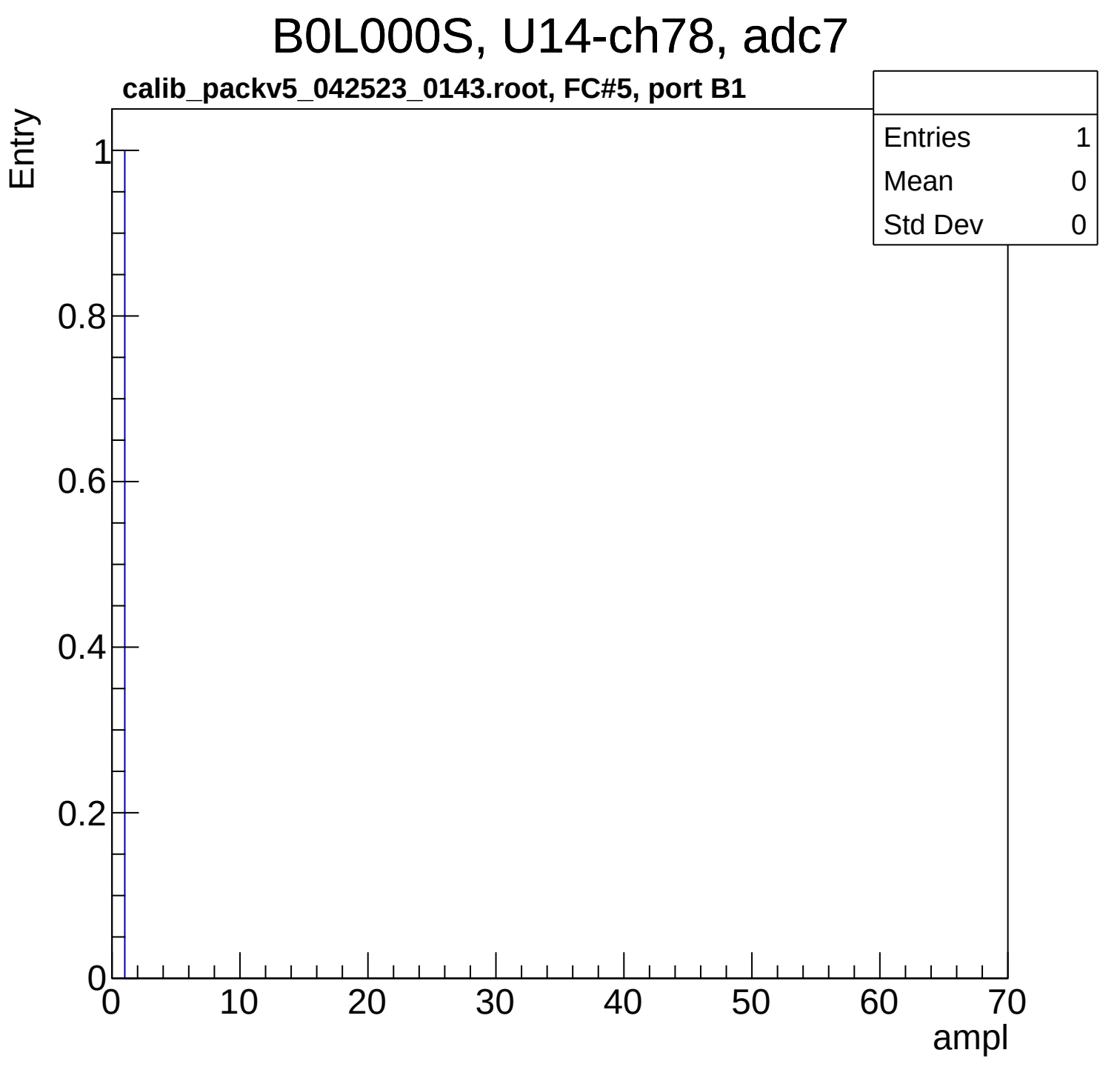
B0L000S, U14-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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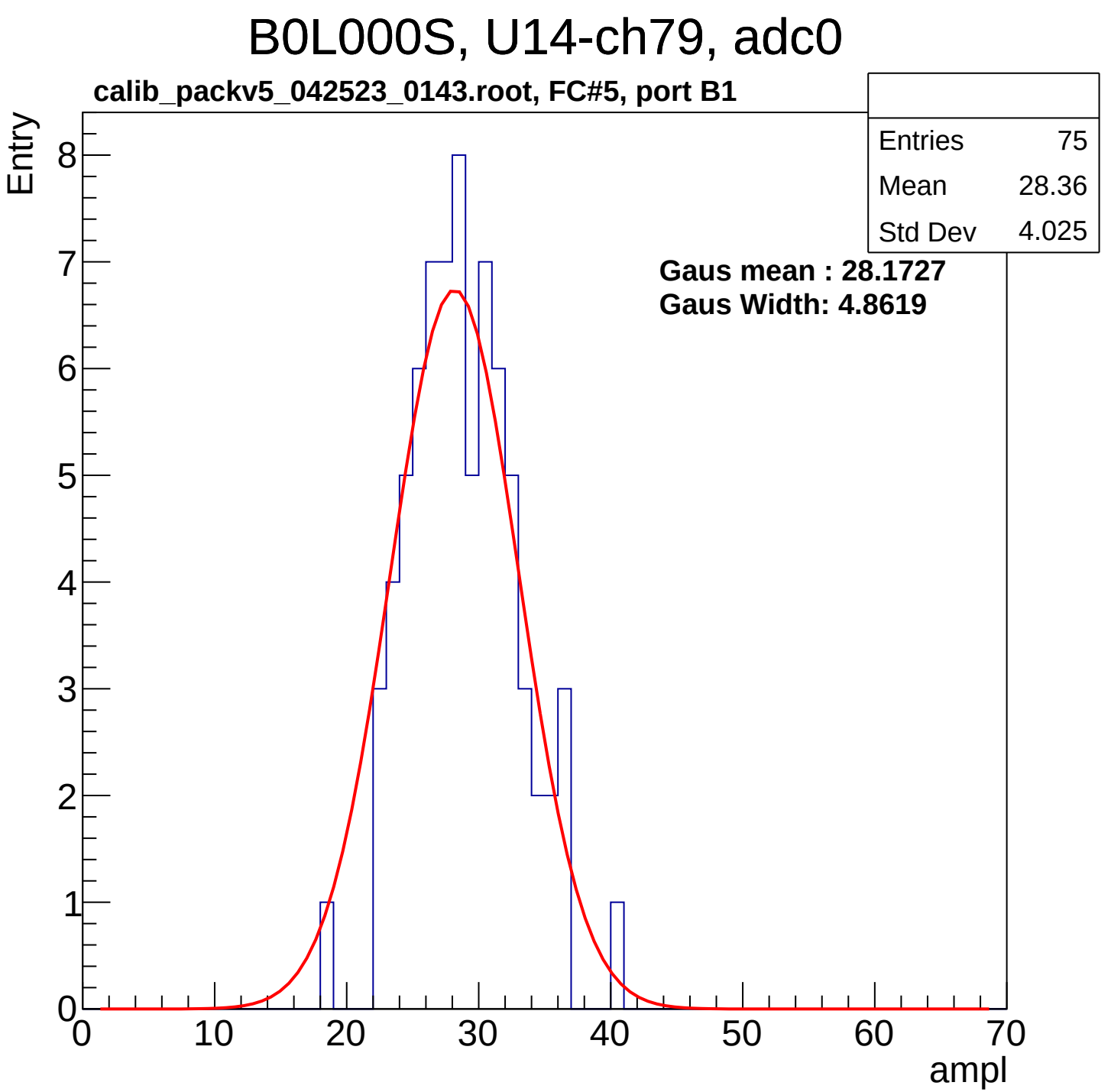
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.36
Std Dev 4.025
Gaus mean : 28.1727
Gaus Width: 4.8619
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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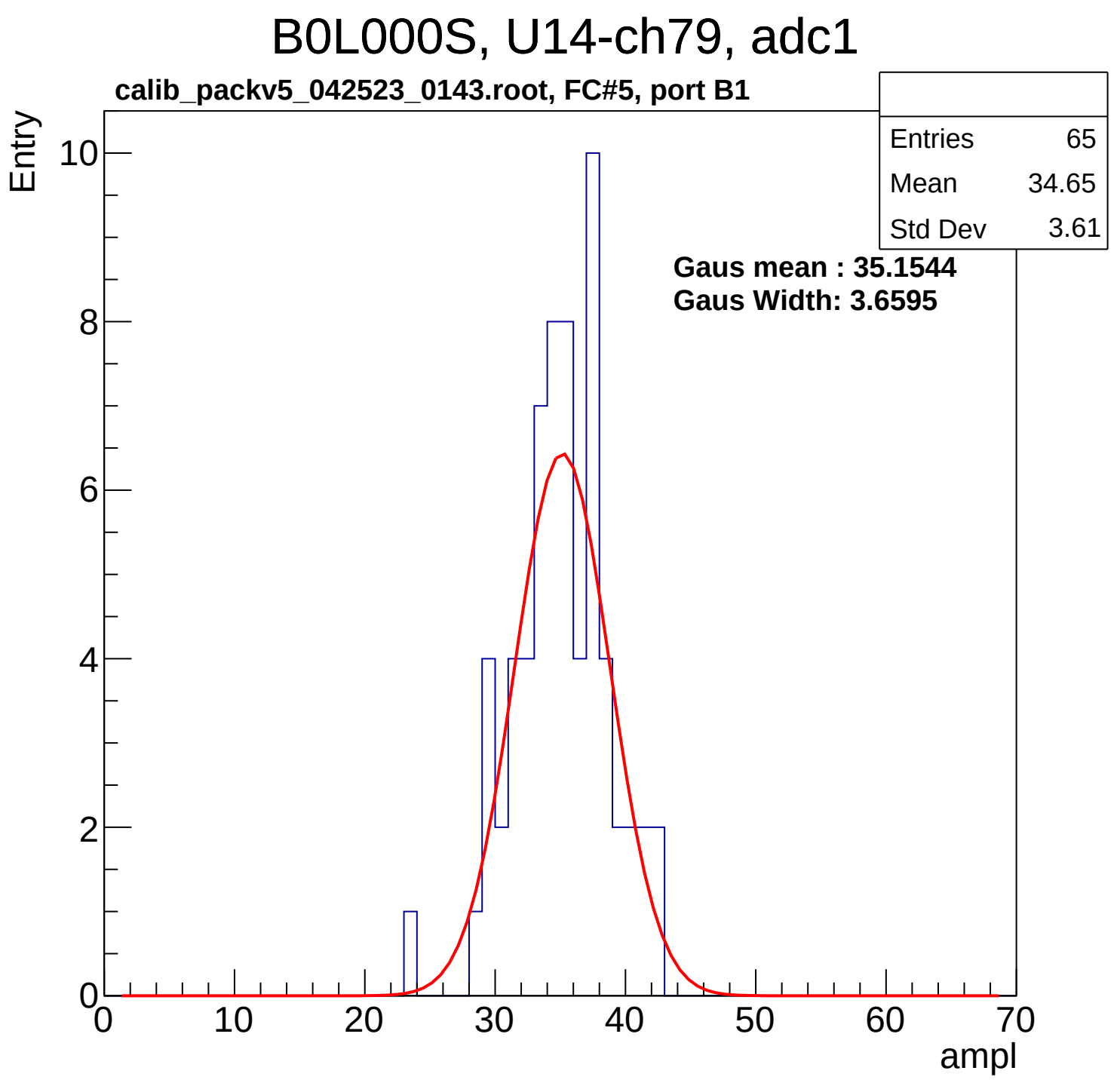
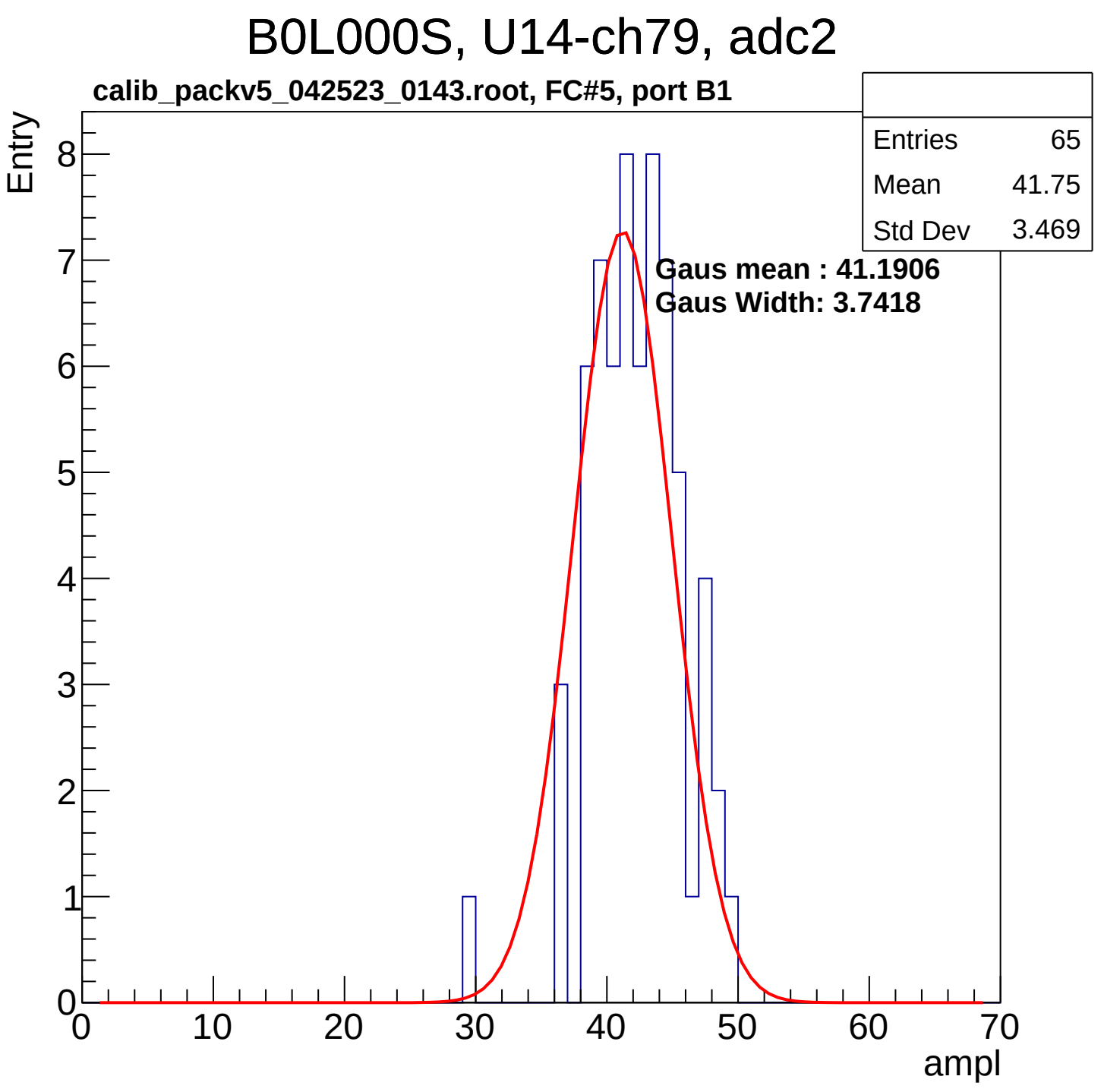

B0L000S, U14-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 34.65
Std Dev 3.61
Gaus mean : 35.1544
Gaus Width: 3.6595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41.75
Std Dev 3.469
Gaus mean : 41.1906
Gaus Width: 3.7418
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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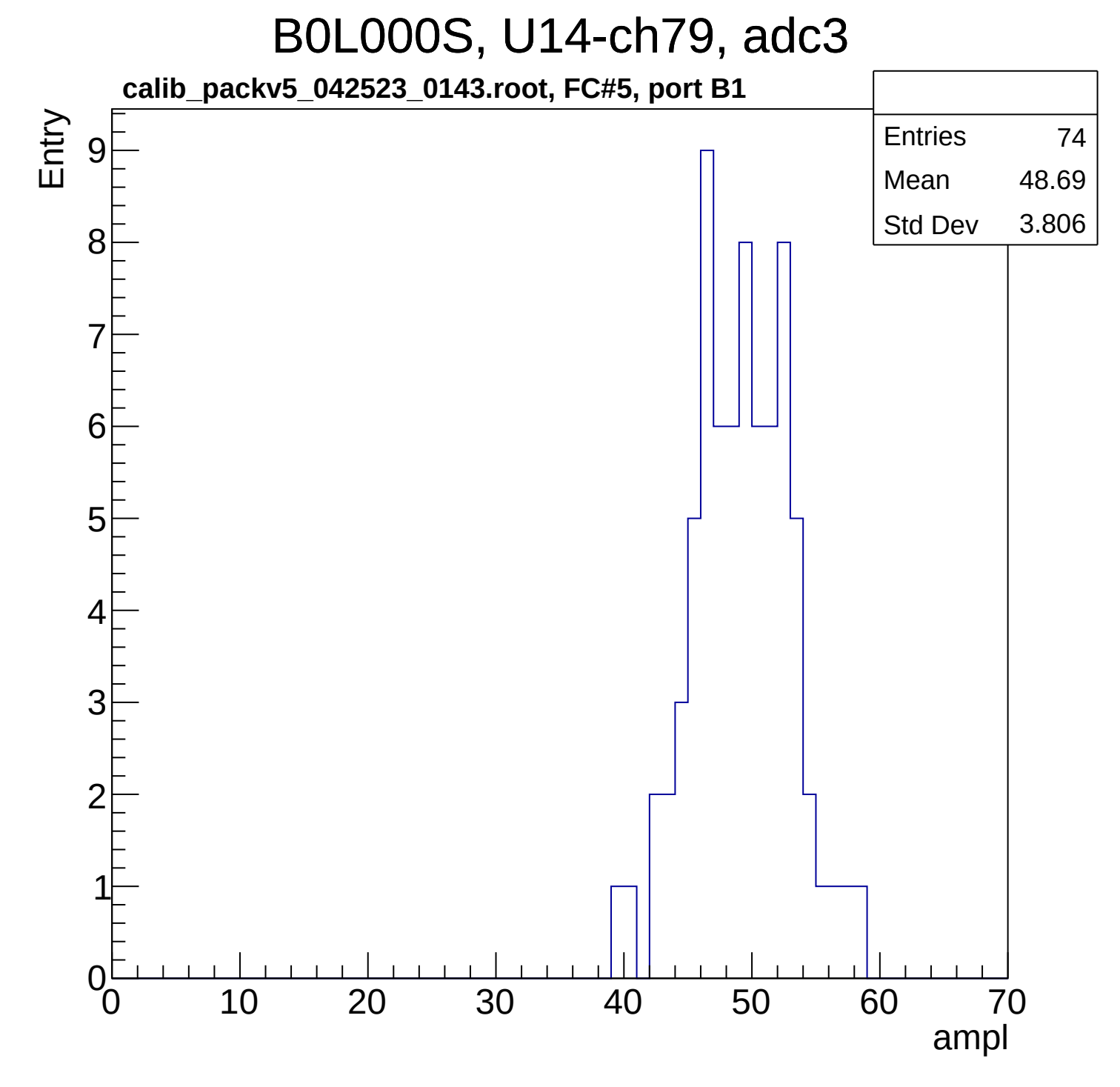
B0L000S, U14-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 48.69
Std Dev 3.806
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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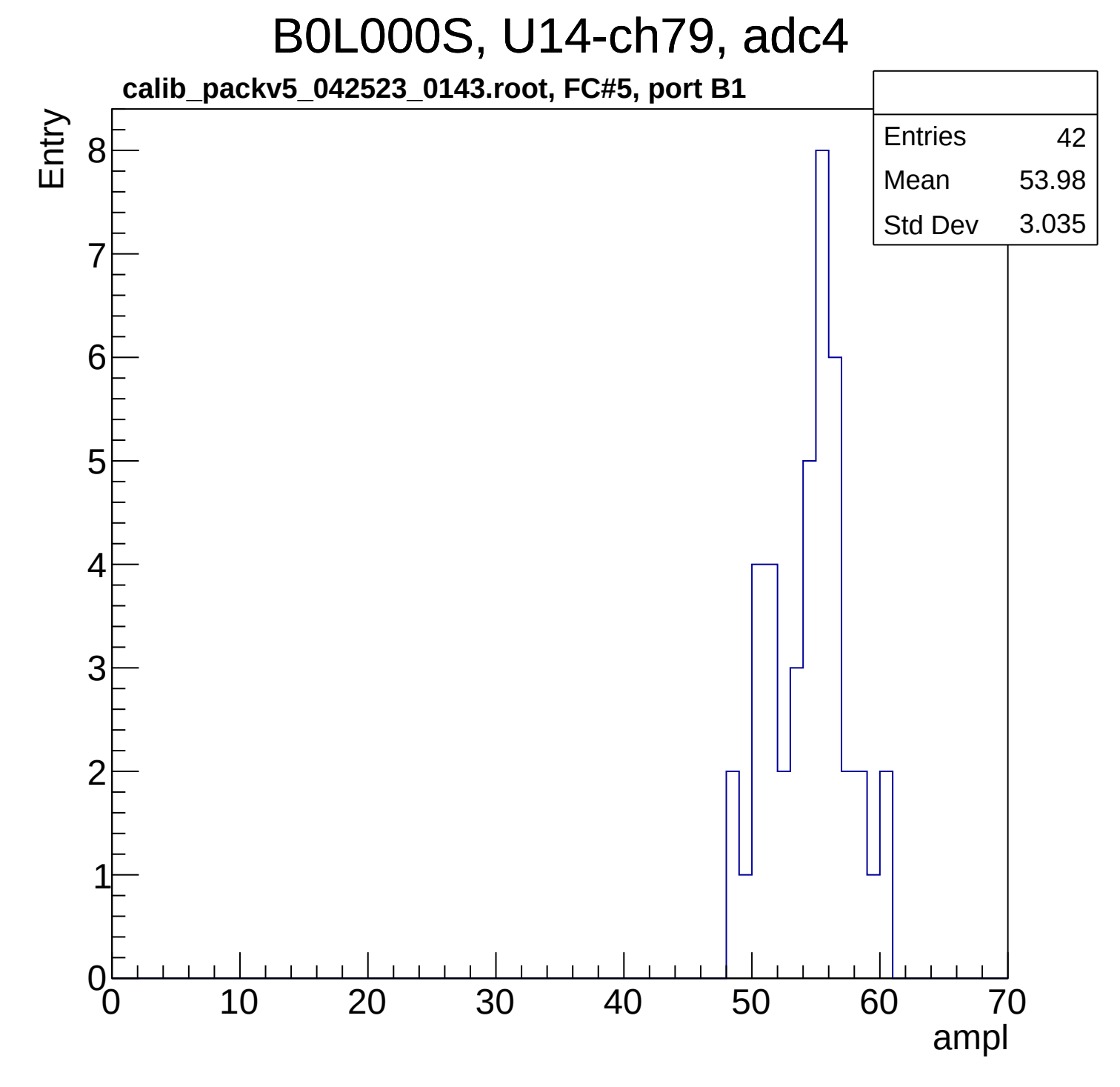
B0L000S, U14-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 53.98
Std Dev 3.035
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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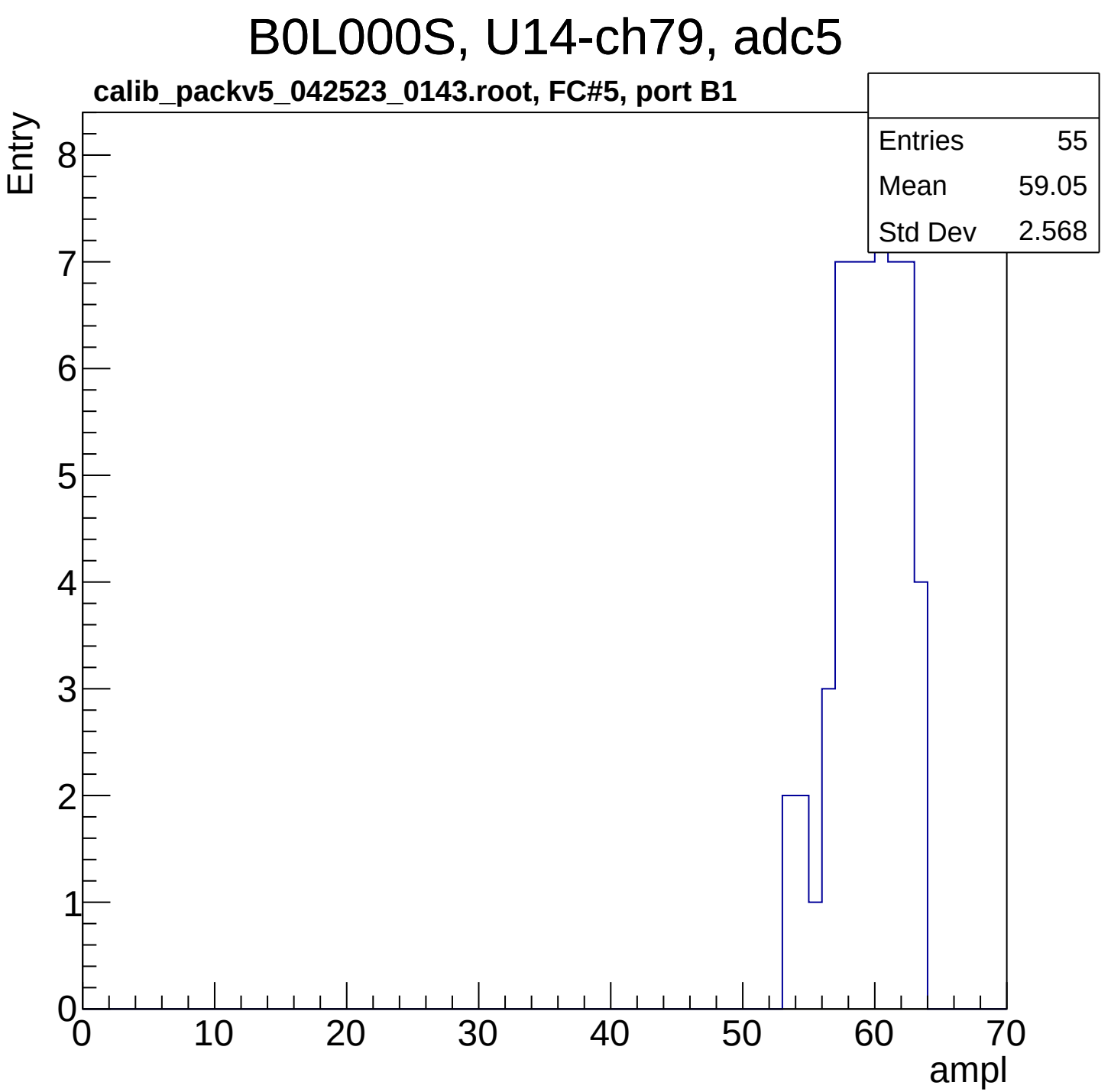
B0L000S, U14-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 59.05
Std Dev 2.568
Entry
ampl
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1
2
3
4
5
6
7
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0
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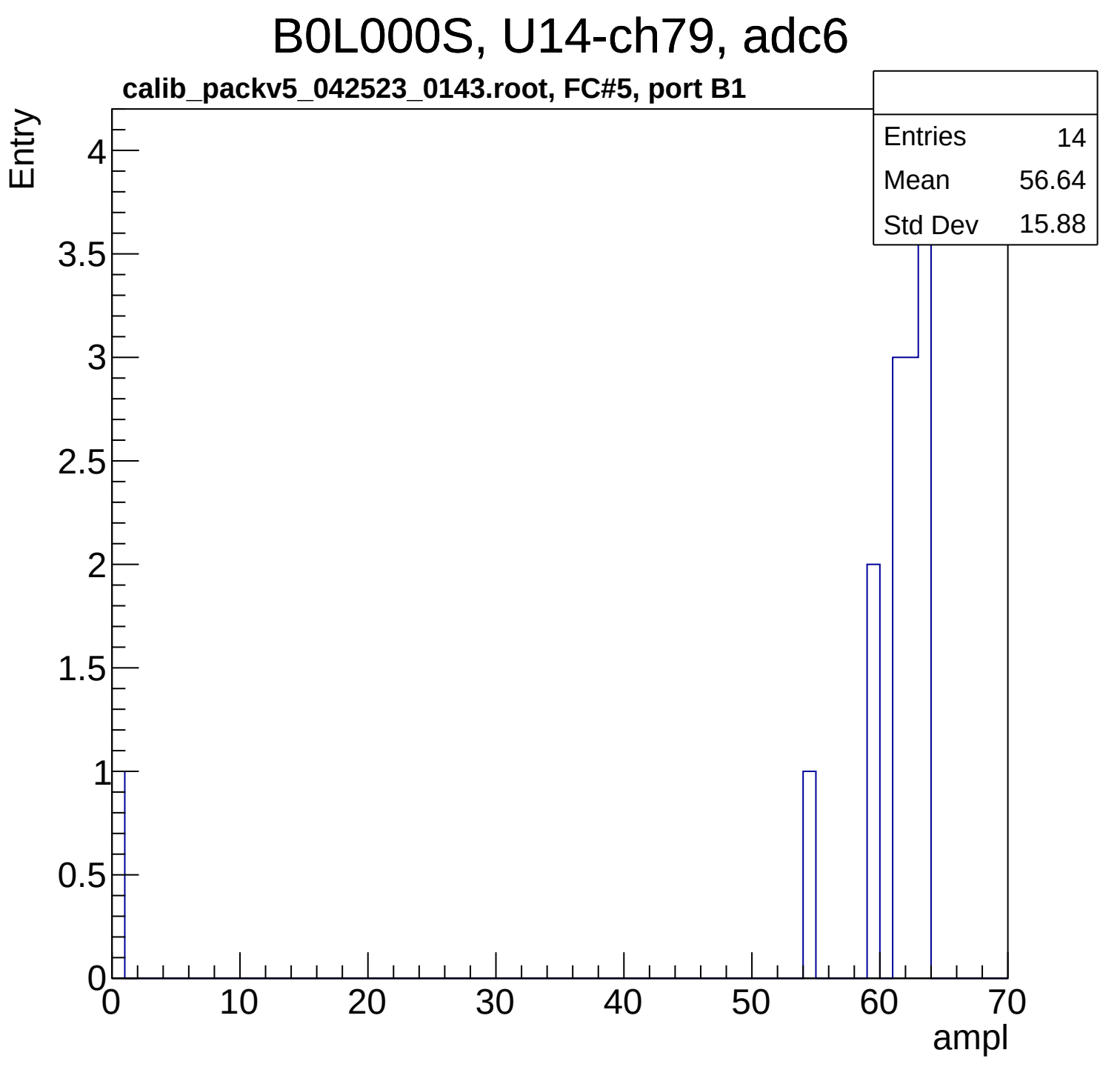
B0L000S, U14-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 56.64
Std Dev 15.88
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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20
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40
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60
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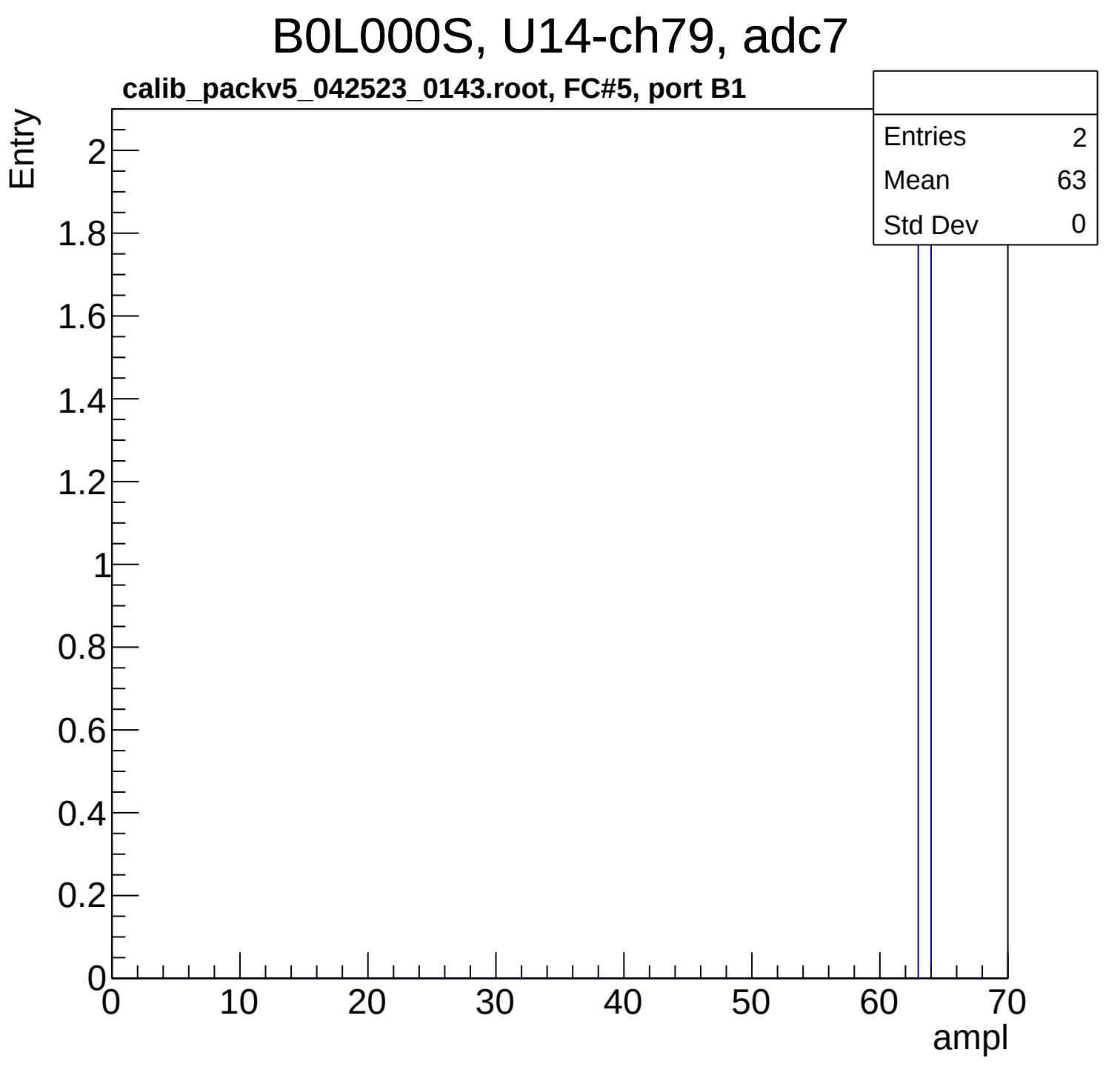
B0L000S, U14-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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60
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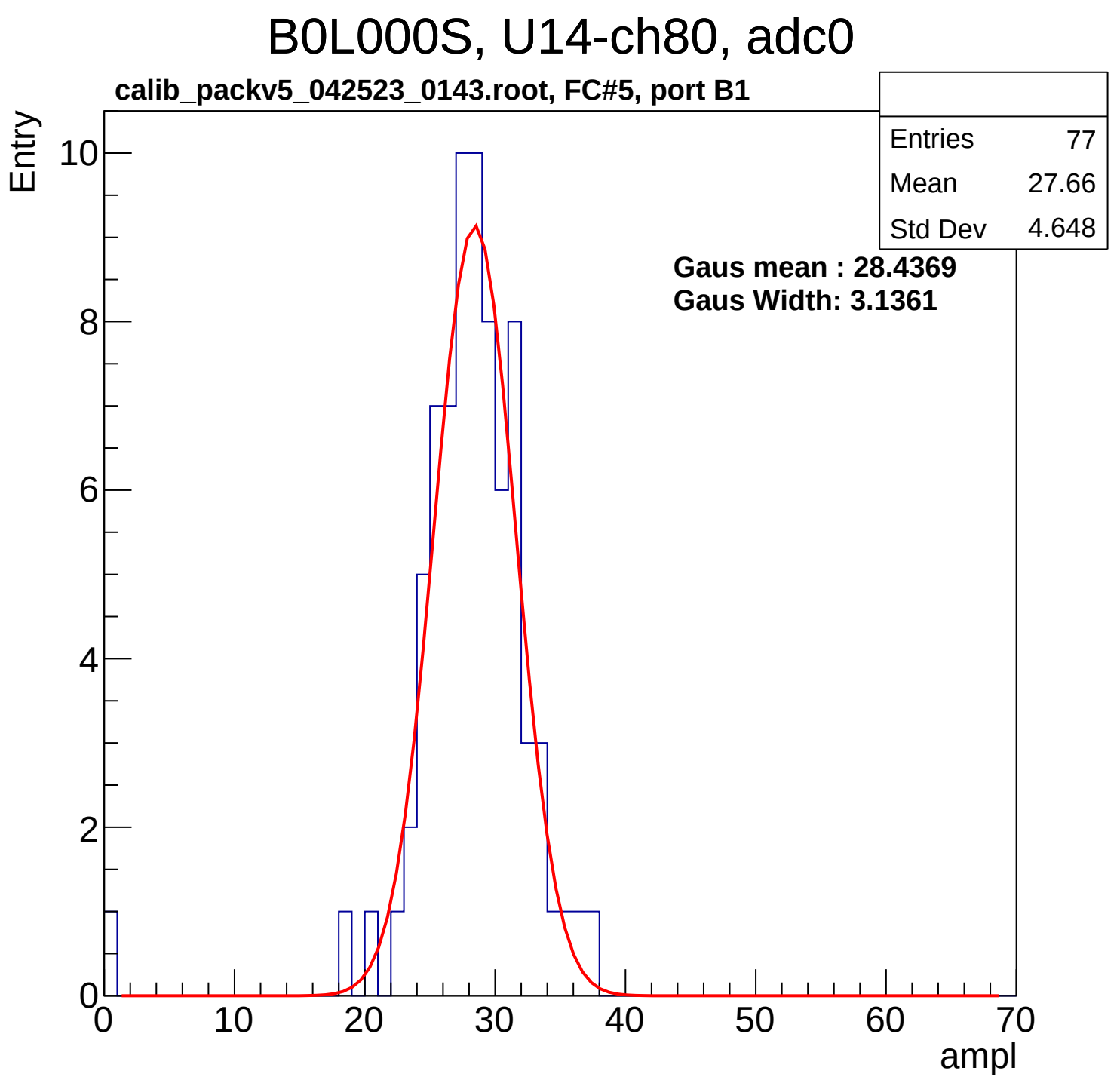
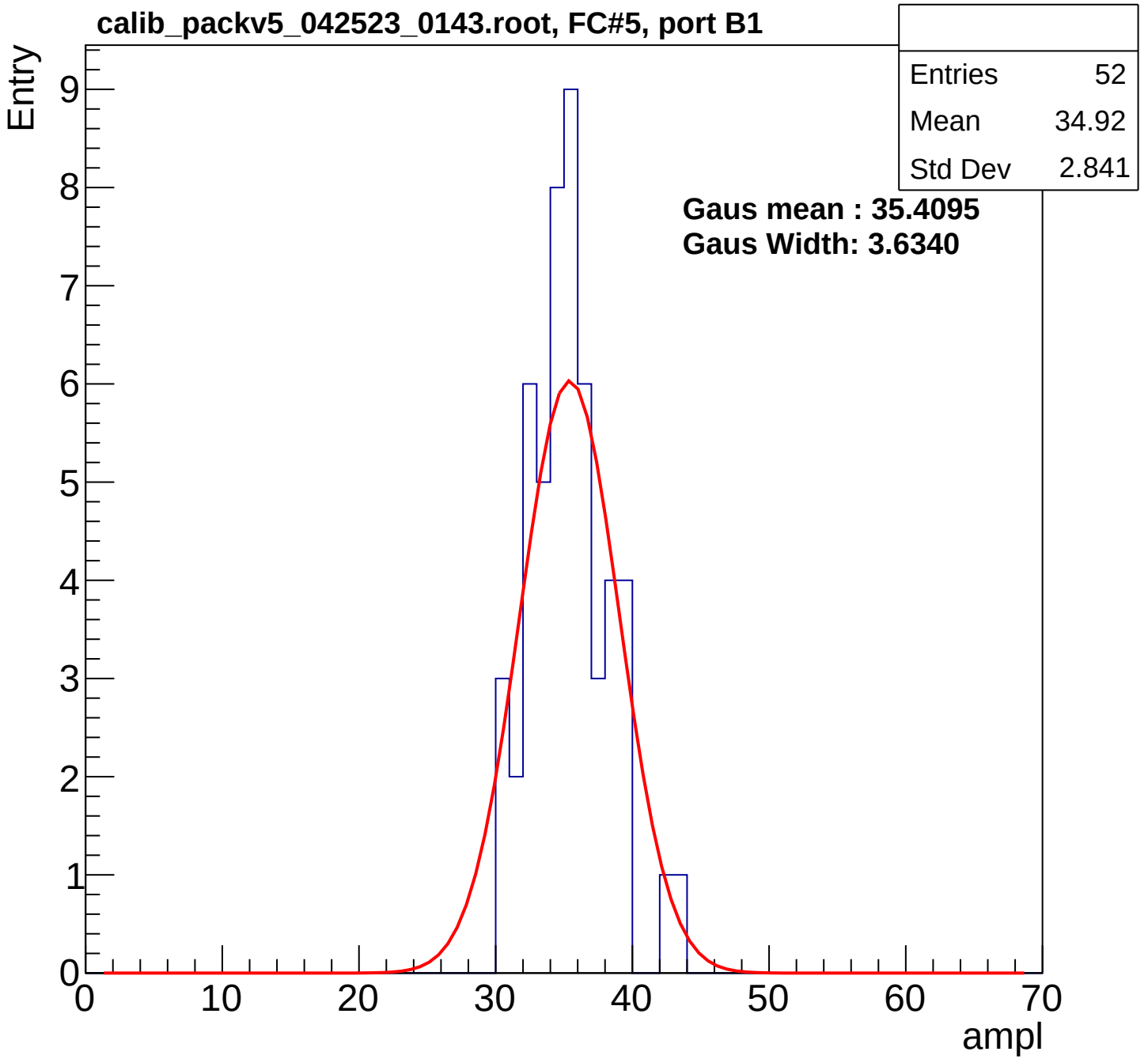

B0L000S, U14-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 27.66
Std Dev 4.648
Gaus mean : 28.4369
Gaus Width: 3.1361
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 34.92
Std Dev 2.841
Gaus mean : 35.4095
Gaus Width: 3.6340
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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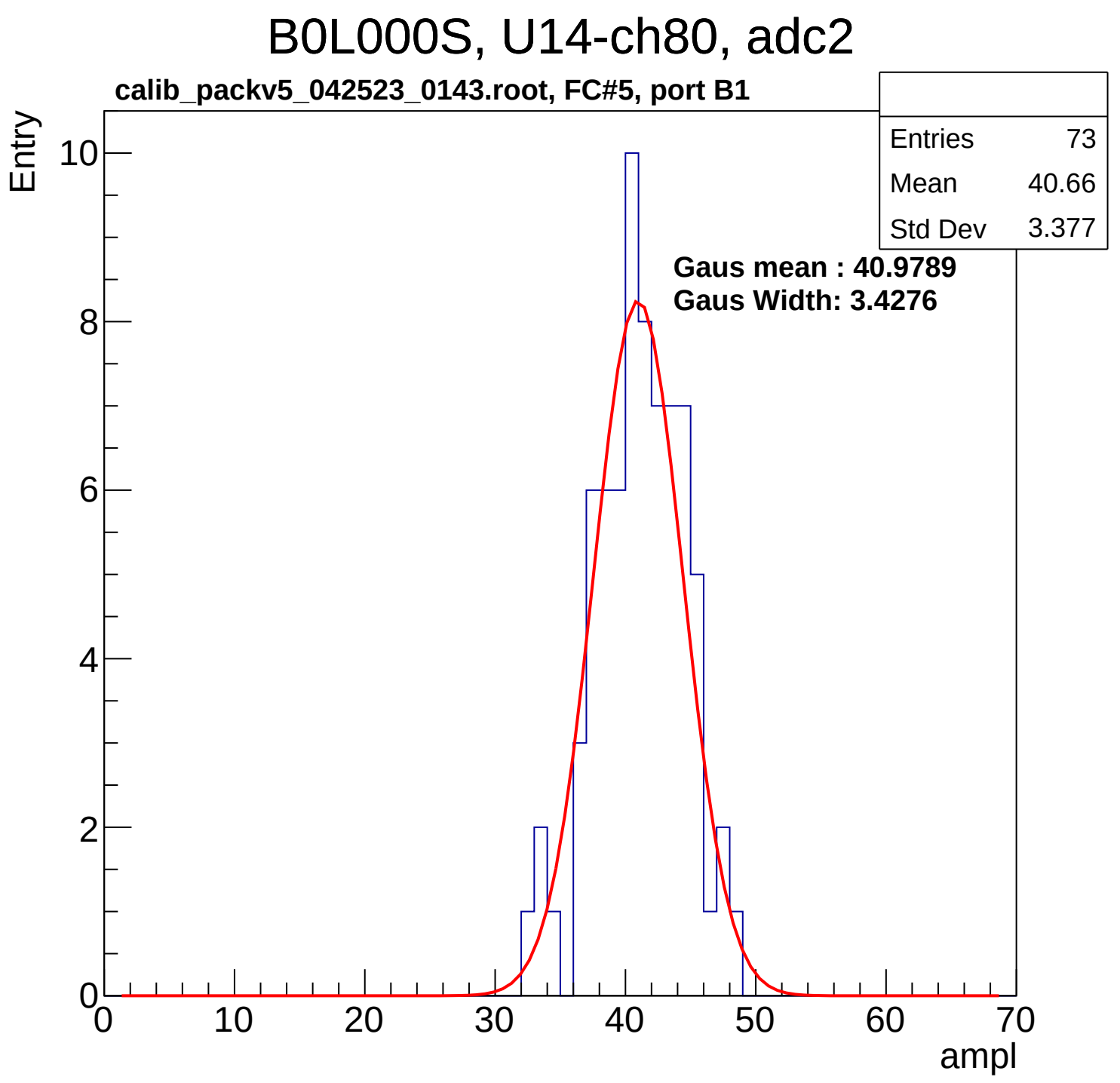
B0L000S, U14-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 40.66
Std Dev 3.377
Gaus mean : 40.9789
Gaus Width: 3.4276
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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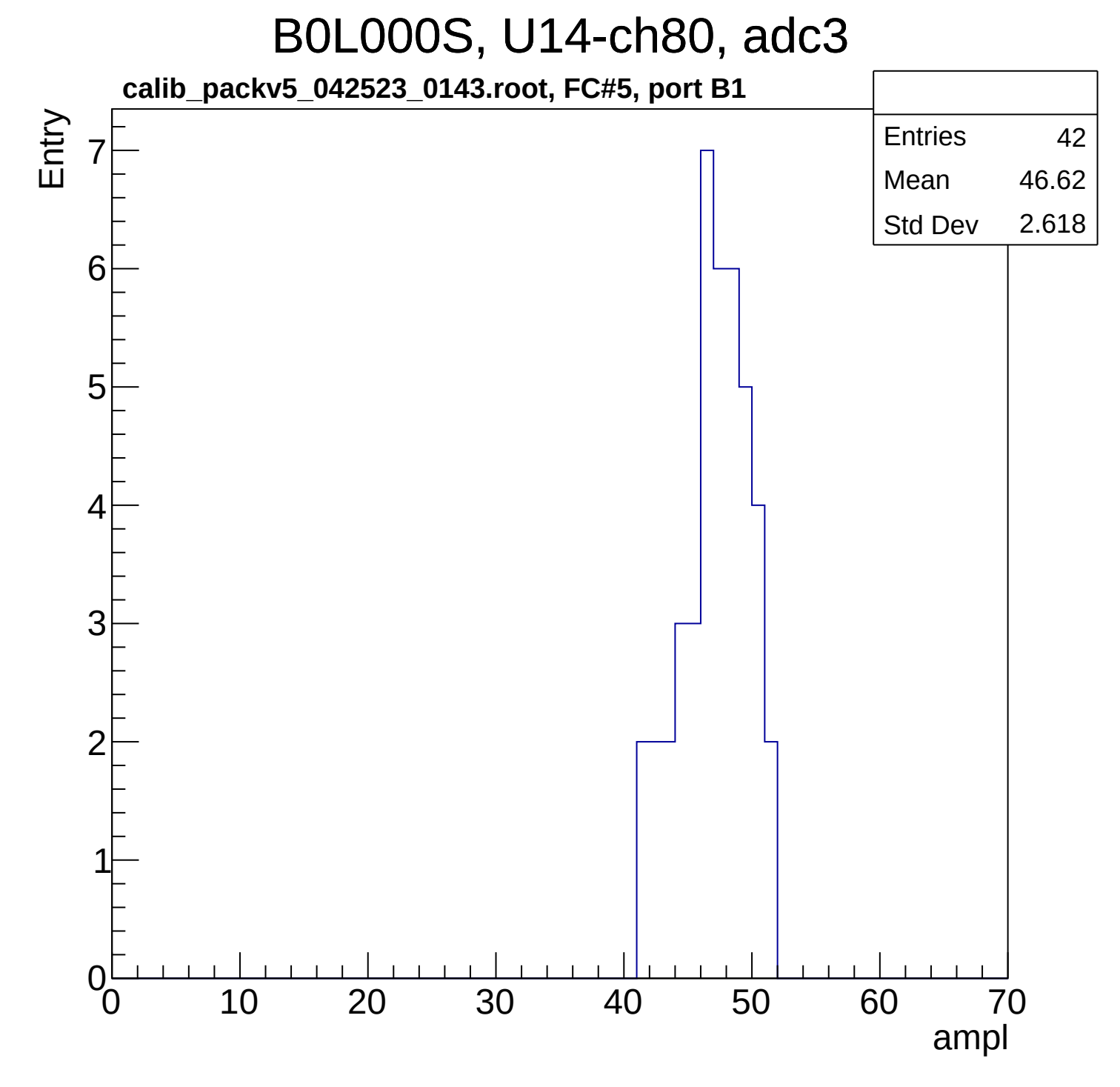
B0L000S, U14-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 46.62
Std Dev 2.618
Entry
ampl
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2
3
4
5
6
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0
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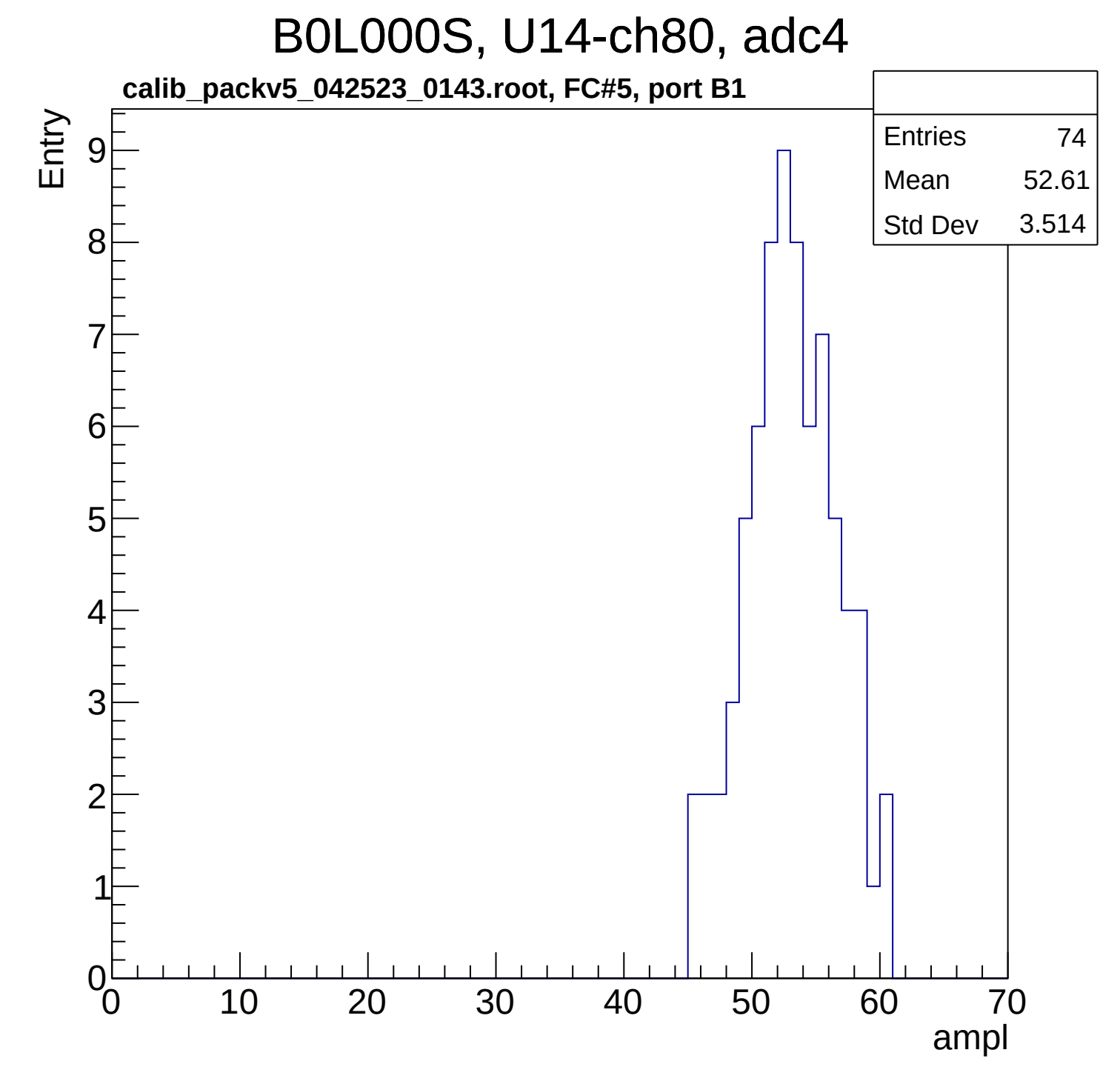
B0L000S, U14-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 52.61
Std Dev 3.514
Entry
ampl
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2
3
4
5
6
7
8
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0
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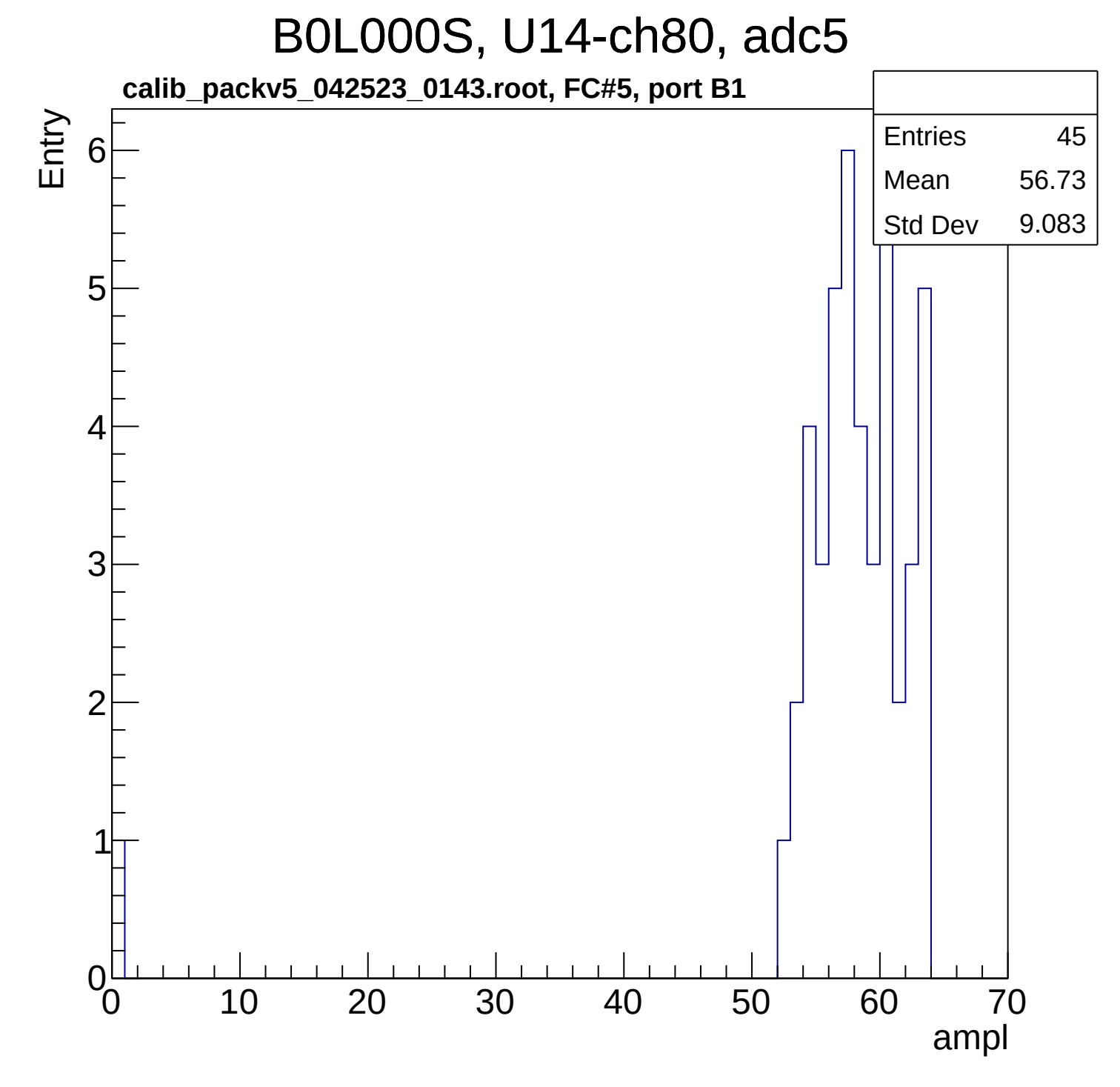
B0L000S, U14-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.73
Std Dev 9.083
Entry
ampl
0
1
2
3
4
5
6
0
10
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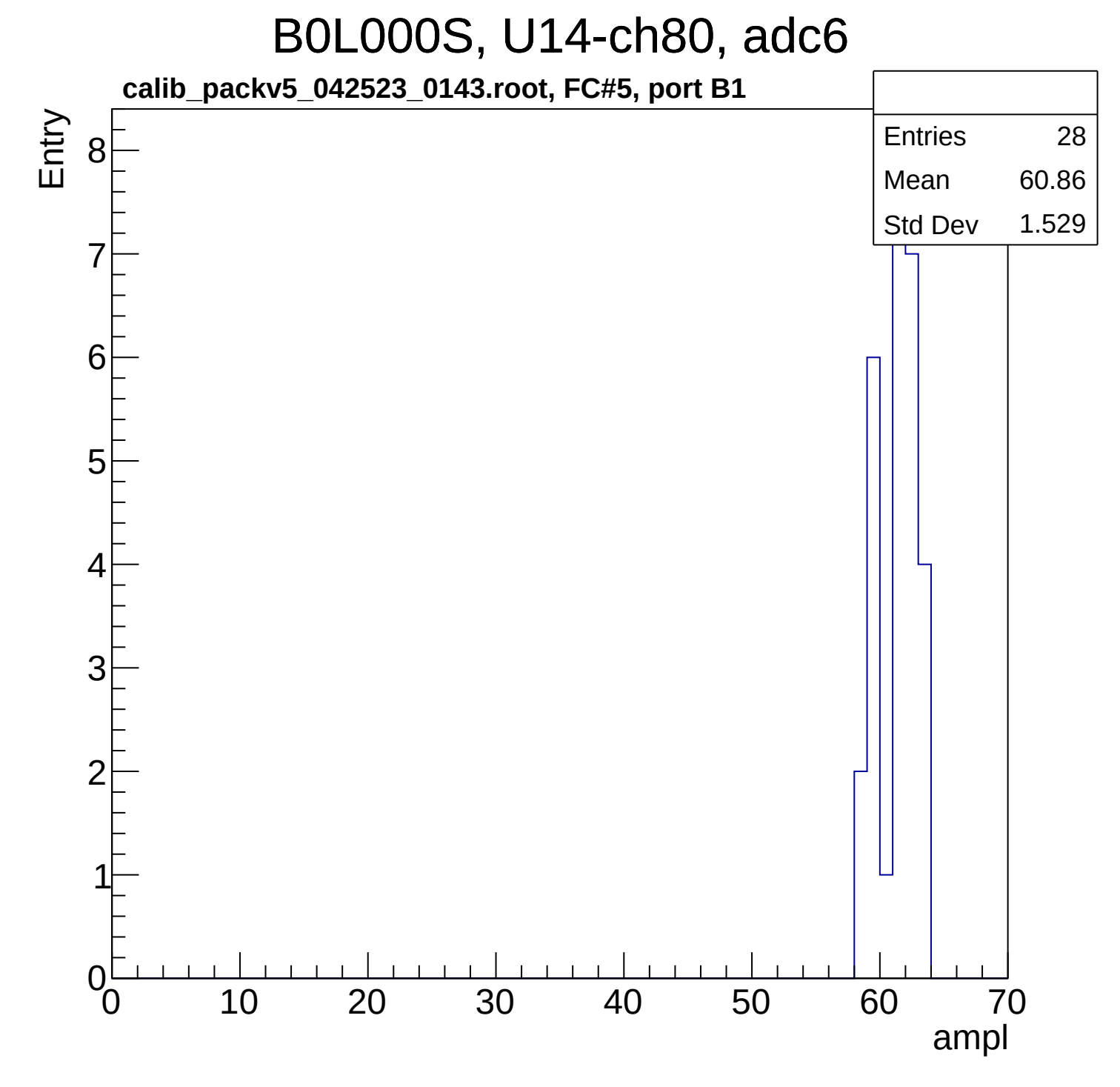
B0L000S, U14-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 60.86
Std Dev 1.529
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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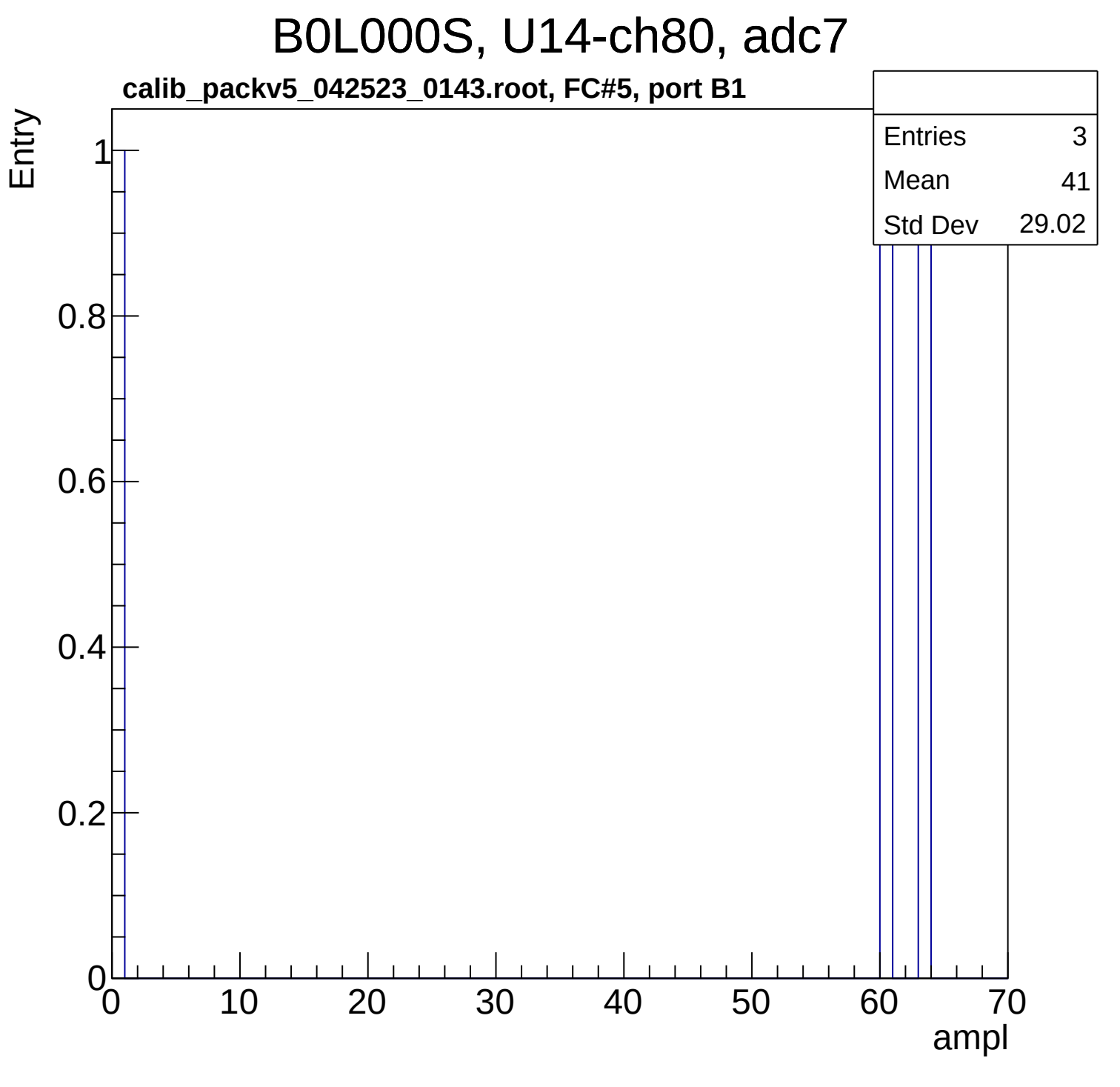
B0L000S, U14-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 41
Std Dev 29.02
Entry
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0.2
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10
20
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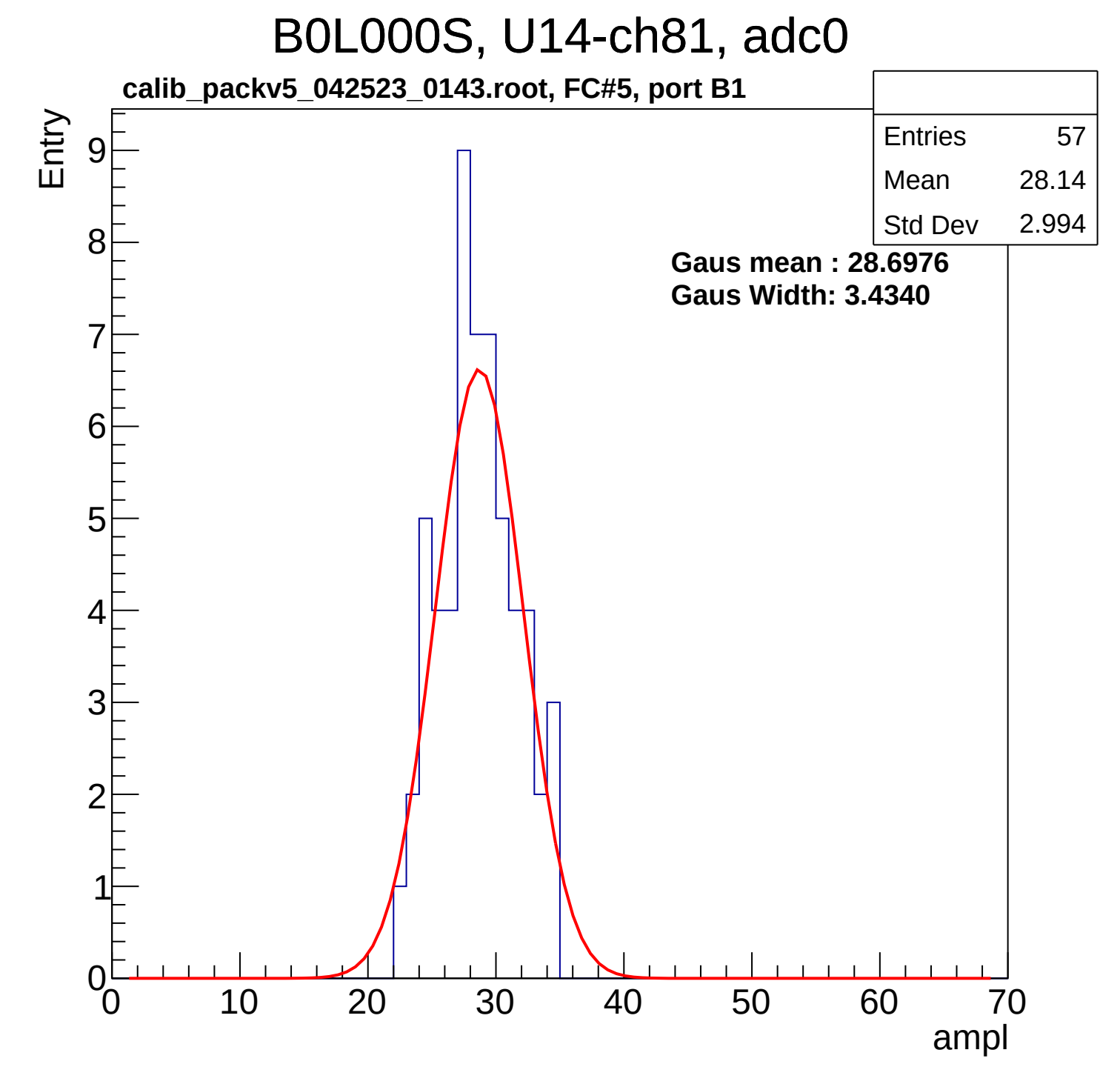
50
60
70
ampl


B0L000S, U14-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 28.14
Std Dev 2.994
Gaus mean : 28.6976
Gaus Width: 3.4340
Entry
ampl
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2
3
4
5
6
7
8
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0
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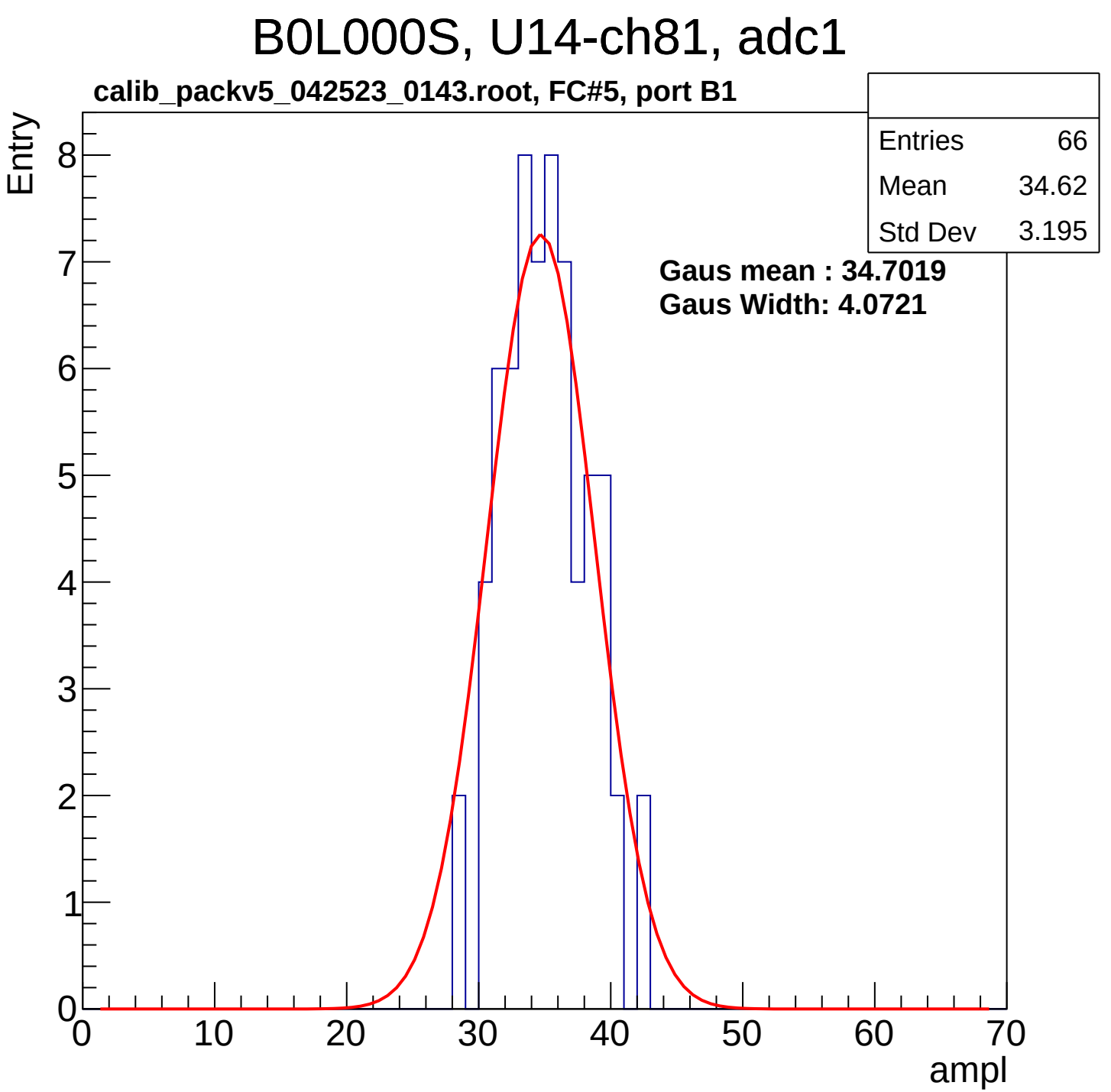
B0L000S, U14-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 34.62
Std Dev 3.195
Gaus mean : 34.7019
Gaus Width: 4.0721
Entry
ampl
0
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0
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60
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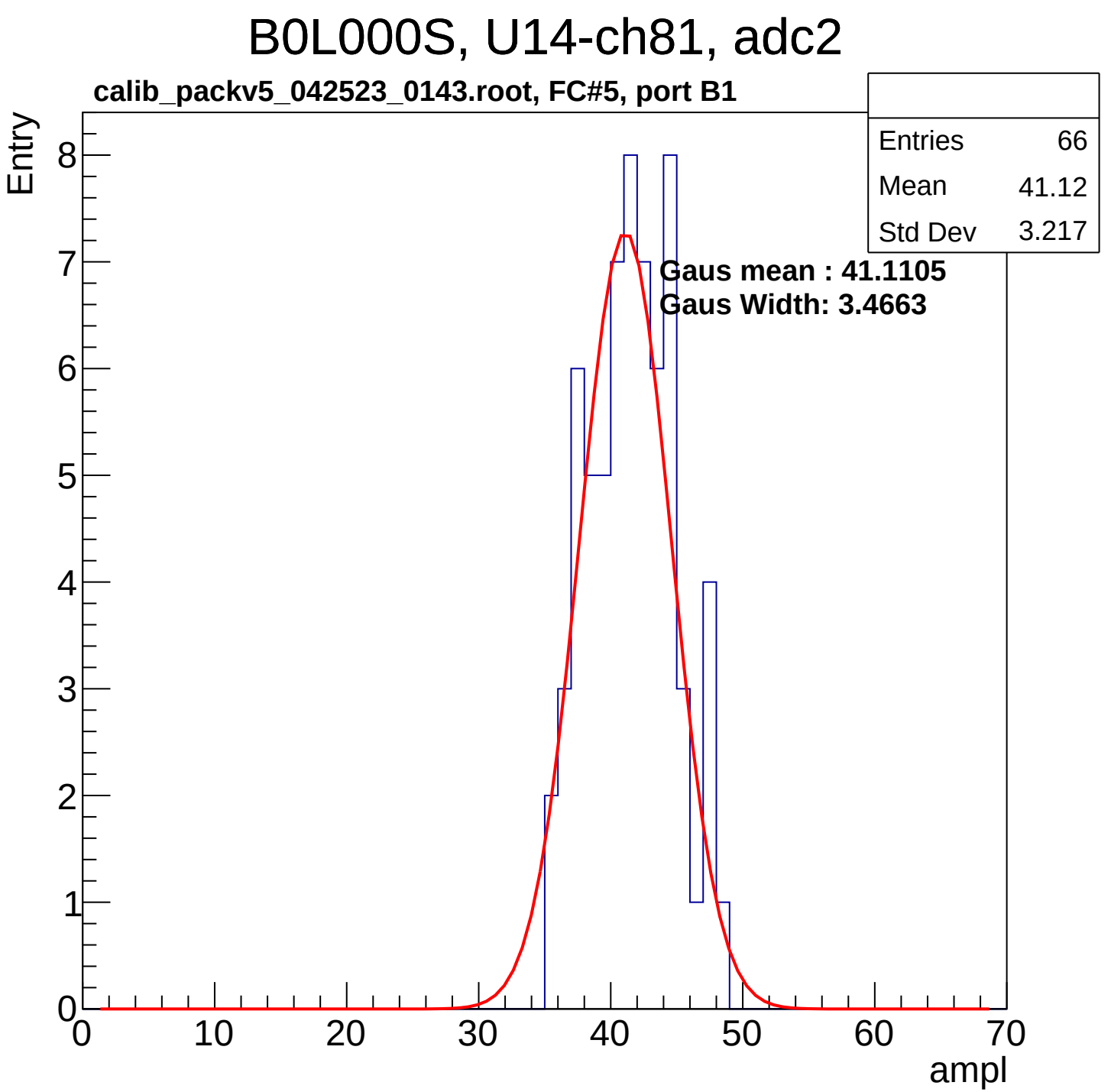
B0L000S, U14-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.12
Std Dev 3.217
Gaus mean : 41.1105
Gaus Width: 3.4663
Entry
ampl
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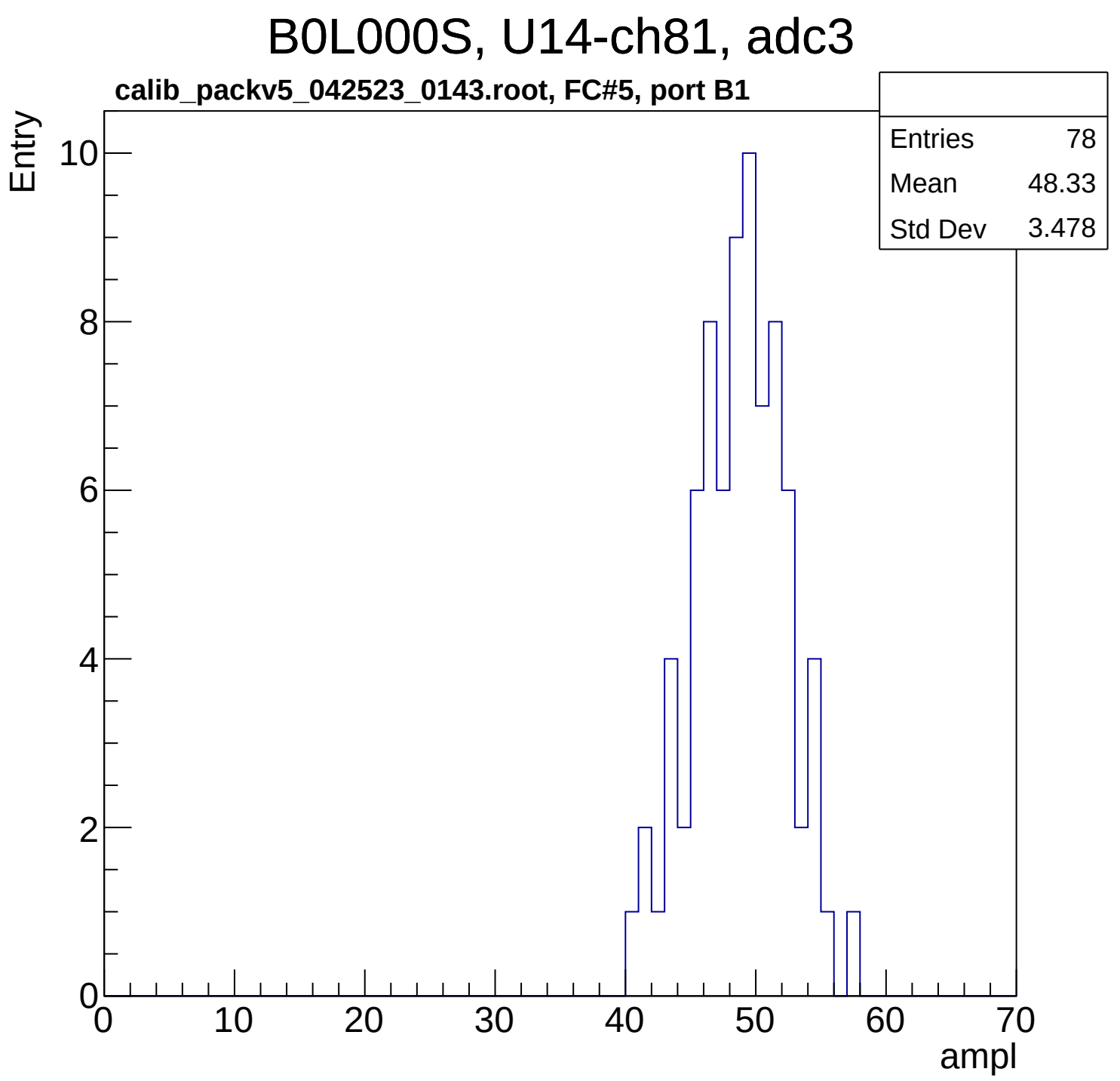
B0L000S, U14-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 48.33
Std Dev 3.478
Entry
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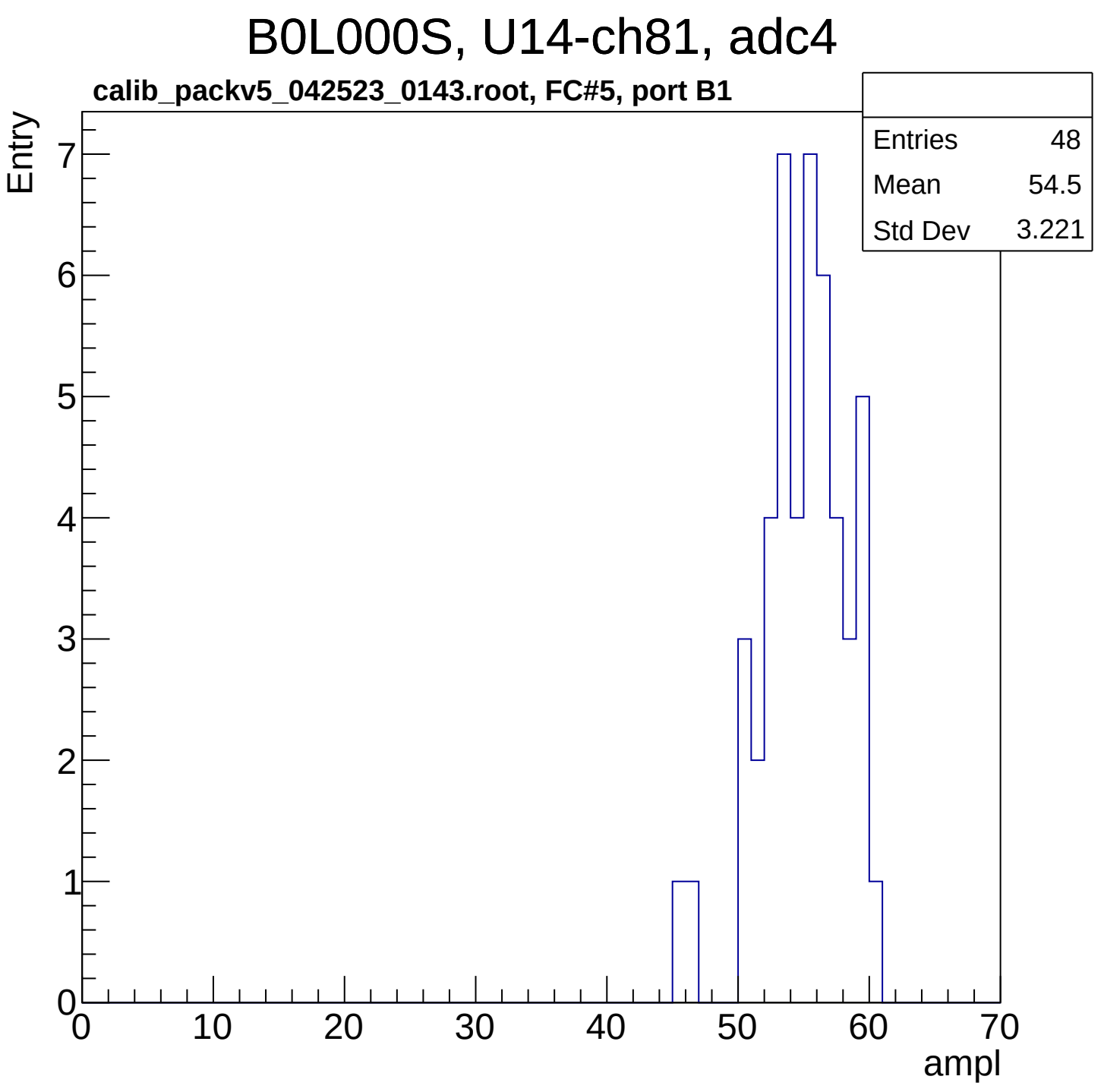
70
ampl


B0L000S, U14-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.5
Std Dev 3.221
Entry
ampl
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3
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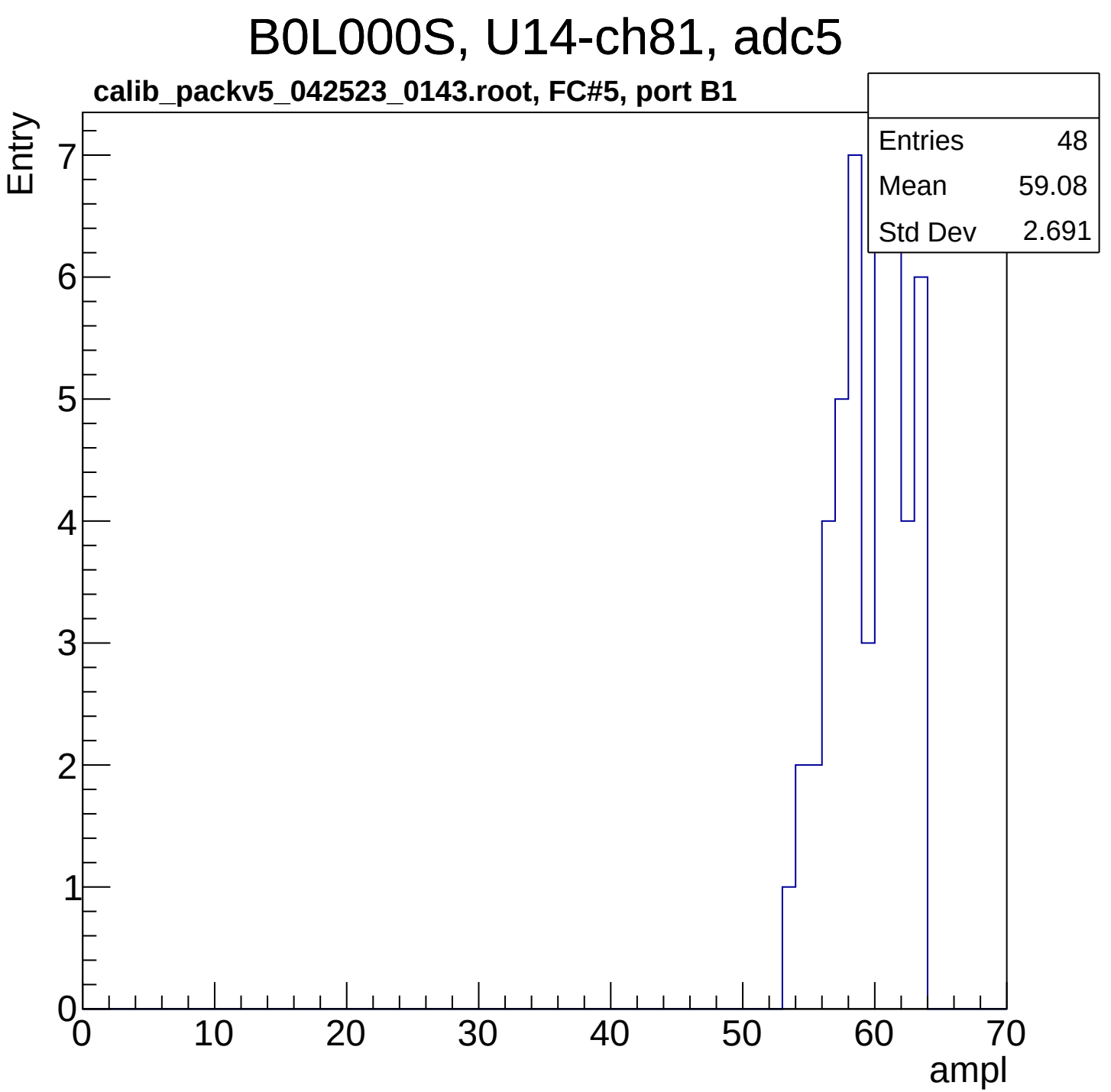
B0L000S, U14-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.08
Std Dev 2.691
Entry
ampl
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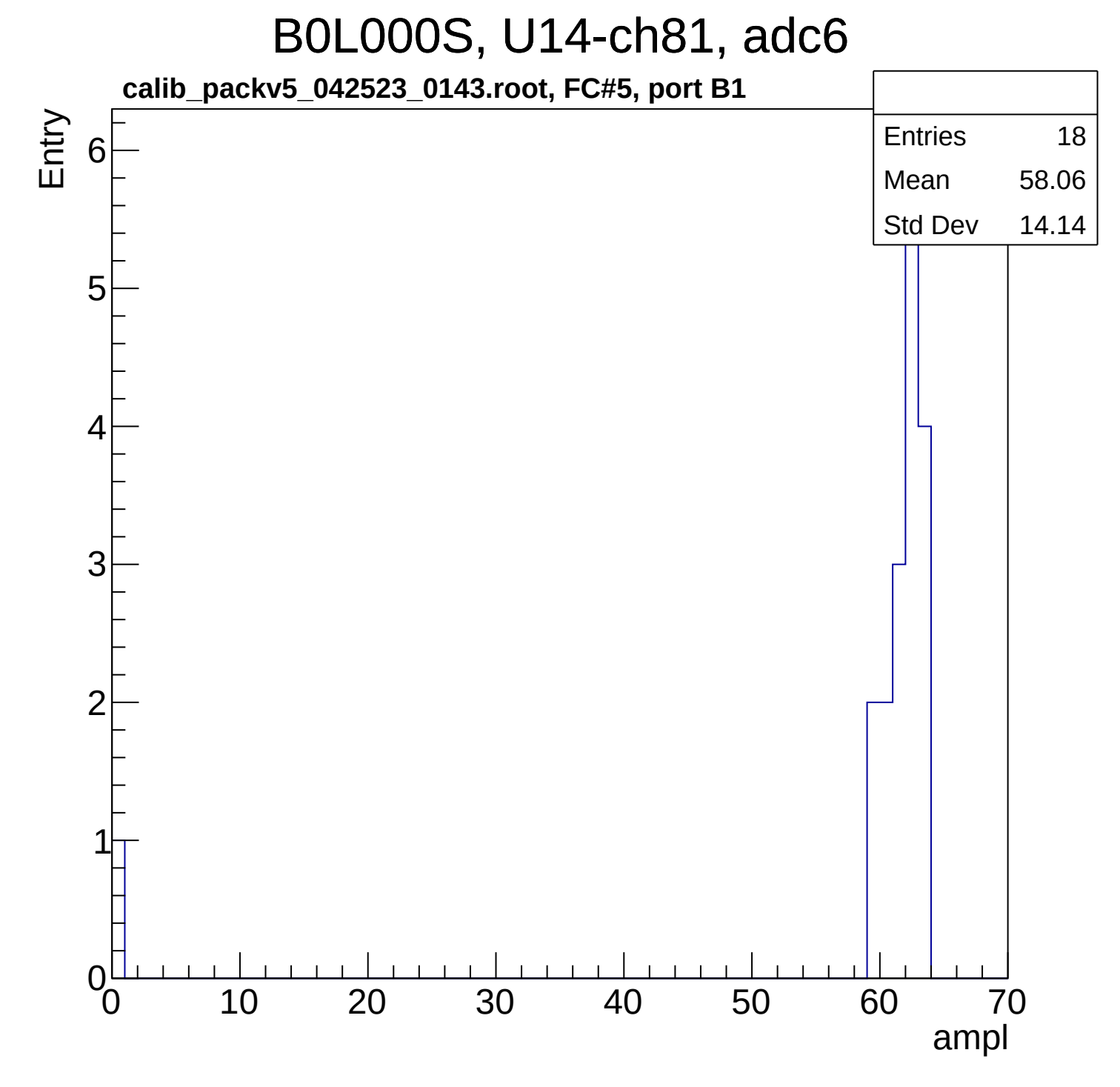
B0L000S, U14-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.06
Std Dev 14.14
Entry
0
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2
3
4
5
6
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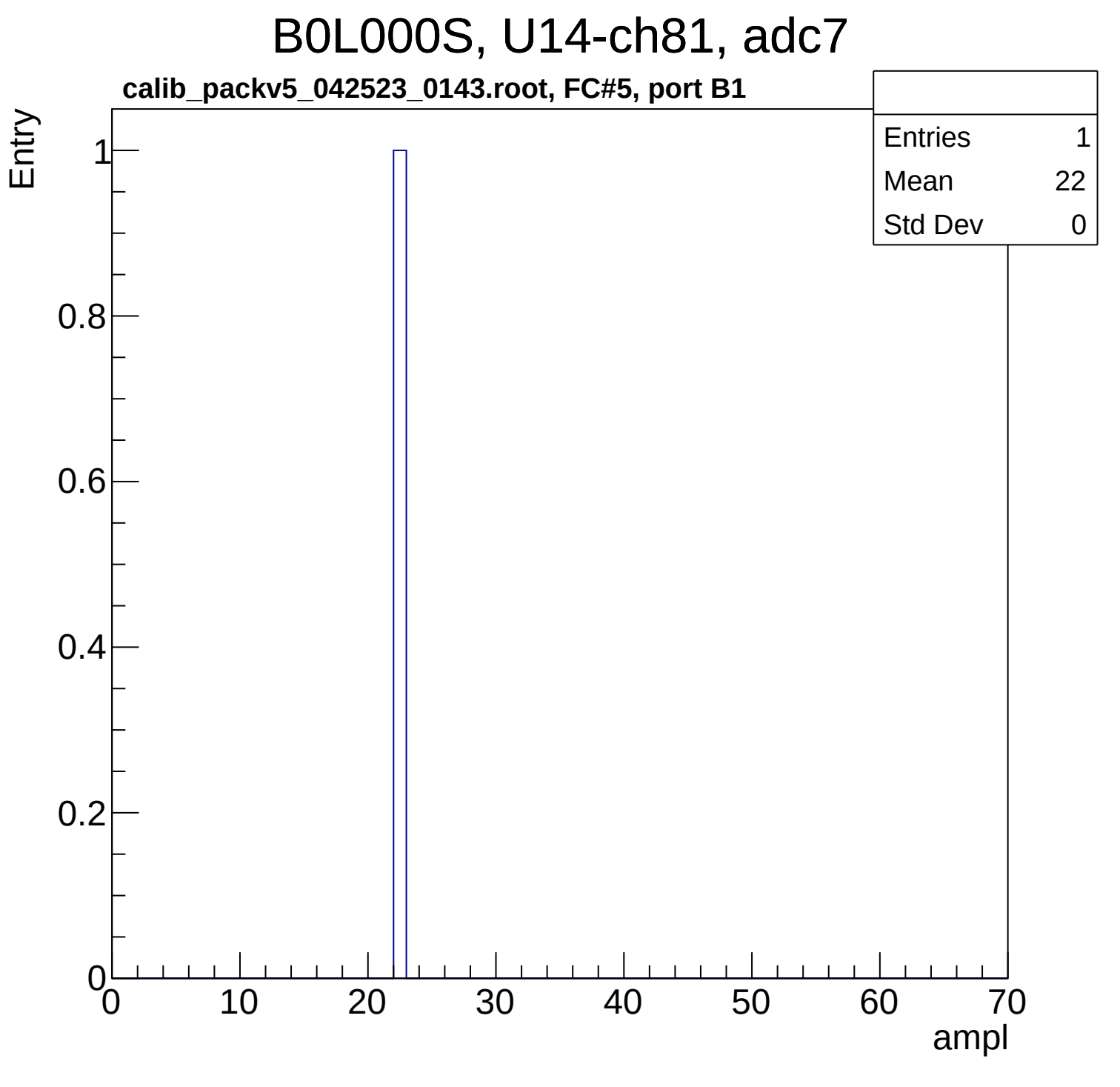
60
70
ampl


B0L000S, U14-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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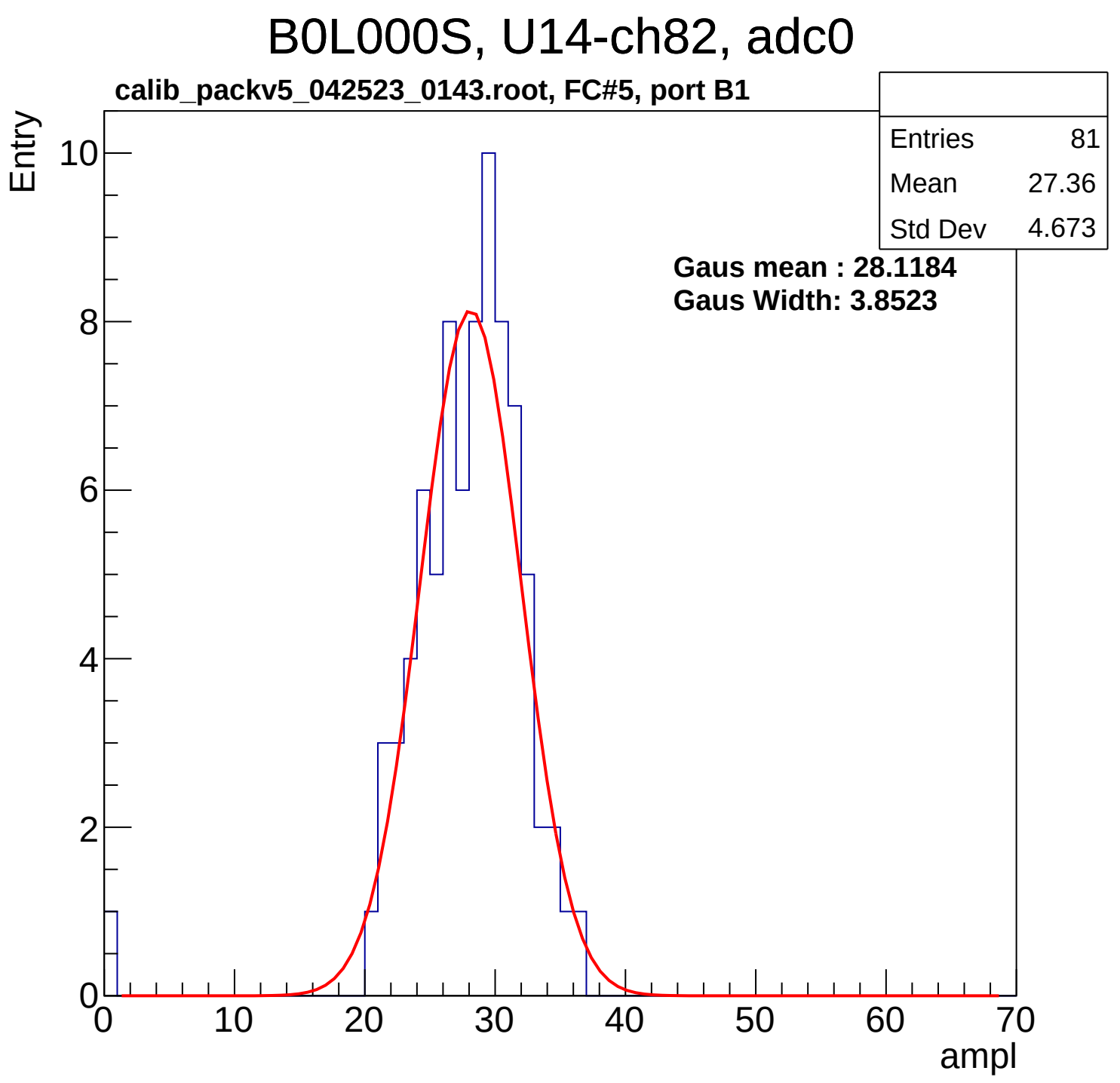
40
50
60
70
ampl


B0L000S, U14-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 27.36
Std Dev 4.673
Gaus mean : 28.1184
Gaus Width: 3.8523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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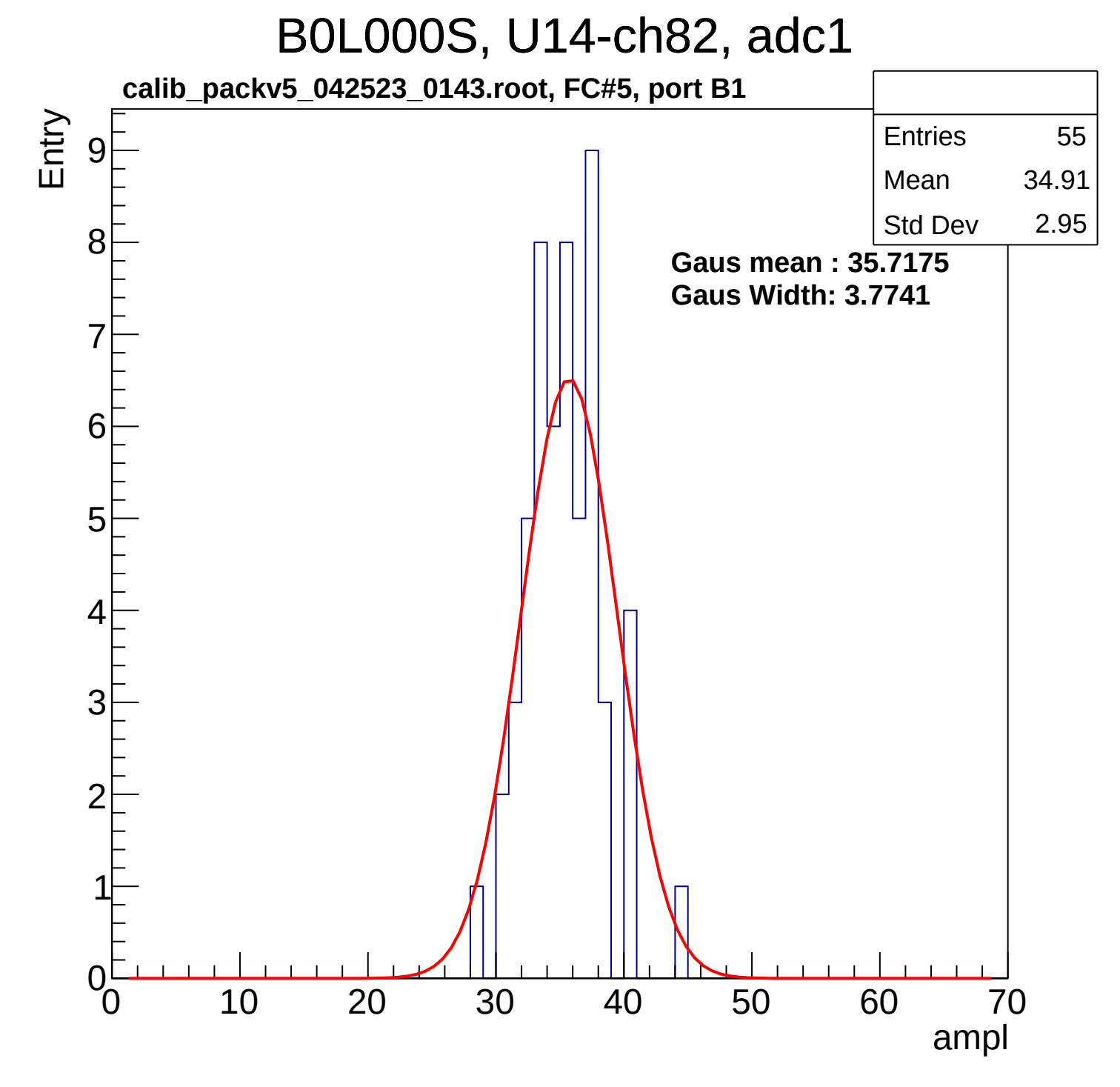
B0L000S, U14-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 34.91
Std Dev 2.95
Gaus mean : 35.7175
Gaus Width: 3.7741
Entry
ampl
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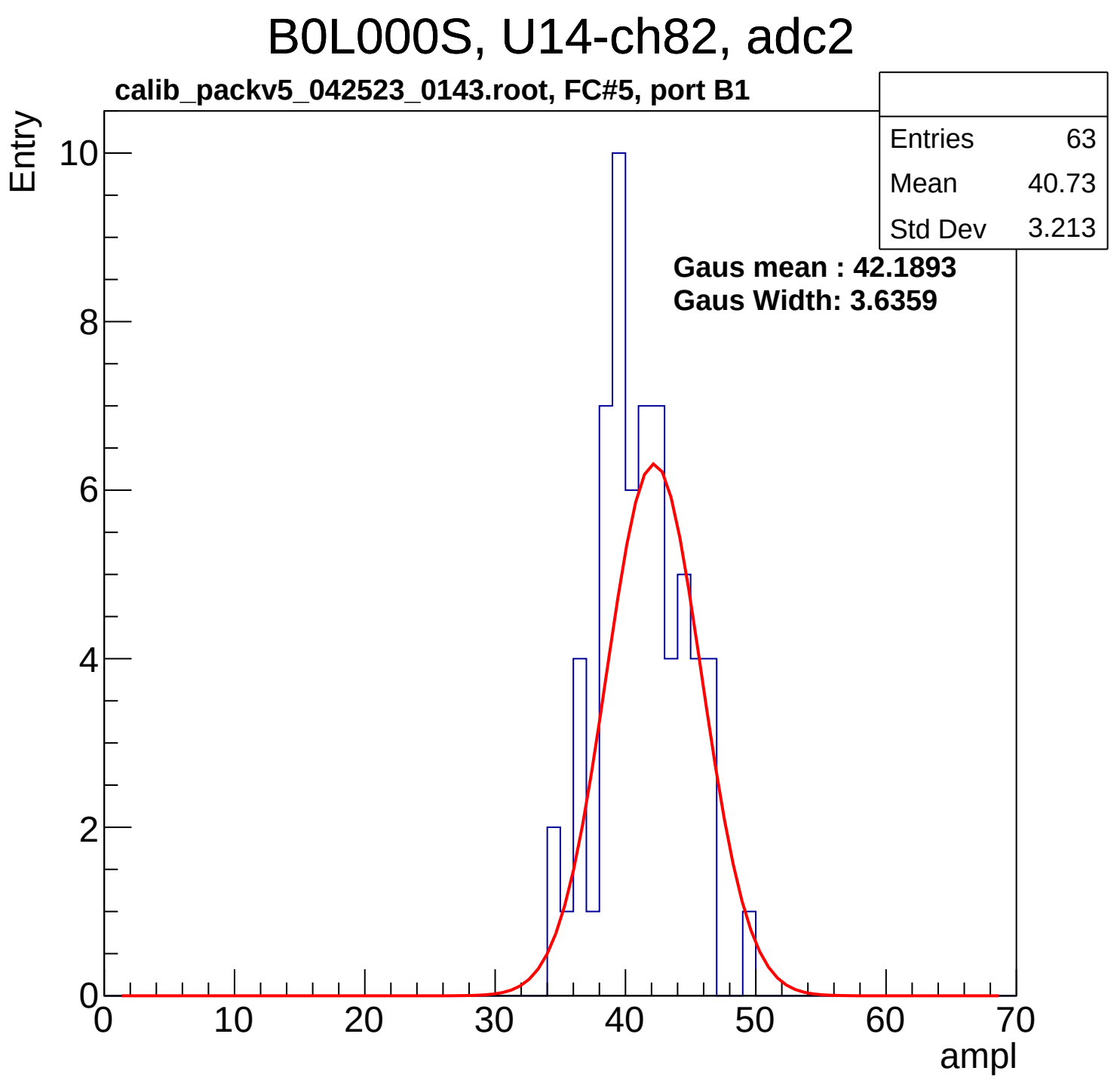
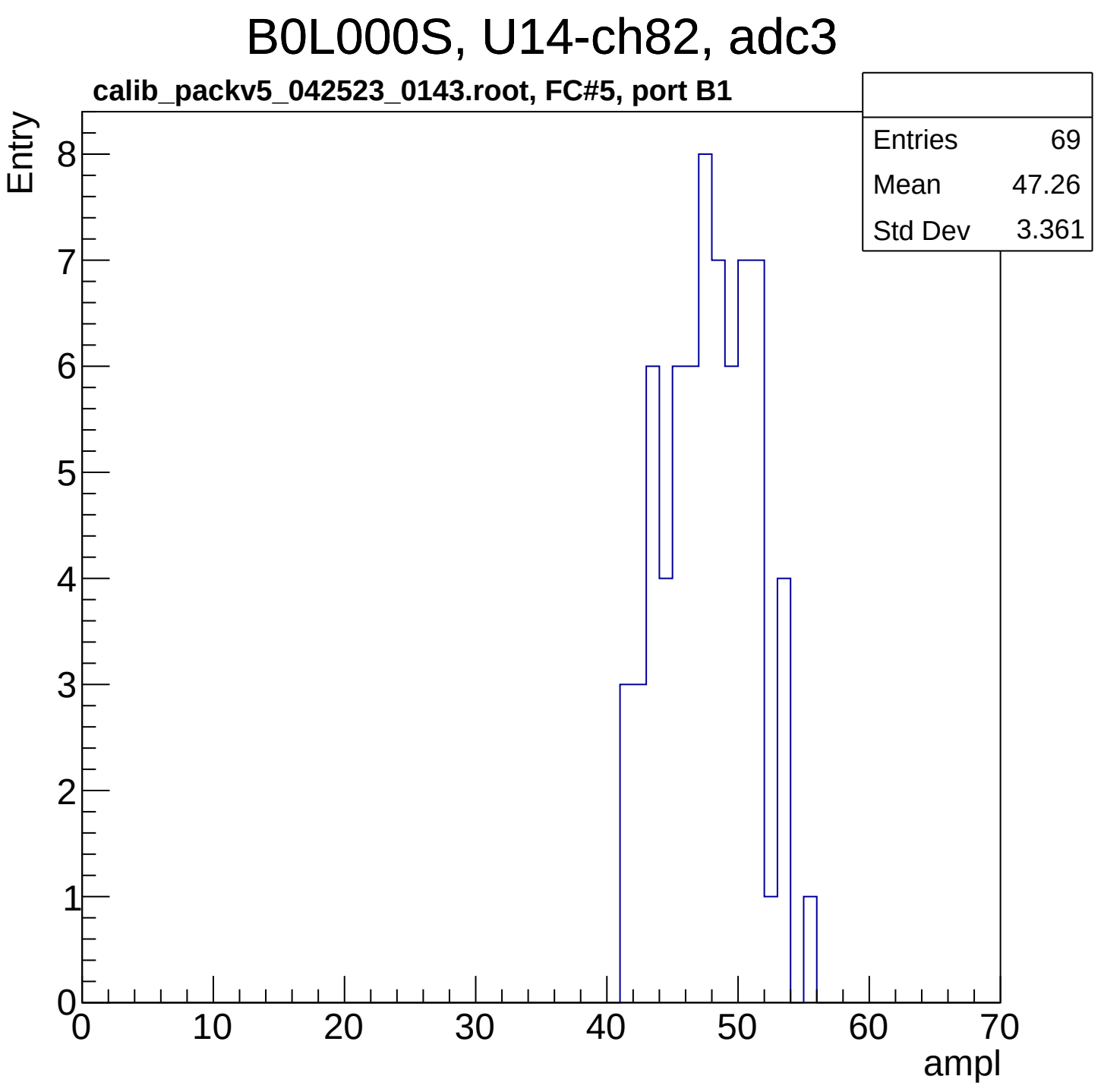
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 40.73
Std Dev 3.213
Gaus mean : 42.1893
Gaus Width: 3.6359
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.26
Std Dev 3.361
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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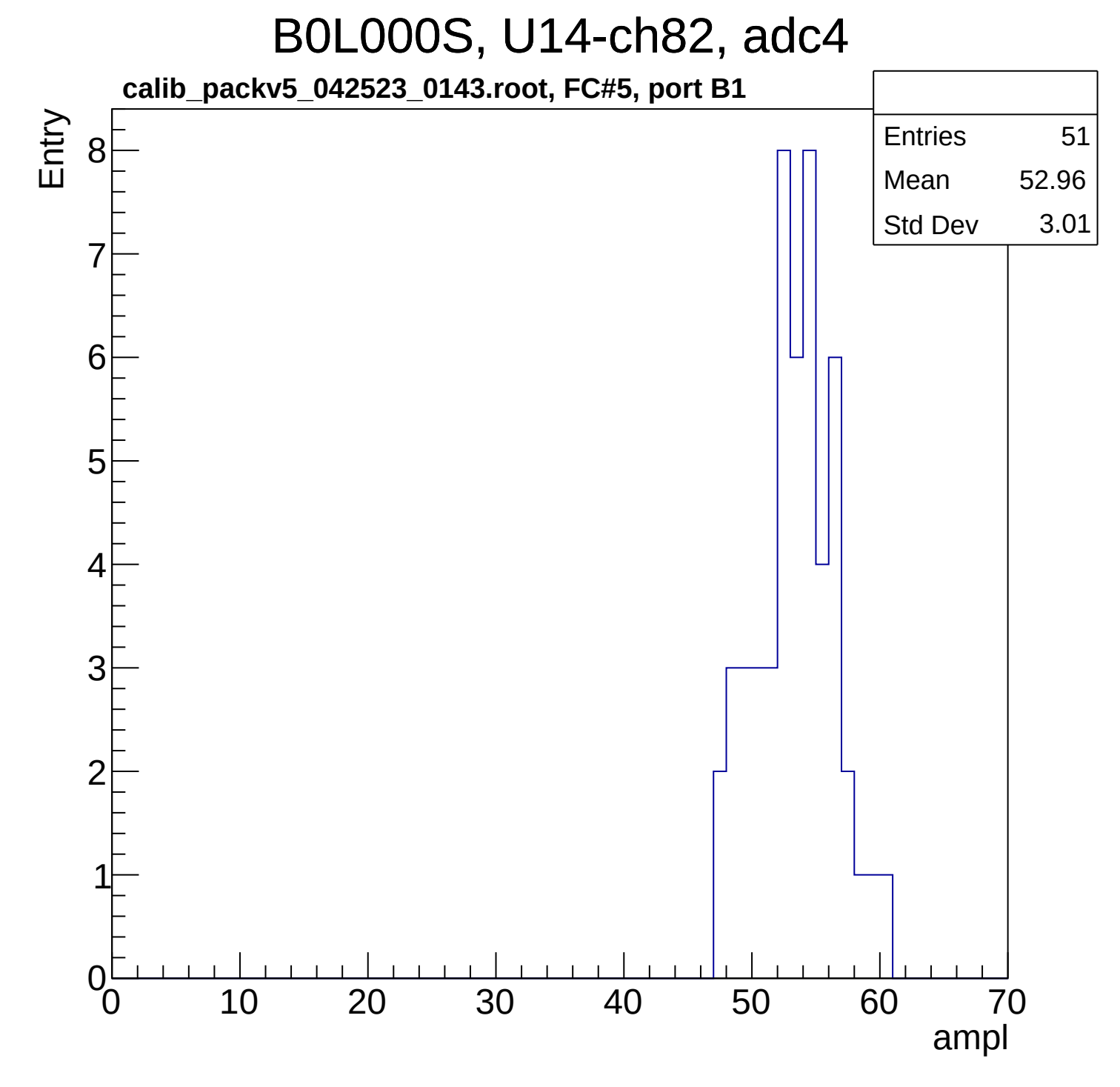
B0L000S, U14-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 52.96
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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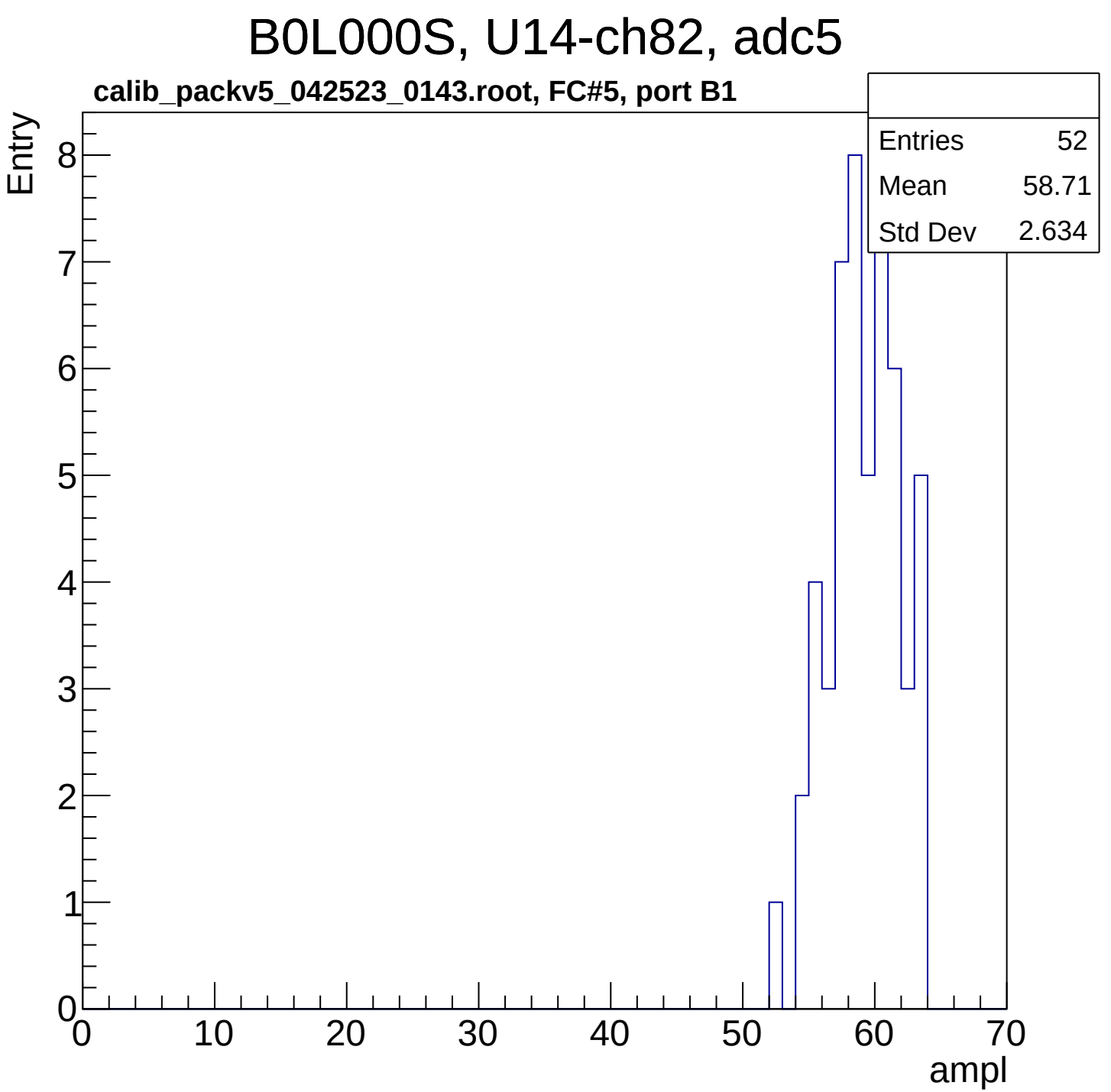
B0L000S, U14-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.71
Std Dev 2.634
Entry
ampl
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1
2
3
4
5
6
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0
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60
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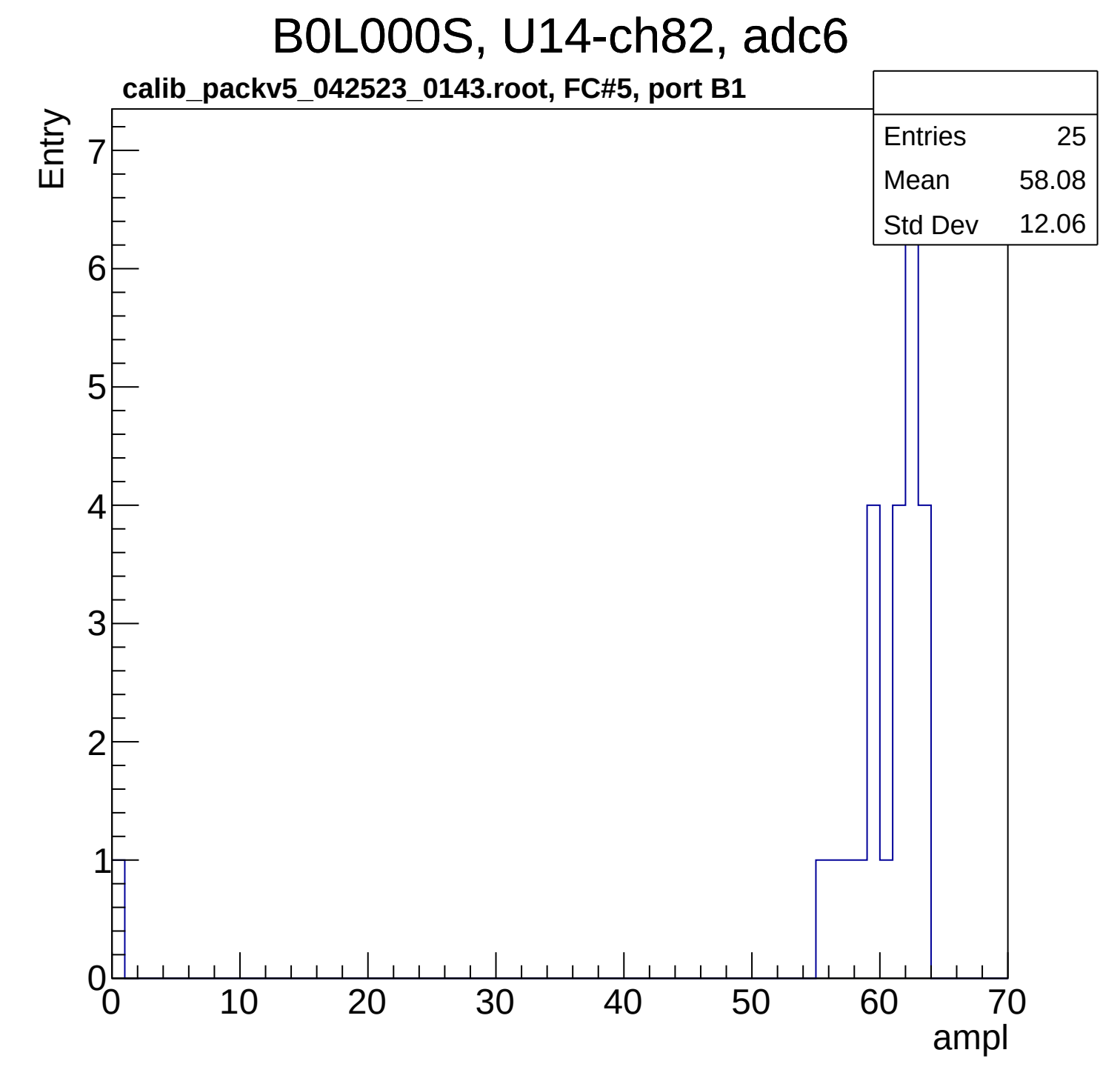
B0L000S, U14-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 58.08
Std Dev 12.06
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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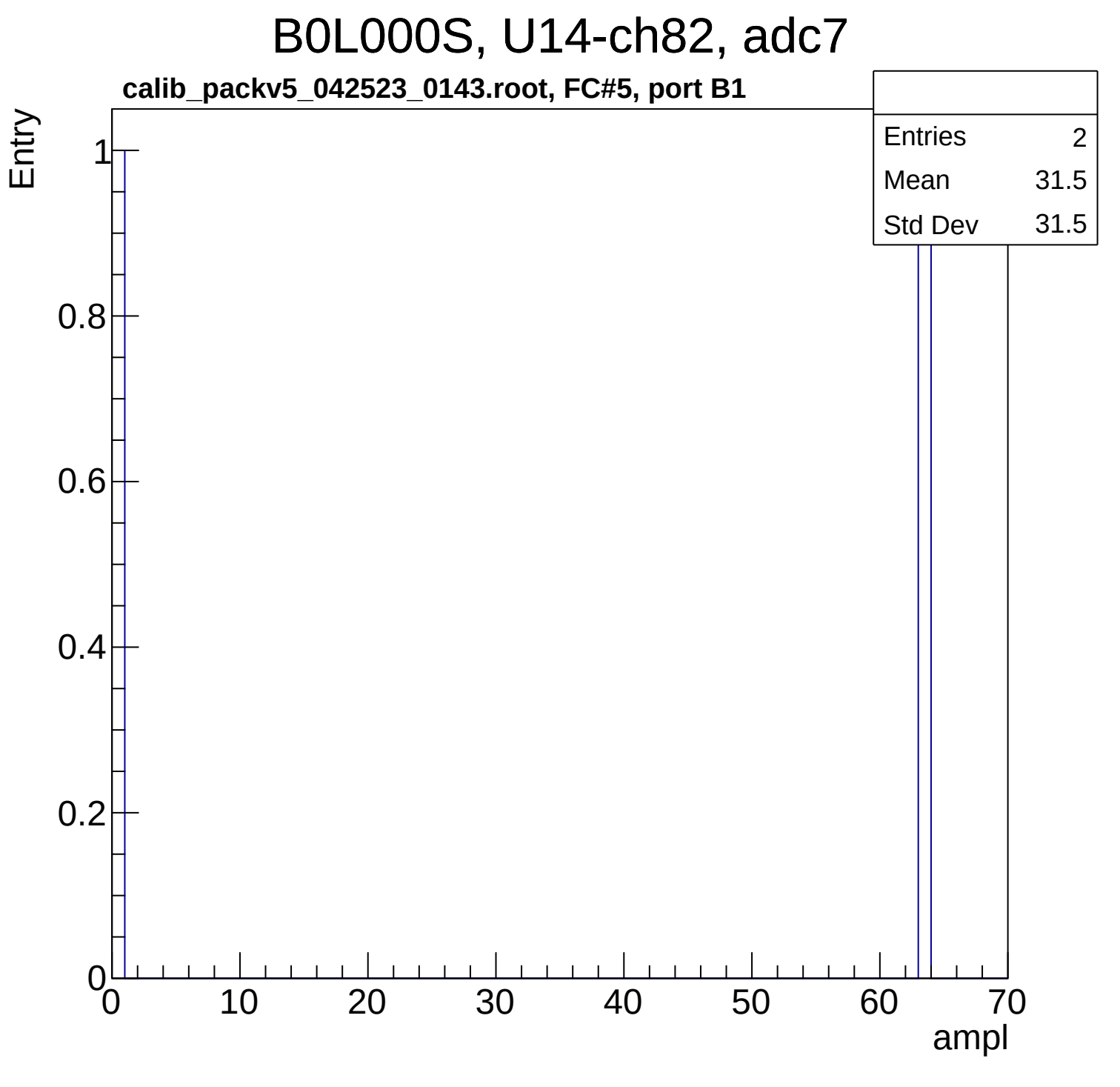
B0L000S, U14-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
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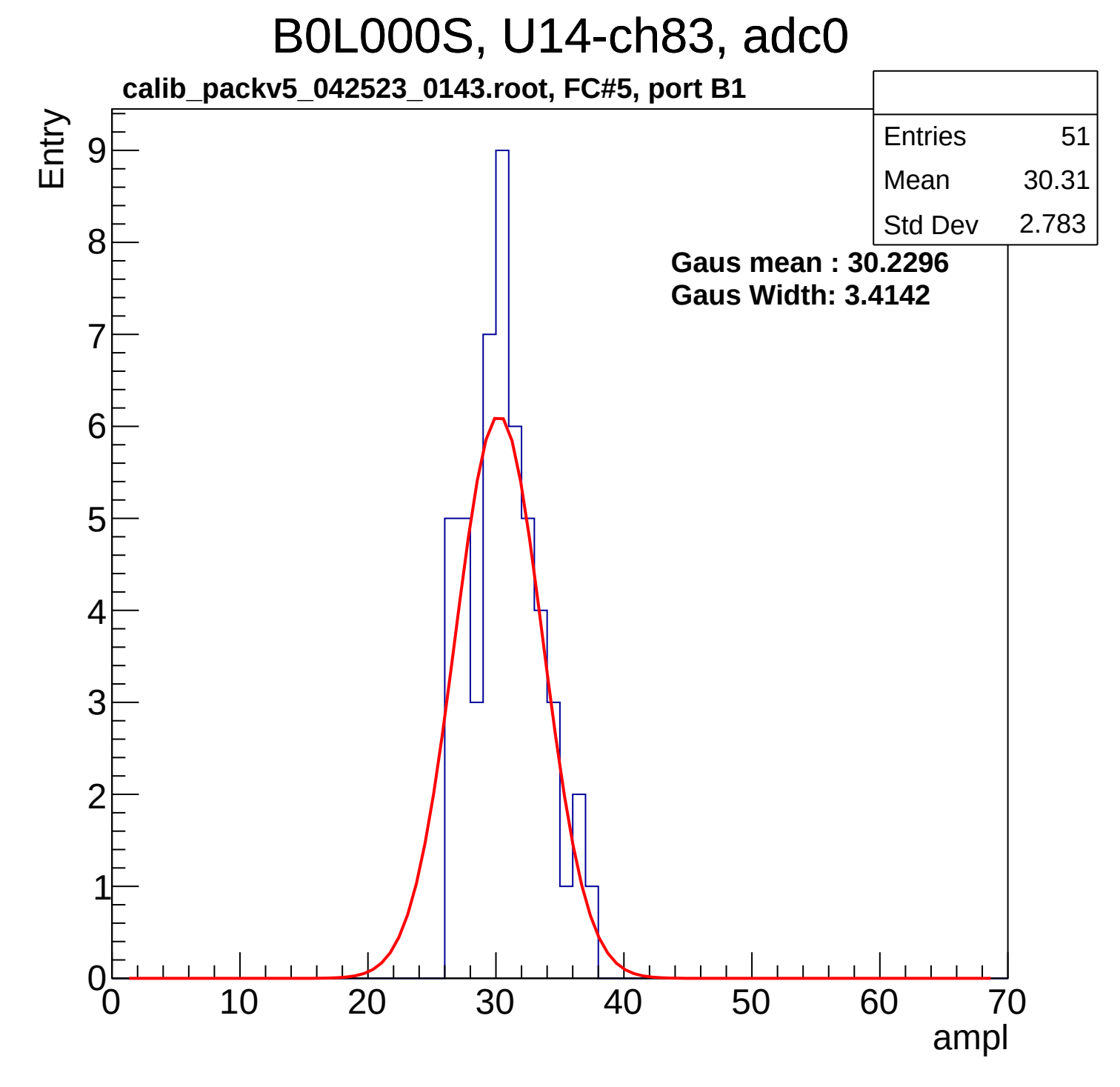
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 30.31
Std Dev 2.783
Gaus mean : 30.2296
Gaus Width: 3.4142
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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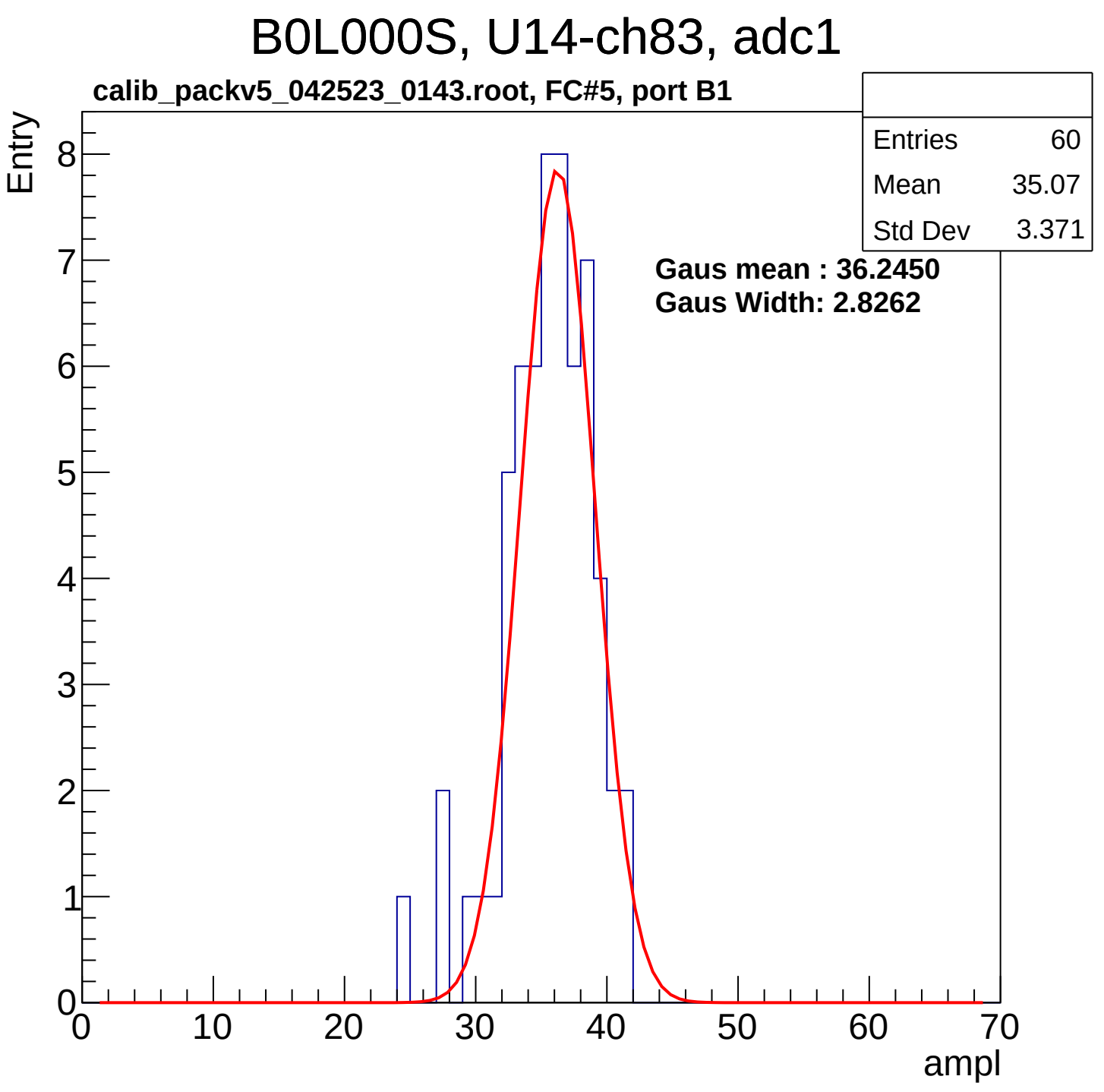
B0L000S, U14-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.07
Std Dev 3.371
Gaus mean : 36.2450
Gaus Width: 2.8262
Entry
ampl
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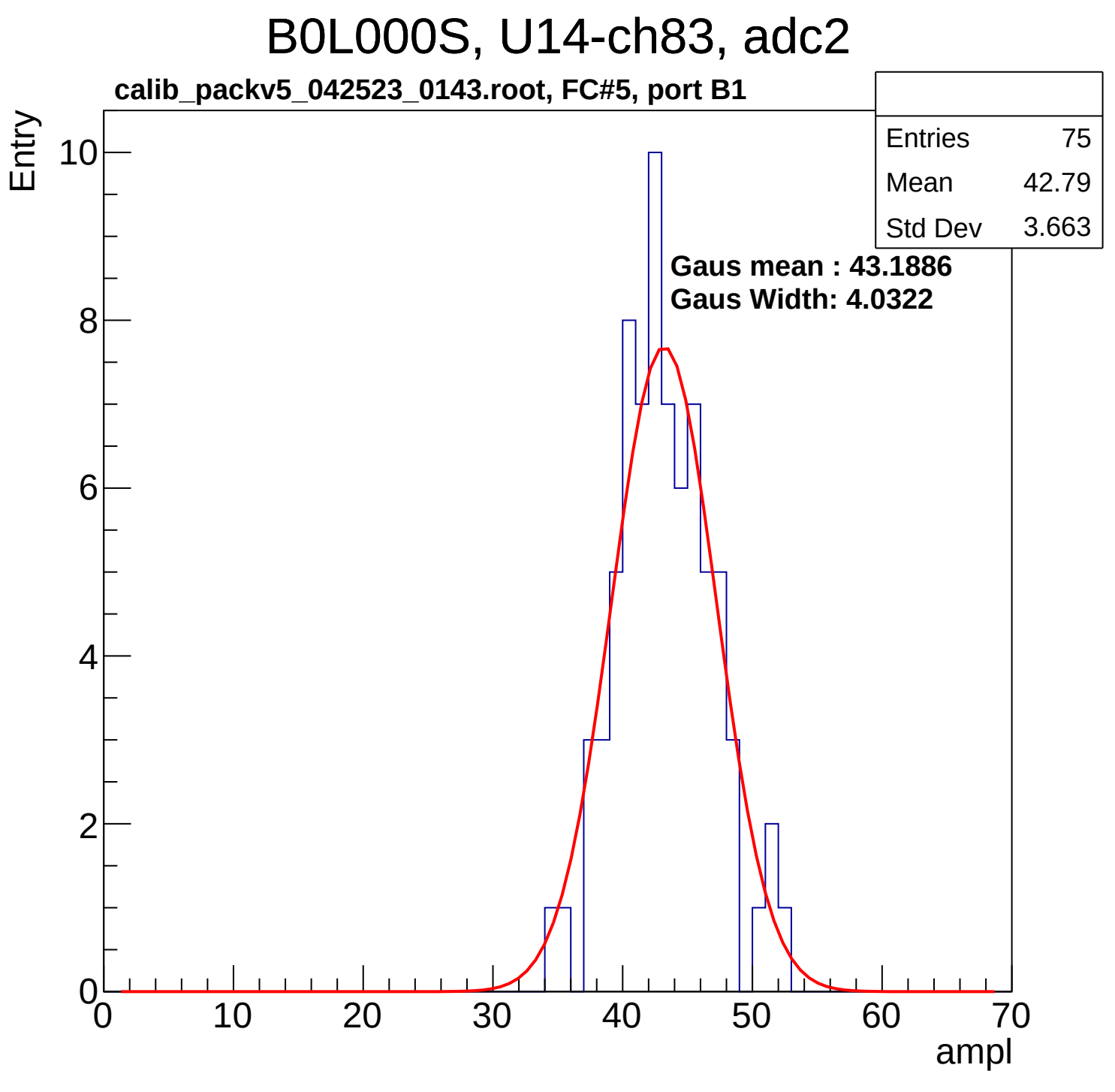
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 42.79
Std Dev 3.663
Gaus mean : 43.1886
Gaus Width: 4.0322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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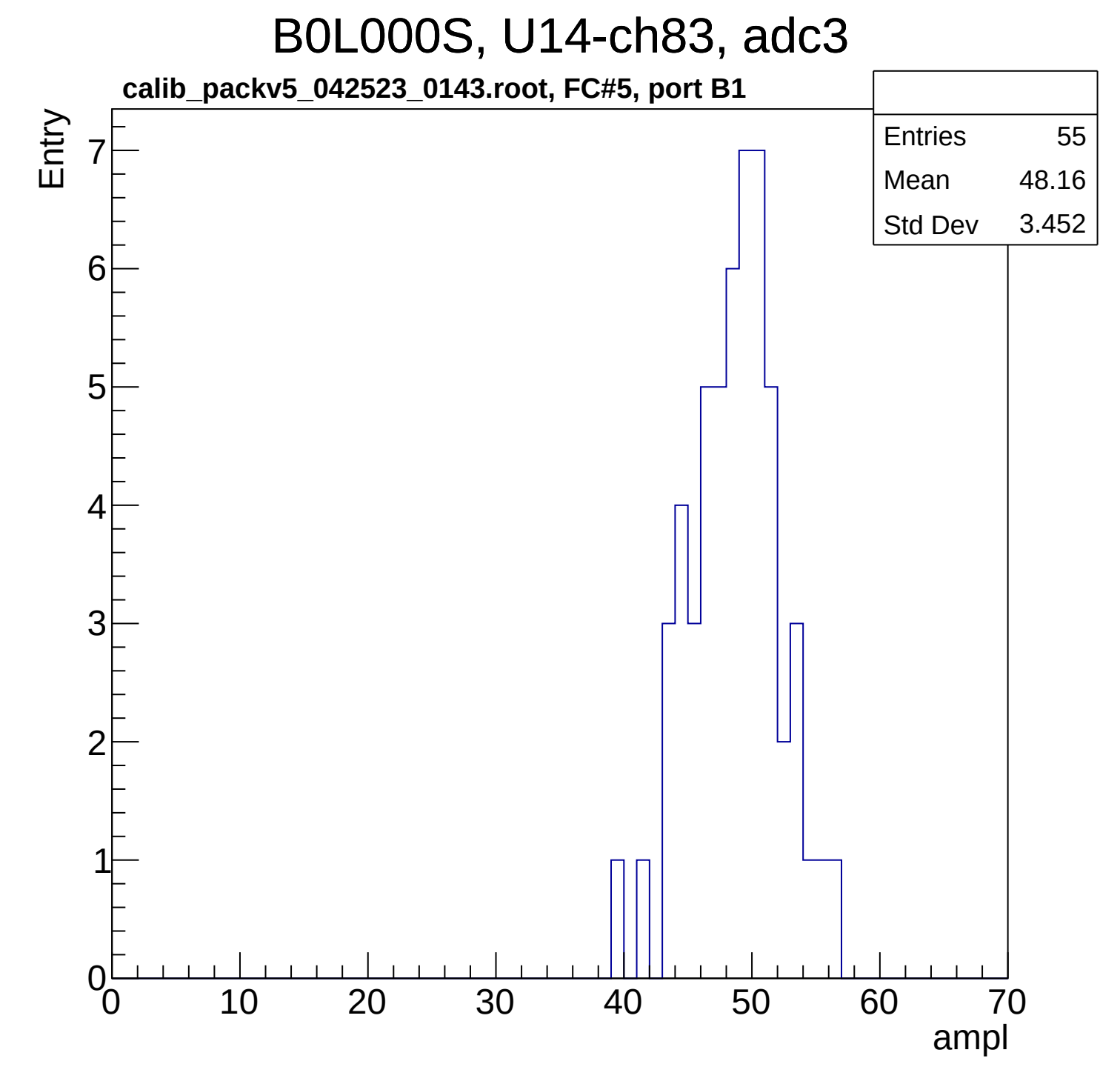
B0L000S, U14-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.16
Std Dev 3.452
Entry
ampl
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1
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3
4
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6
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0
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20
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40
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60
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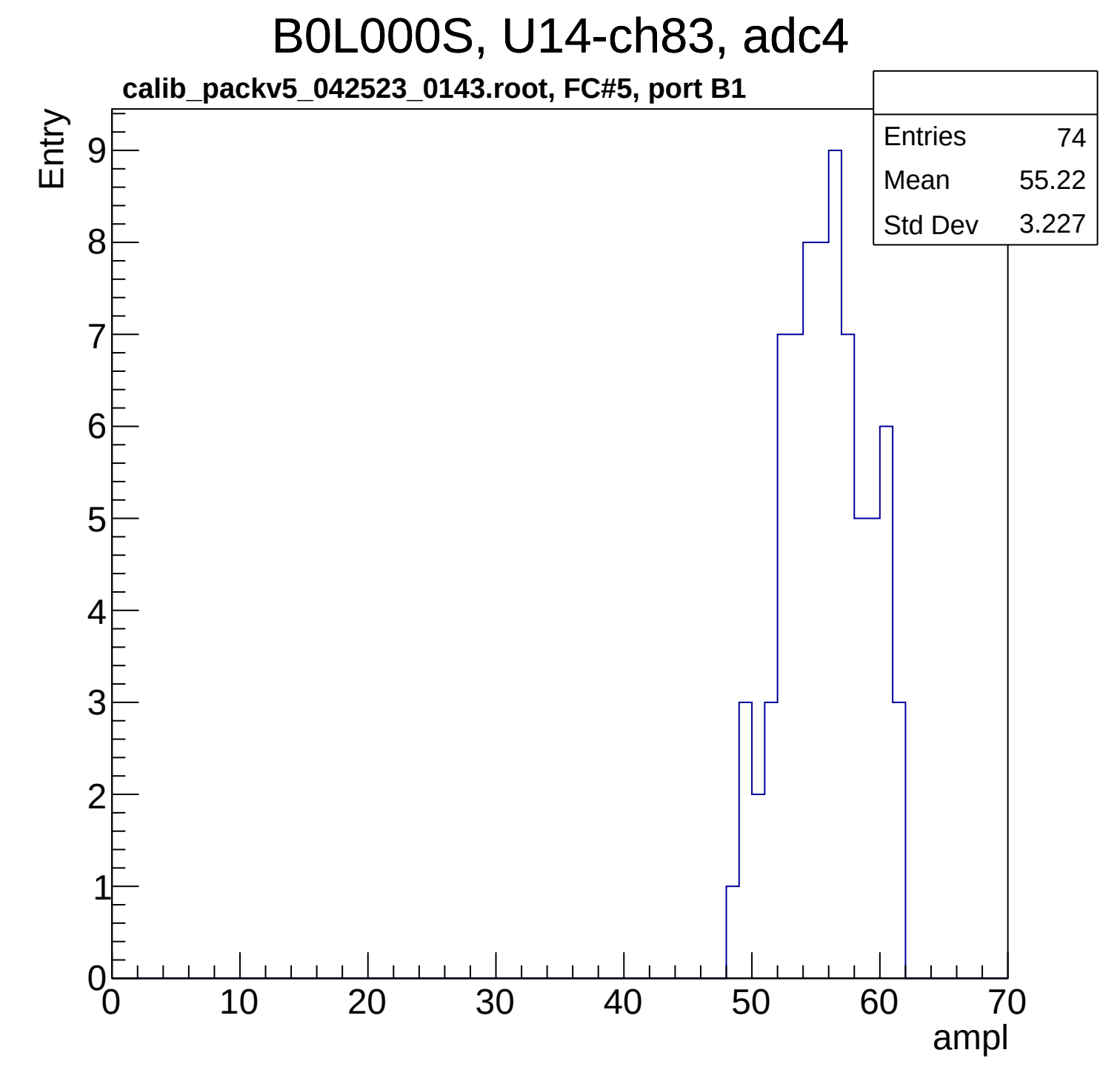
B0L000S, U14-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 55.22
Std Dev 3.227
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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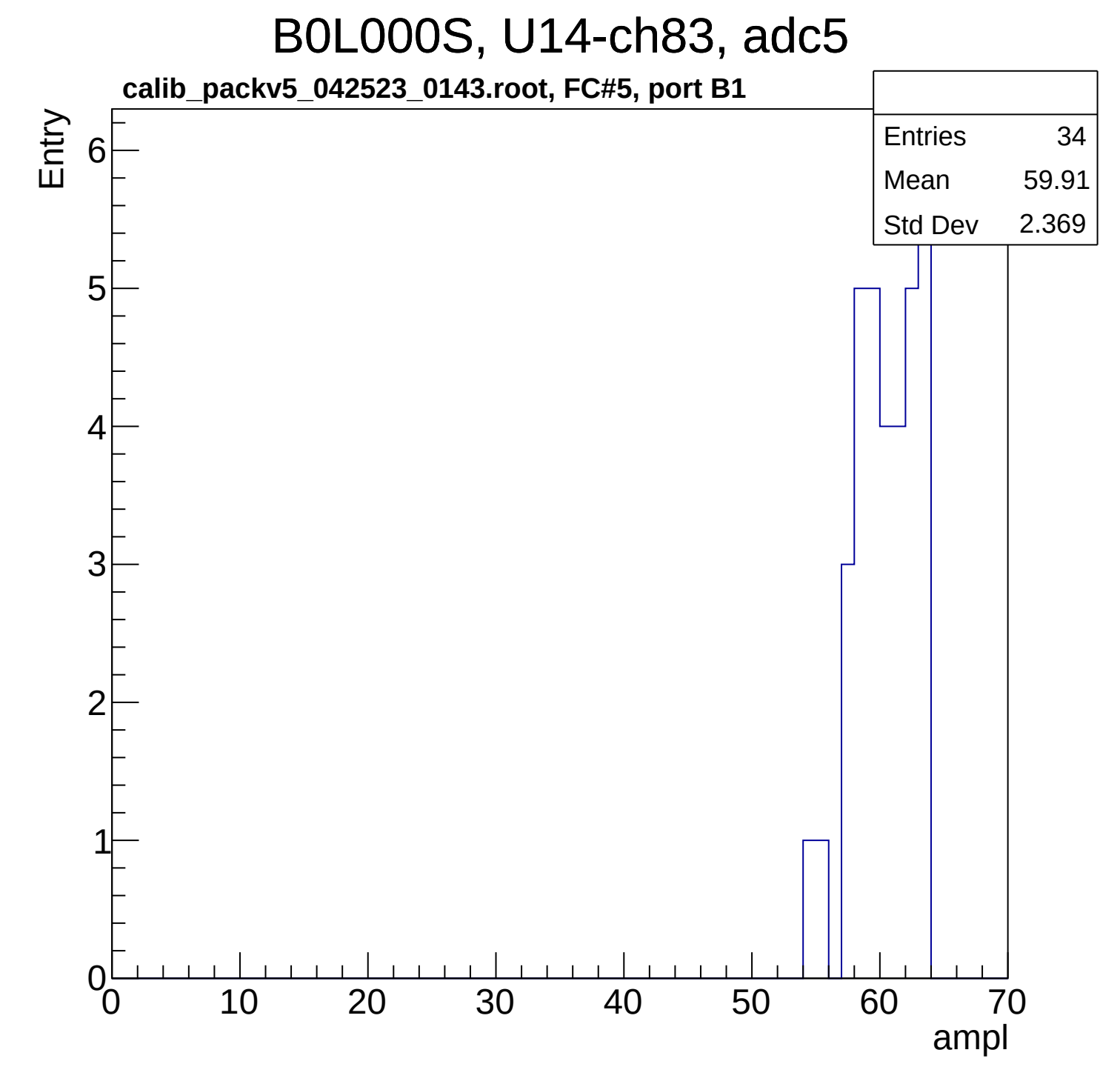
B0L000S, U14-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 59.91
Std Dev 2.369
Entry
ampl
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0
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60
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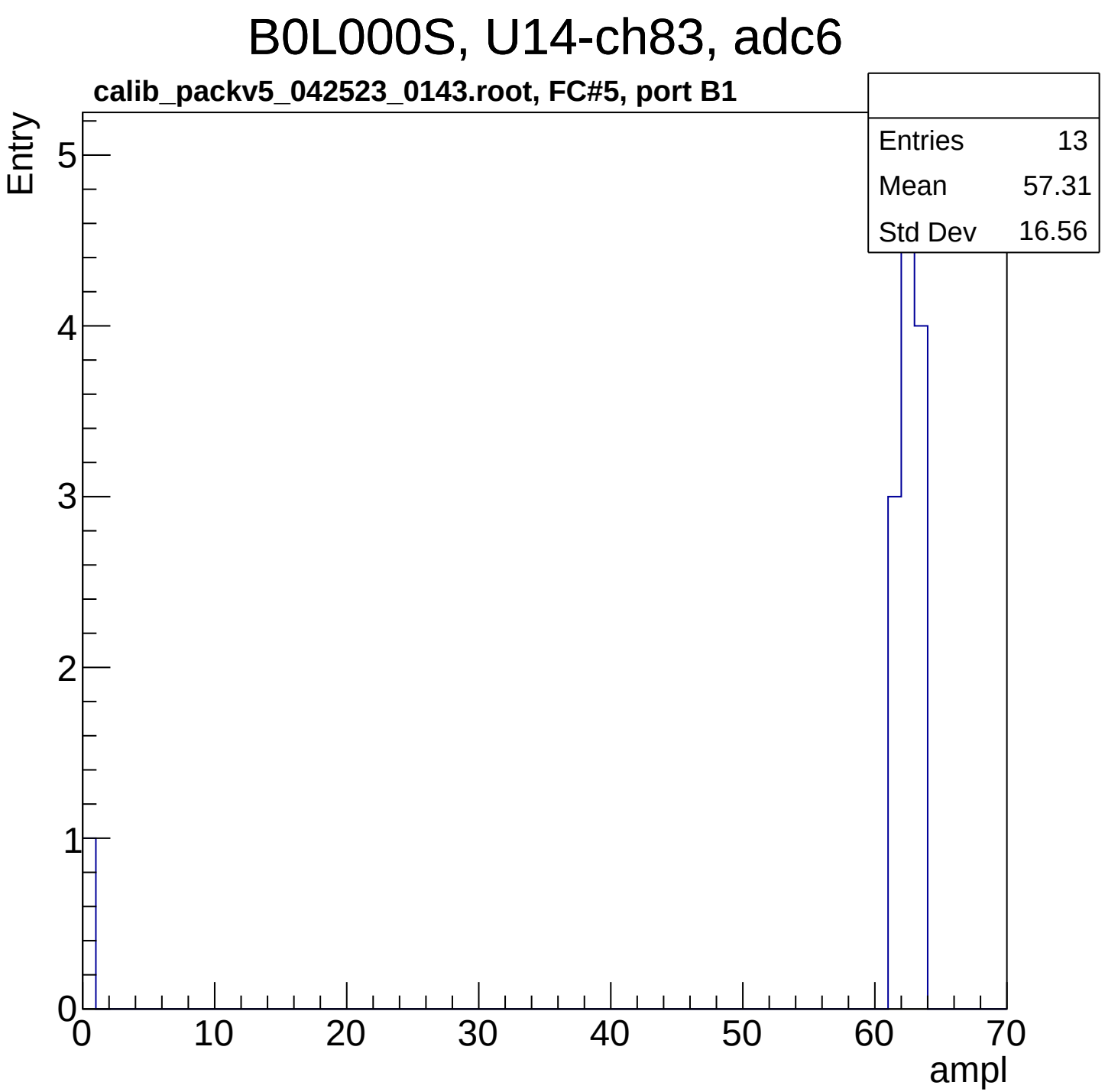
B0L000S, U14-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 57.31
Std Dev 16.56
Entry
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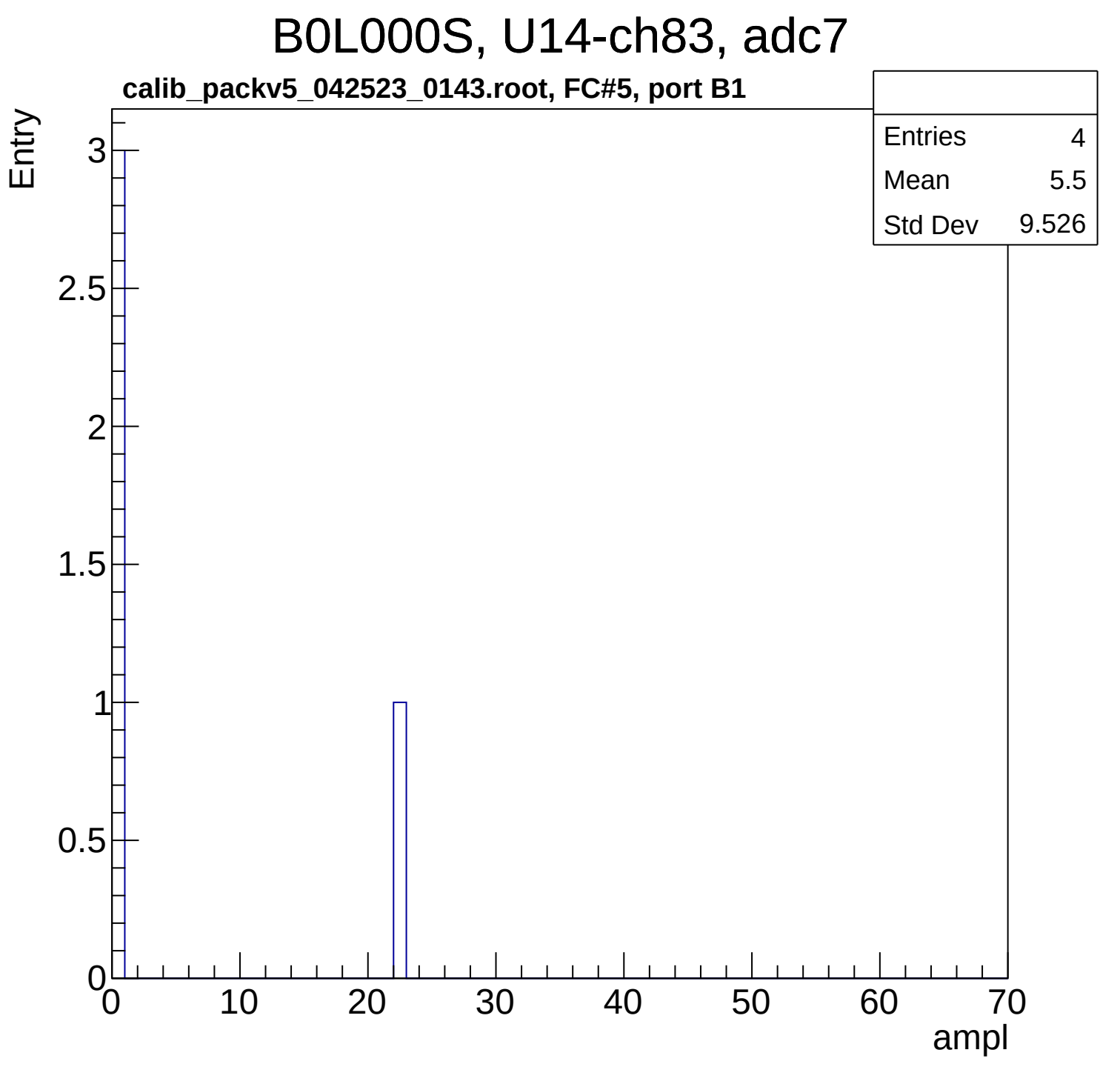
70
ampl


B0L000S, U14-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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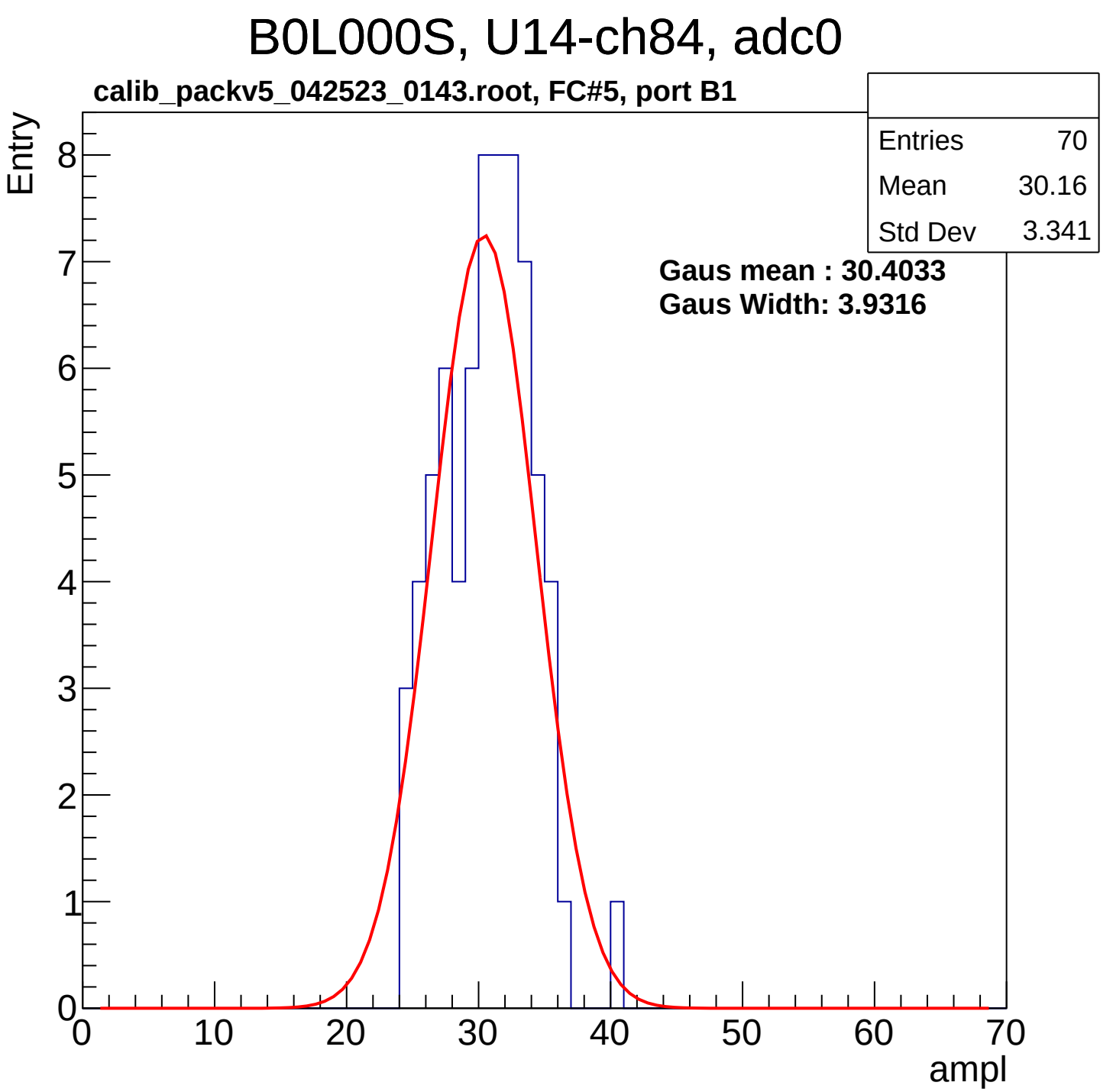
B0L000S, U14-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.16
Std Dev 3.341
Gaus mean : 30.4033
Gaus Width: 3.9316
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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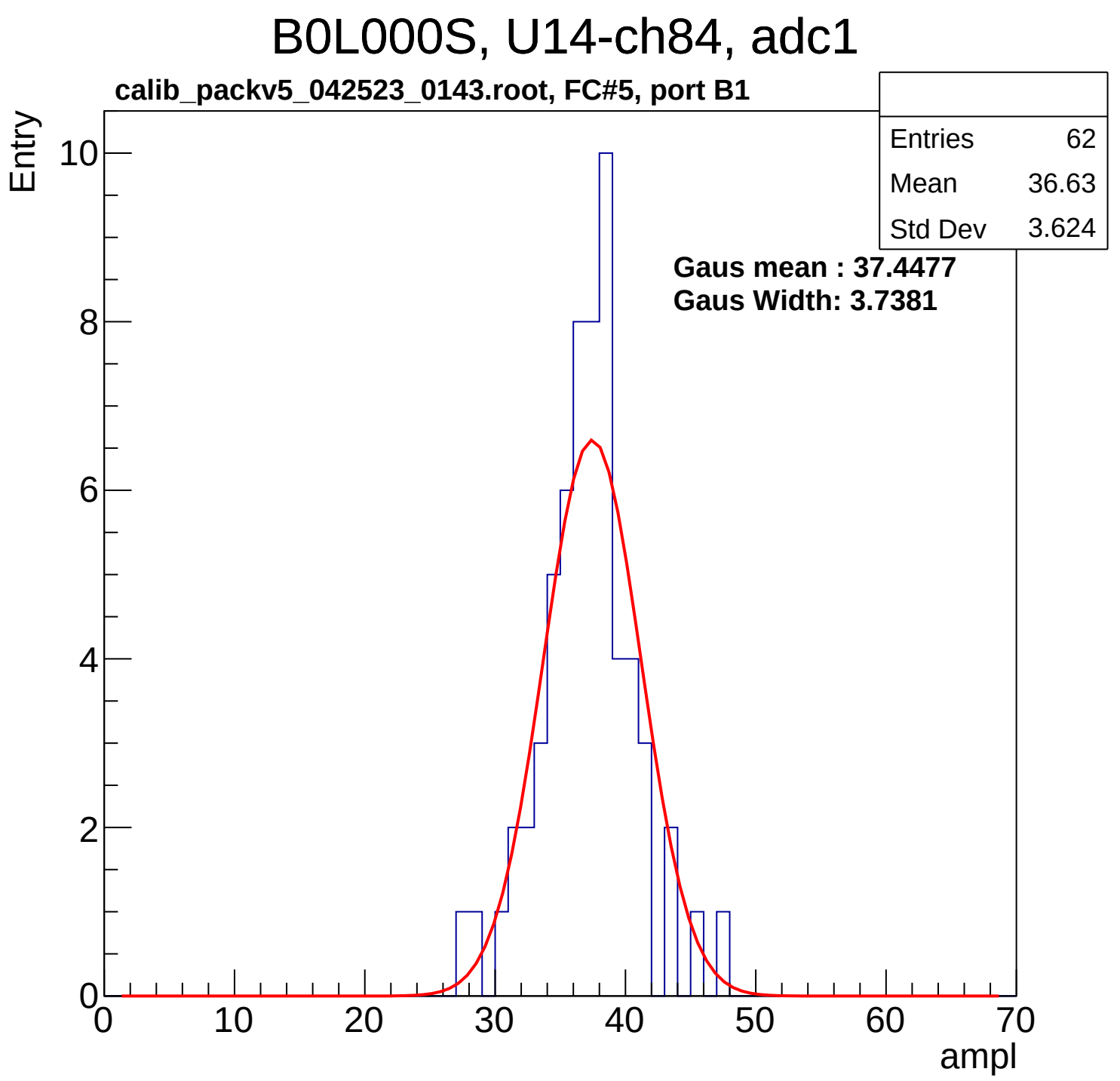
B0L000S, U14-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.63
Std Dev 3.624
Gaus mean : 37.4477
Gaus Width: 3.7381
Entry
ampl
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4
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8
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0
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20
30
40
50
60
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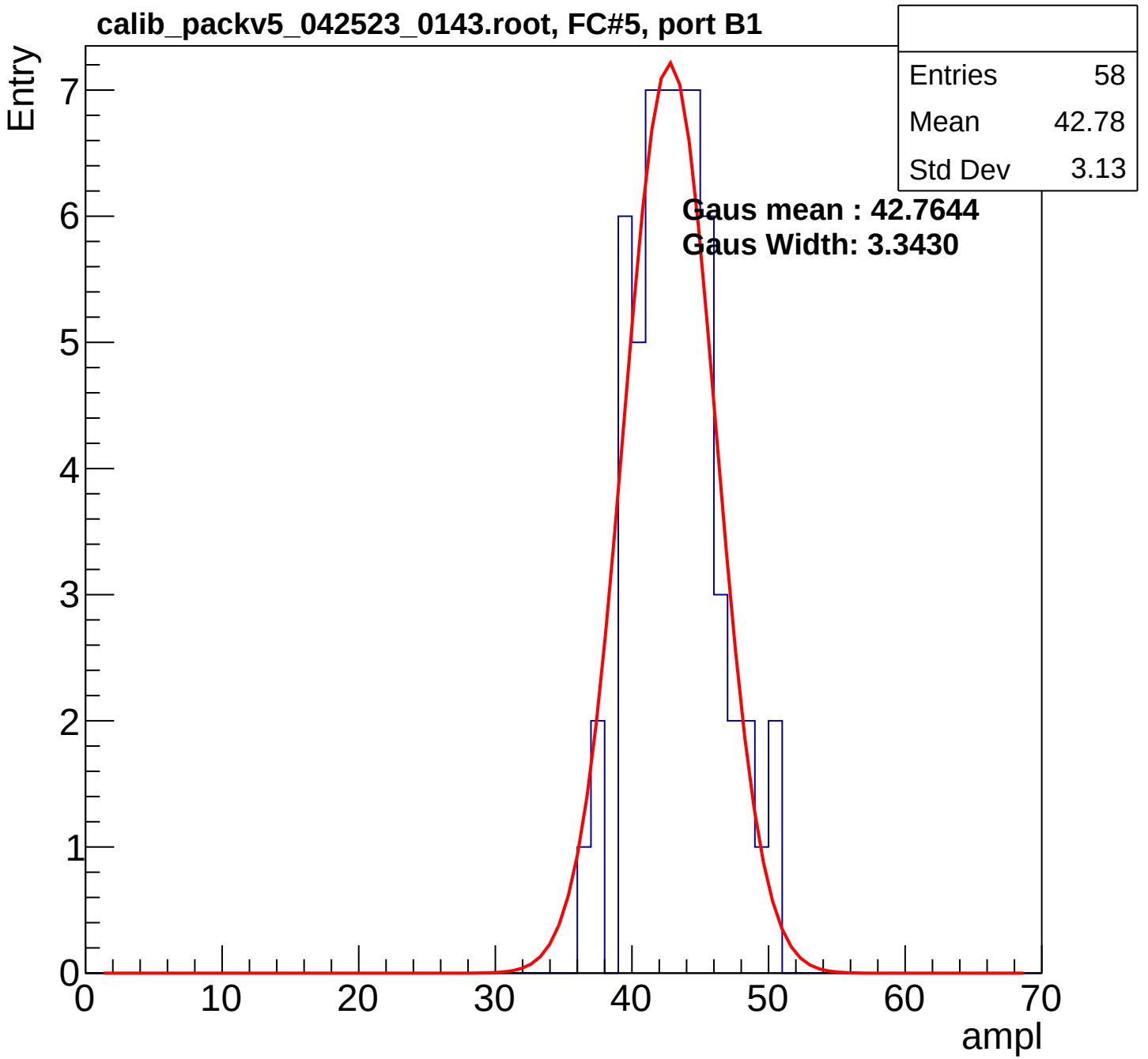
B0L000S, U14-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.78
Std Dev 3.13
Gaus mean : 42.7644
Gaus Width: 3.3430
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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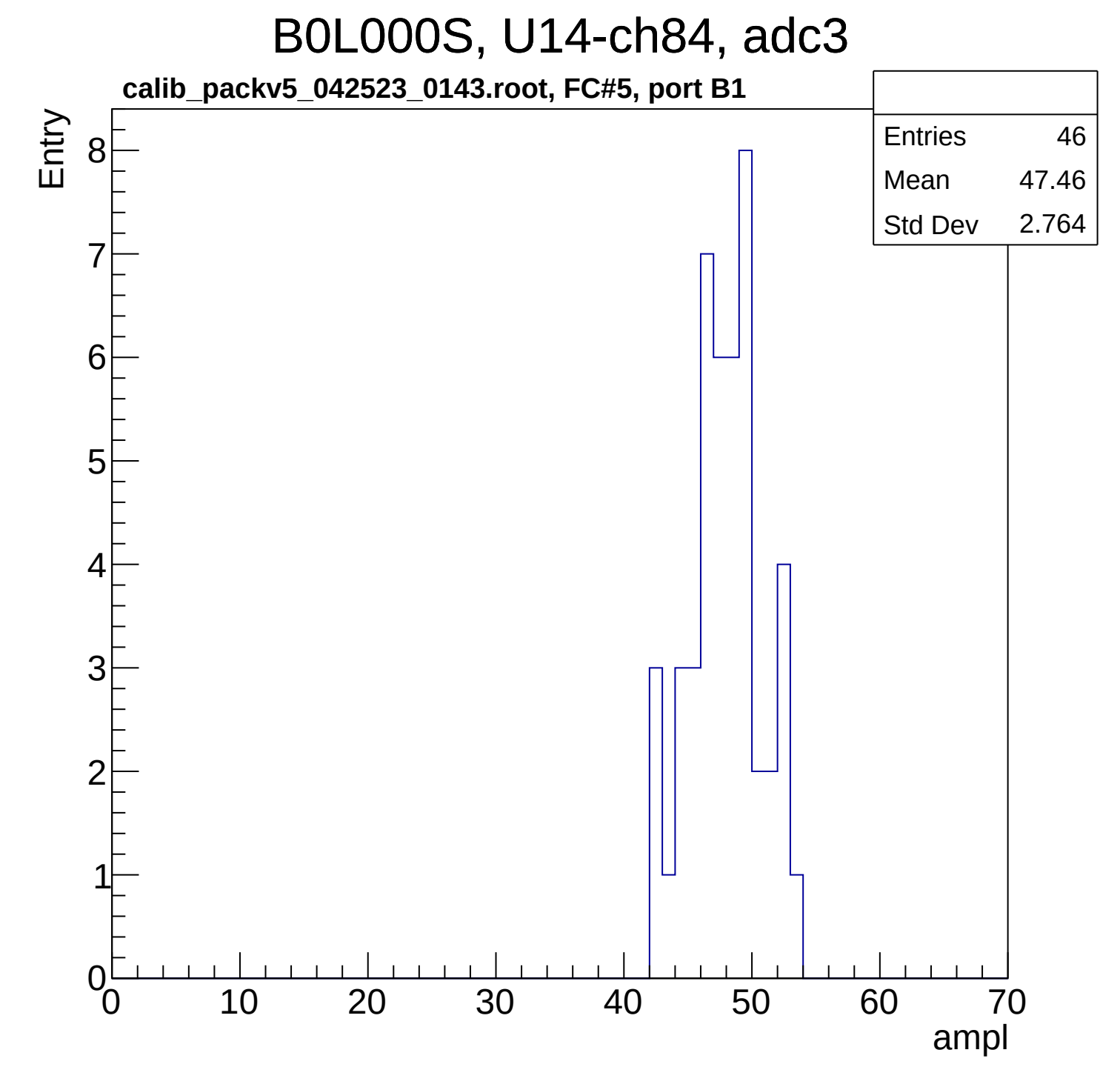
B0L000S, U14-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 47.46
Std Dev 2.764
Entry
ampl
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3
4
5
6
7
8
0
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20
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40
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60
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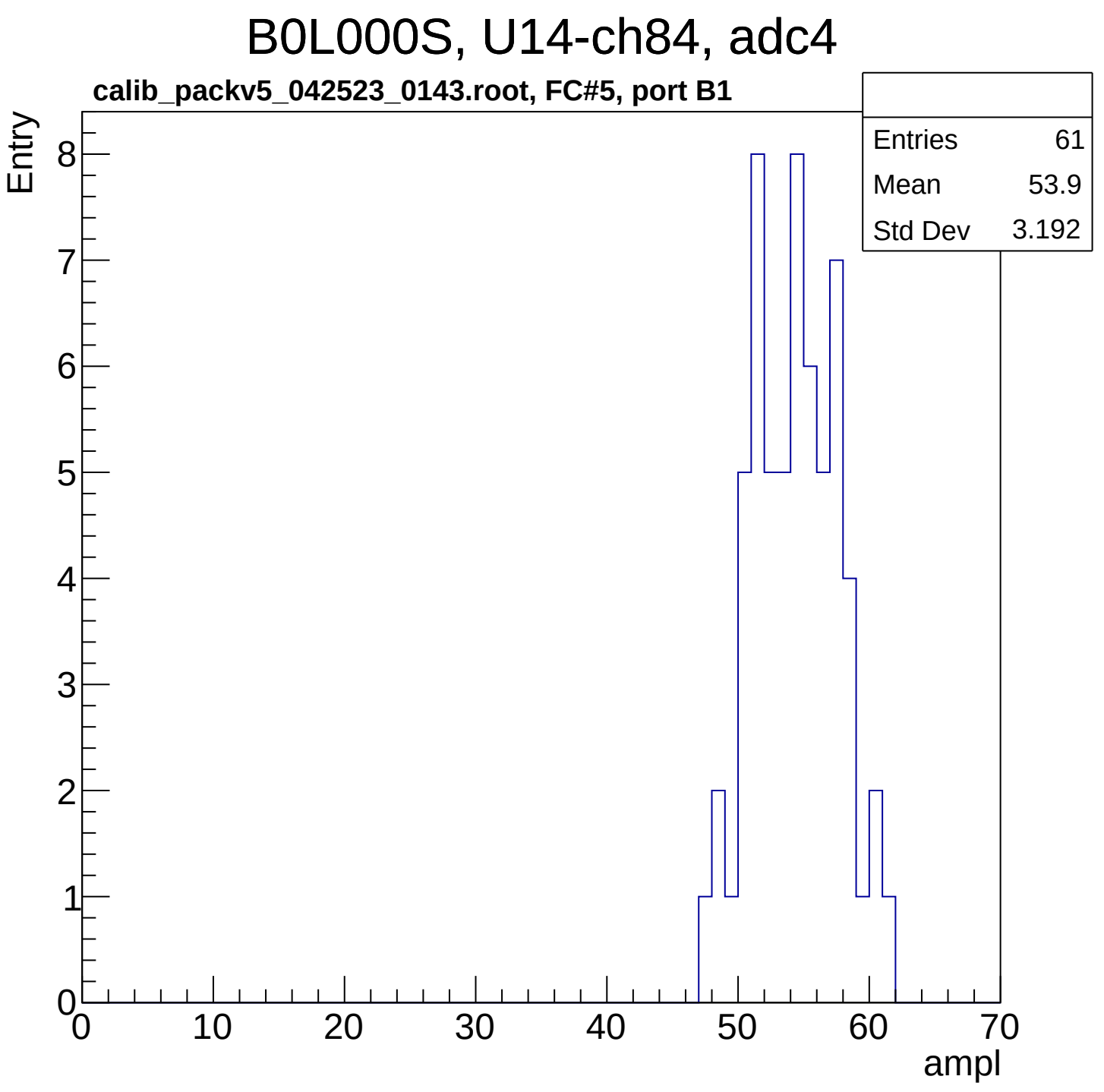
B0L000S, U14-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.9
Std Dev 3.192
Entry
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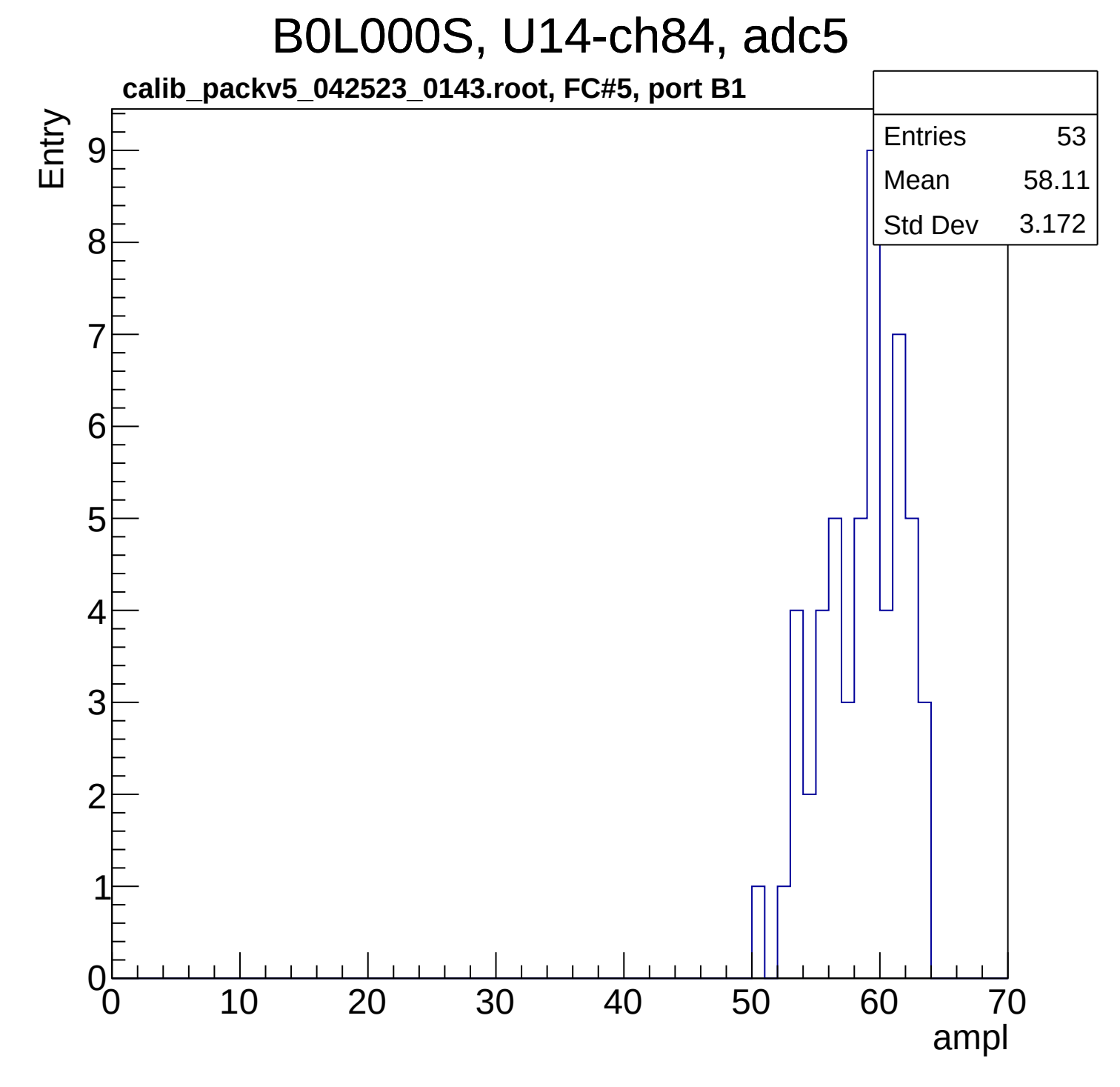
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 58.11
Std Dev 3.172
Entry
ampl
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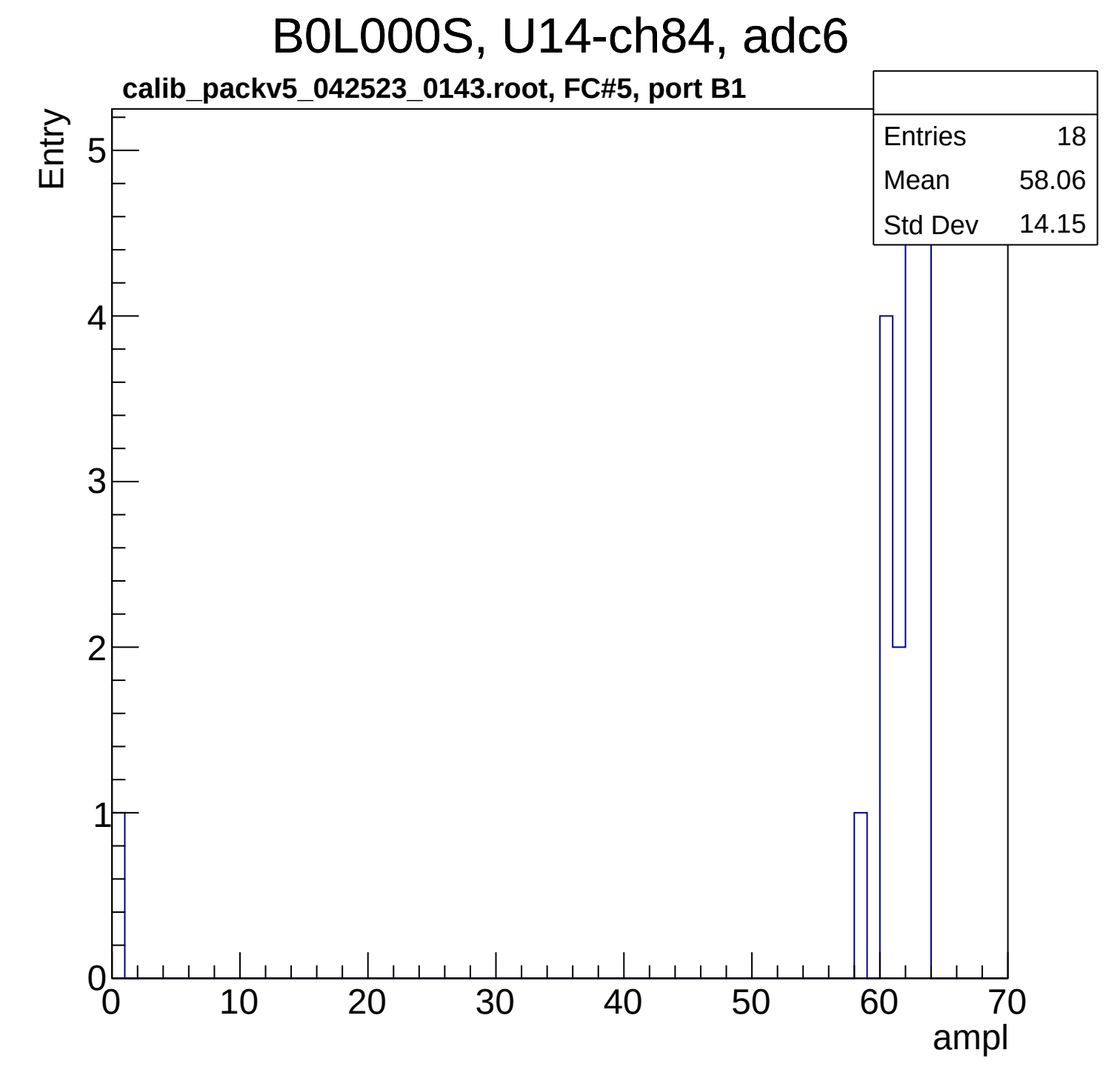
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 58.06
Std Dev 14.15
Entry
0
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2
3
4
5
0
10
20
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50
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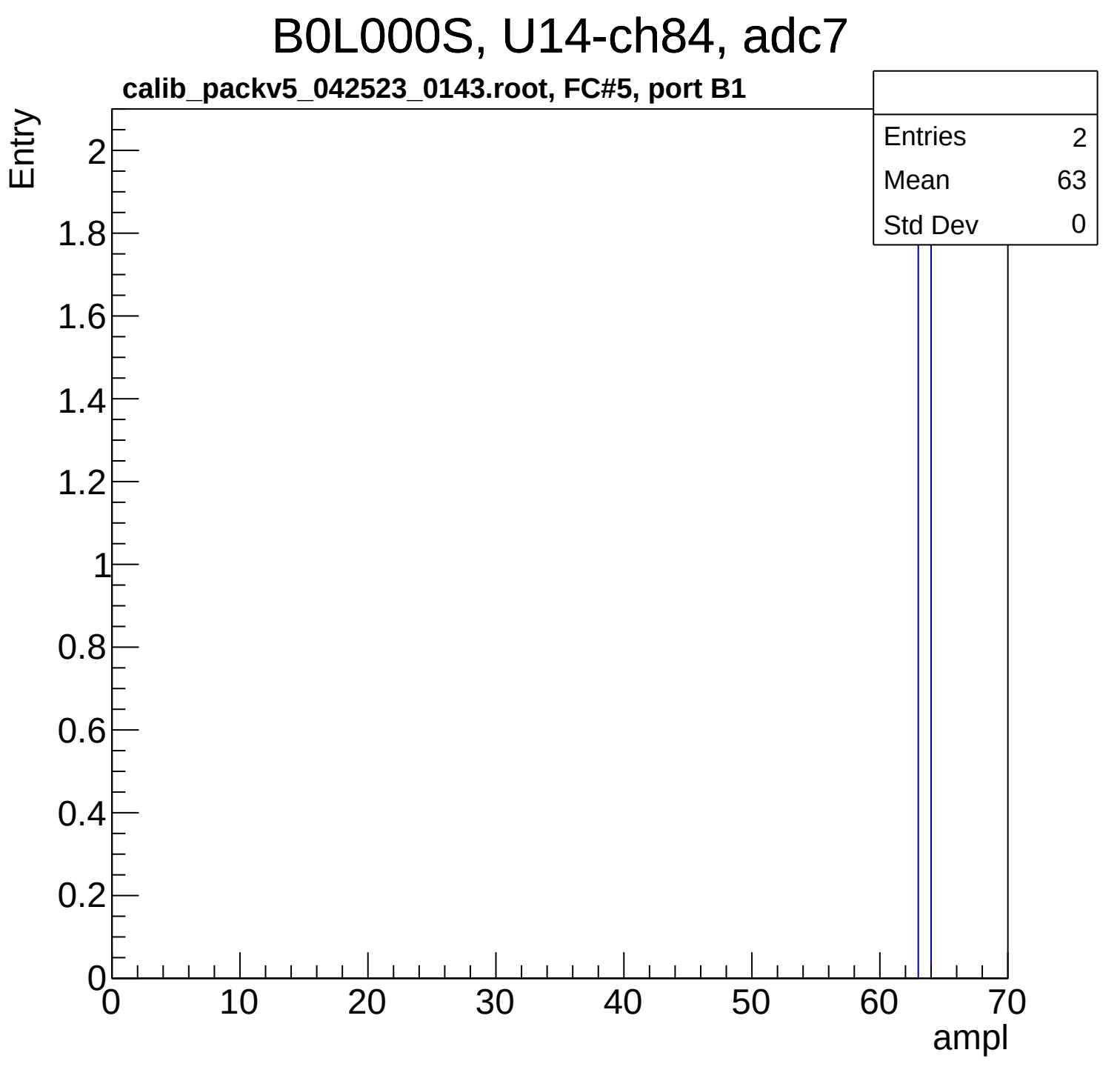
70
ampl


B0L000S, U14-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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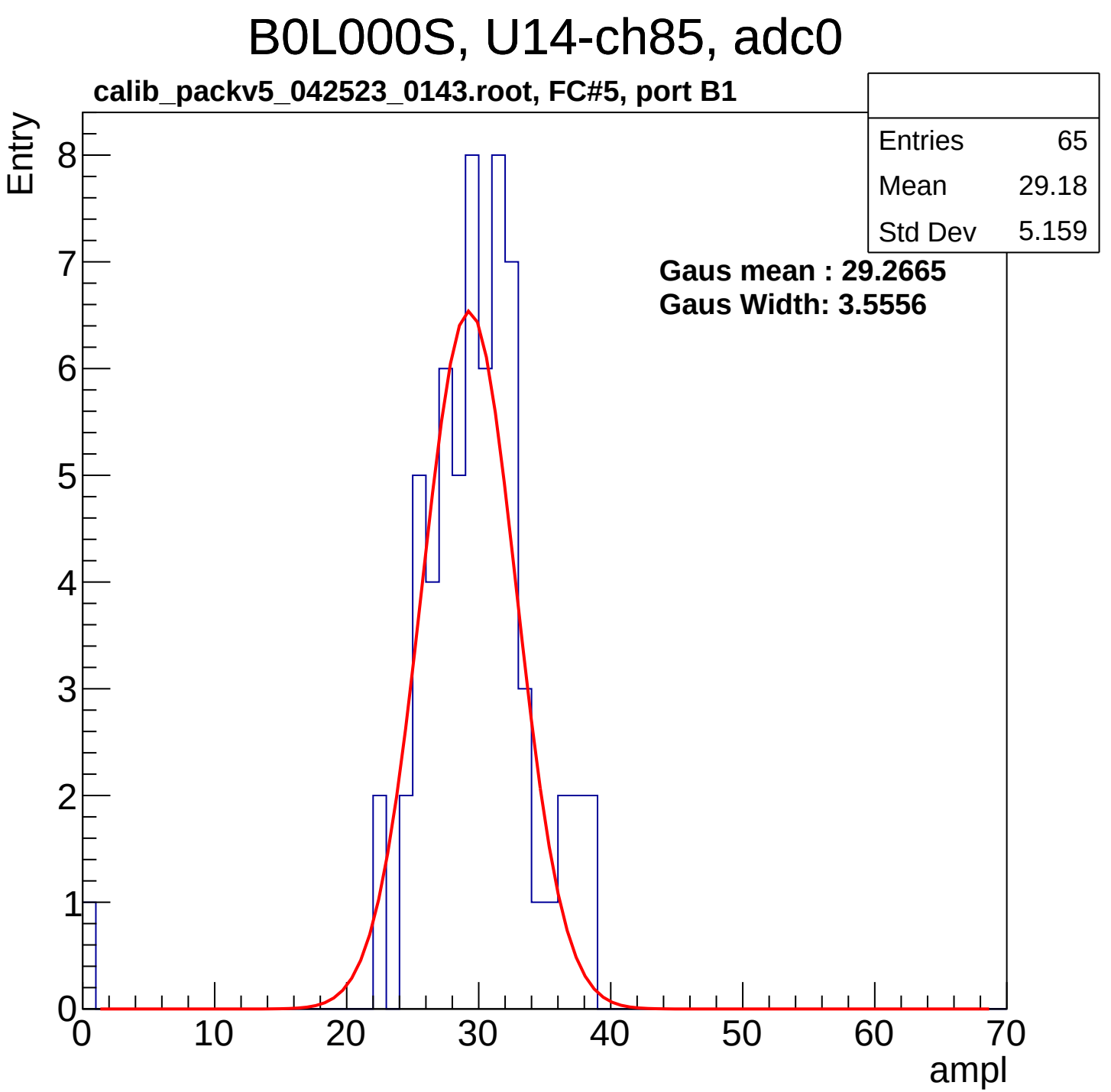
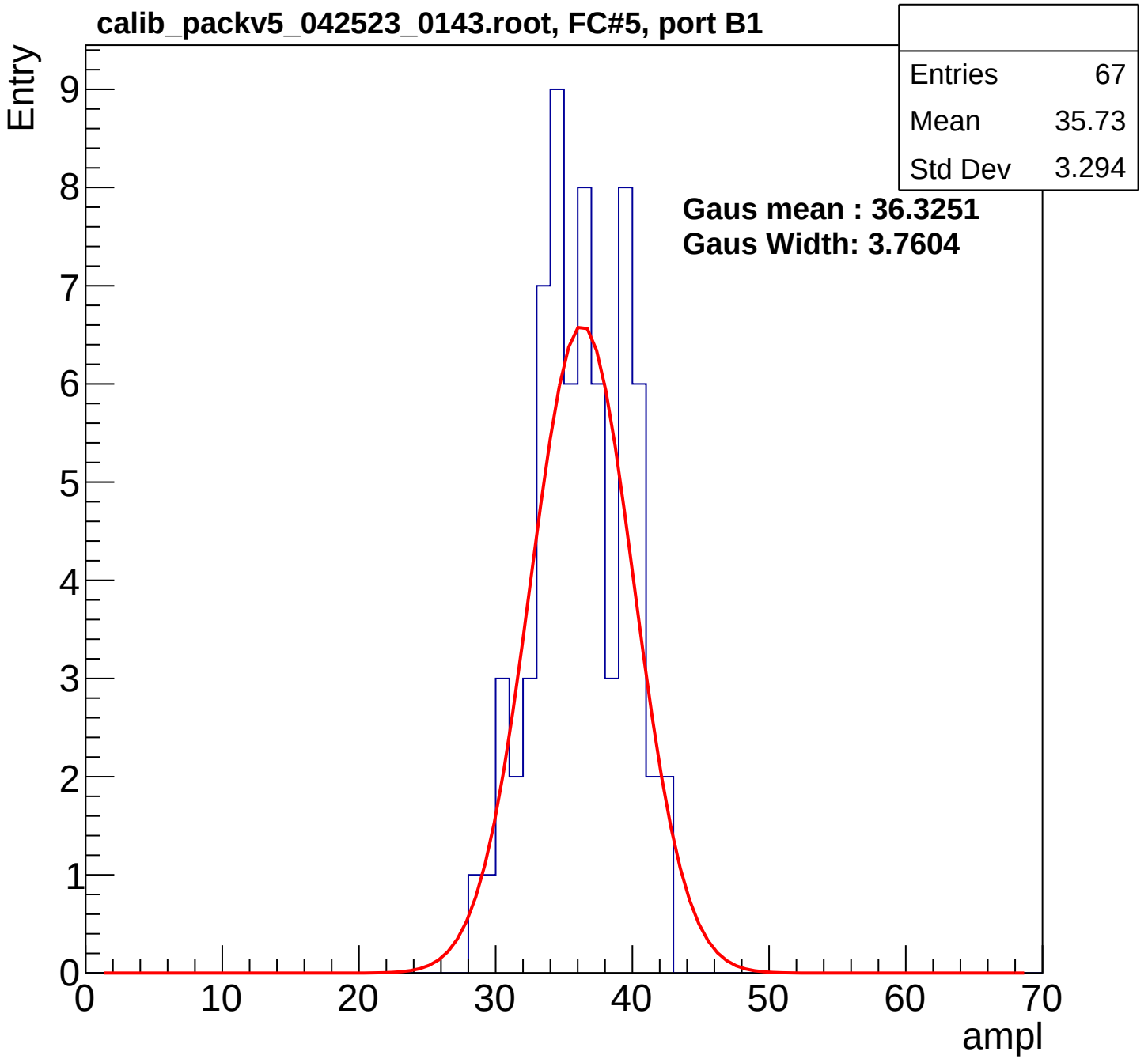

B0L000S, U14-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.18
Std Dev 5.159
Gaus mean : 29.2665
Gaus Width: 3.5556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 35.73
Std Dev 3.294
Gaus mean : 36.3251
Gaus Width: 3.7604
Entry
ampl
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2
3
4
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6
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0
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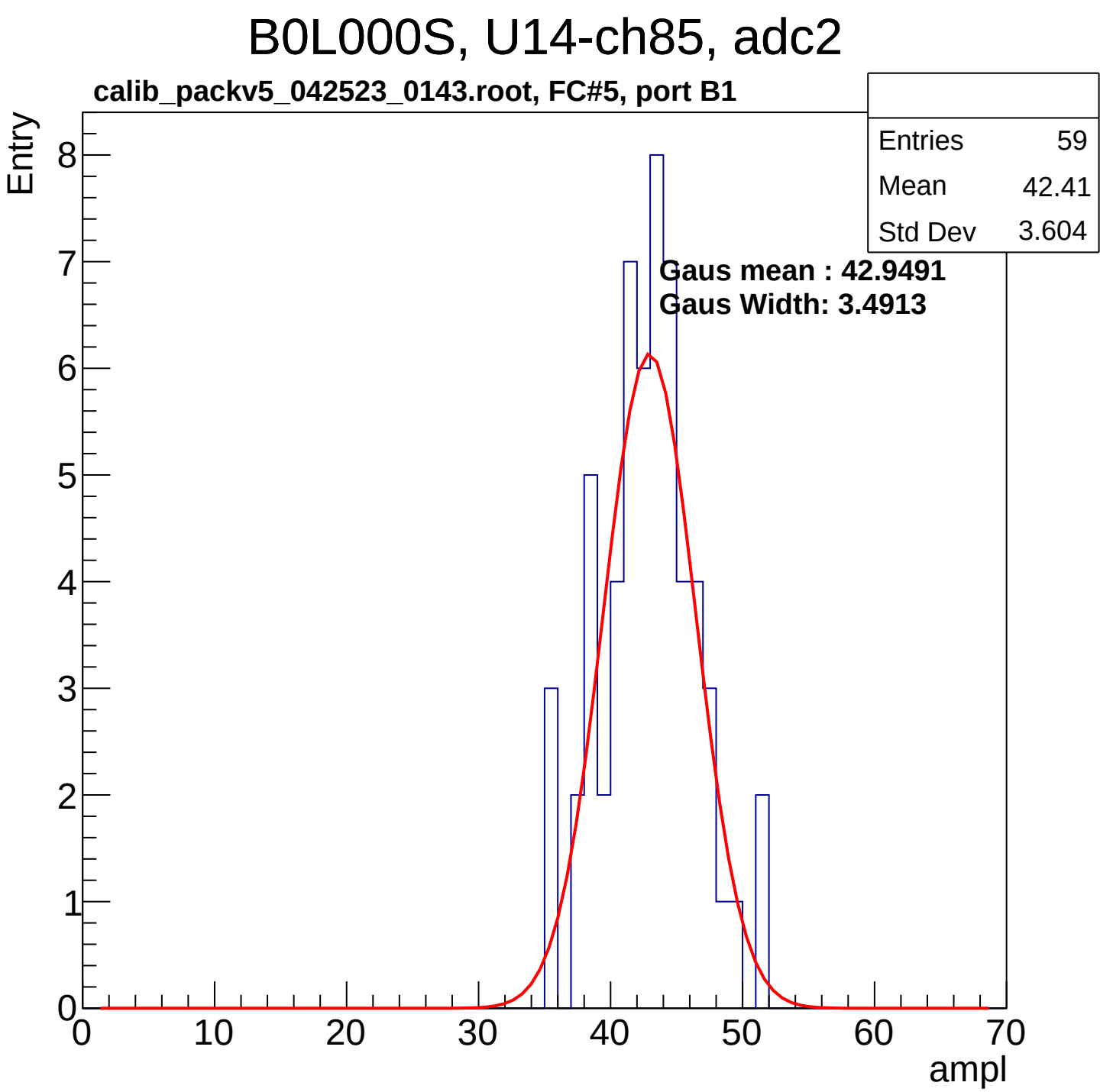
B0L000S, U14-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.41
Std Dev 3.604
Gaus mean : 42.9491
Gaus Width: 3.4913
Entry
ampl
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2
3
4
5
6
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8
0
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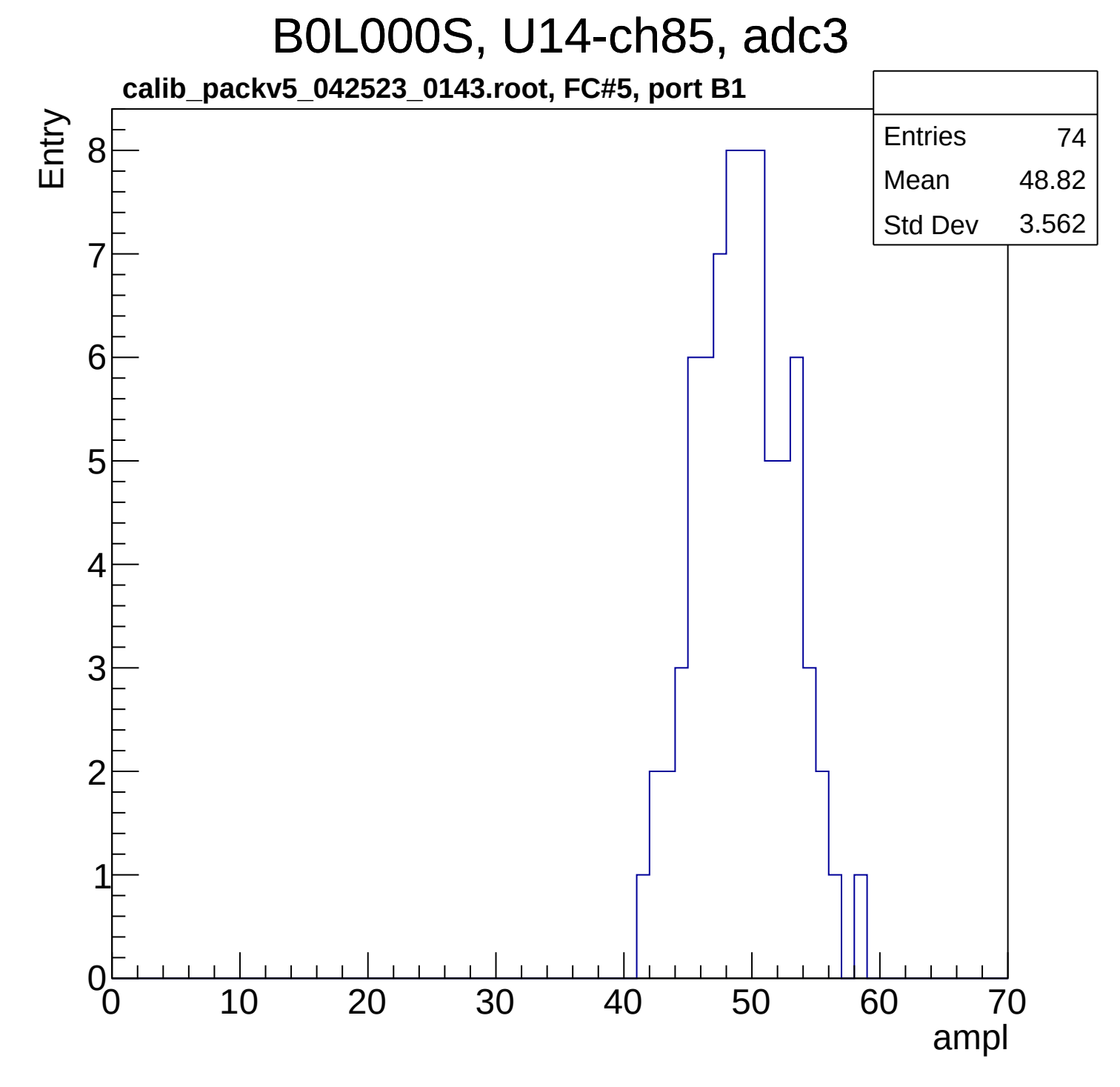
B0L000S, U14-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 48.82
Std Dev 3.562
Entry
ampl
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2
3
4
5
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7
8
0
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20
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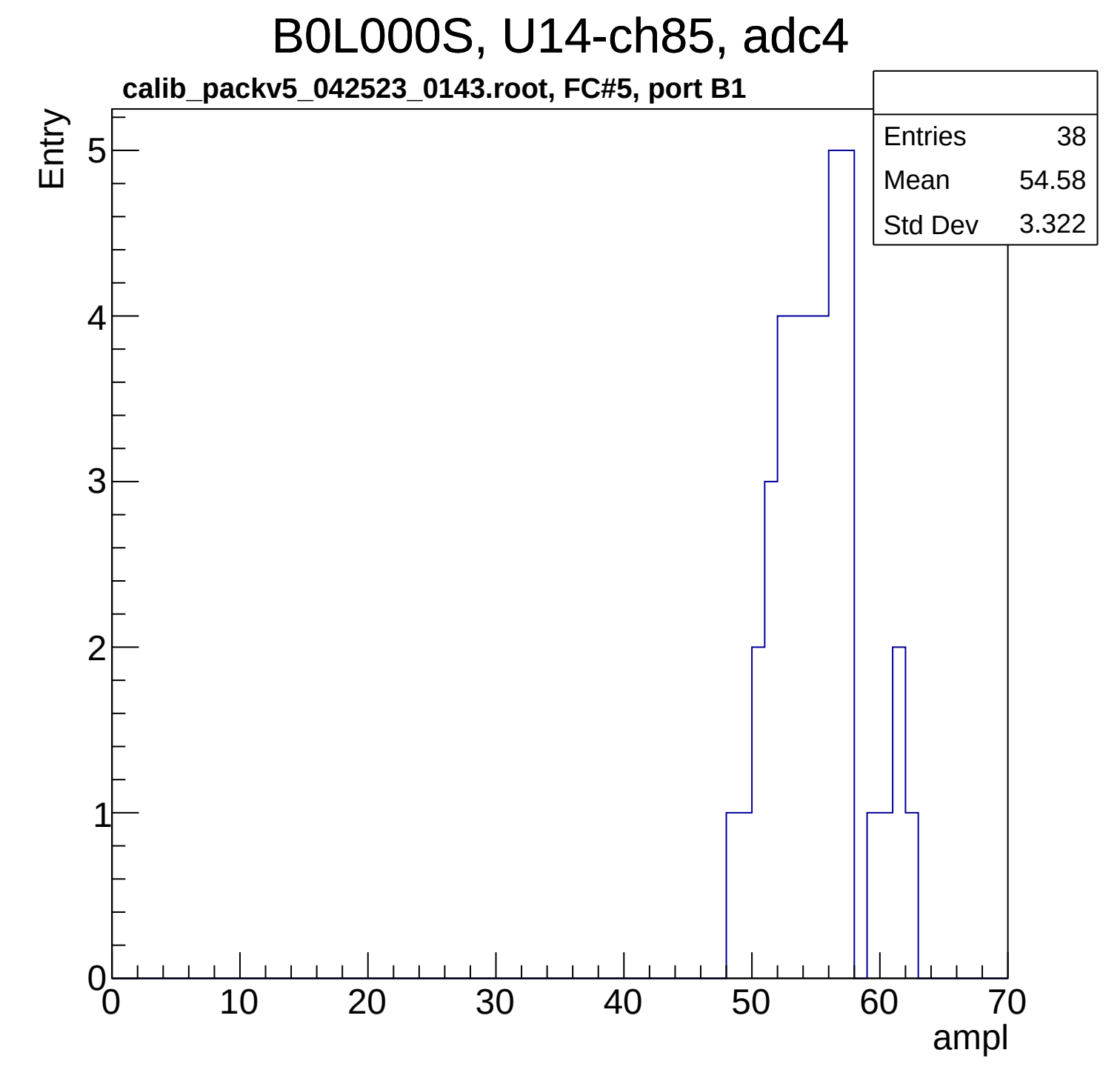
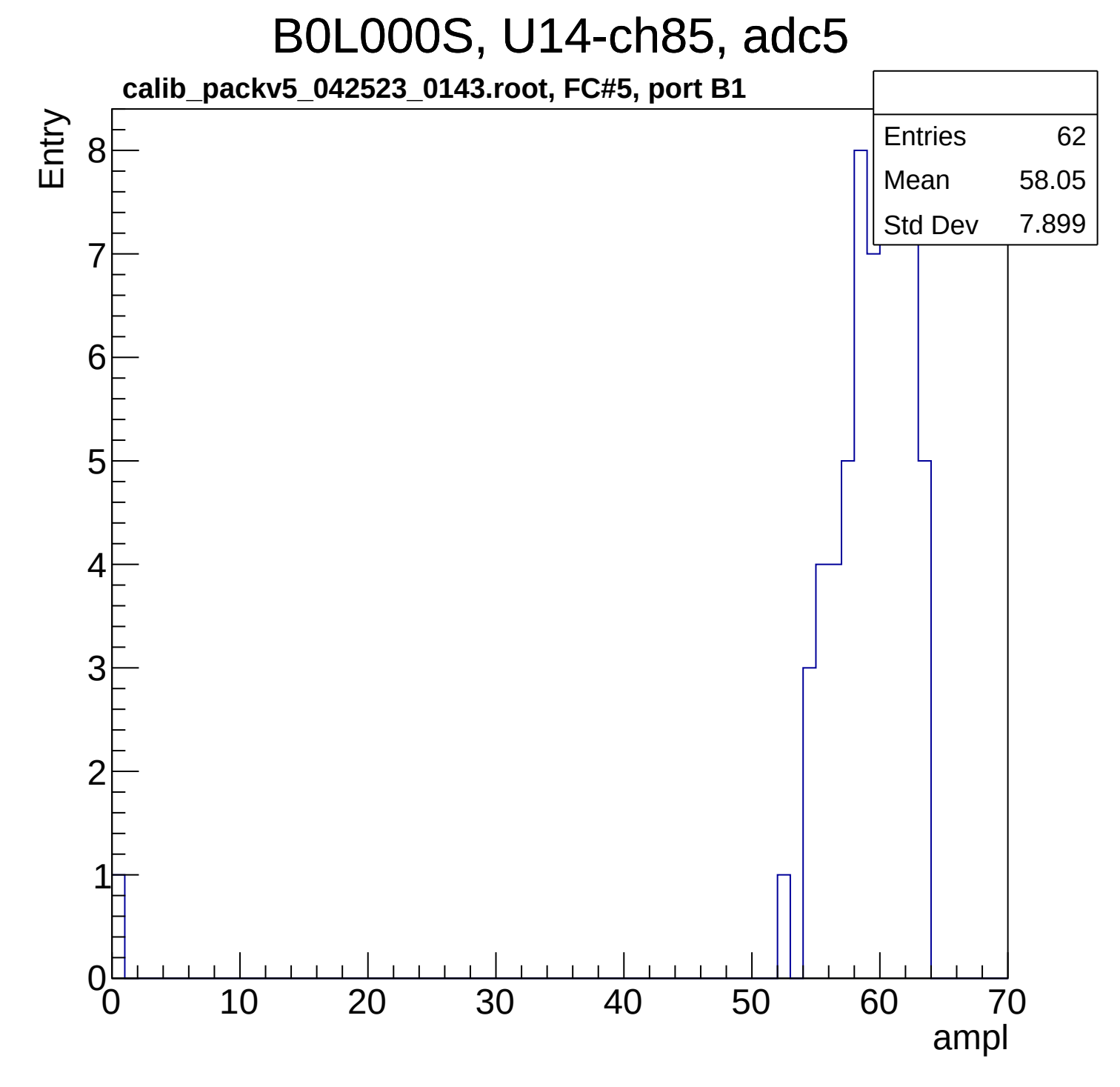

B0L000S, U14-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 54.58
Std Dev 3.322
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.05
Std Dev 7.899
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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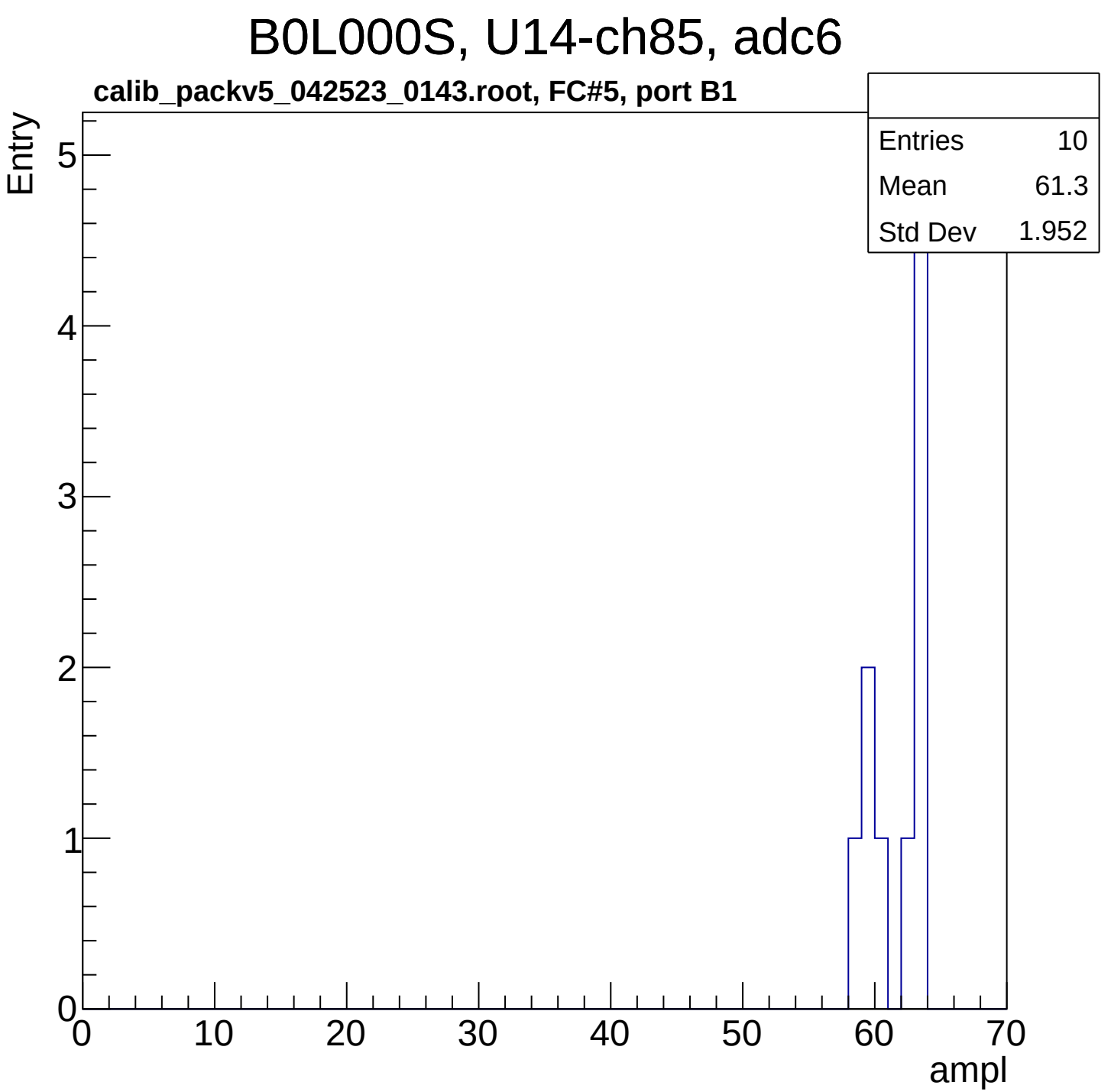
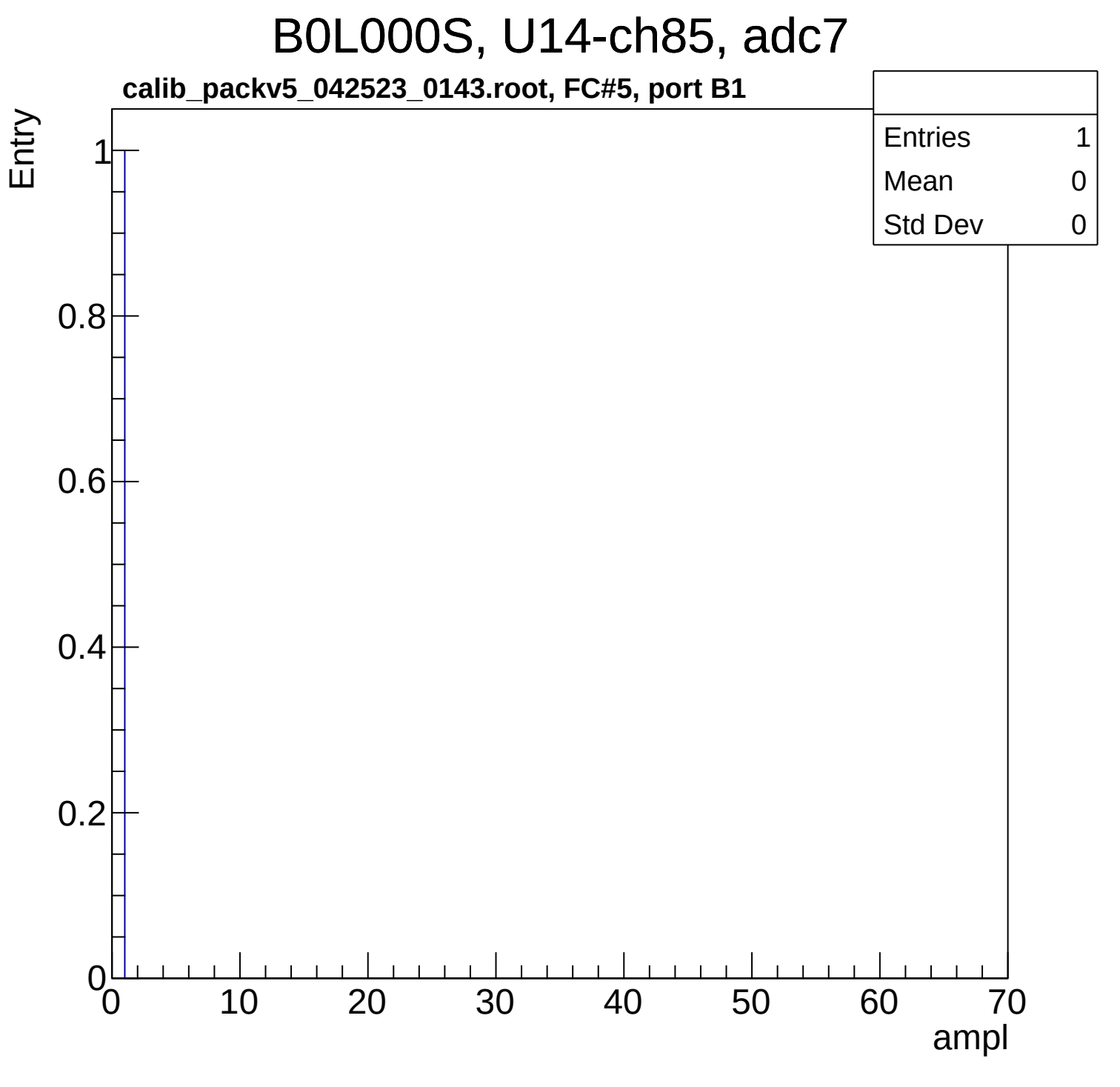
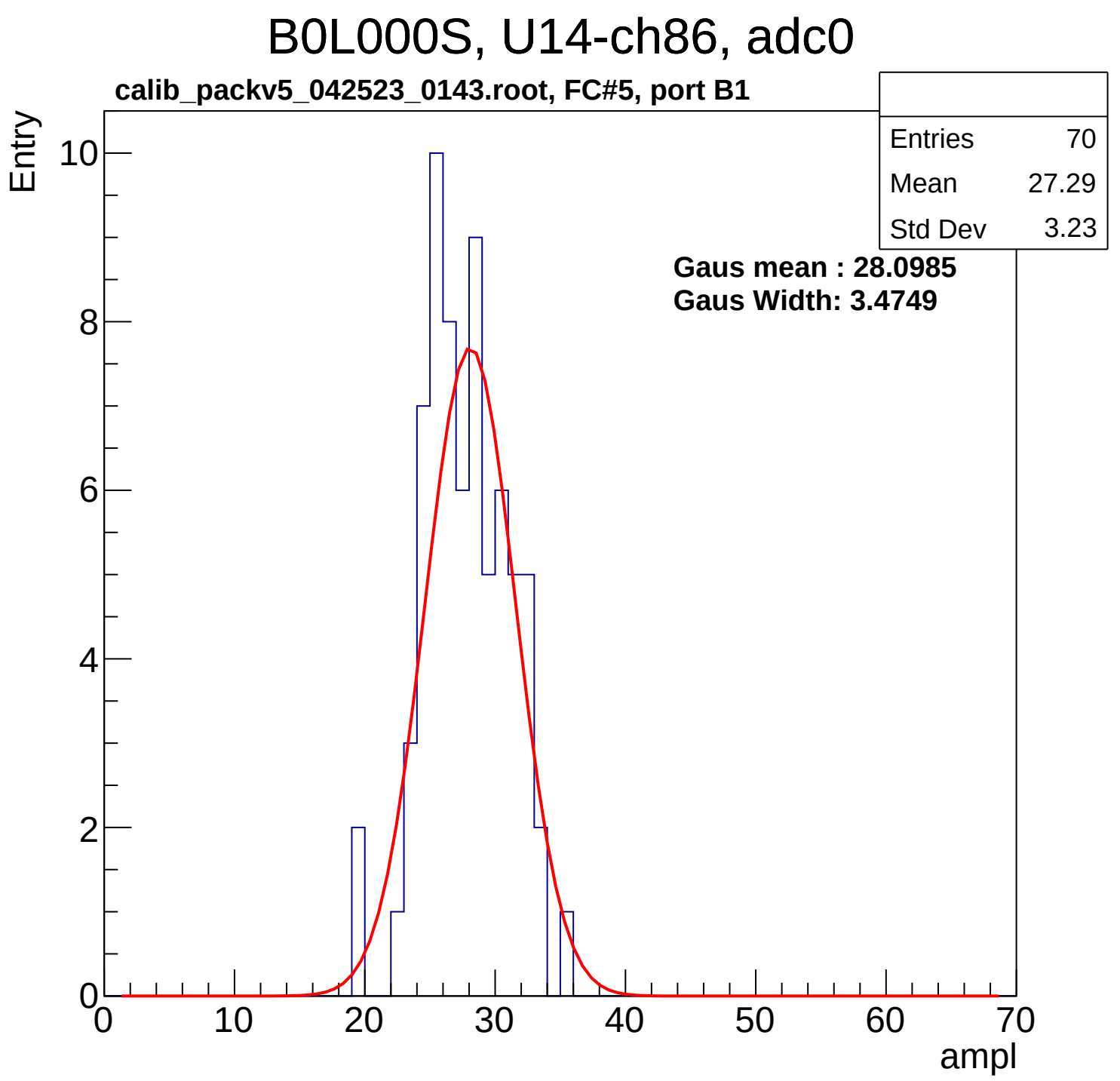

B0L000S, U14-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.3
Std Dev 1.952
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 27.29
Std Dev 3.23
Gaus mean : 28.0985
Gaus Width: 3.4749
Entry
ampl
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2
4
6
8
10
0
10
20
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40
50
60
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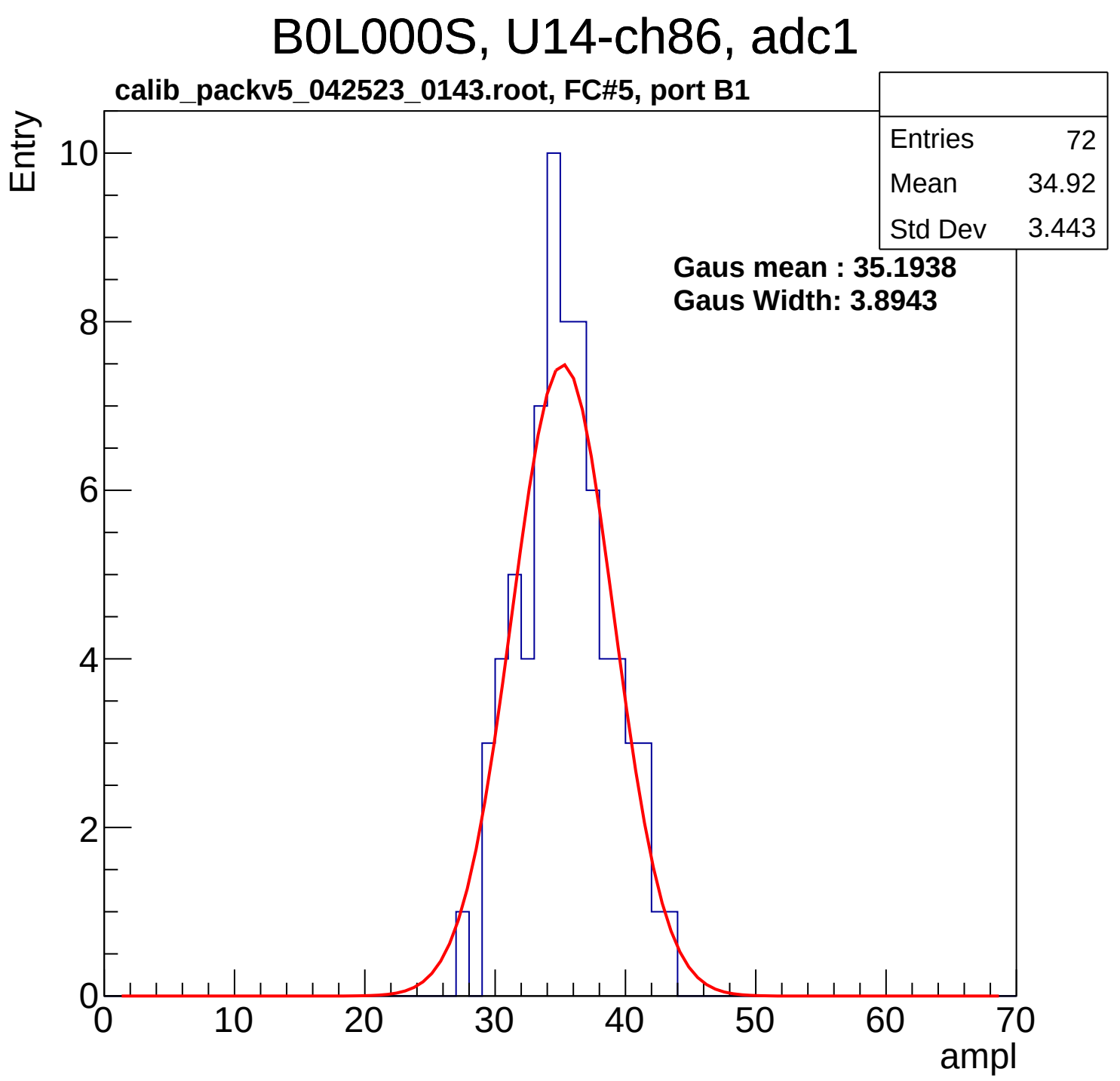
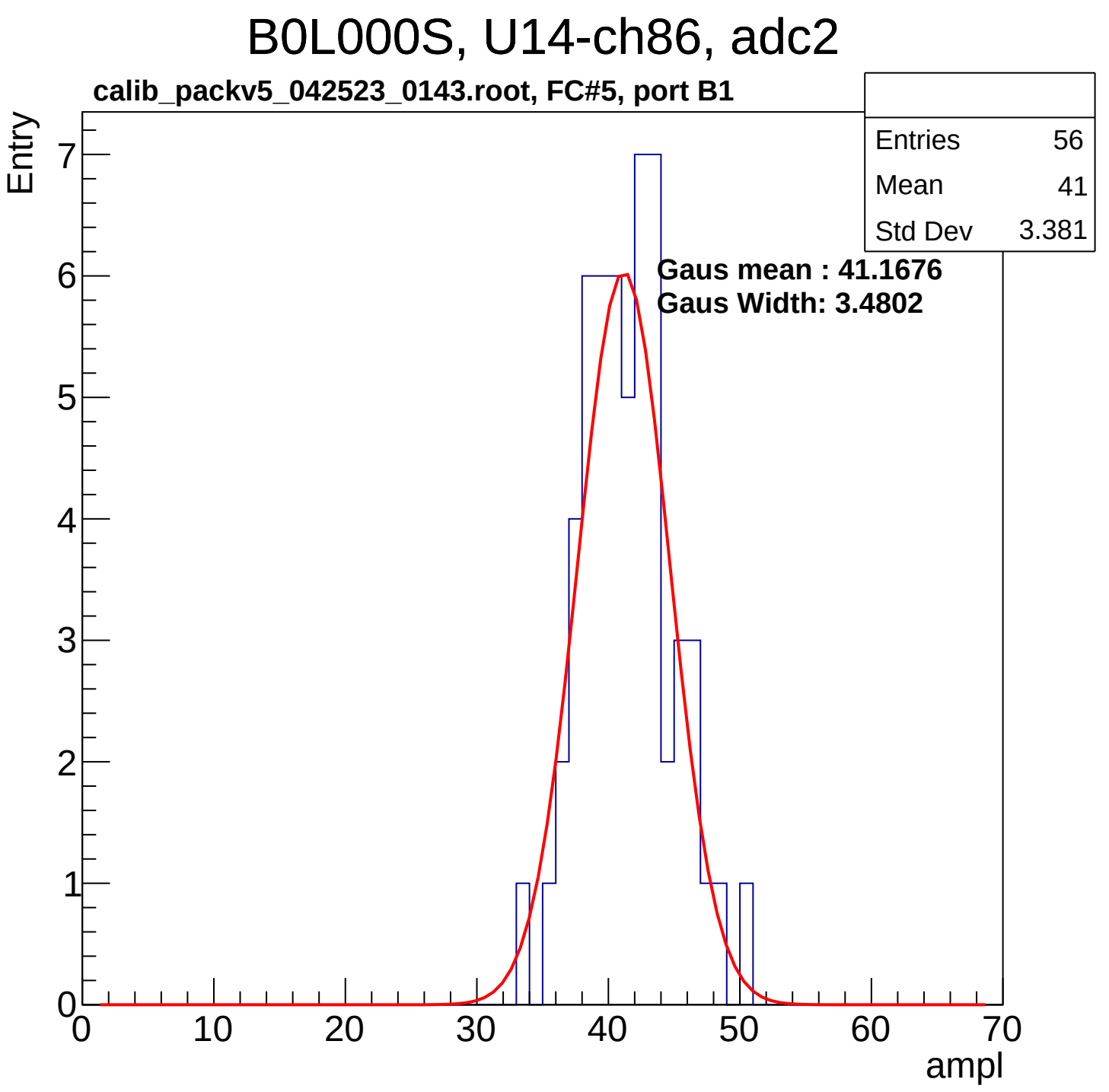

B0L000S, U14-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 34.92
Std Dev 3.443
Gaus mean : 35.1938
Gaus Width: 3.8943
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 41
Std Dev 3.381
Gaus mean : 41.1676
Gaus Width: 3.4802
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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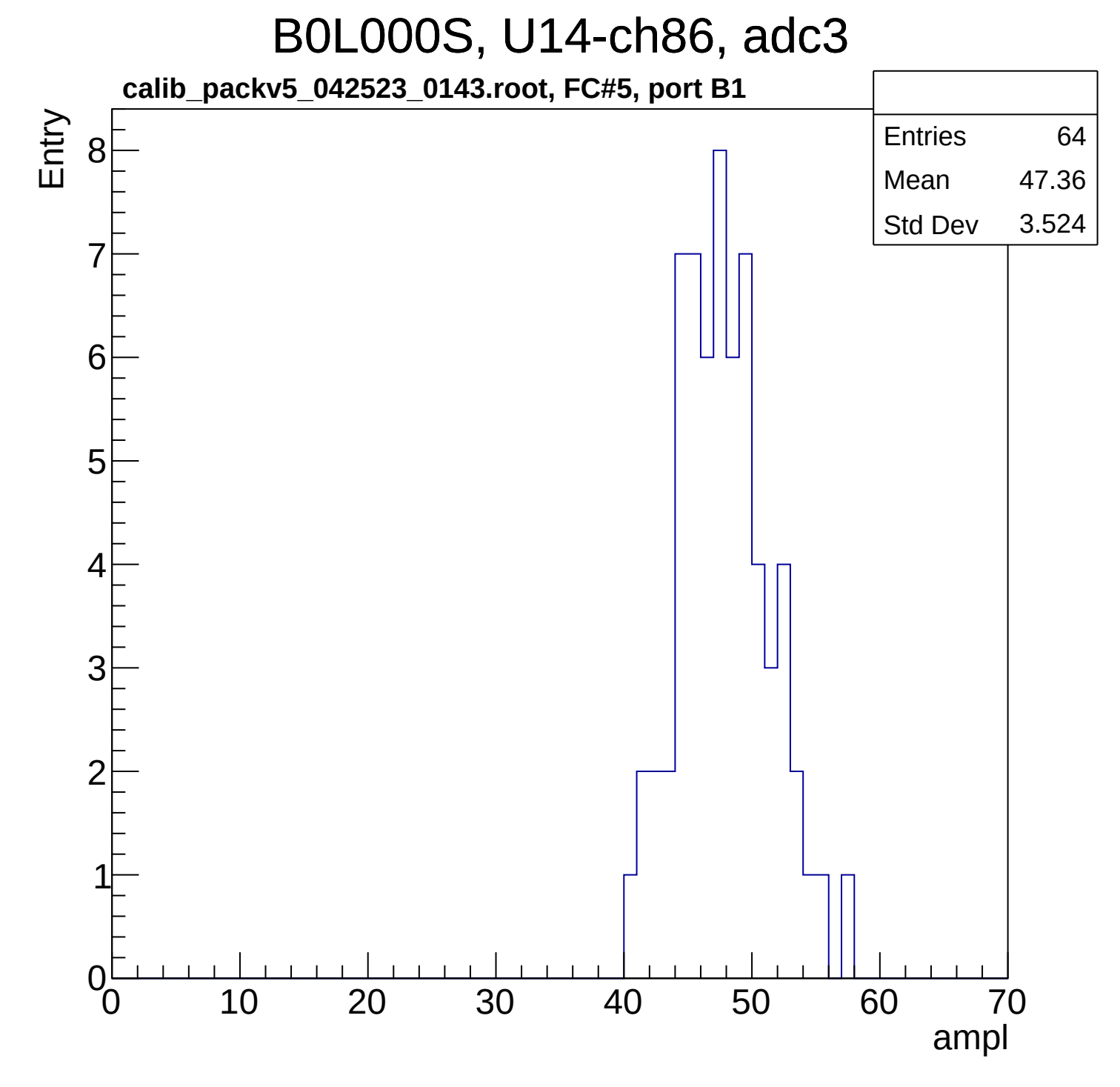
B0L000S, U14-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 47.36
Std Dev 3.524
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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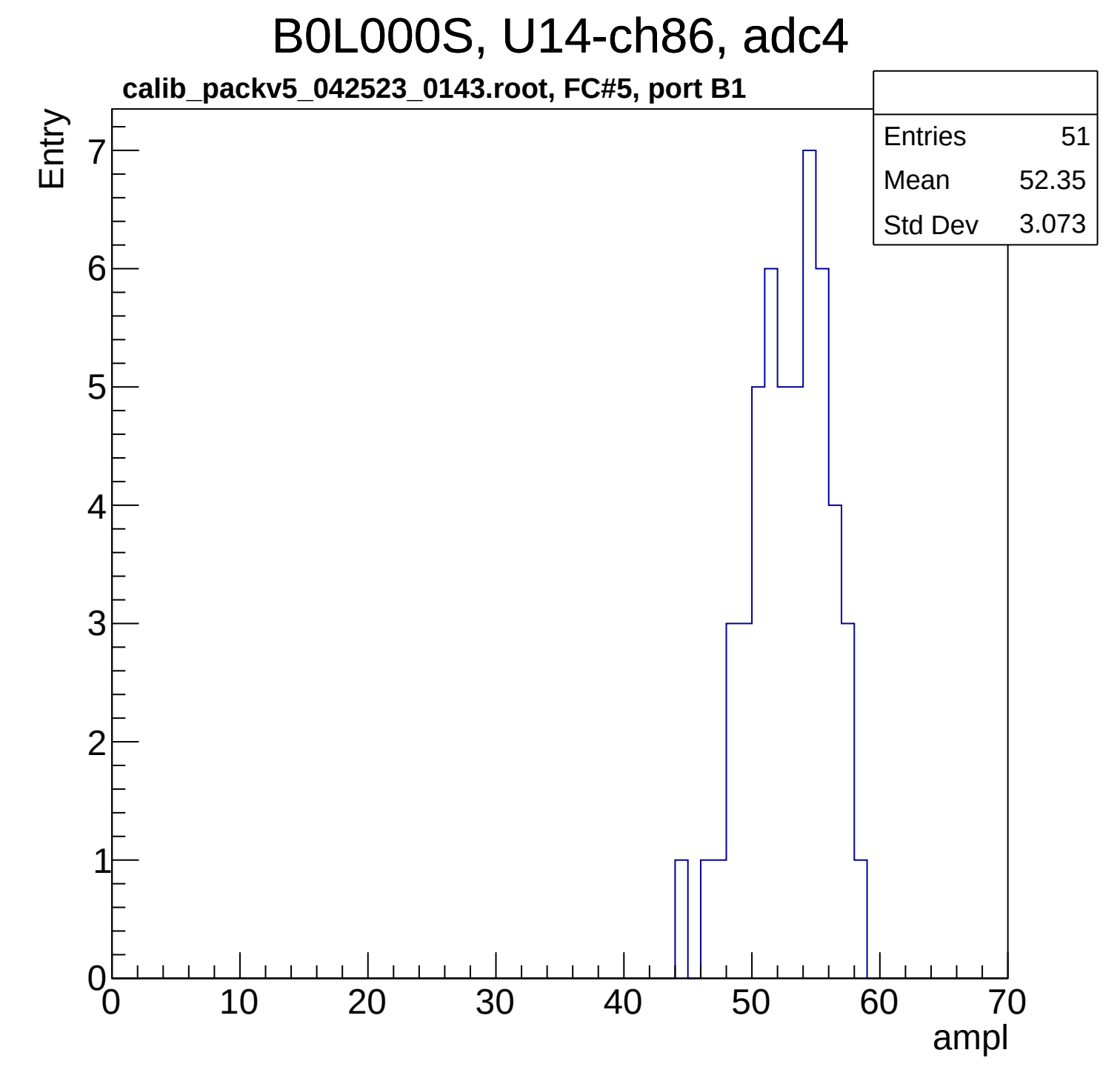
B0L000S, U14-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 52.35
Std Dev 3.073
Entry
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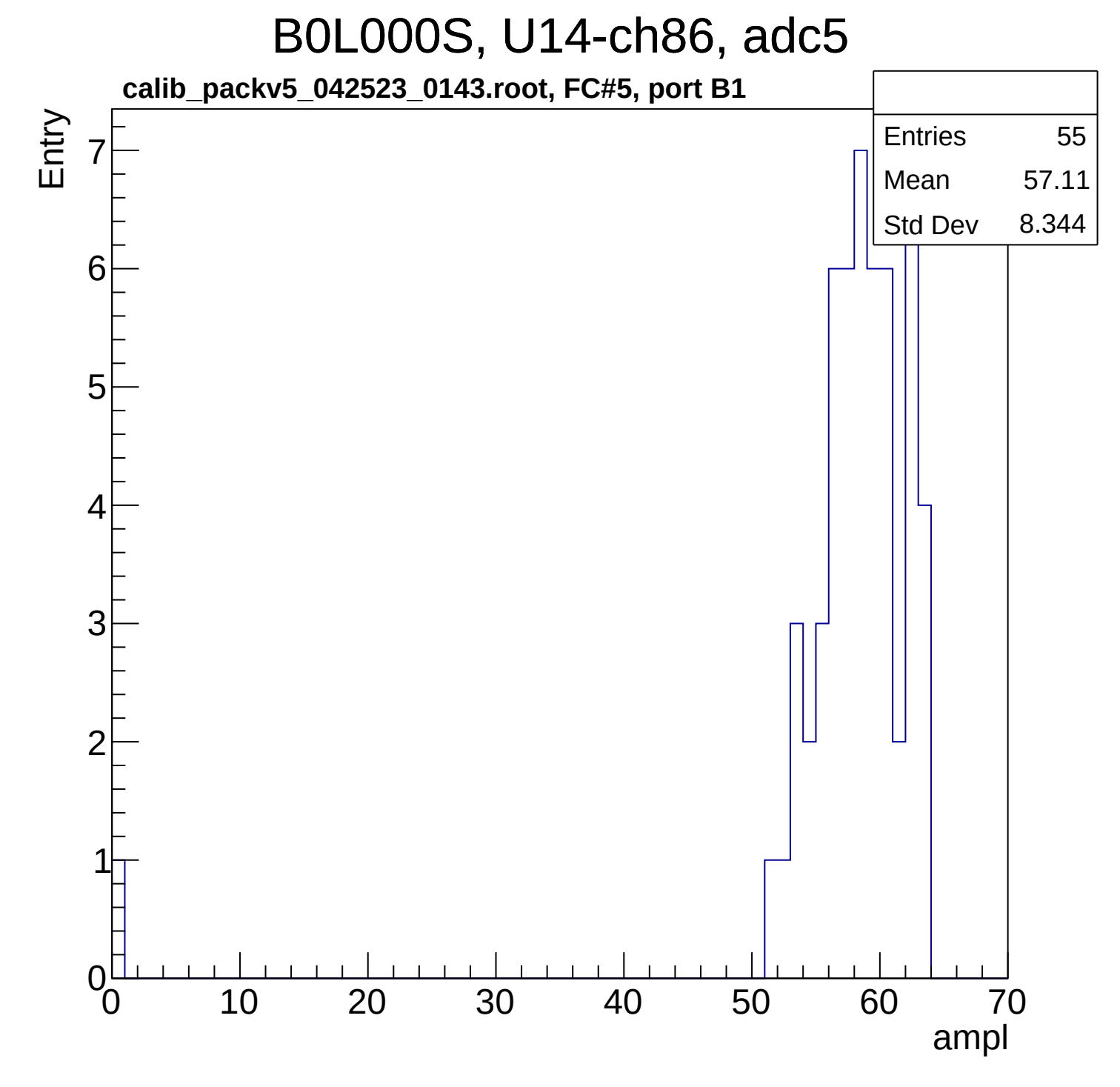
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.11
Std Dev 8.344
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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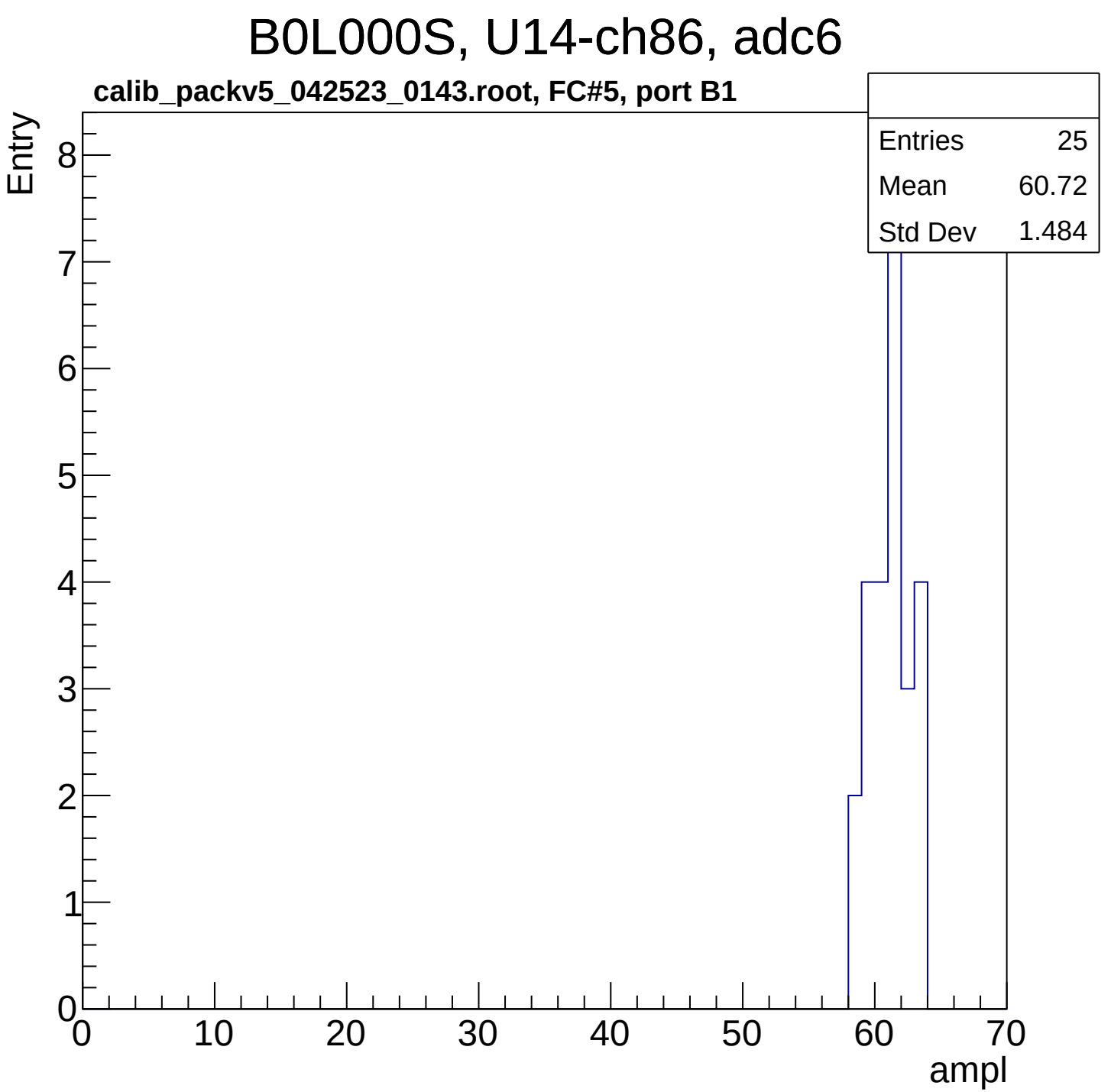
B0L000S, U14-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 60.72
Std Dev 1.484
Entry
ampl
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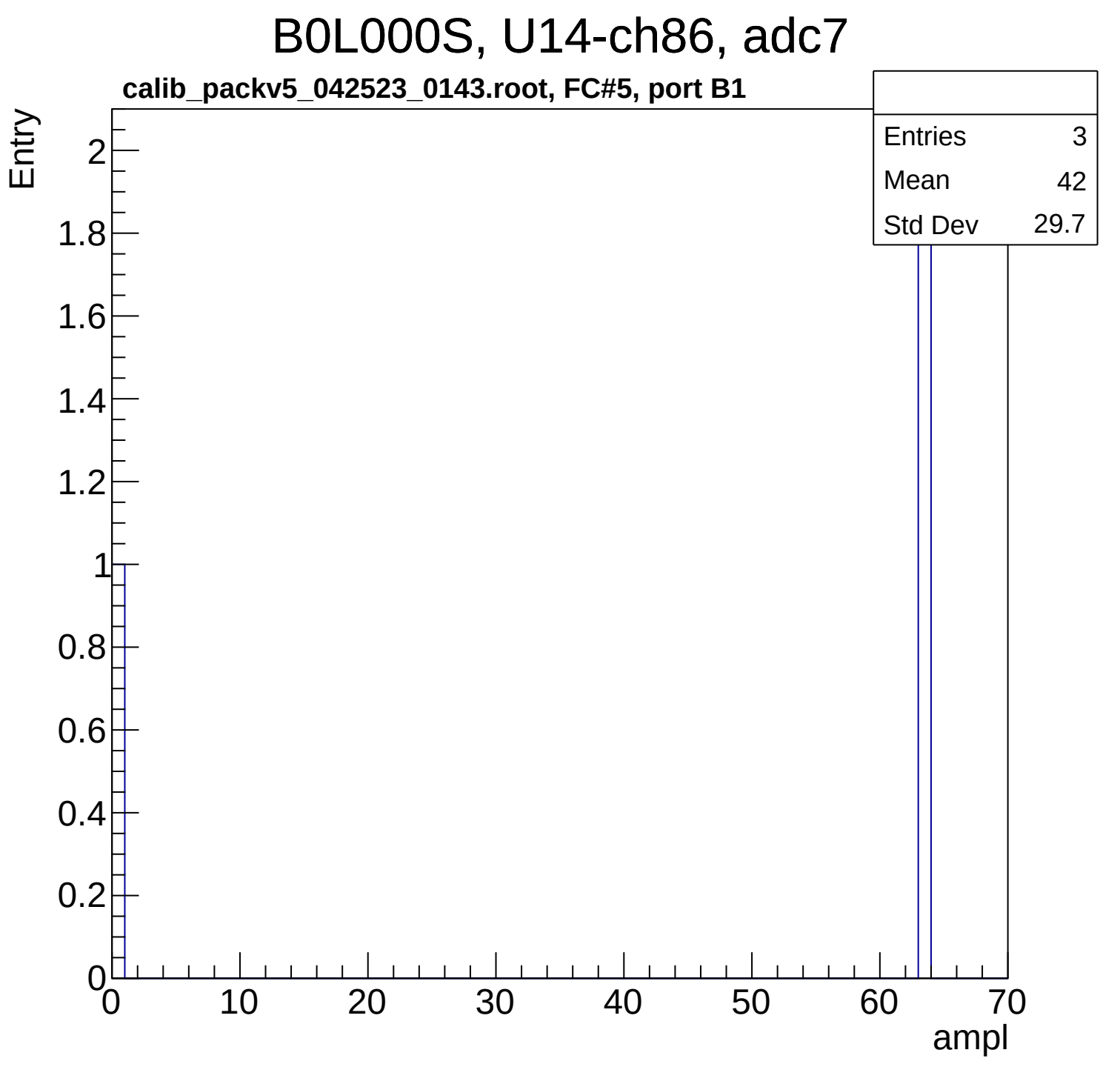
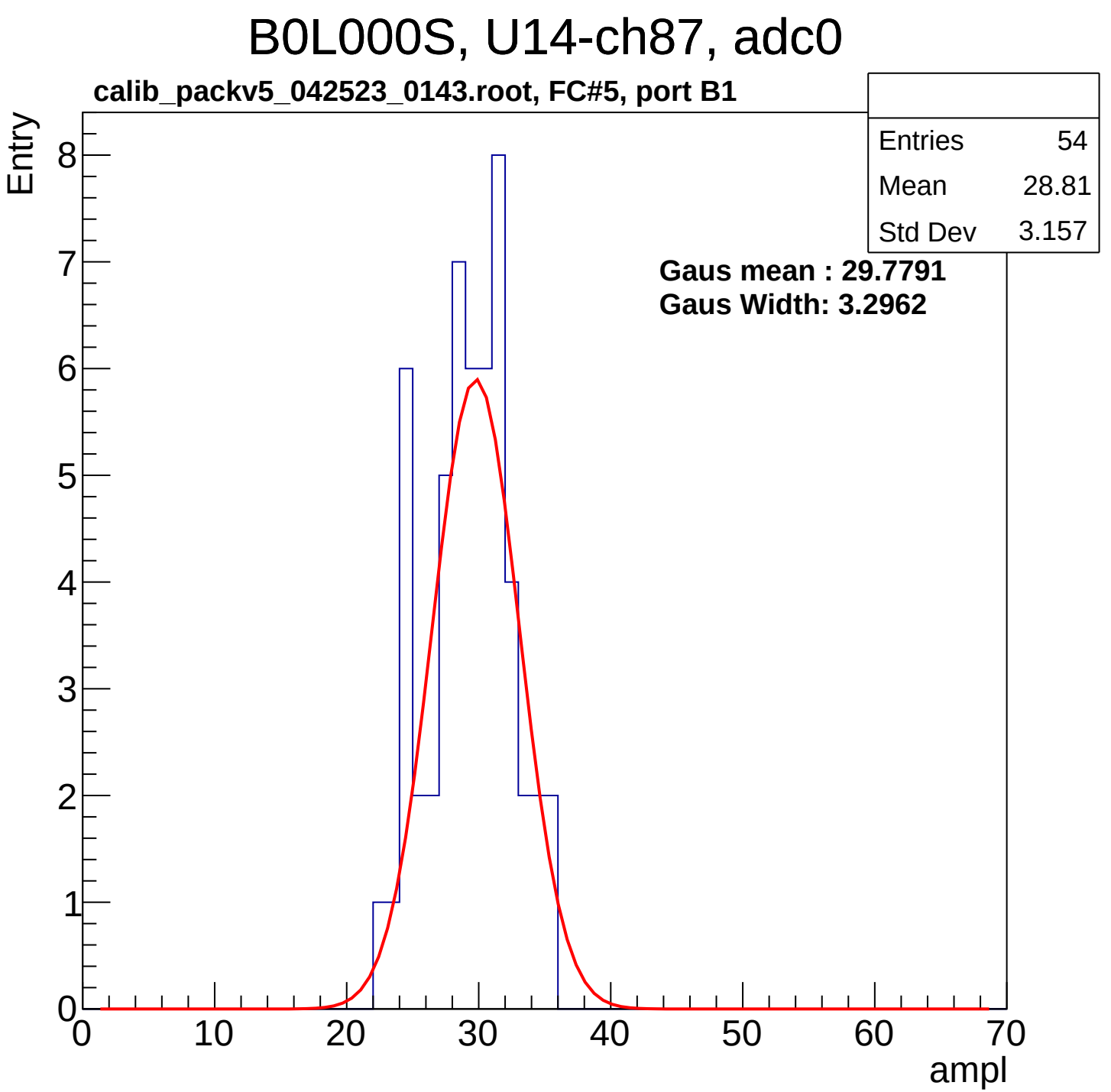
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 28.81
Std Dev 3.157
Gaus mean : 29.7791
Gaus Width: 3.2962
Entry
ampl
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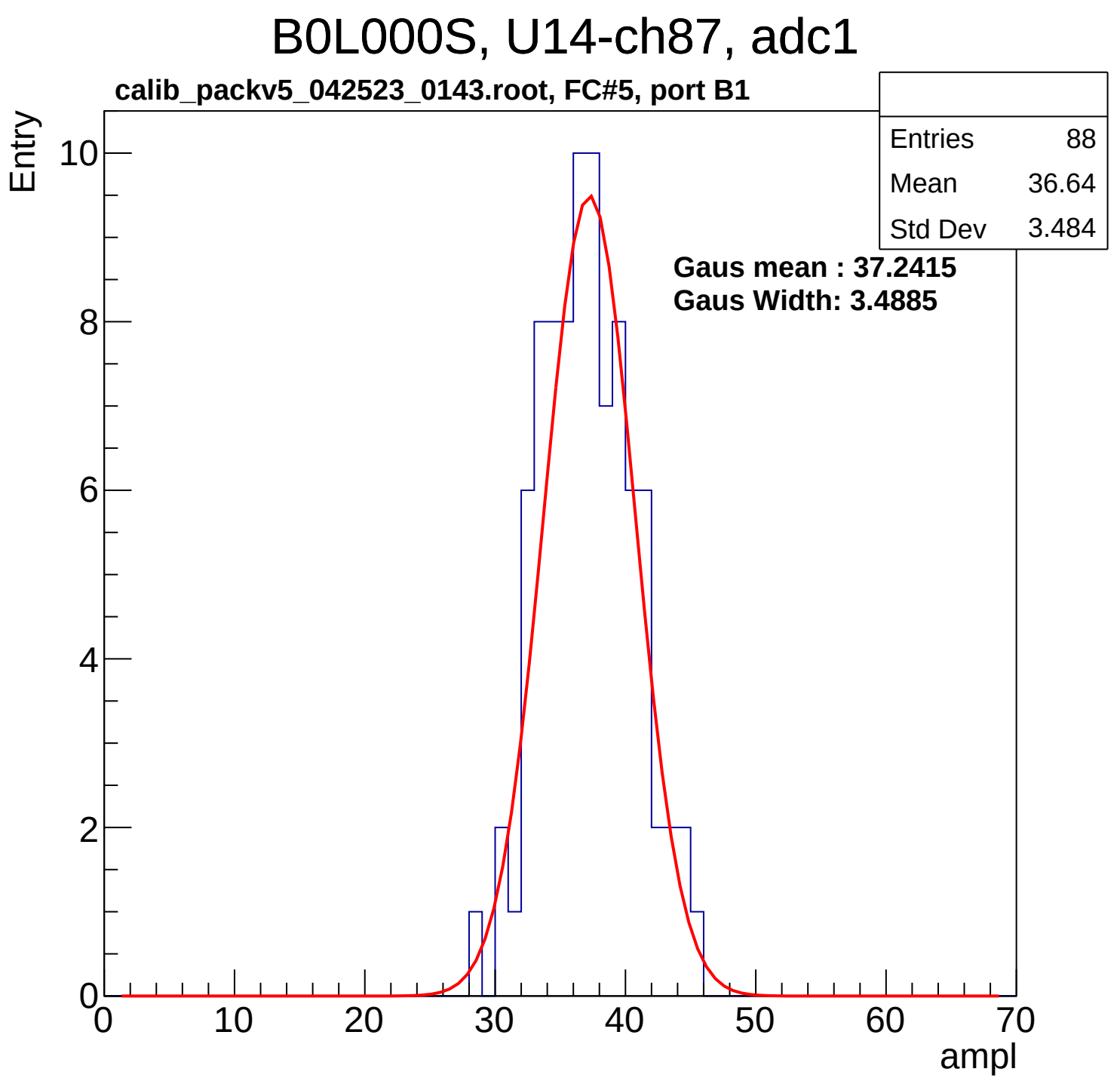
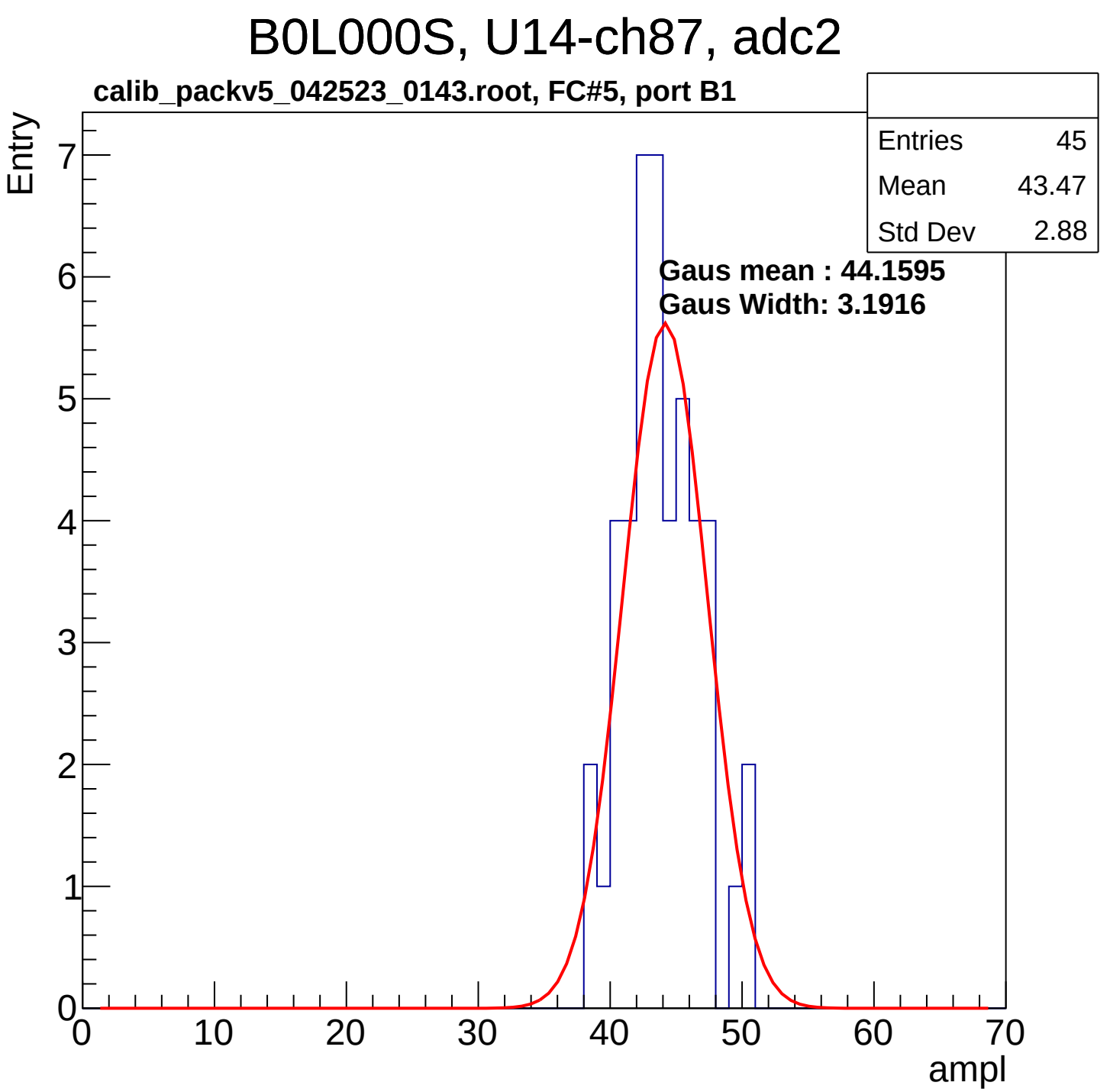
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 36.64
Std Dev 3.484
Gaus mean : 37.2415
Gaus Width: 3.4885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 43.47
Std Dev 2.88
Gaus mean : 44.1595
Gaus Width: 3.1916
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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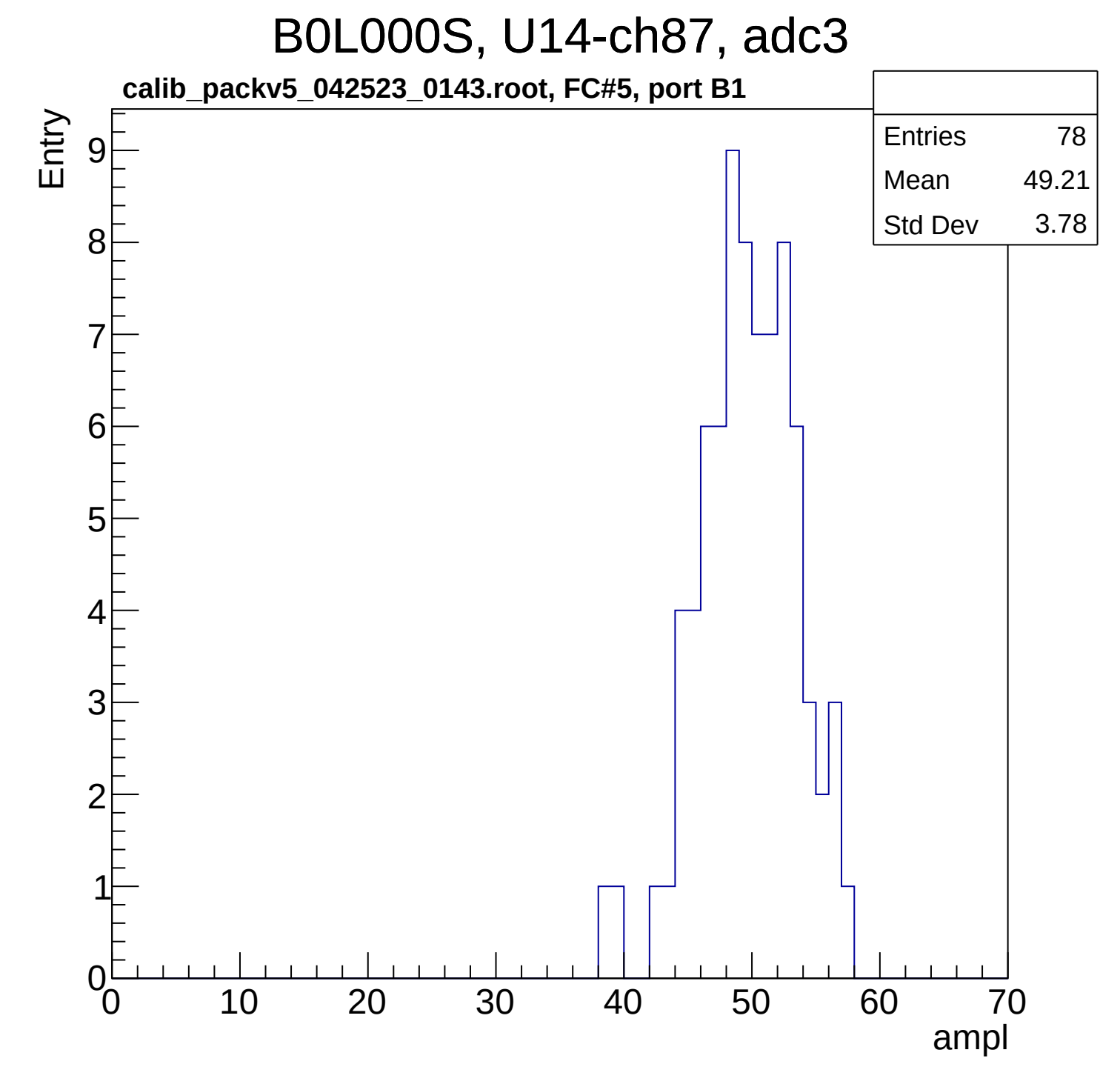
B0L000S, U14-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 49.21
Std Dev 3.78
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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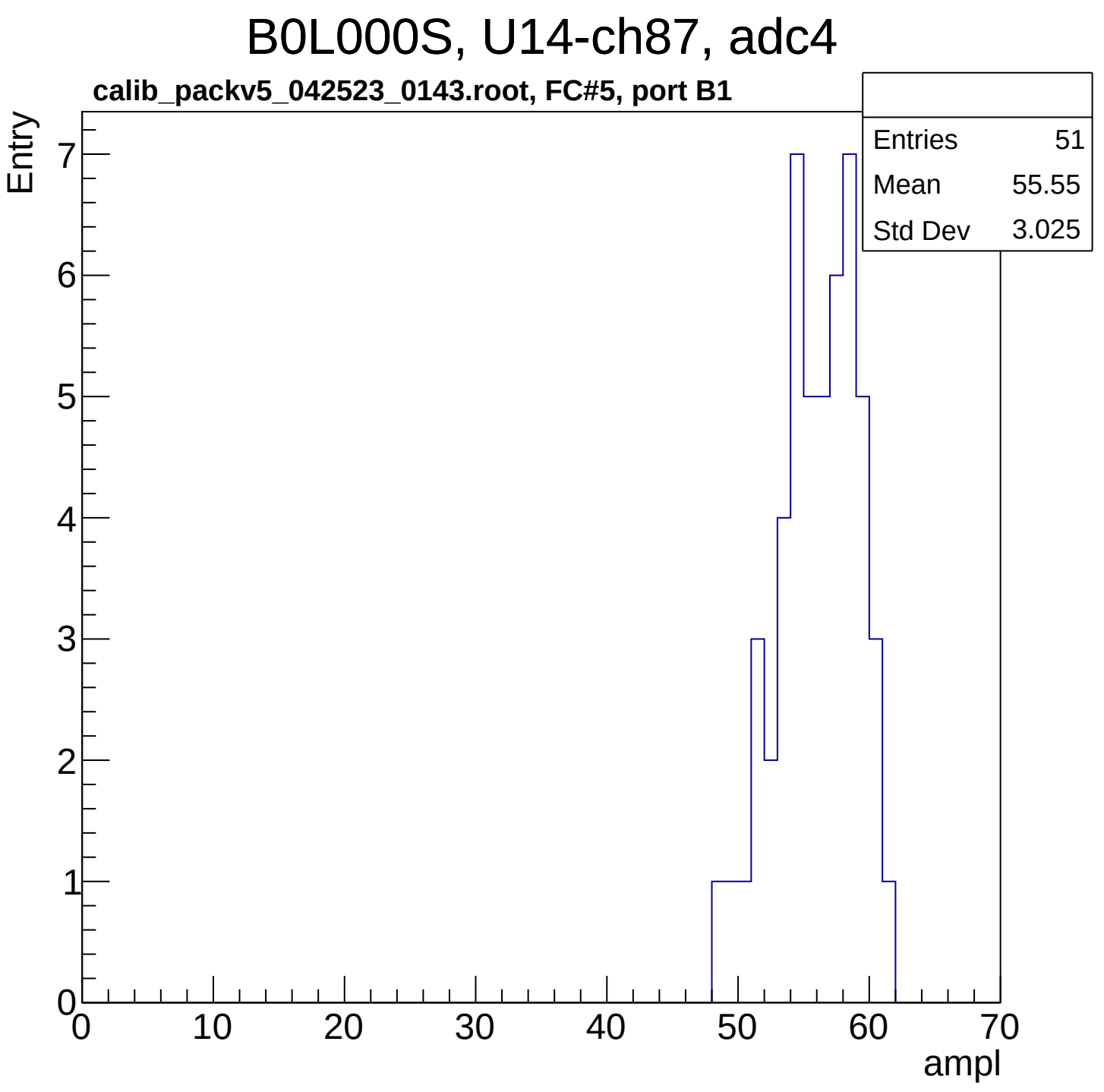
B0L000S, U14-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 55.55
Std Dev 3.025
Entry
ampl
0
1
2
3
4
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6
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0
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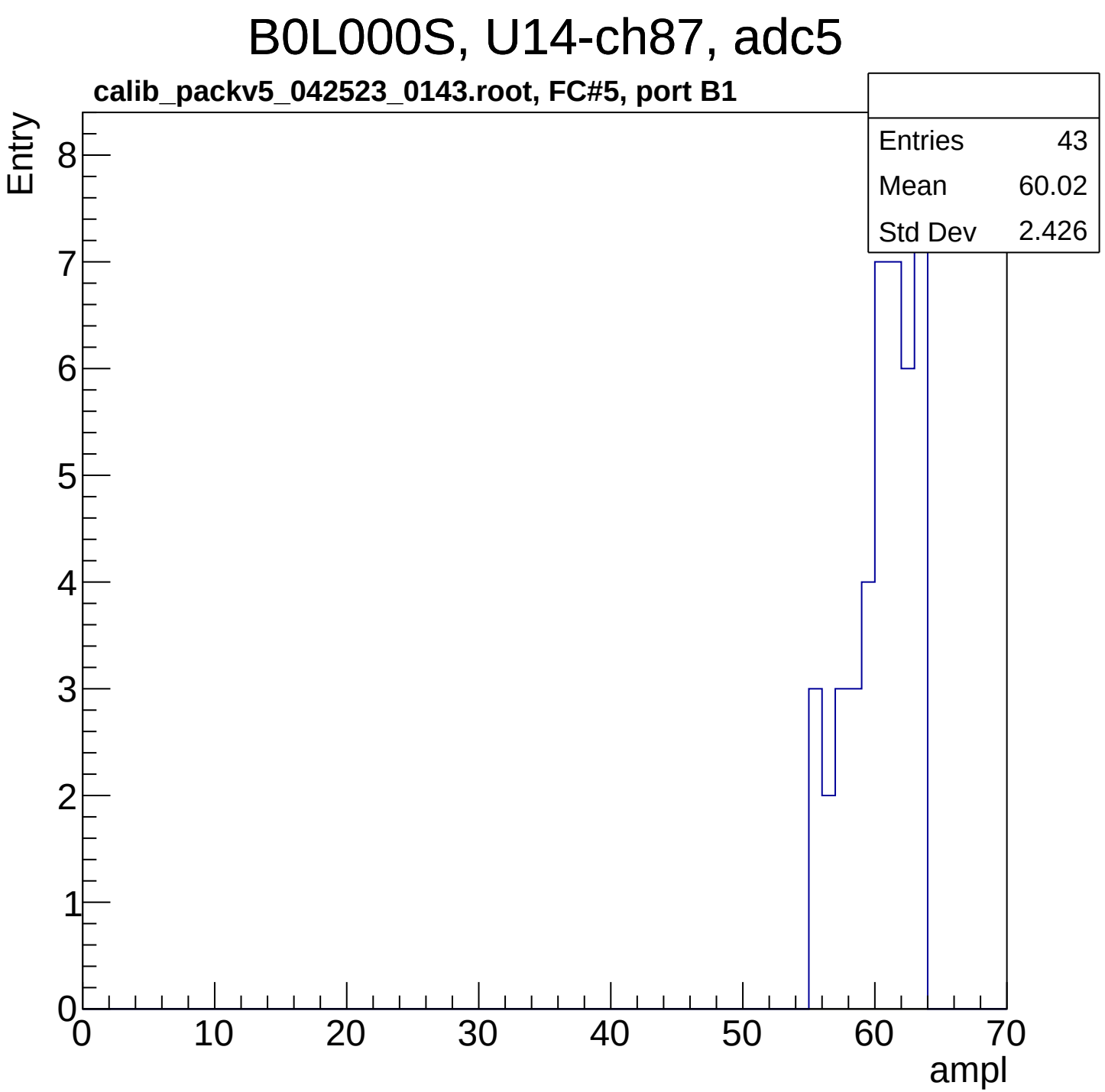
20
30
40
50
60
70


B0L000S, U14-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.02
Std Dev 2.426
Entry
ampl
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4
5
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7
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0
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60
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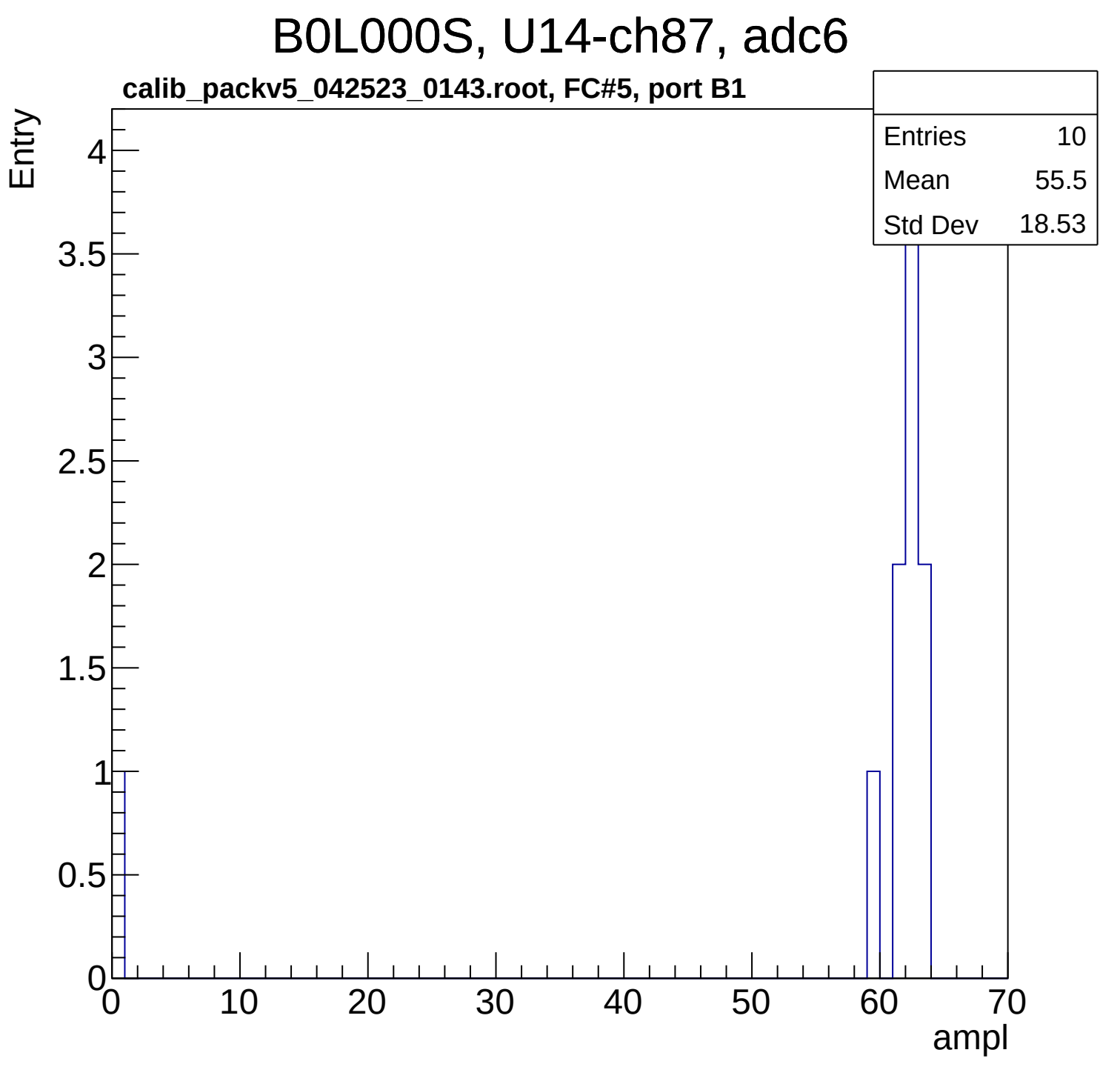
B0L000S, U14-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.5
Std Dev 18.53
Entry
ampl
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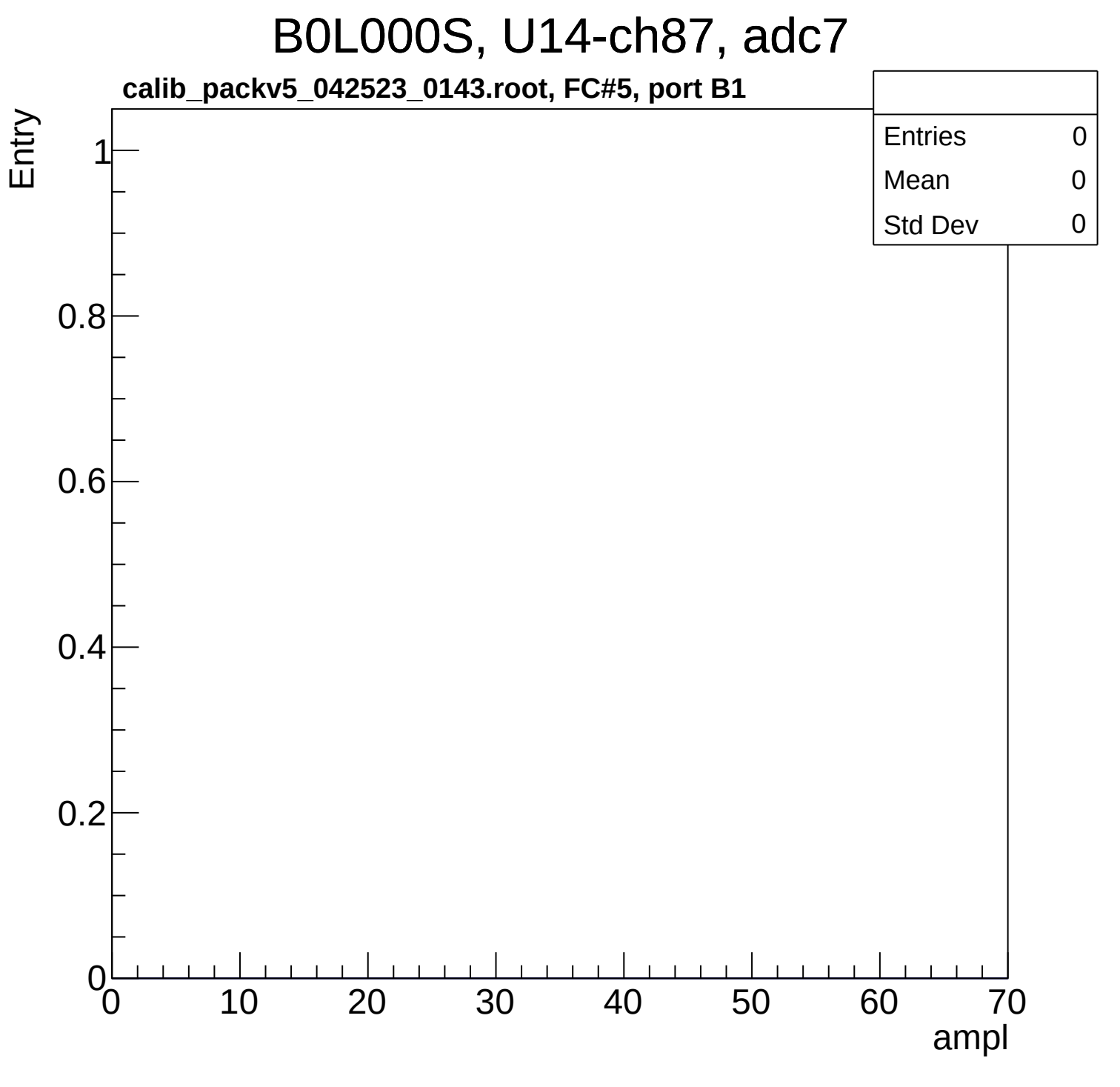
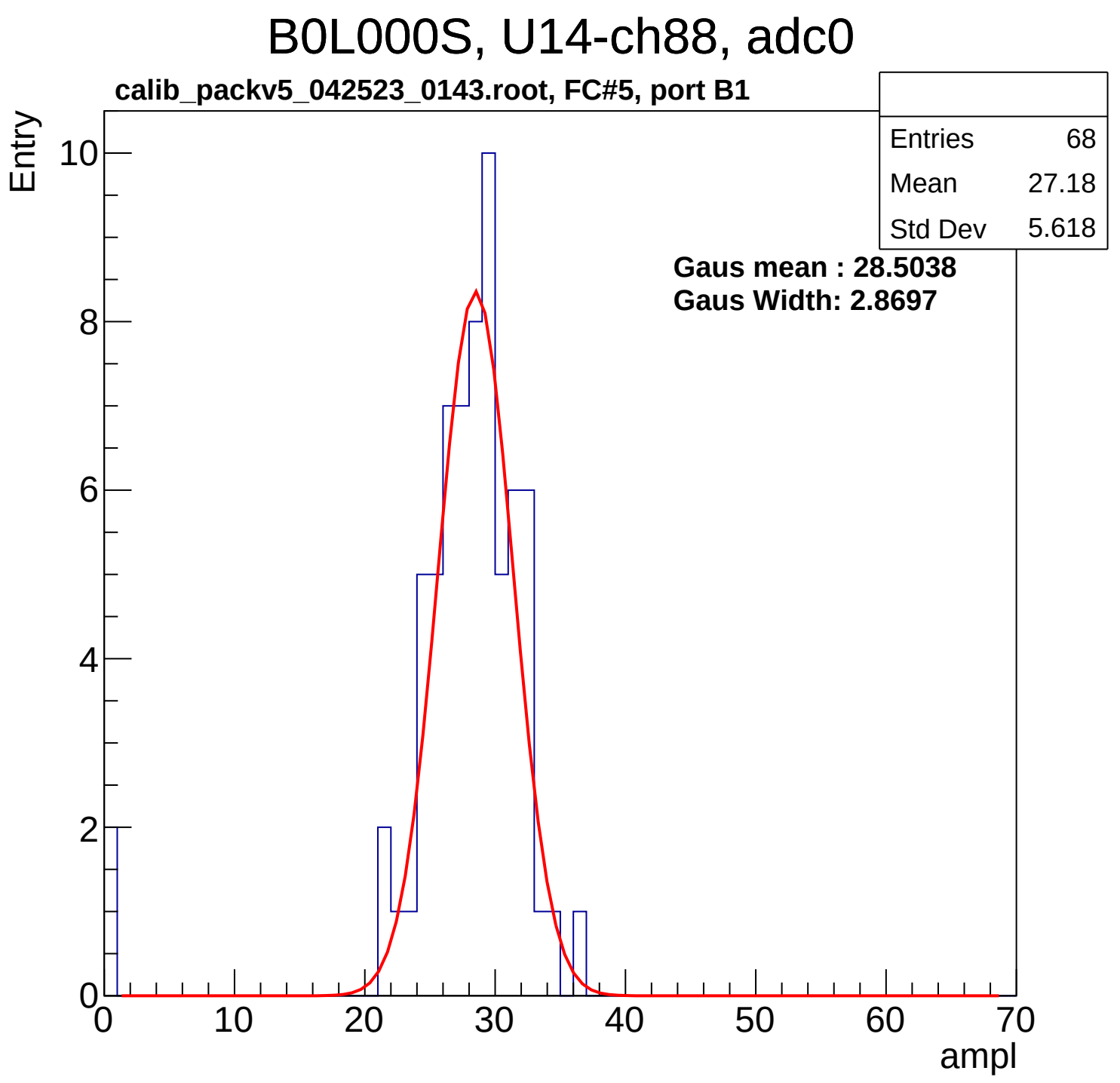
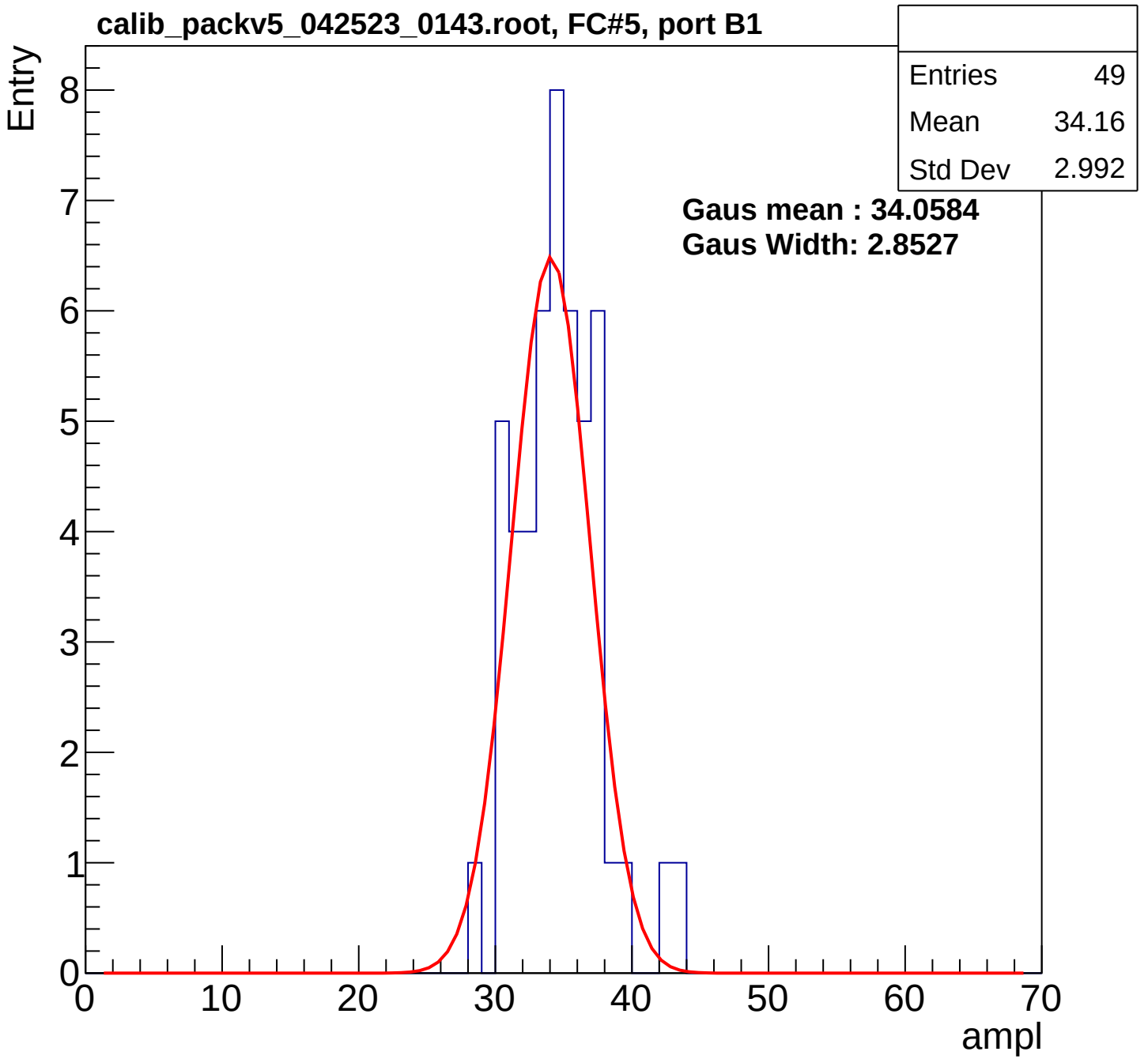
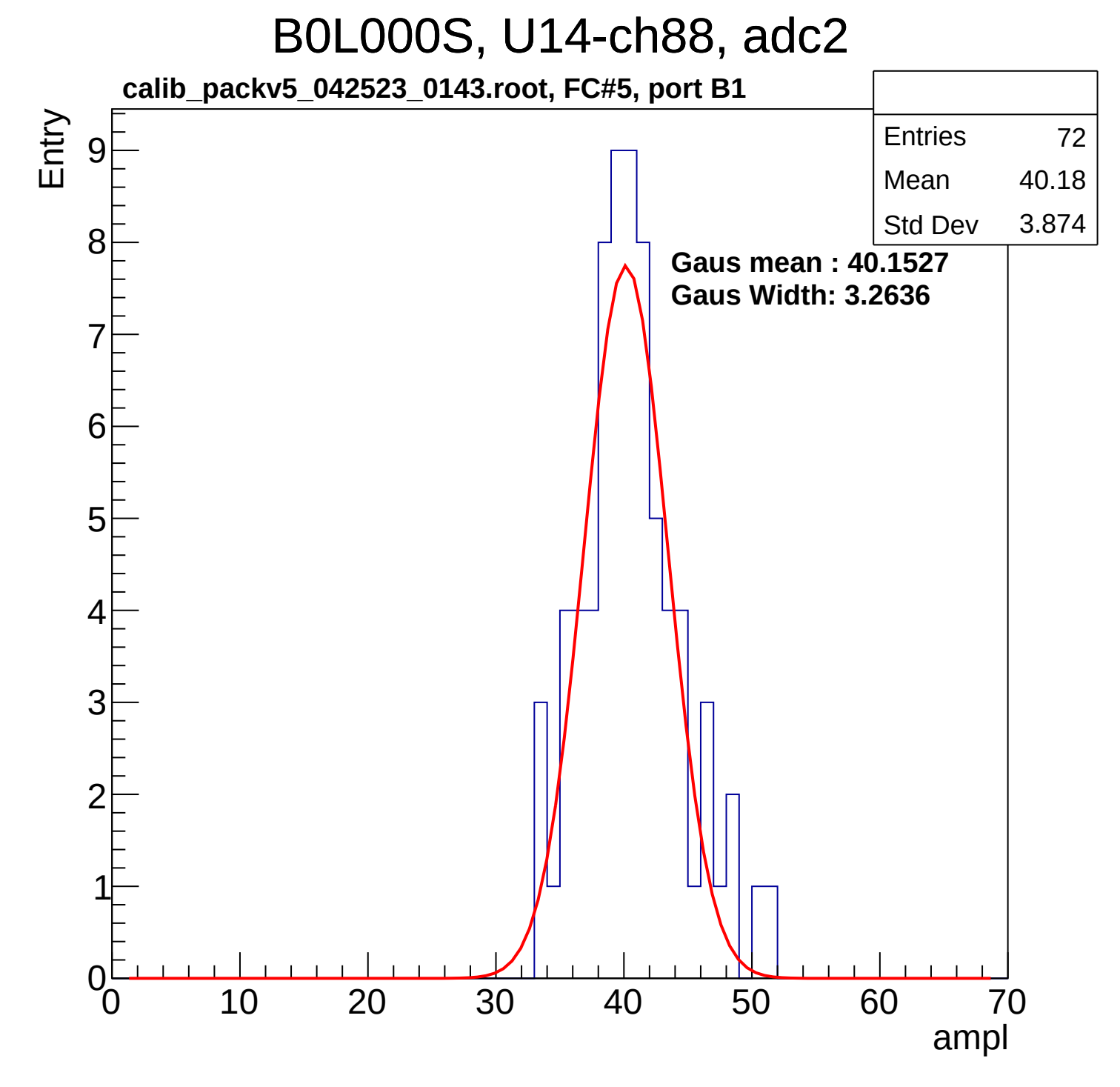
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 27.18
Std Dev 5.618
Gaus mean : 28.5038
Gaus Width: 2.8697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 34.16
Std Dev 2.992
Gaus mean : 34.0584
Gaus Width: 2.8527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 40.18
Std Dev 3.874
Gaus mean : 40.1527
Gaus Width: 3.2636
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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60
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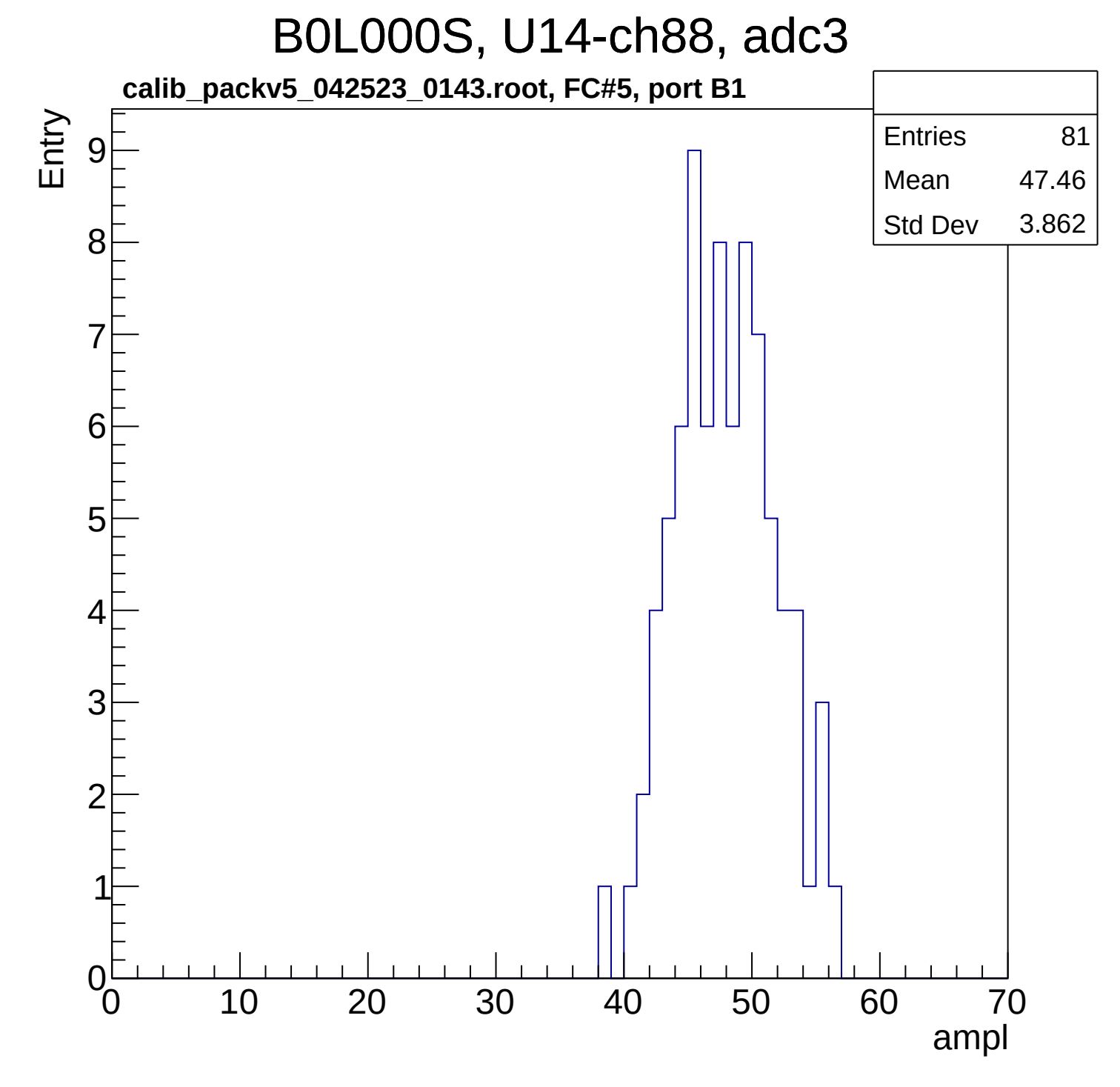
B0L000S, U14-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 47.46
Std Dev 3.862
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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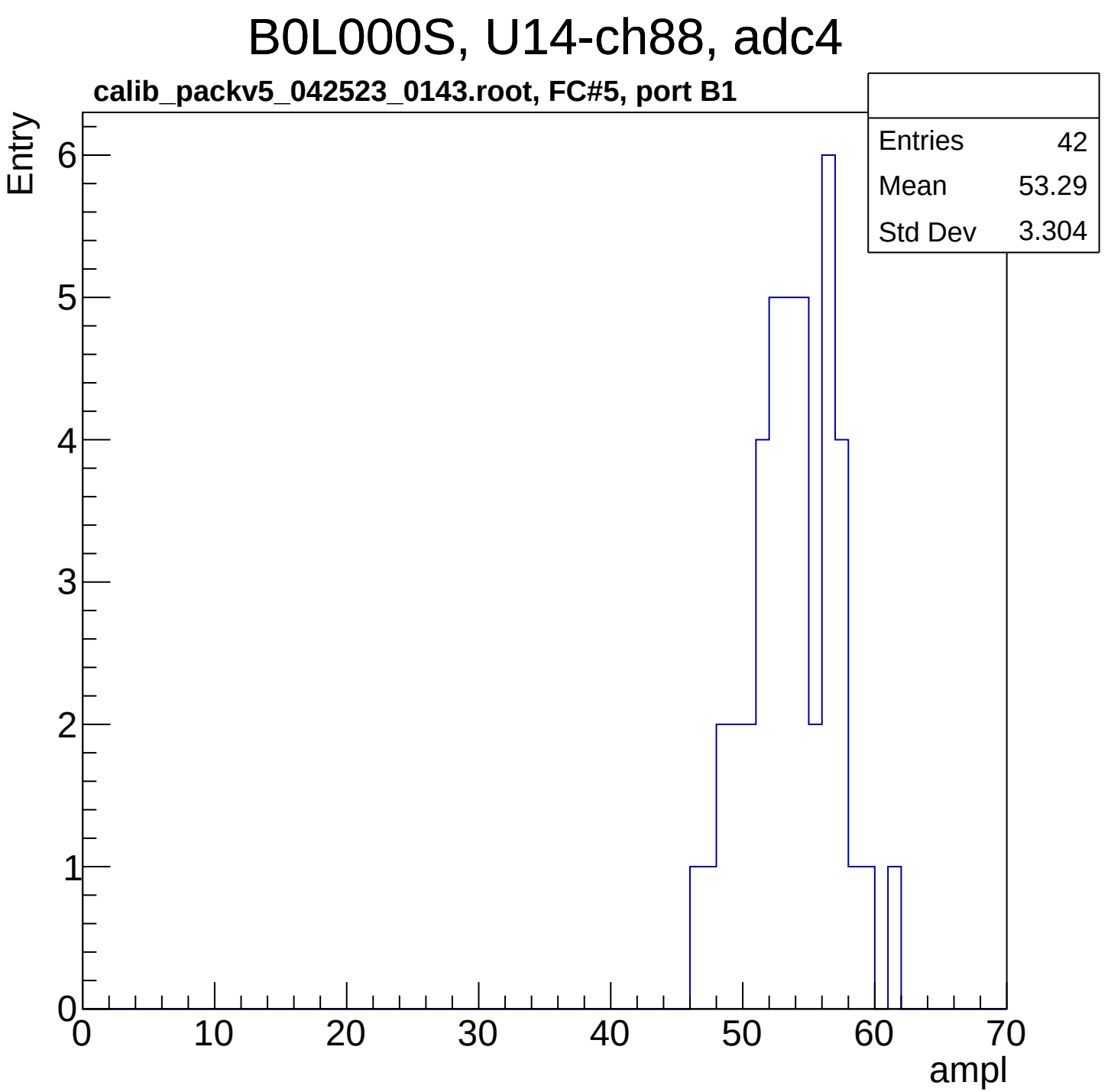
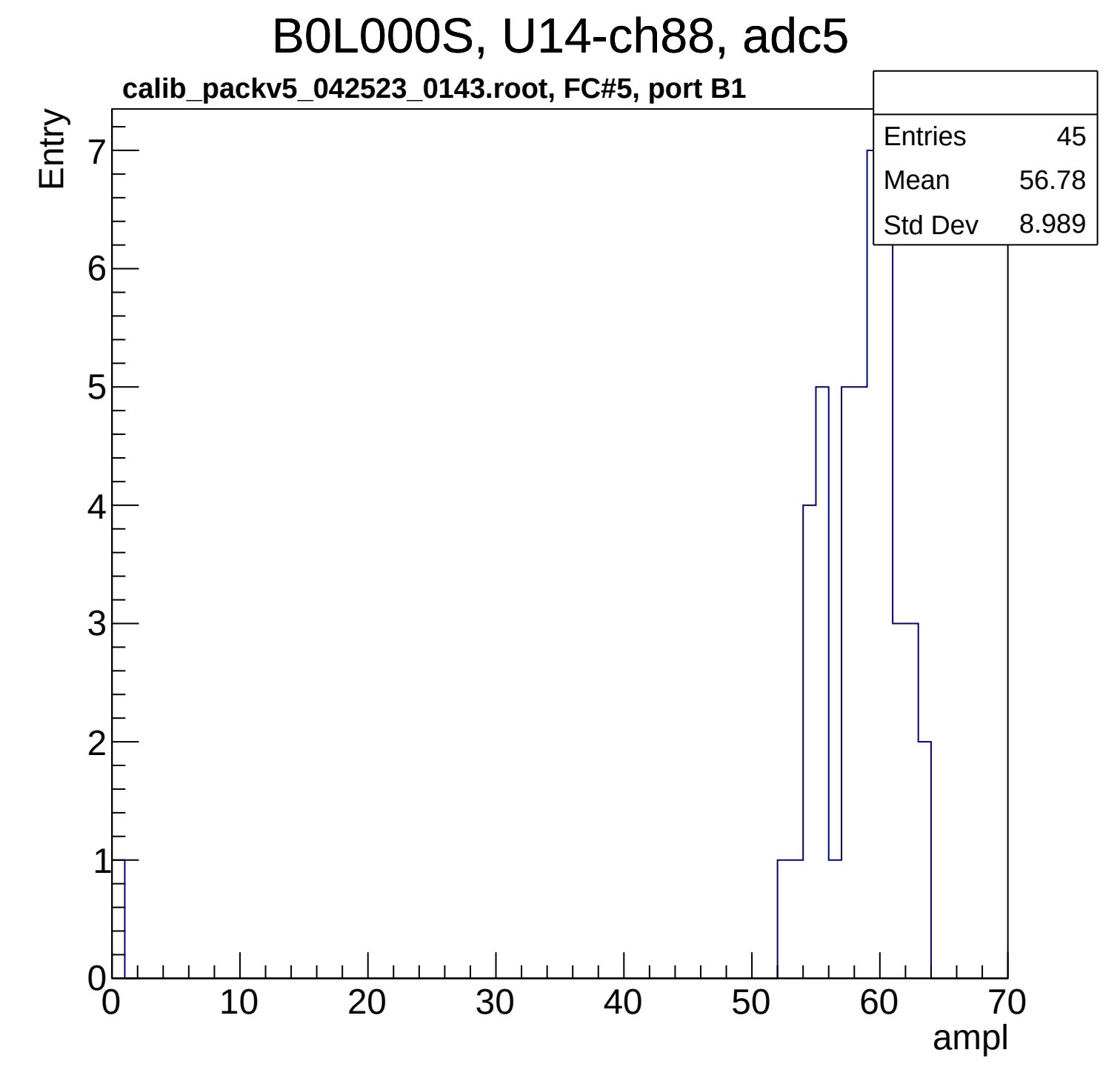

B0L000S, U14-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 53.29
Std Dev 3.304
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 56.78
Std Dev 8.989
Entry
ampl
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2
3
4
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7
0
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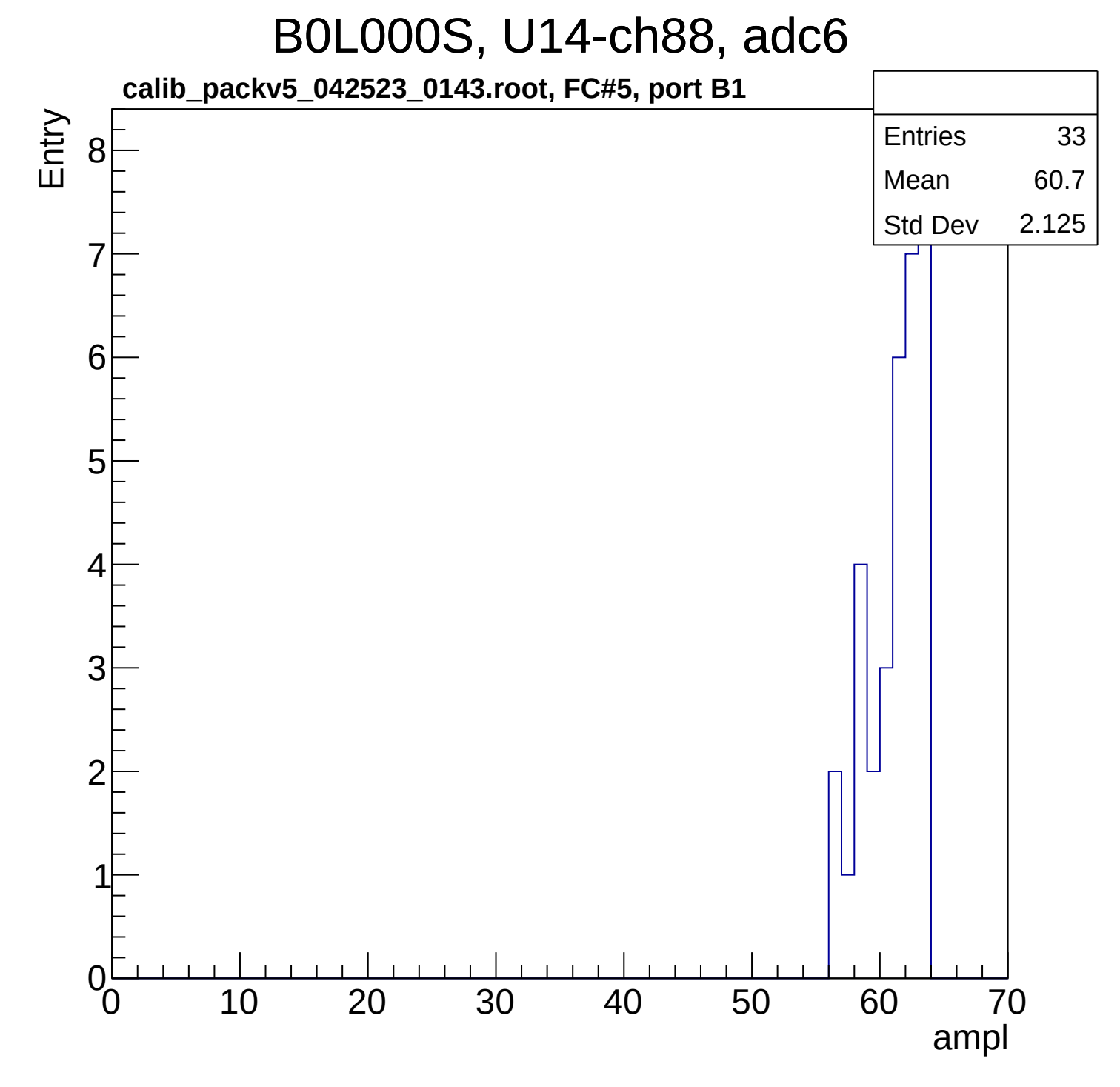
B0L000S, U14-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.7
Std Dev 2.125
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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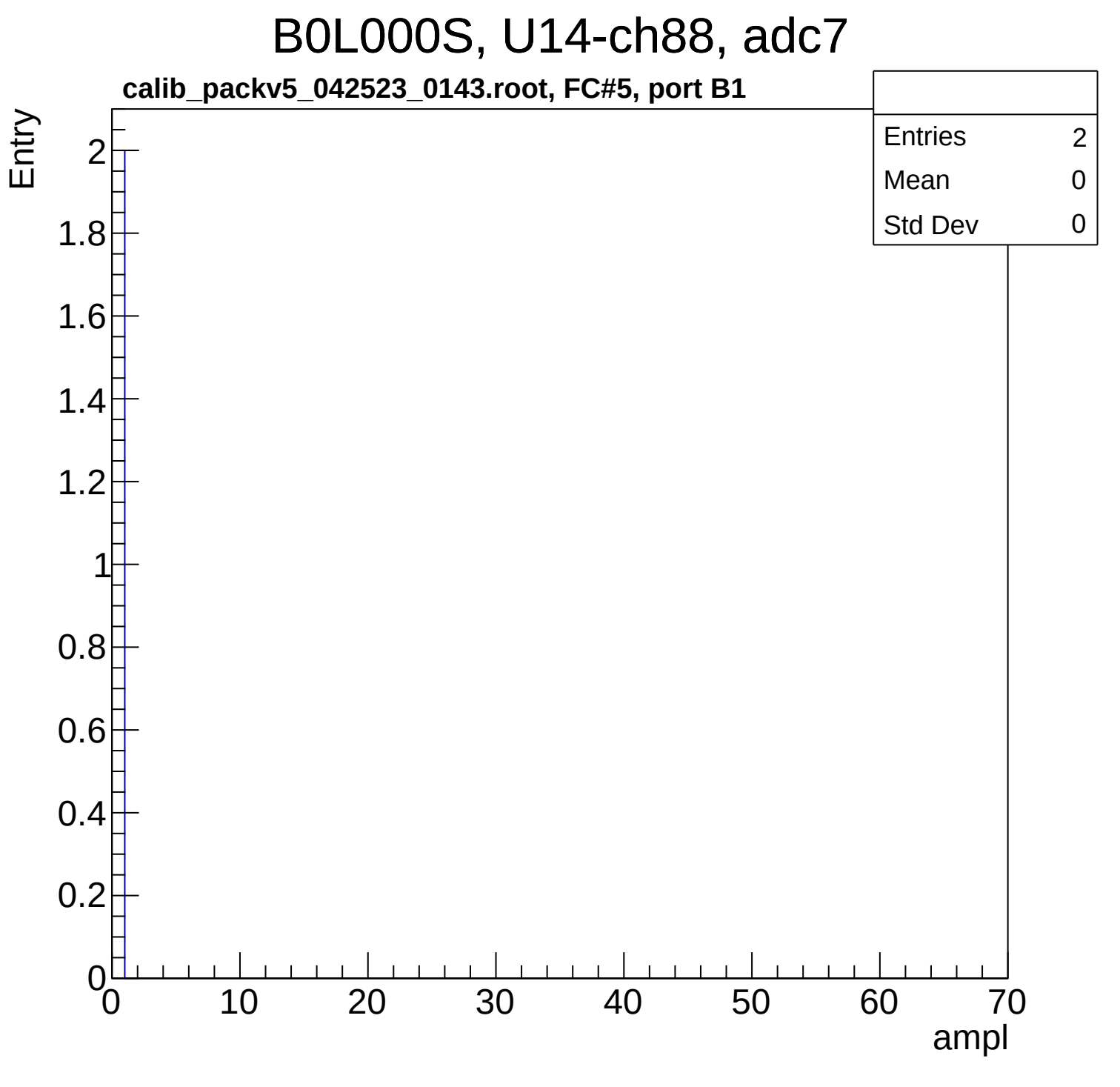
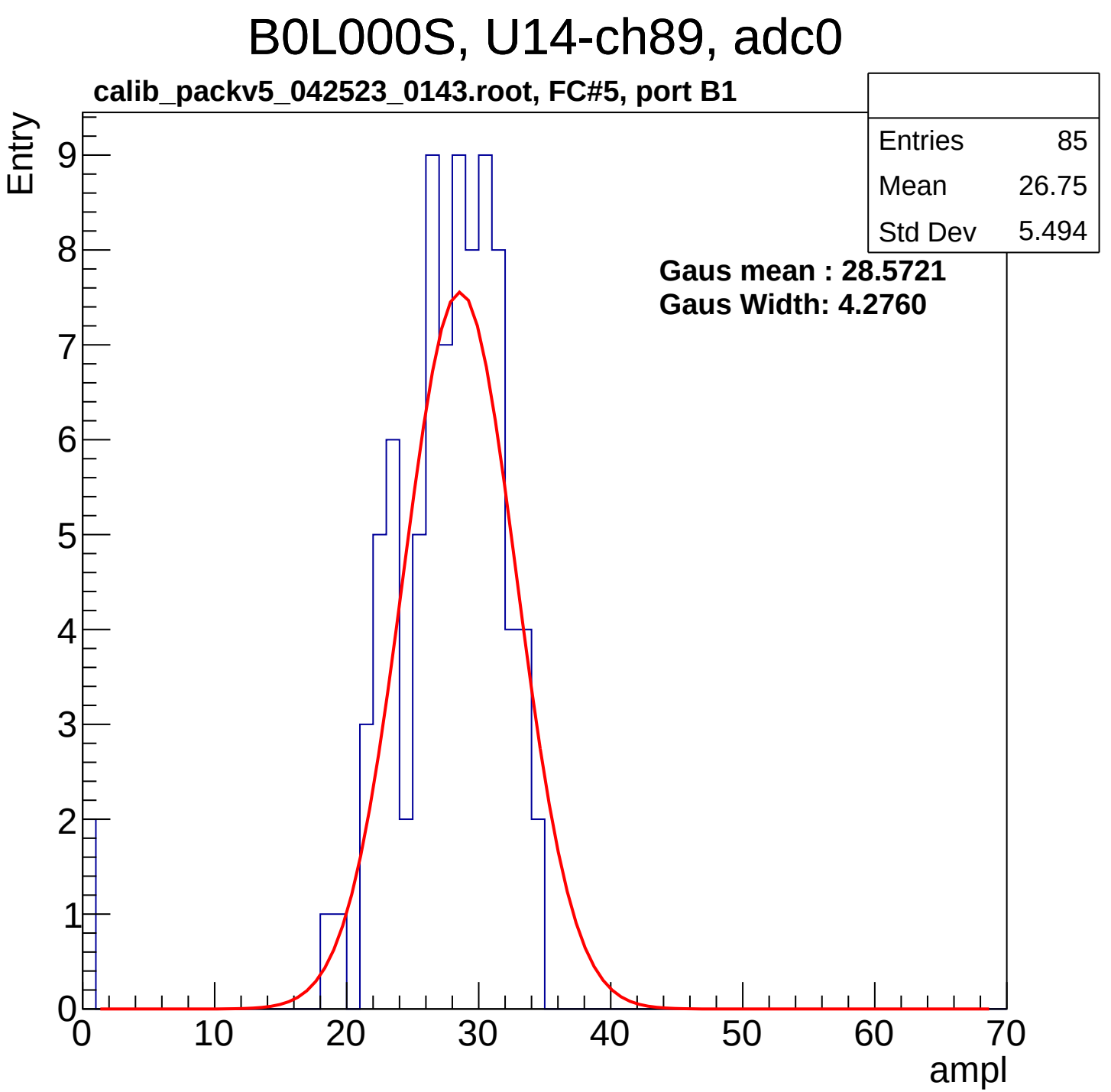

B0L000S, U14-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 26.75
Std Dev 5.494
Gaus mean : 28.5721
Gaus Width: 4.2760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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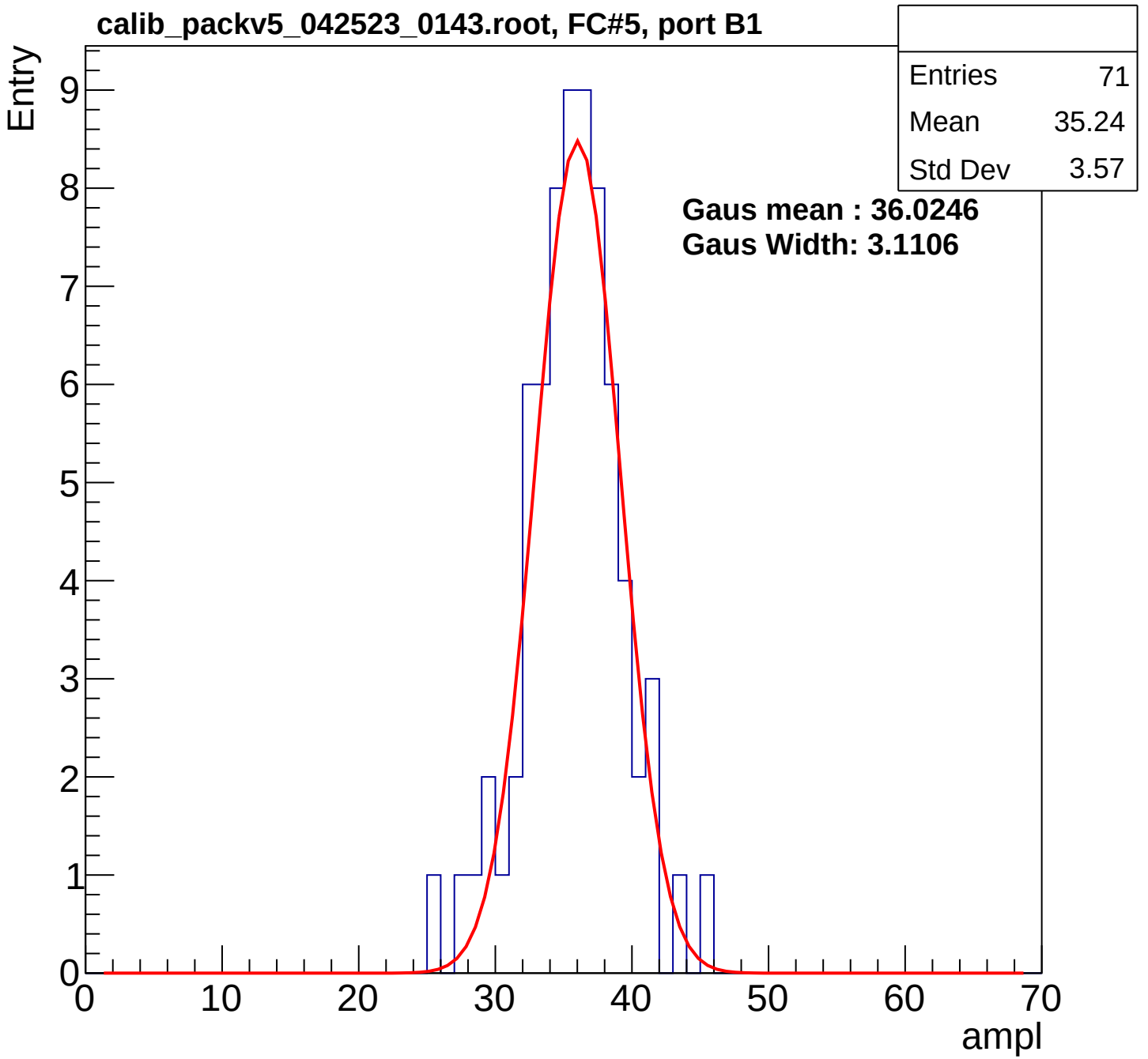
60
70


B0L000S, U14-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 35.24
Std Dev 3.57
Gaus mean : 36.0246
Gaus Width: 3.1106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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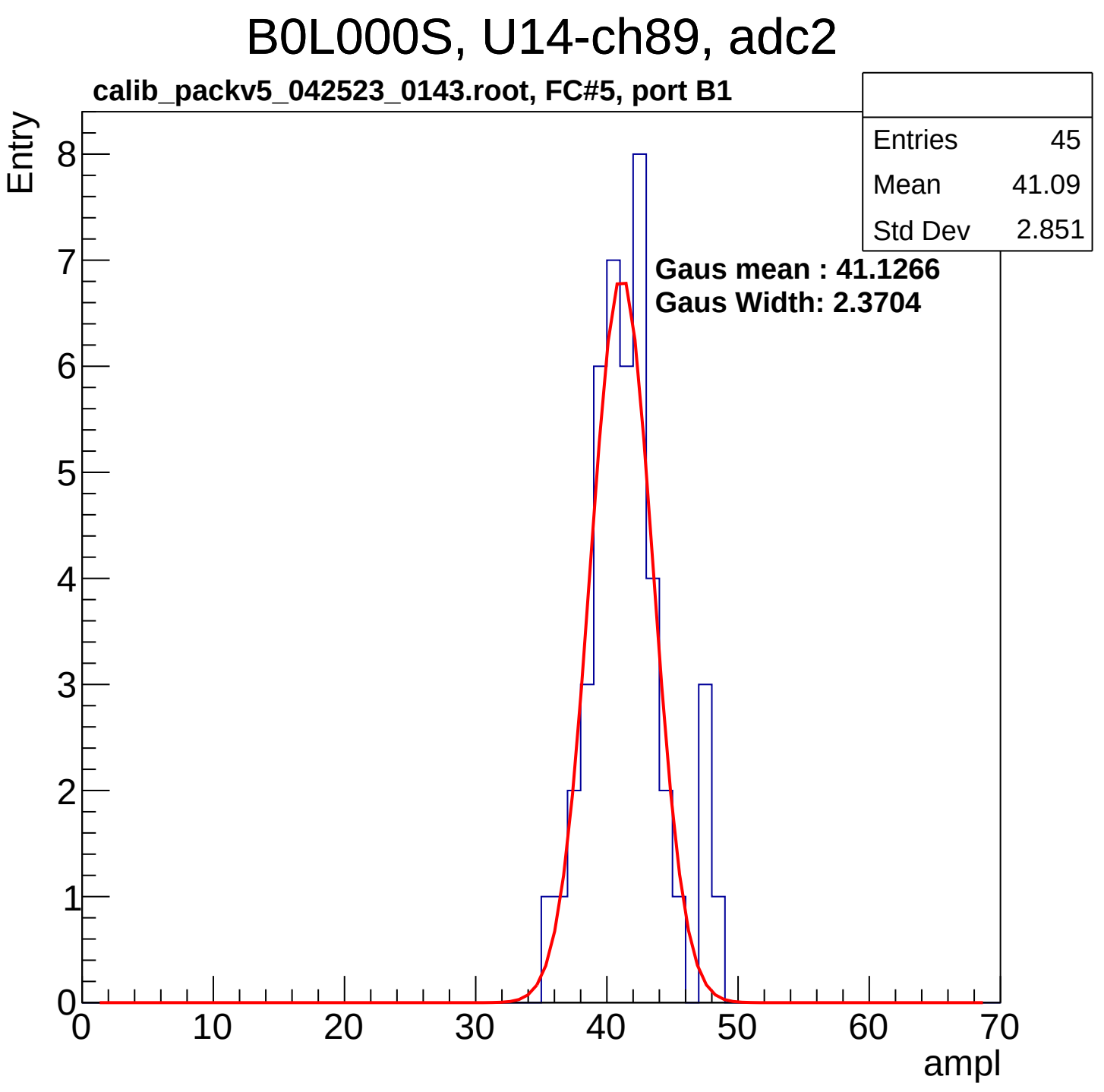
B0L000S, U14-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 41.09
Std Dev 2.851
Gaus mean : 41.1266
Gaus Width: 2.3704
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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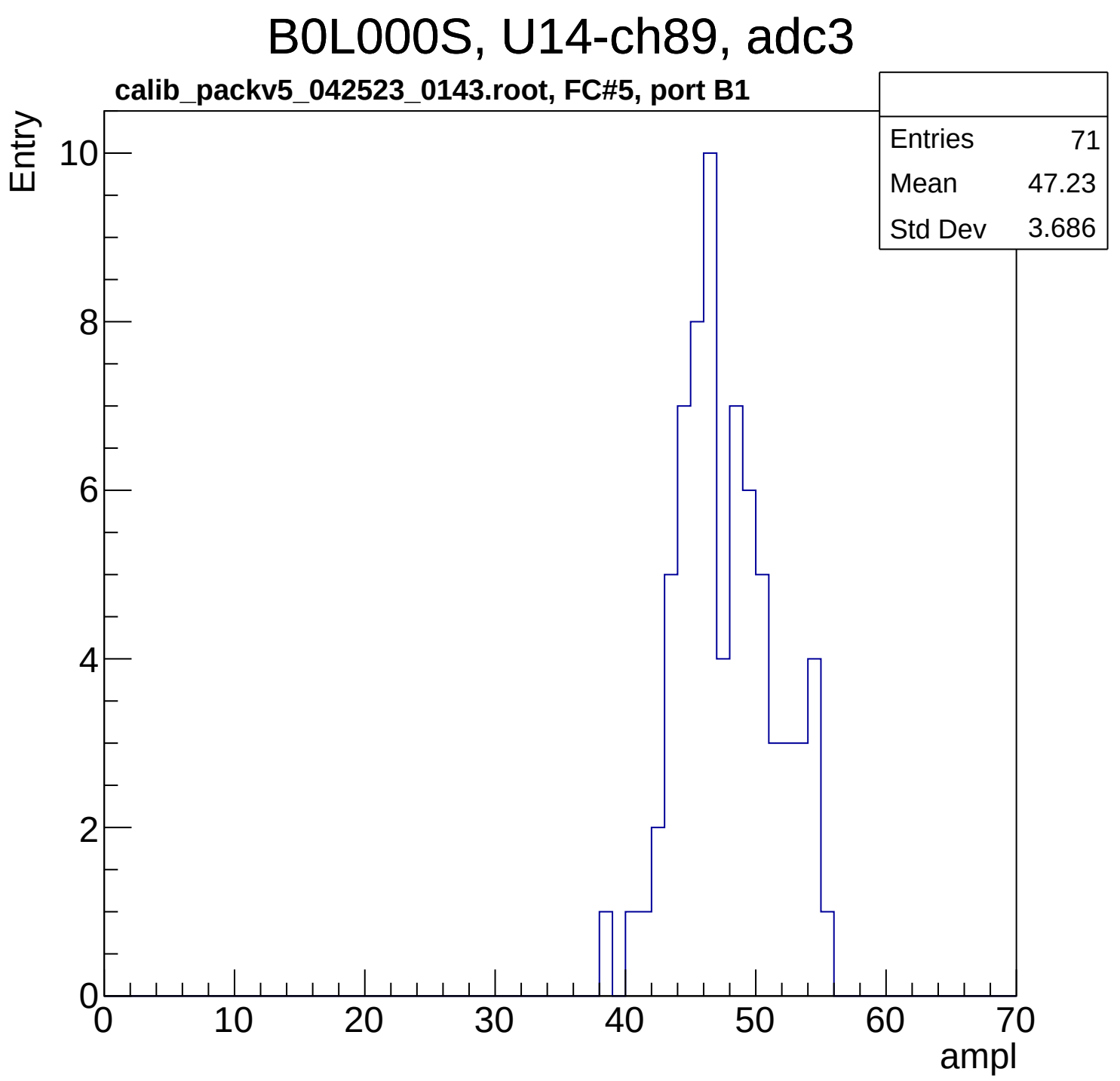
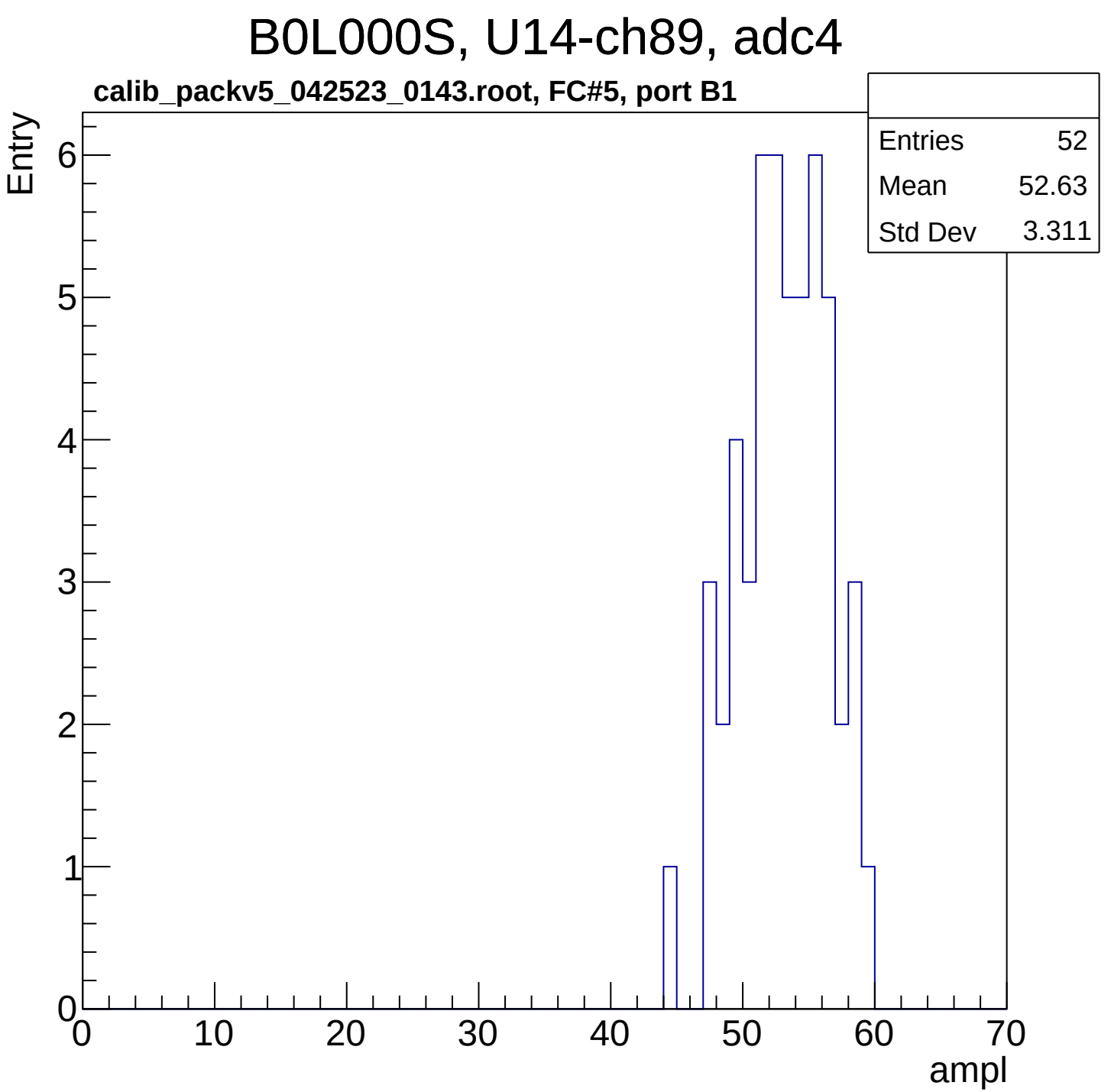
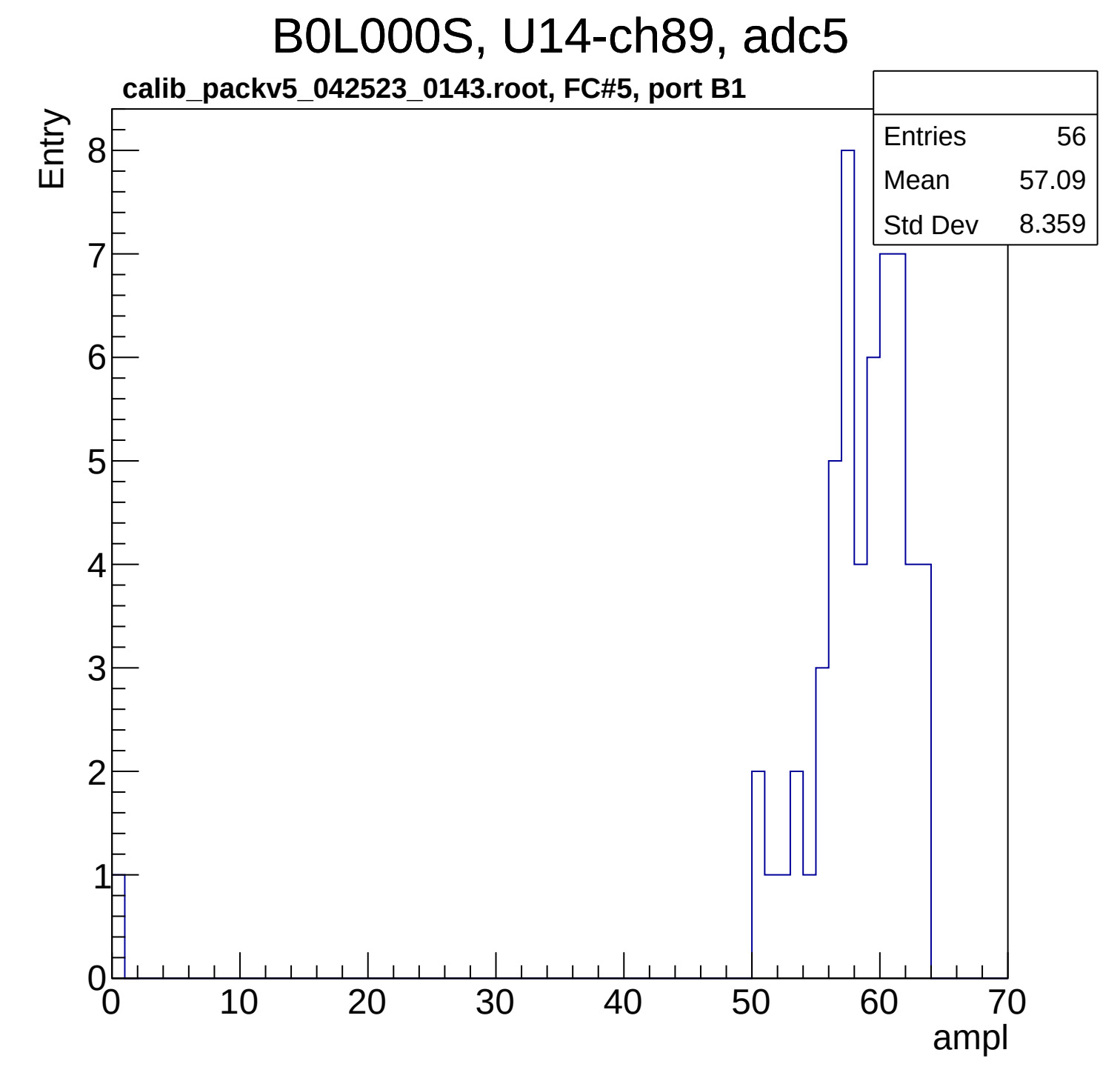

B0L000S, U14-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 47.23
Std Dev 3.686
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 52.63
Std Dev 3.311
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.09
Std Dev 8.359
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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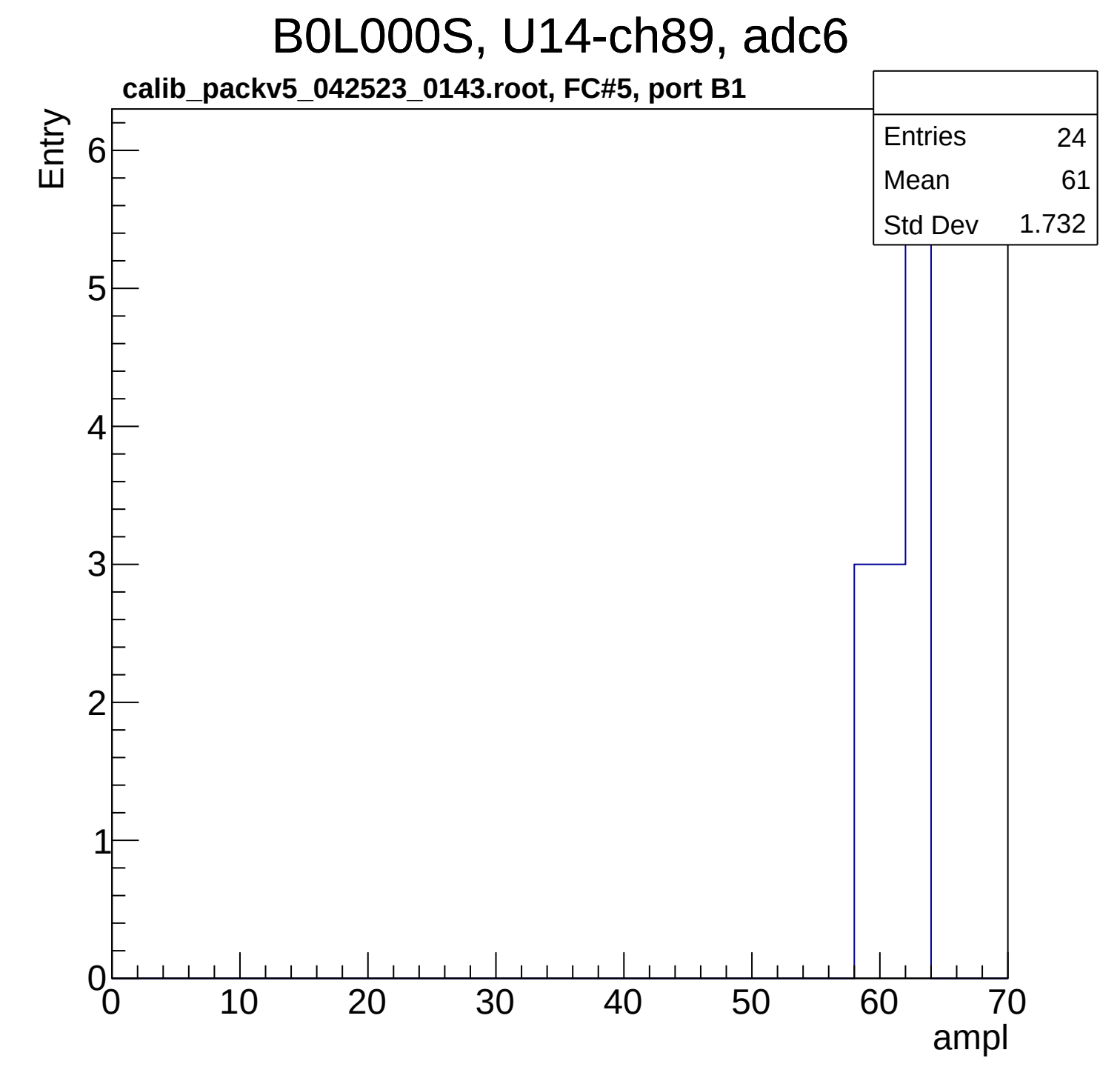
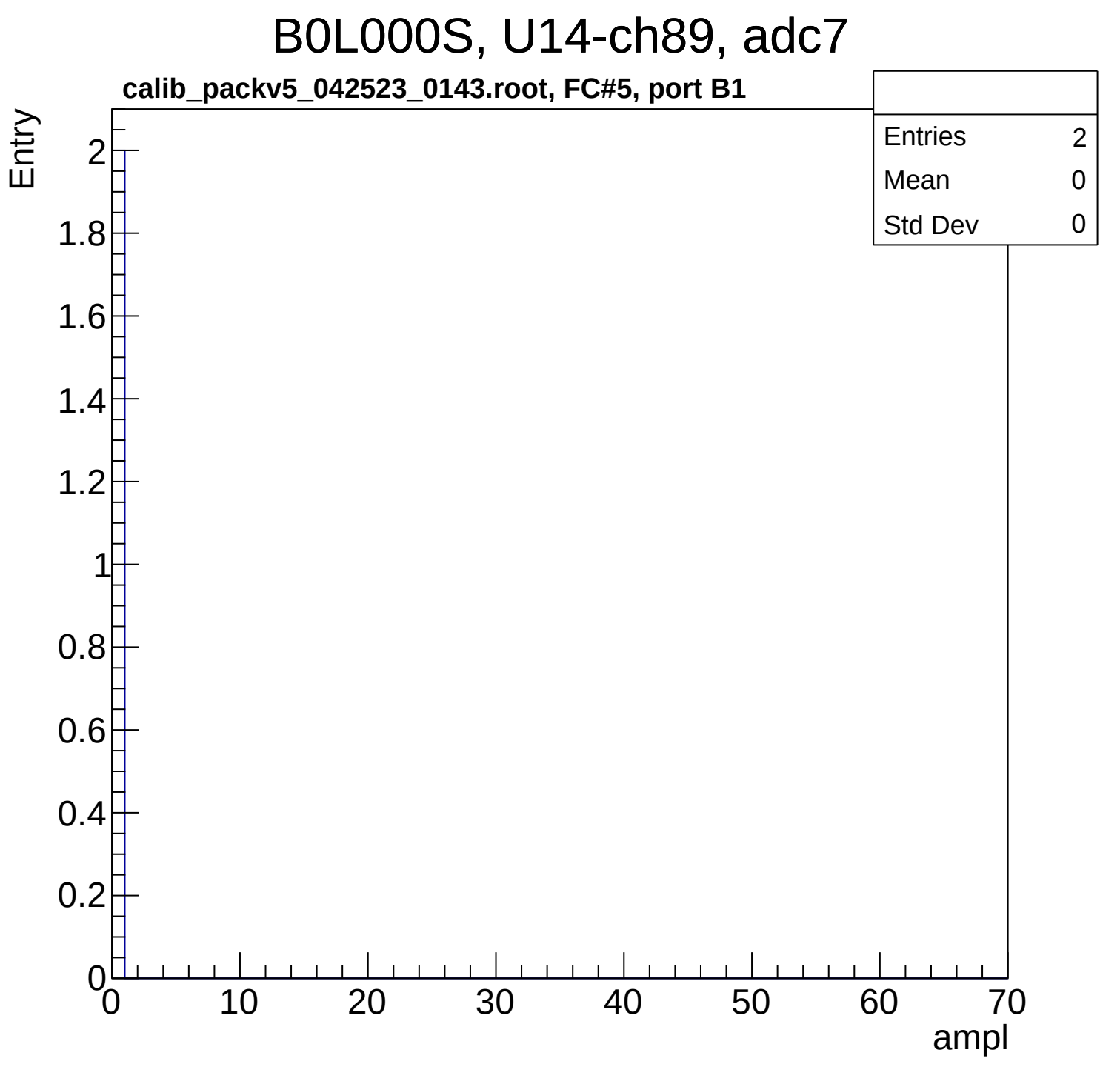
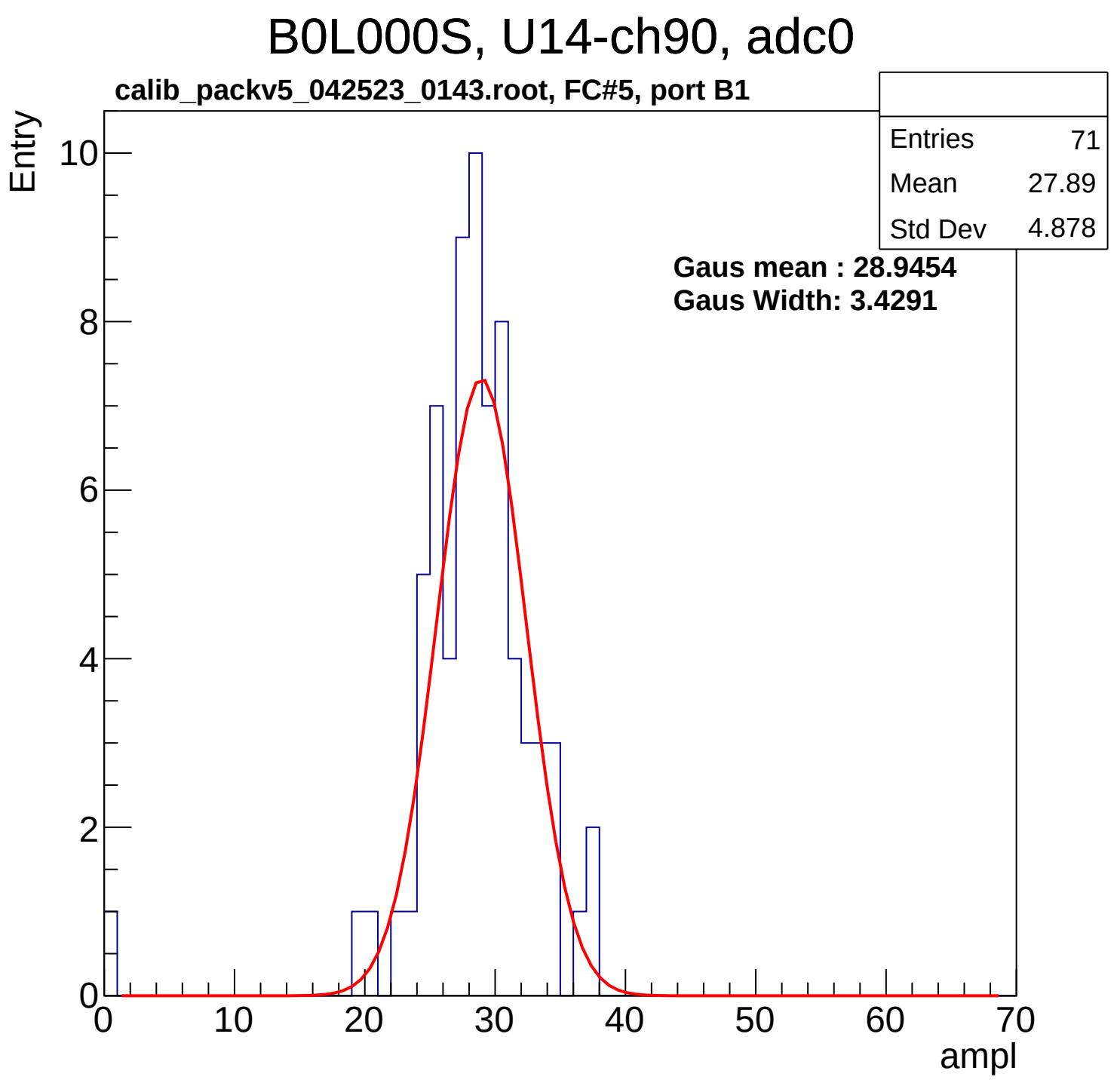
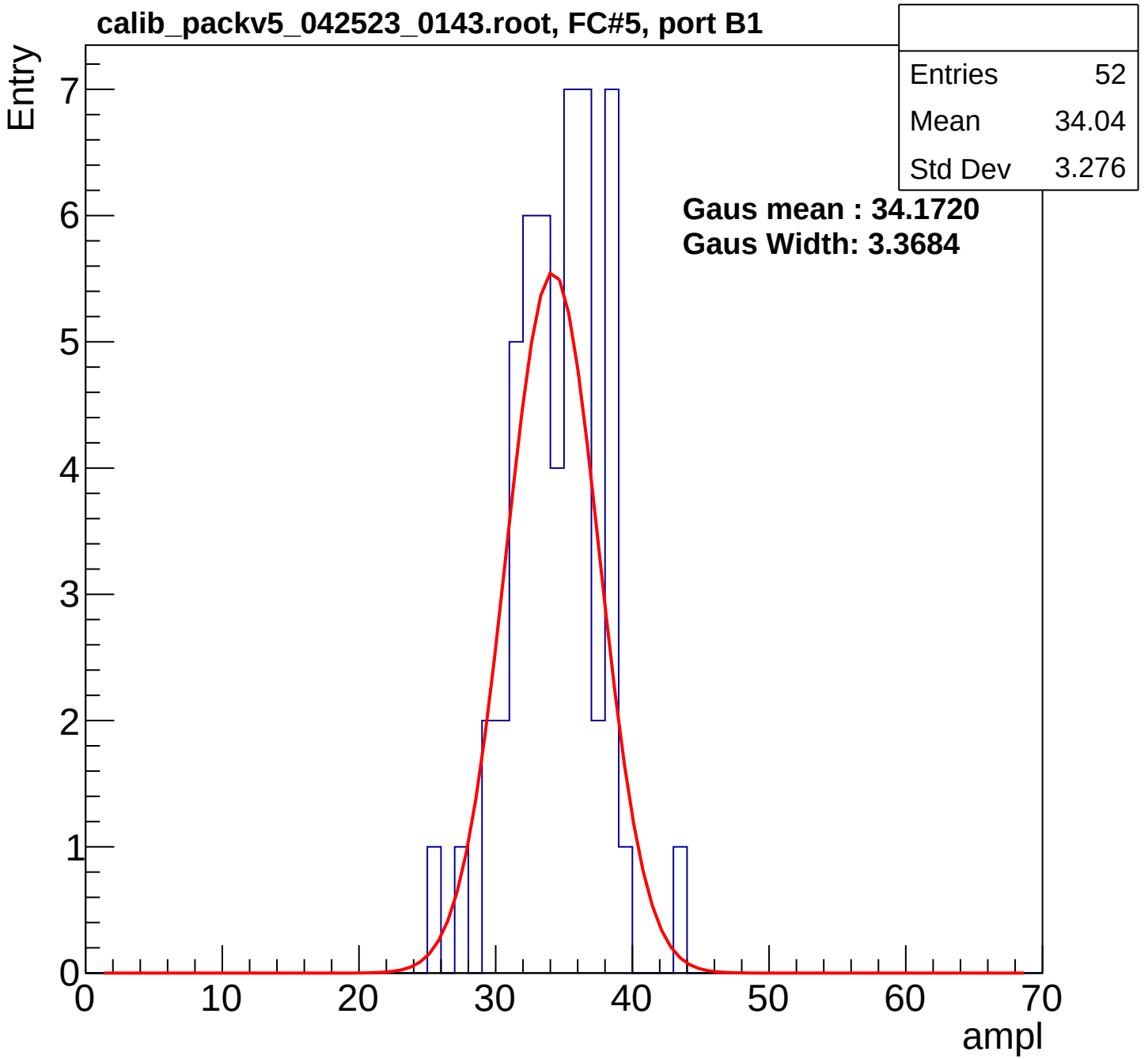
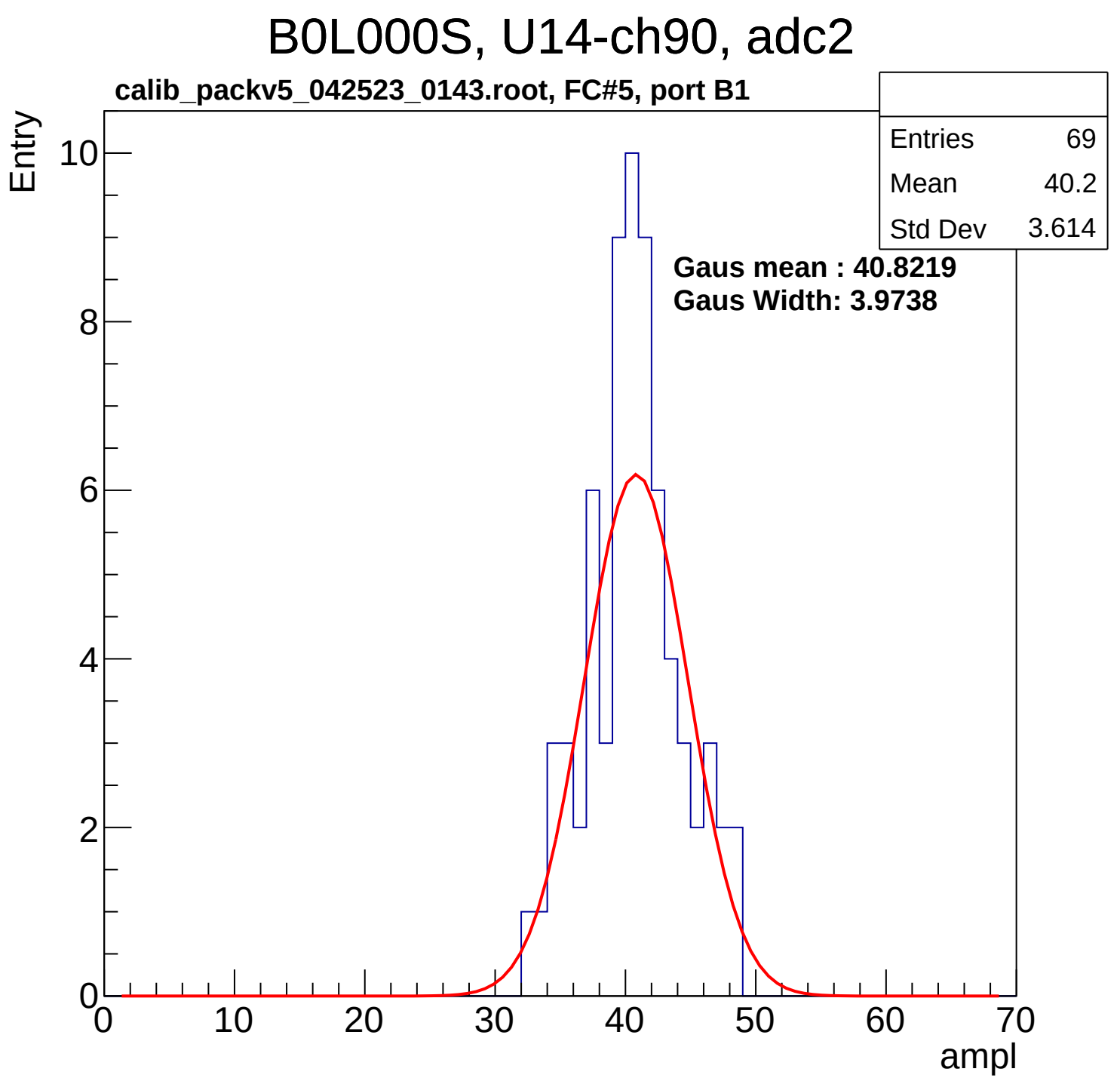
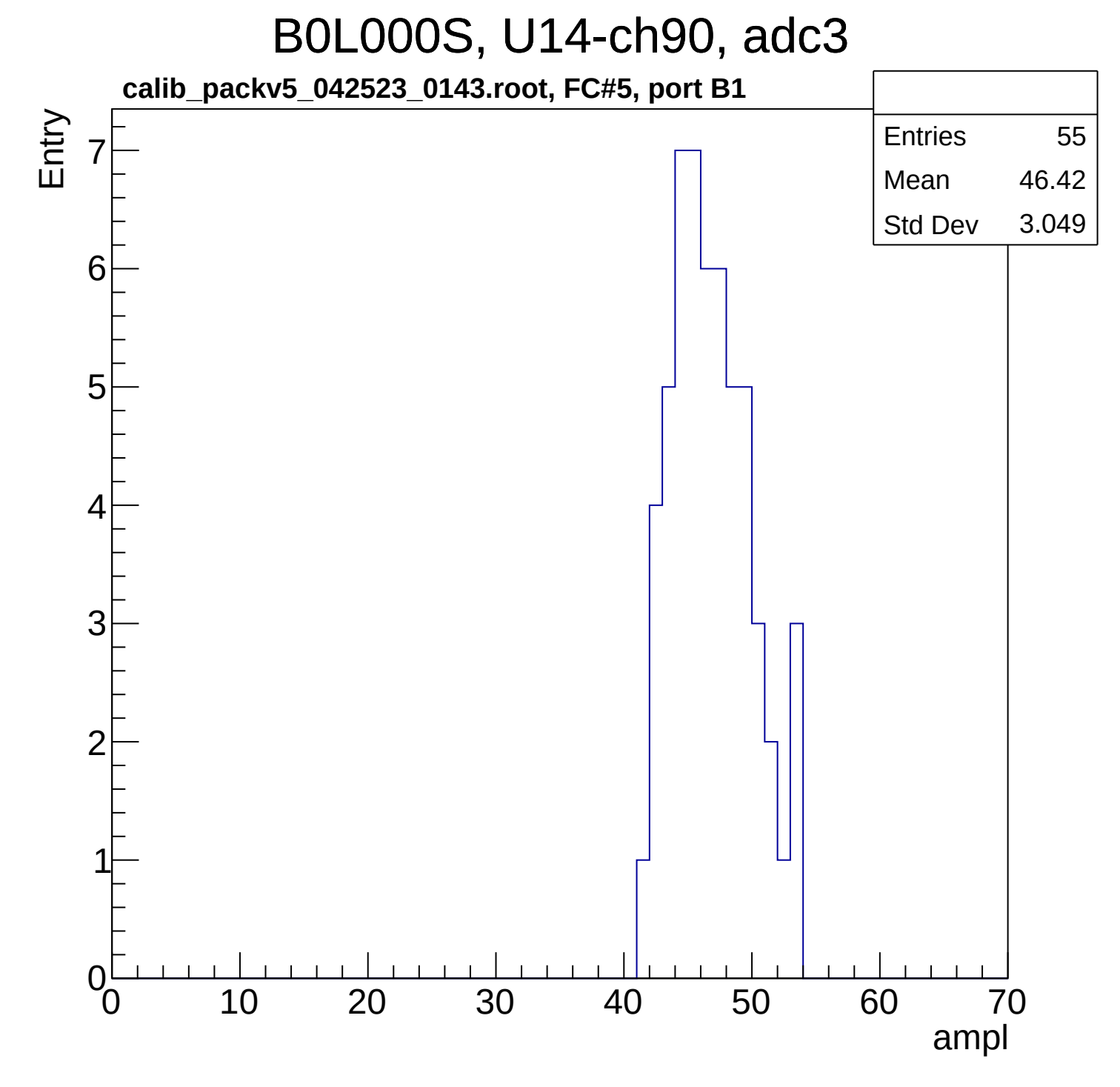

B0L000S, U14-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 61
Std Dev 1.732
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

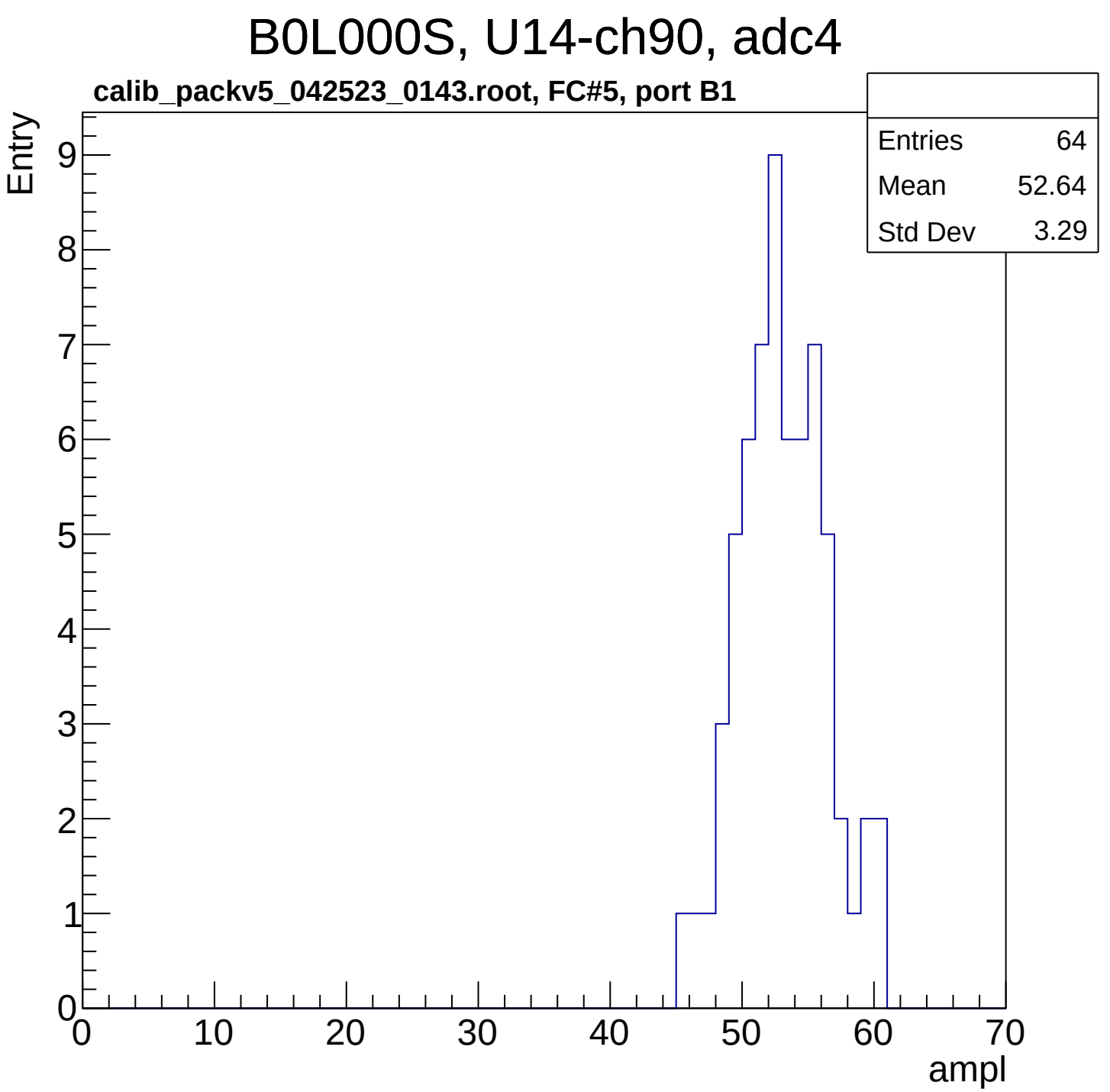

B0L000S, U14-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 27.89
Std Dev 4.878
Gaus mean : 28.9454
Gaus Width: 3.4291
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 34.04
Std Dev 3.276
Gaus mean : 34.1720
Gaus Width: 3.3684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 40.2
Std Dev 3.614
Gaus mean : 40.8219
Gaus Width: 3.9738
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 46.42
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 52.64
Std Dev 3.29
Entry
ampl
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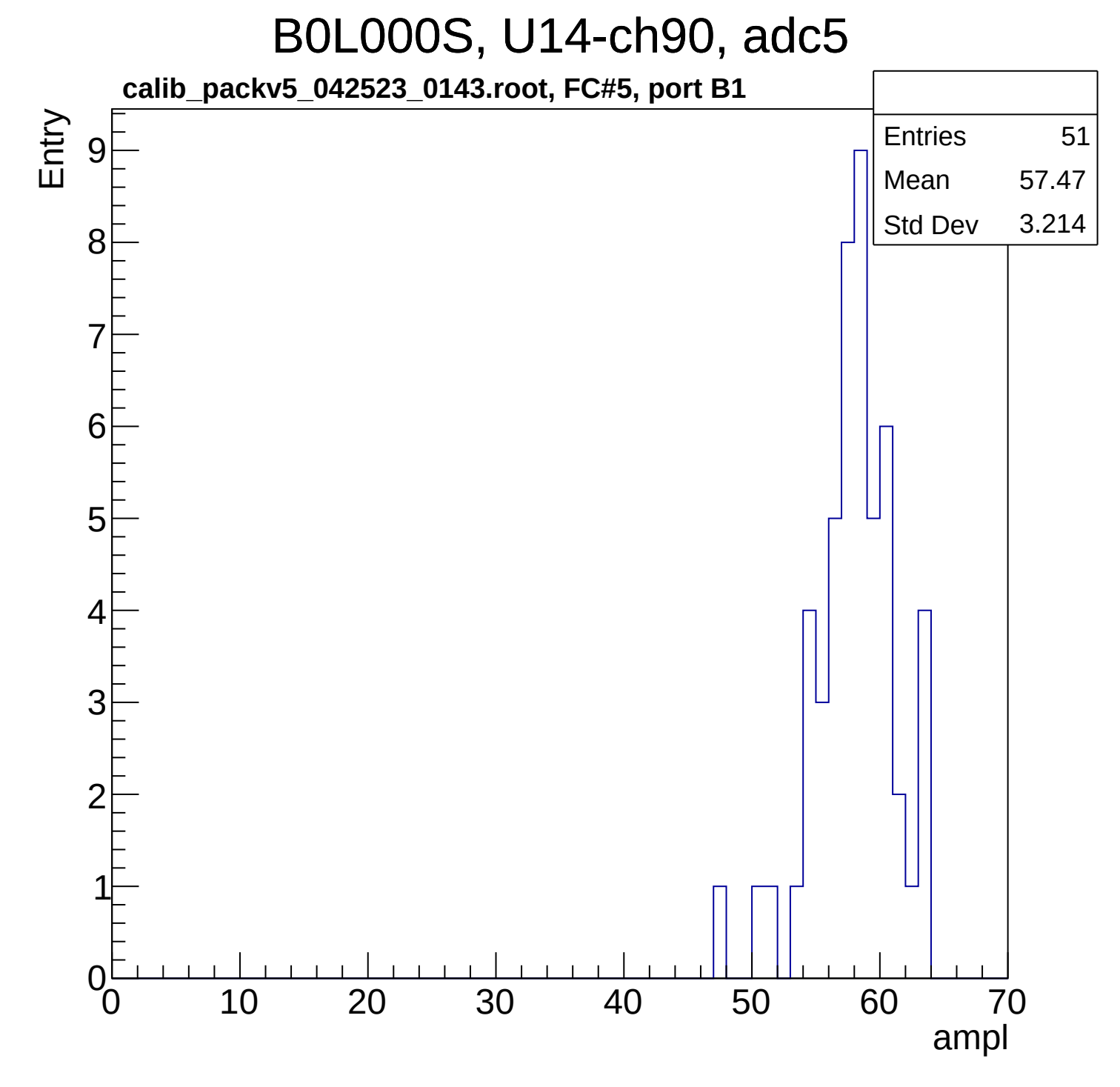
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 57.47
Std Dev 3.214
Entry
ampl
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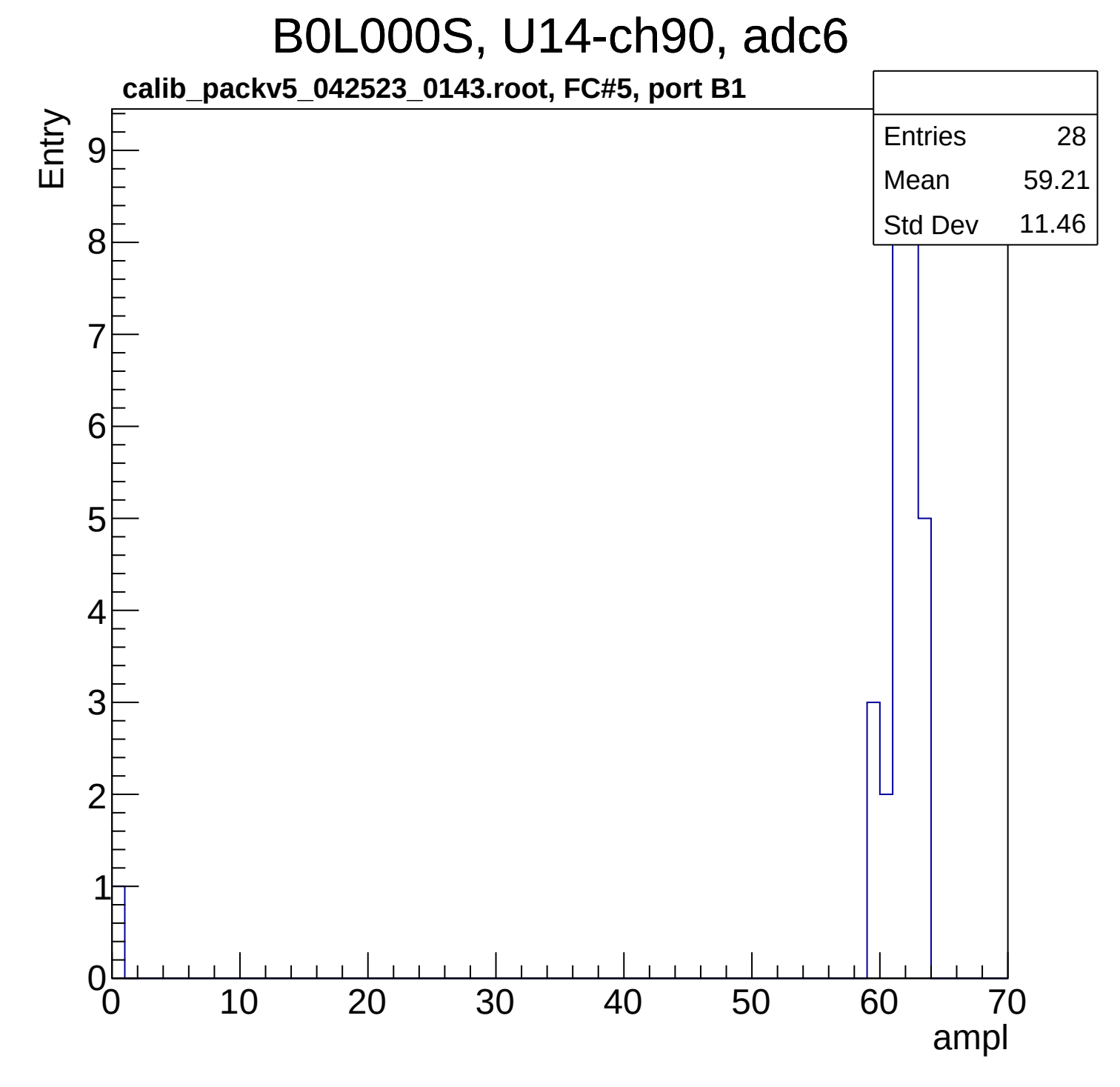
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 59.21
Std Dev 11.46
Entry
ampl
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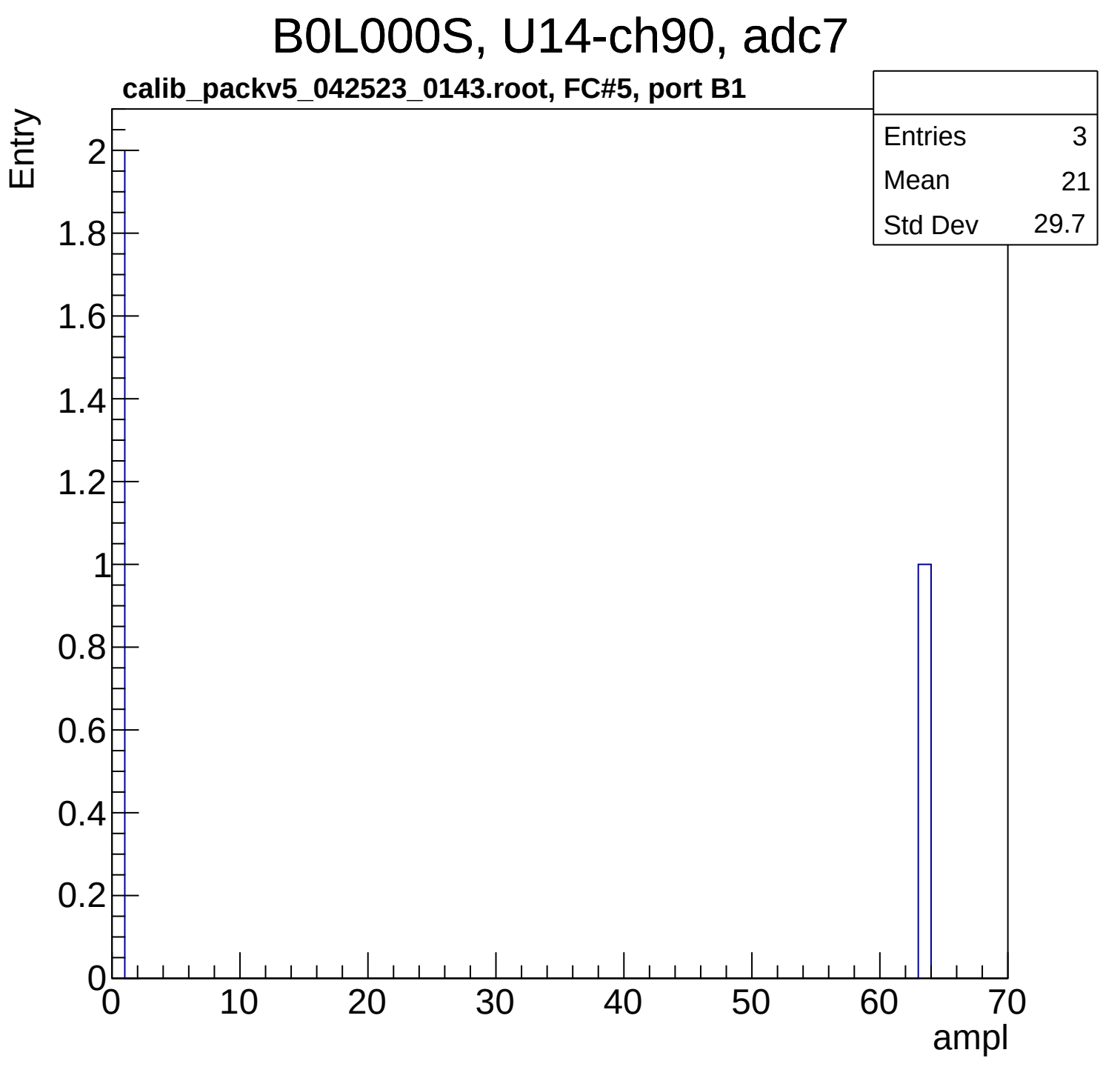
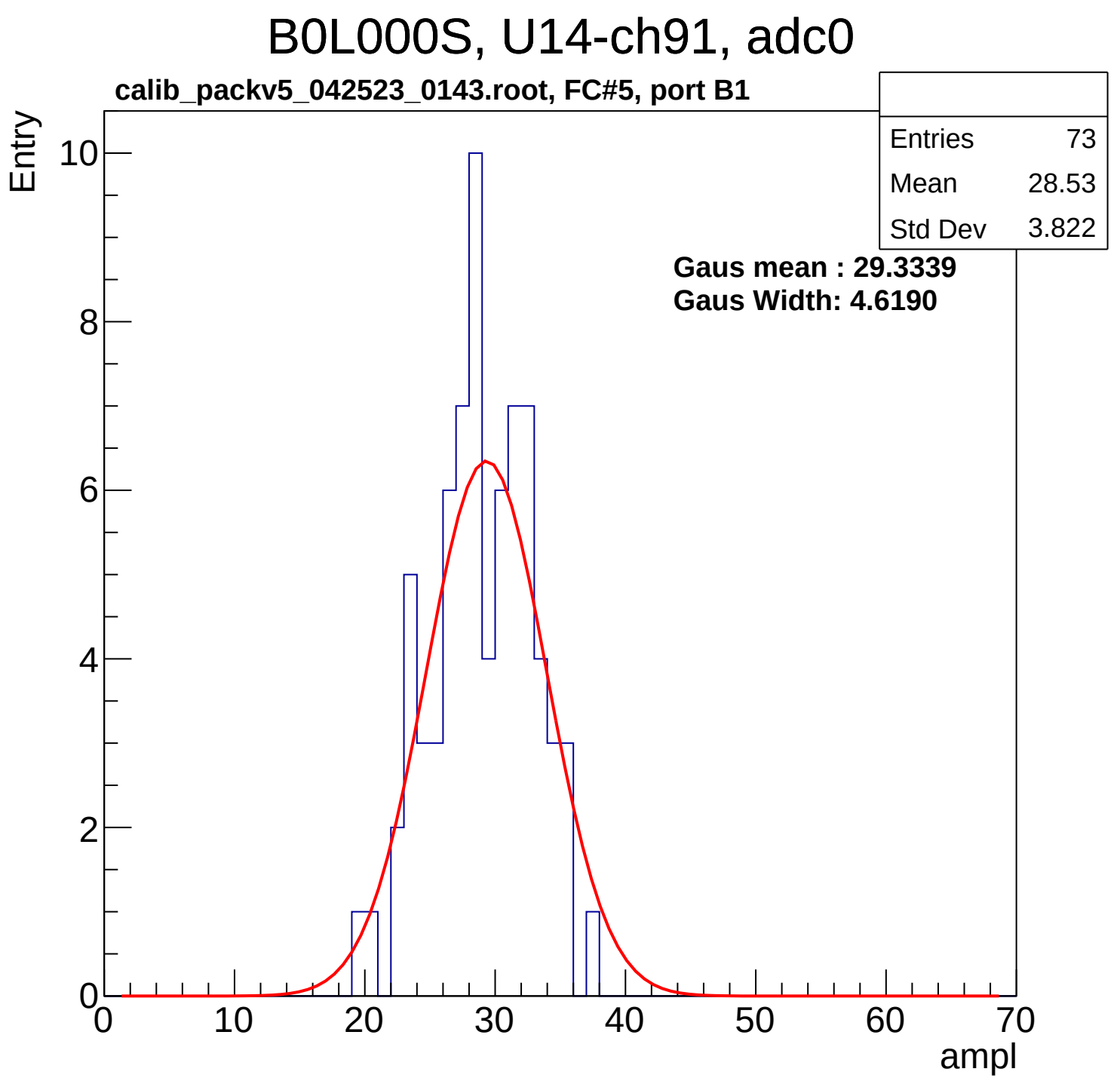
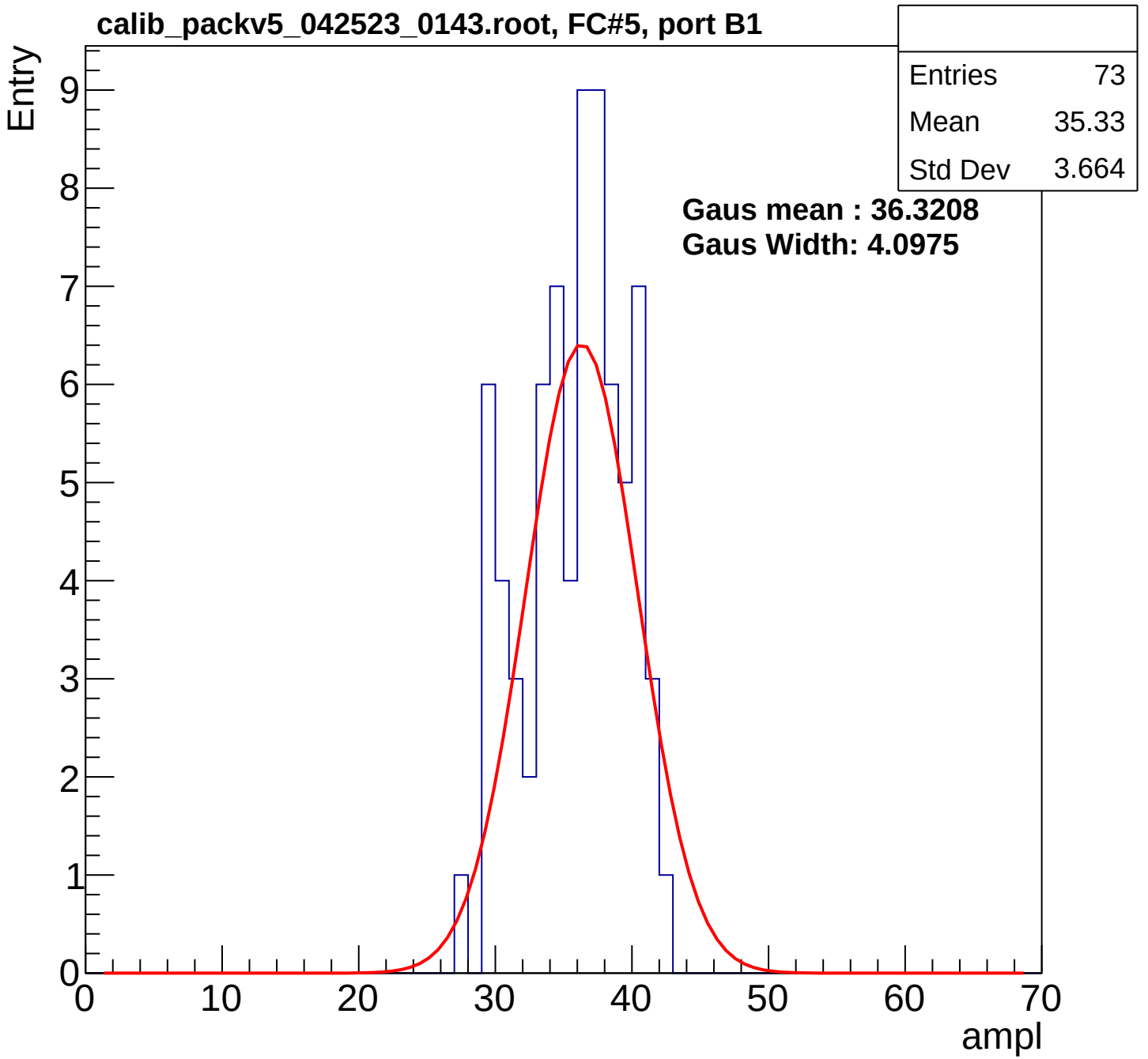
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 28.53
Std Dev 3.822
Gaus mean : 29.3339
Gaus Width: 4.6190
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.33
Std Dev 3.664
Gaus mean : 36.3208
Gaus Width: 4.0975
Entry
ampl
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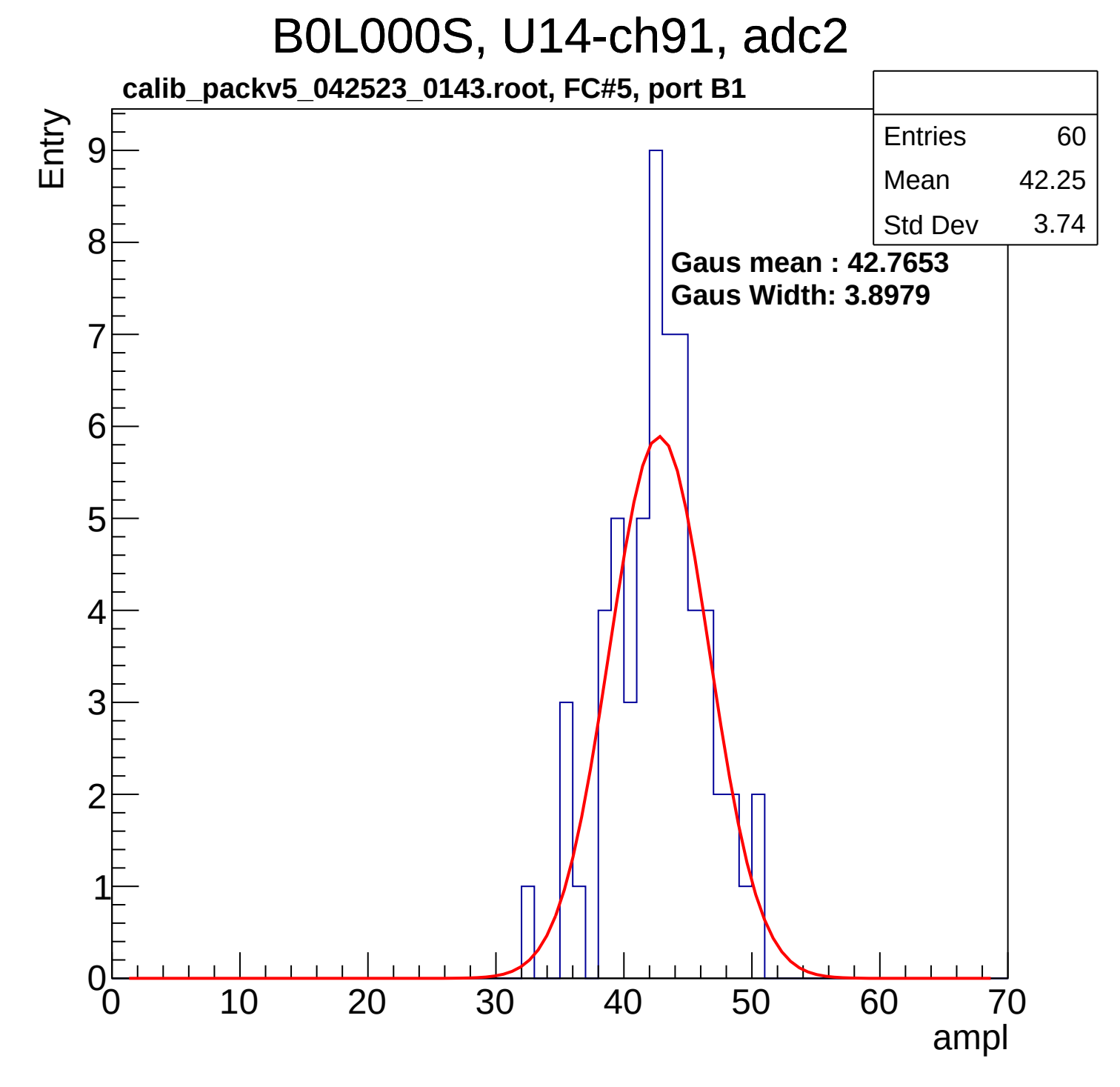
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 42.25
Std Dev 3.74
Gaus mean : 42.7653
Gaus Width: 3.8979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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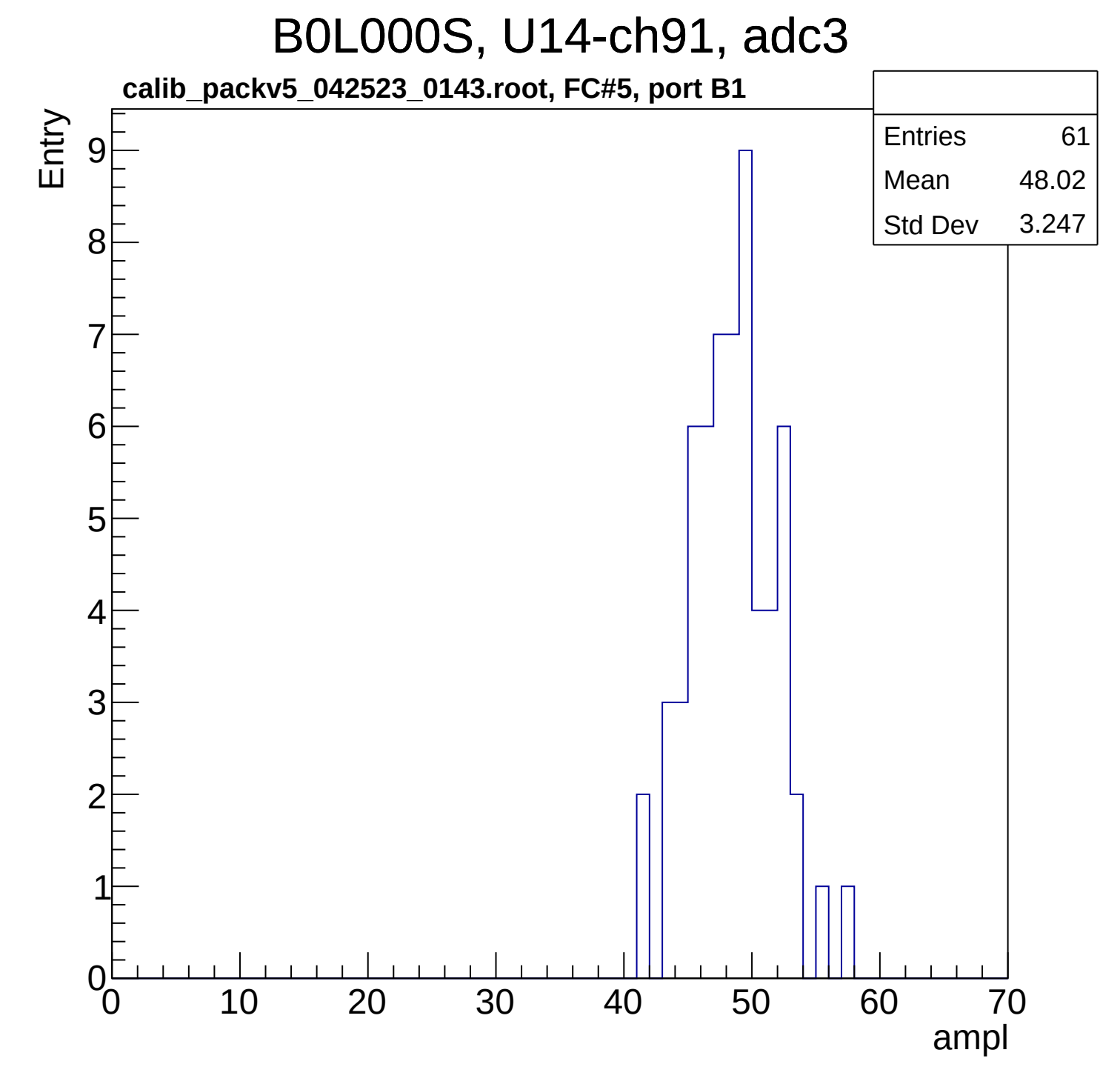
B0L000S, U14-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.02
Std Dev 3.247
Entry
ampl
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3
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5
6
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0
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60
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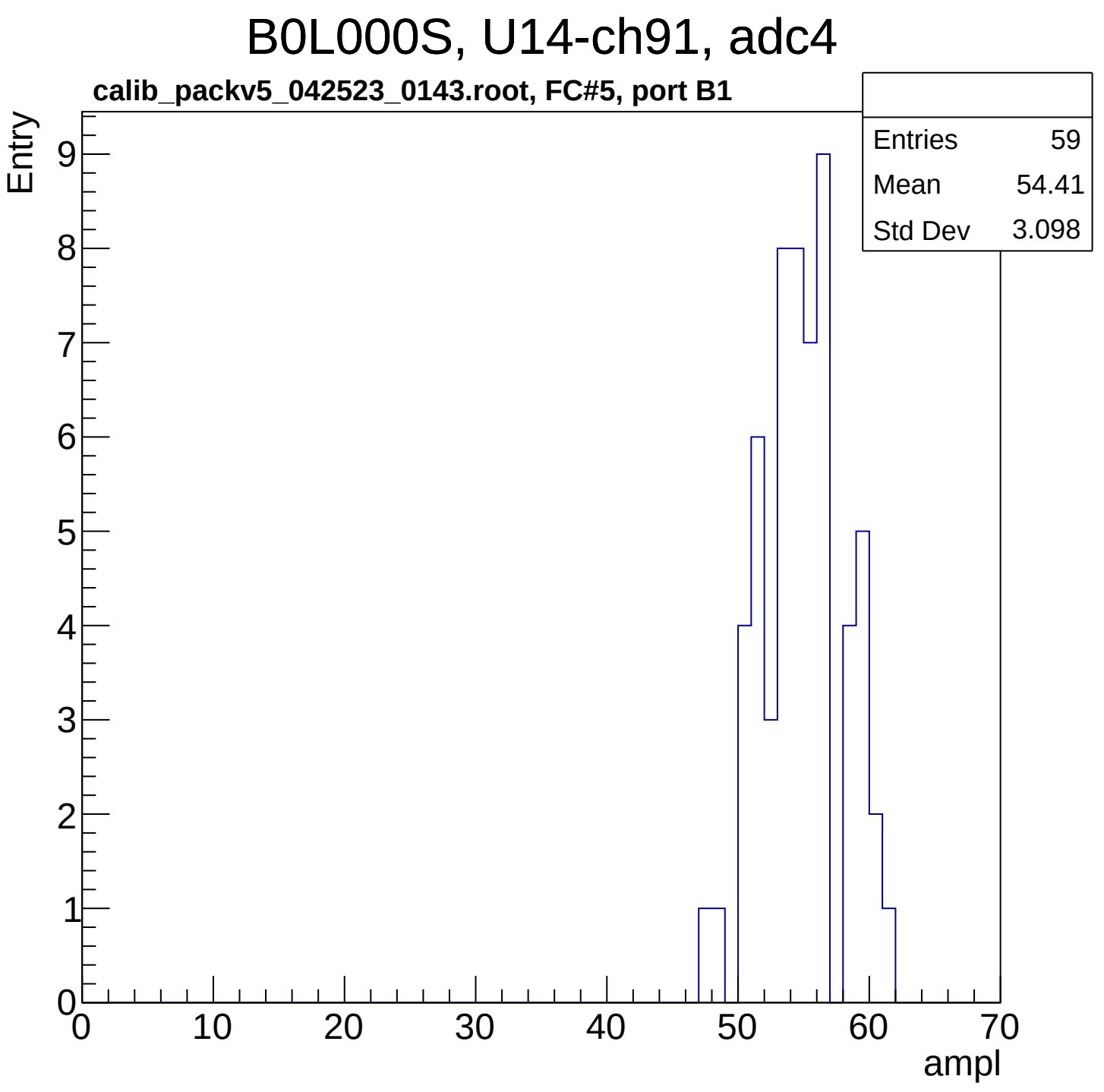
B0L000S, U14-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 54.41
Std Dev 3.098
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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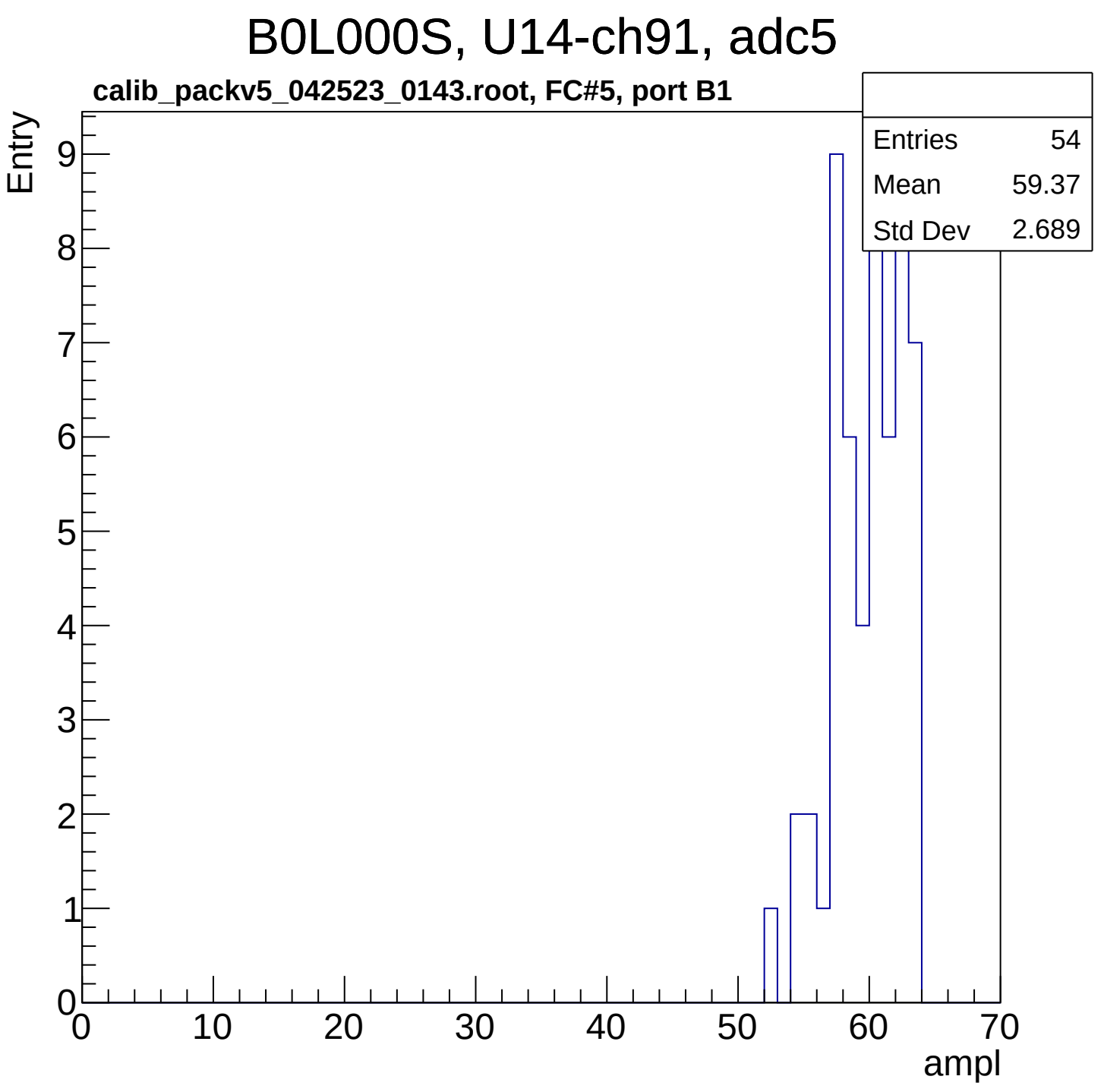
B0L000S, U14-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 59.37
Std Dev 2.689
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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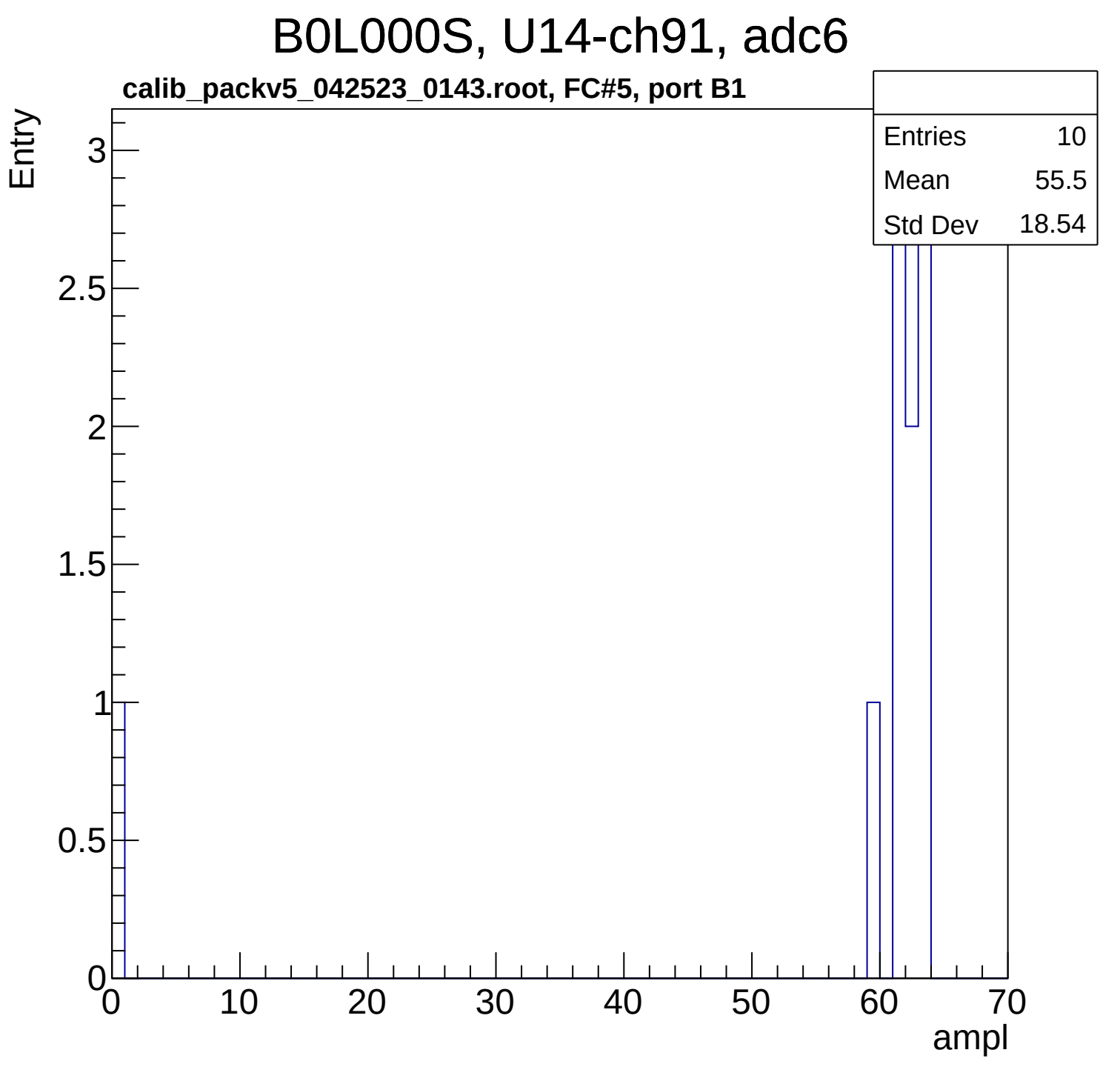
B0L000S, U14-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.5
Std Dev 18.54
Entry
ampl
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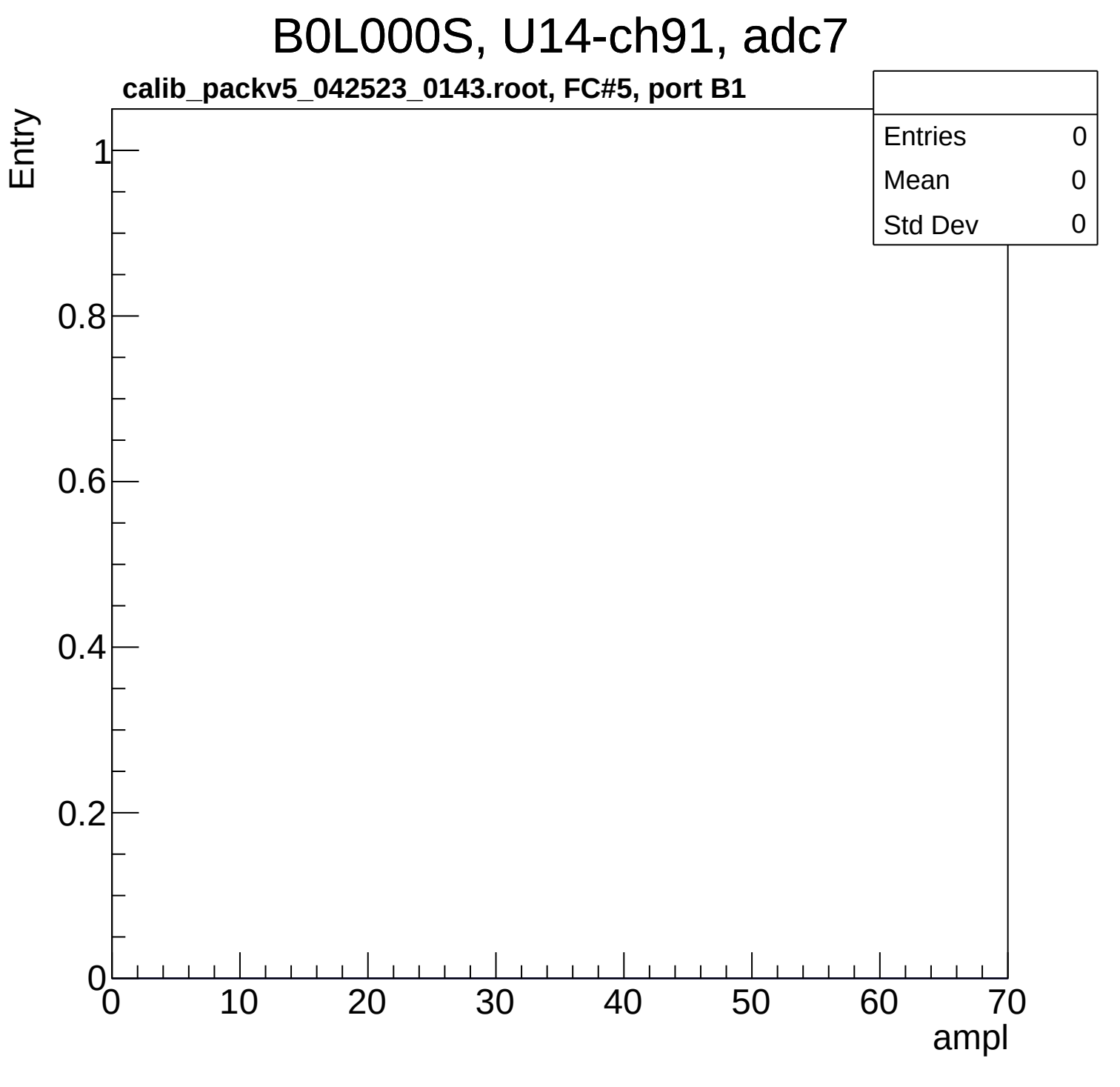
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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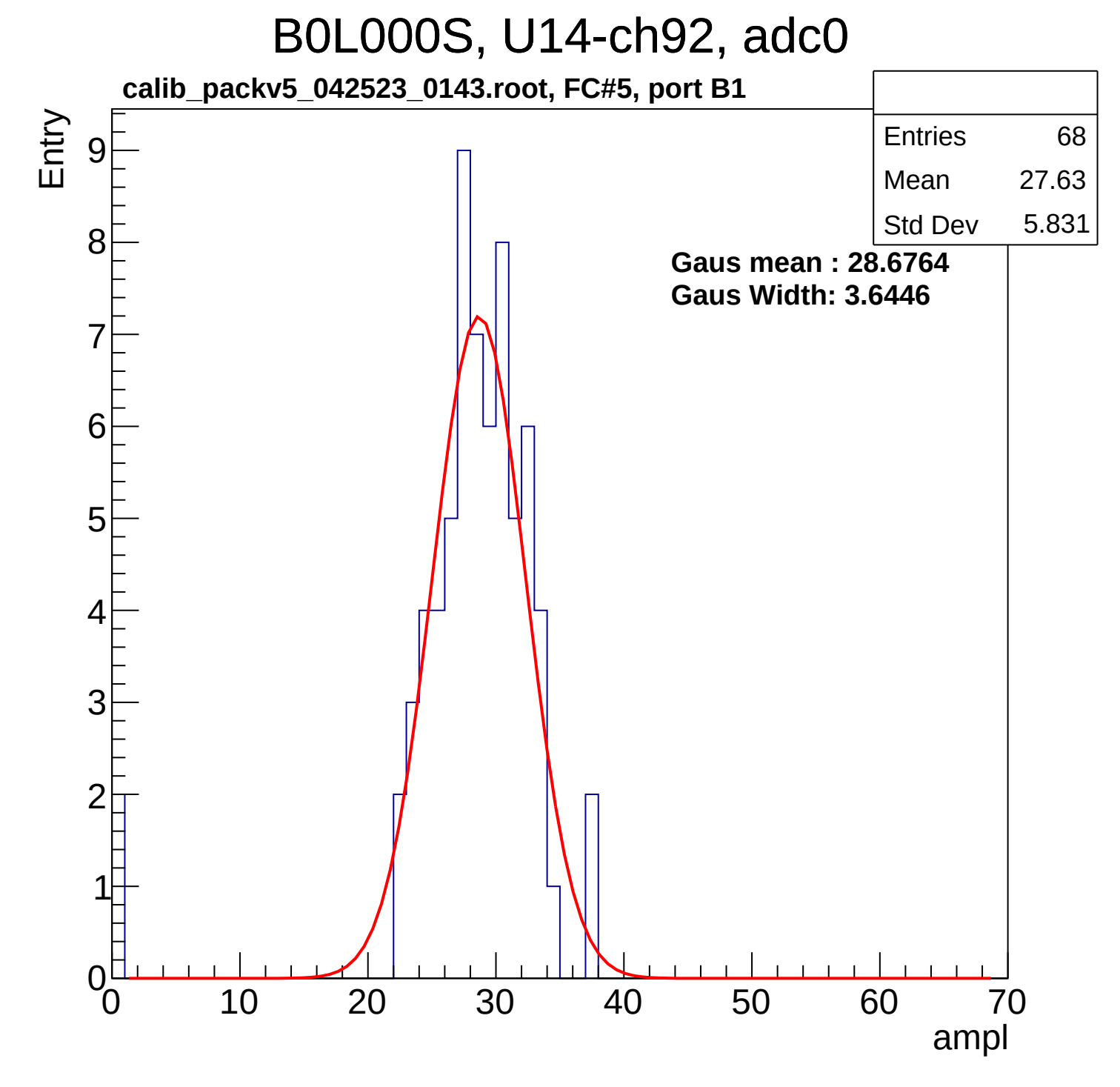
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 27.63
Std Dev 5.831
Gaus mean : 28.6764
Gaus Width: 3.6446
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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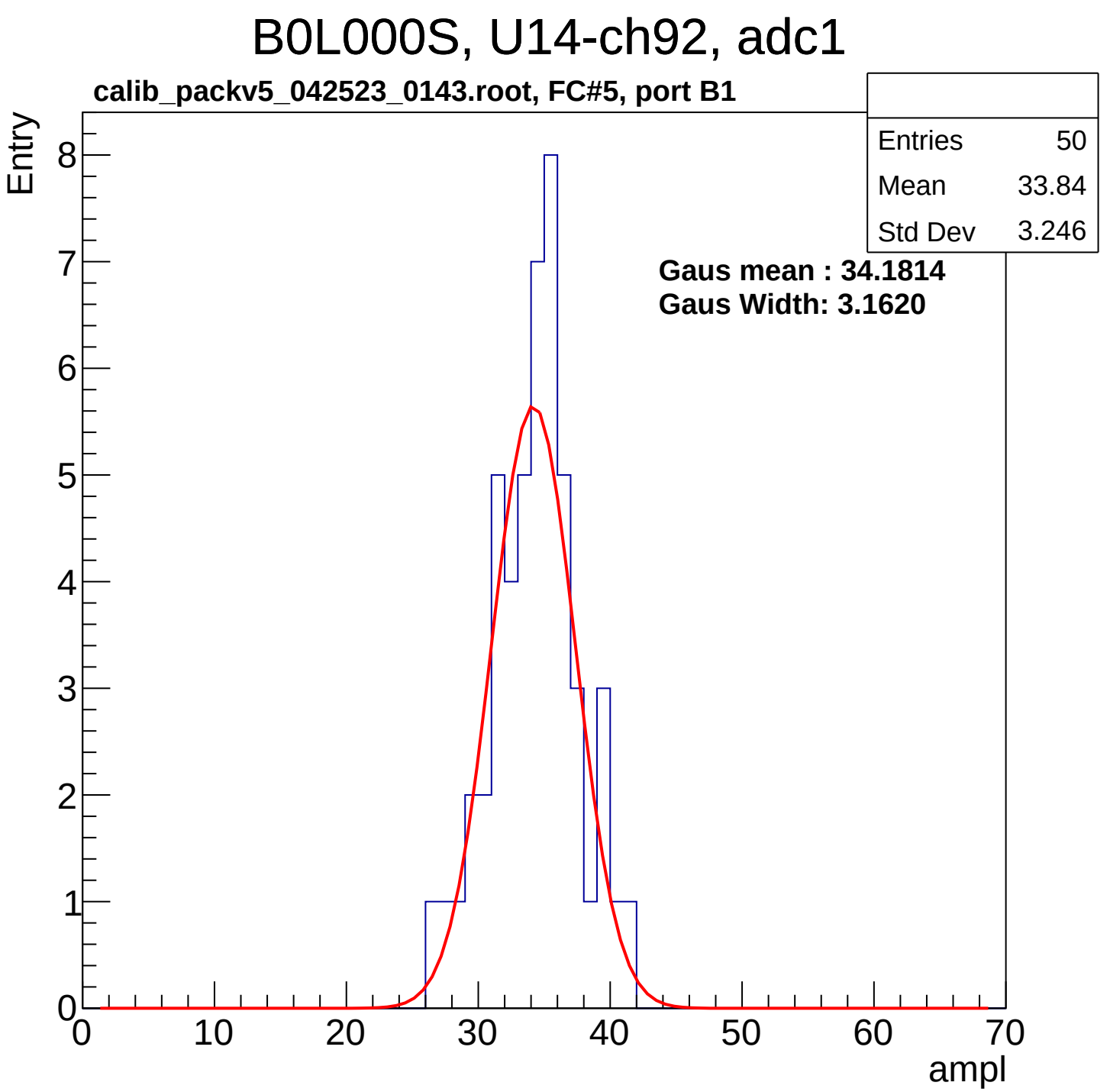
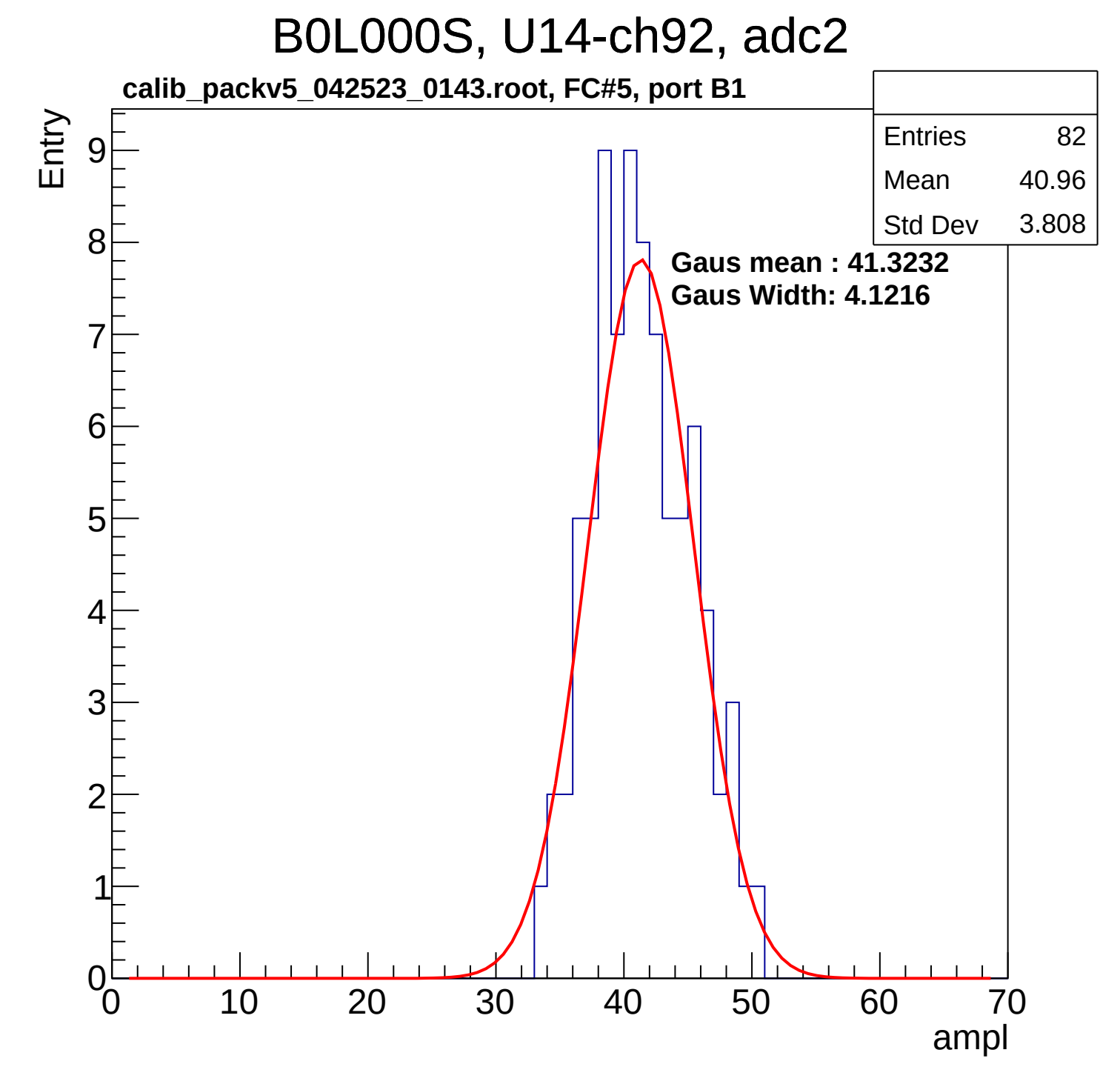

B0L000S, U14-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 33.84
Std Dev 3.246
Gaus mean : 34.1814
Gaus Width: 3.1620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 40.96
Std Dev 3.808
Gaus mean : 41.3232
Gaus Width: 4.1216
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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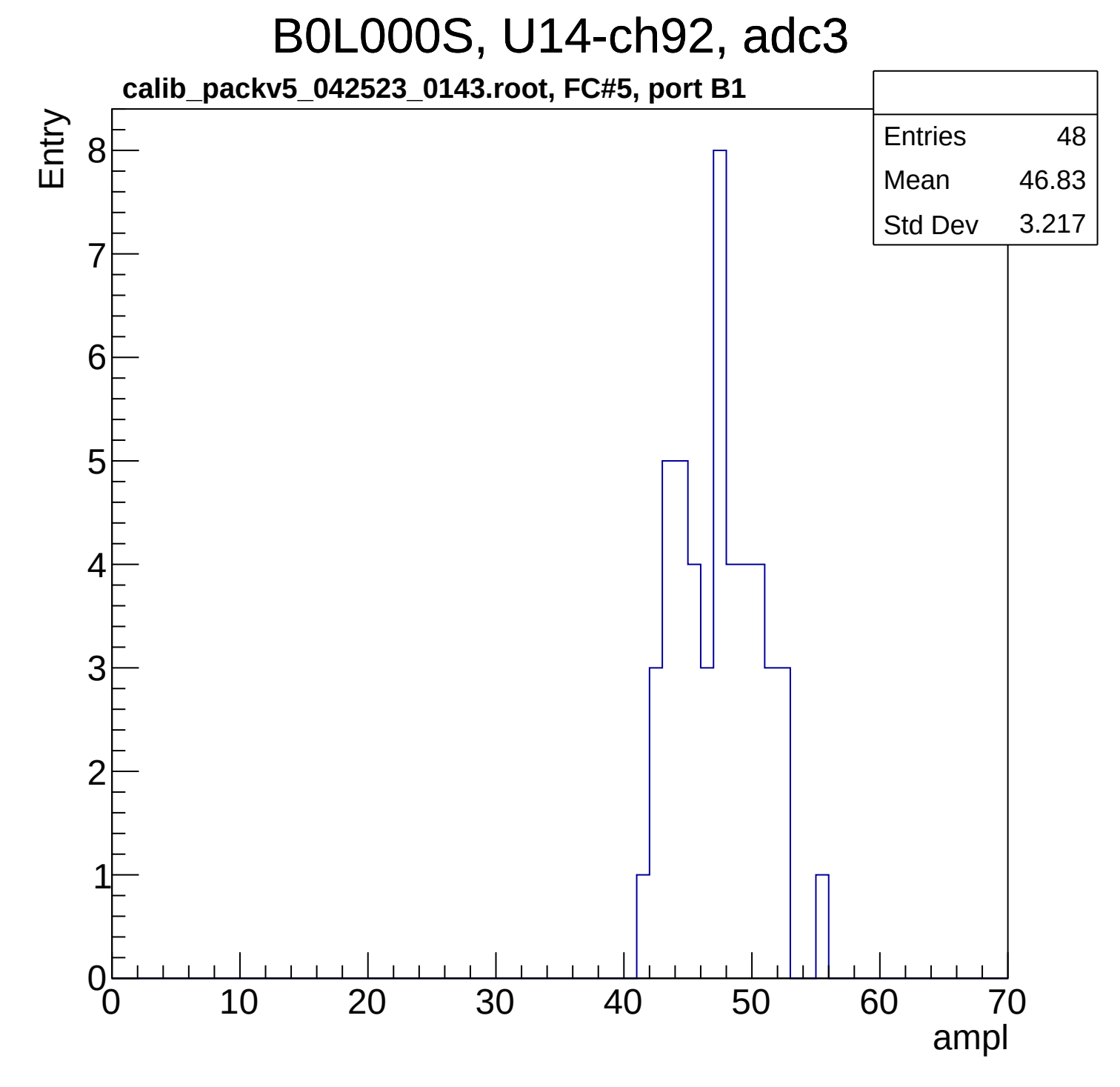
B0L000S, U14-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 46.83
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
50
60
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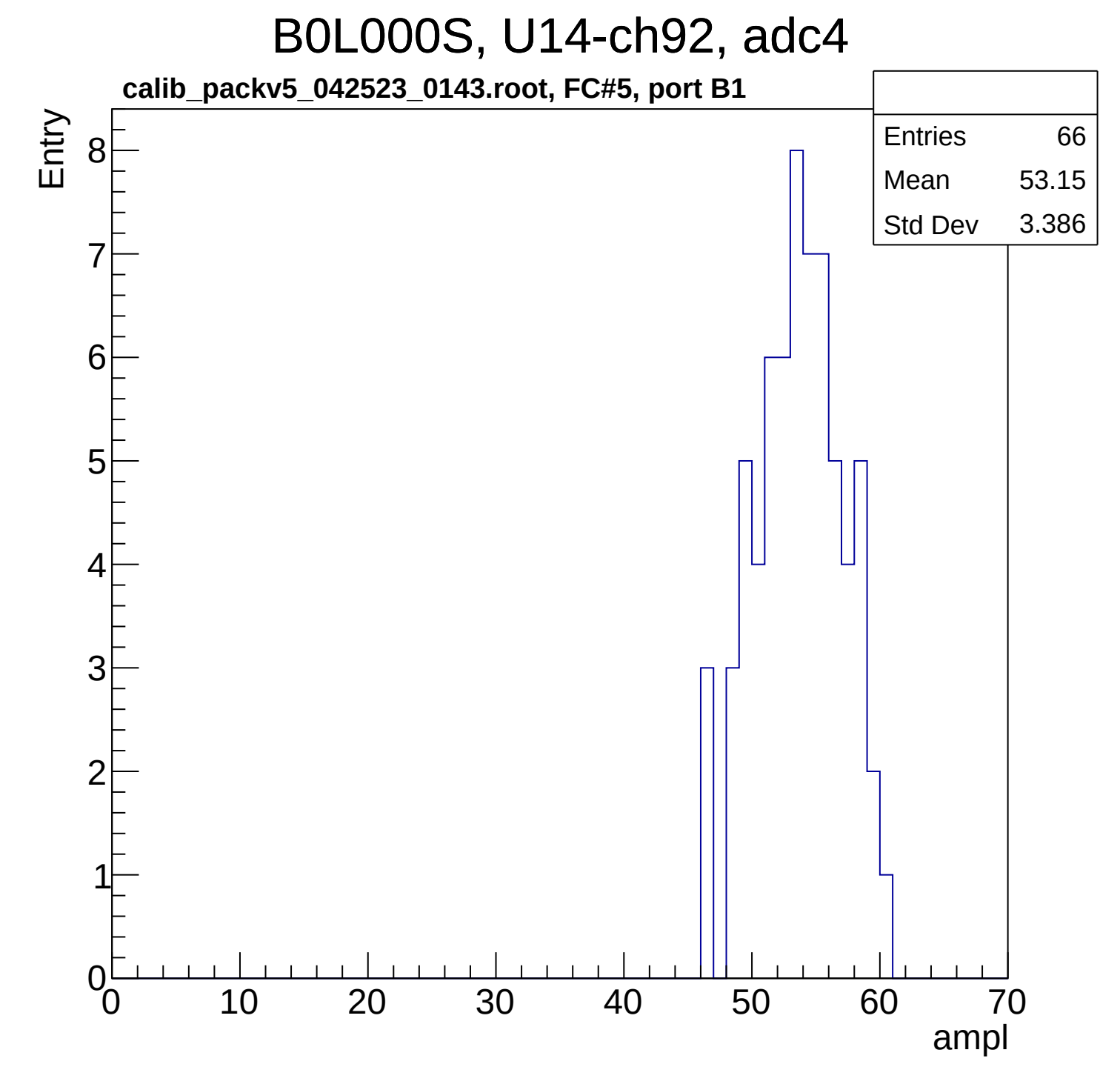
B0L000S, U14-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 53.15
Std Dev 3.386
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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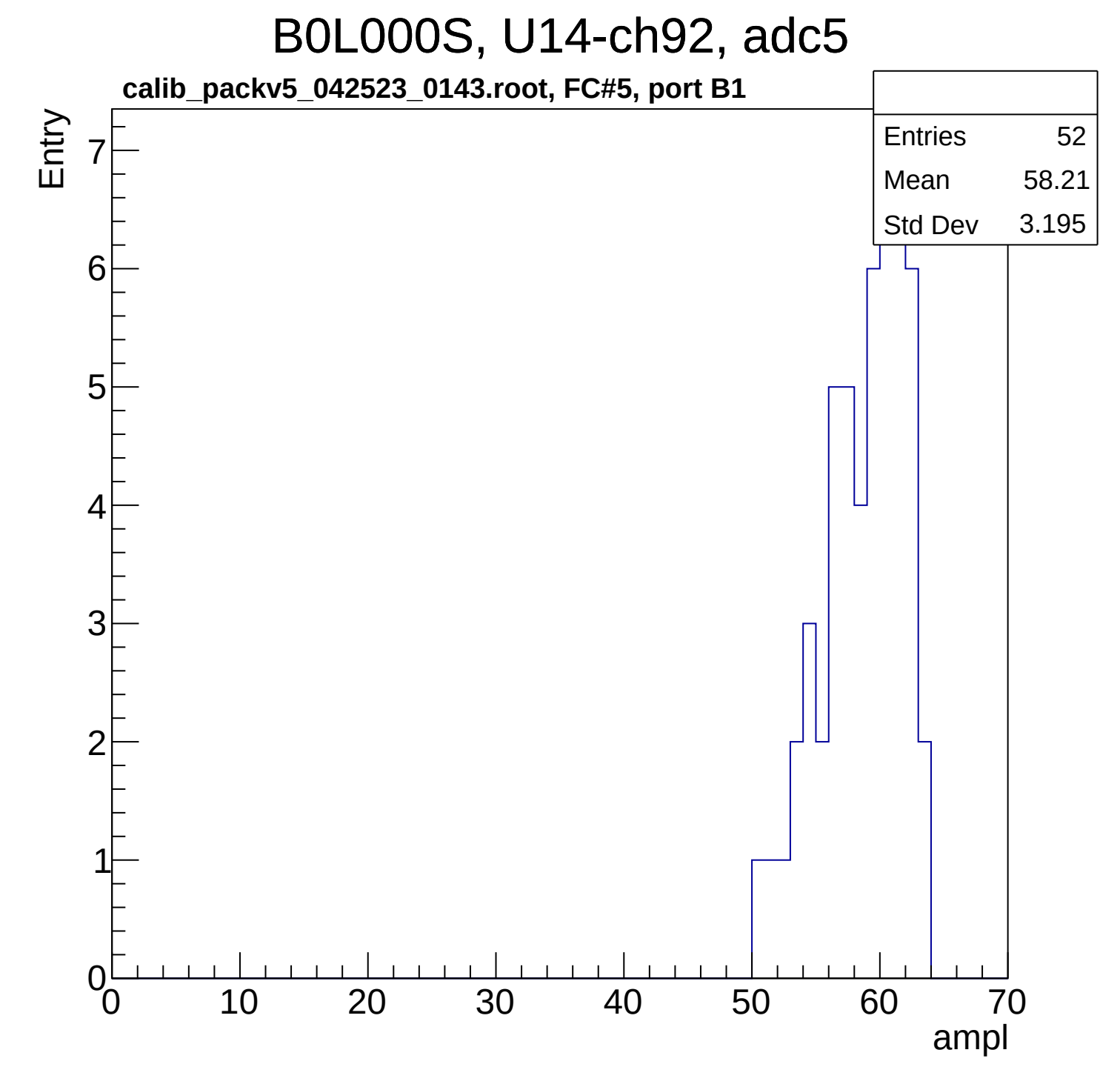
B0L000S, U14-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.21
Std Dev 3.195
Entry
ampl
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1
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3
4
5
6
7
0
10
20
30
40
50
60
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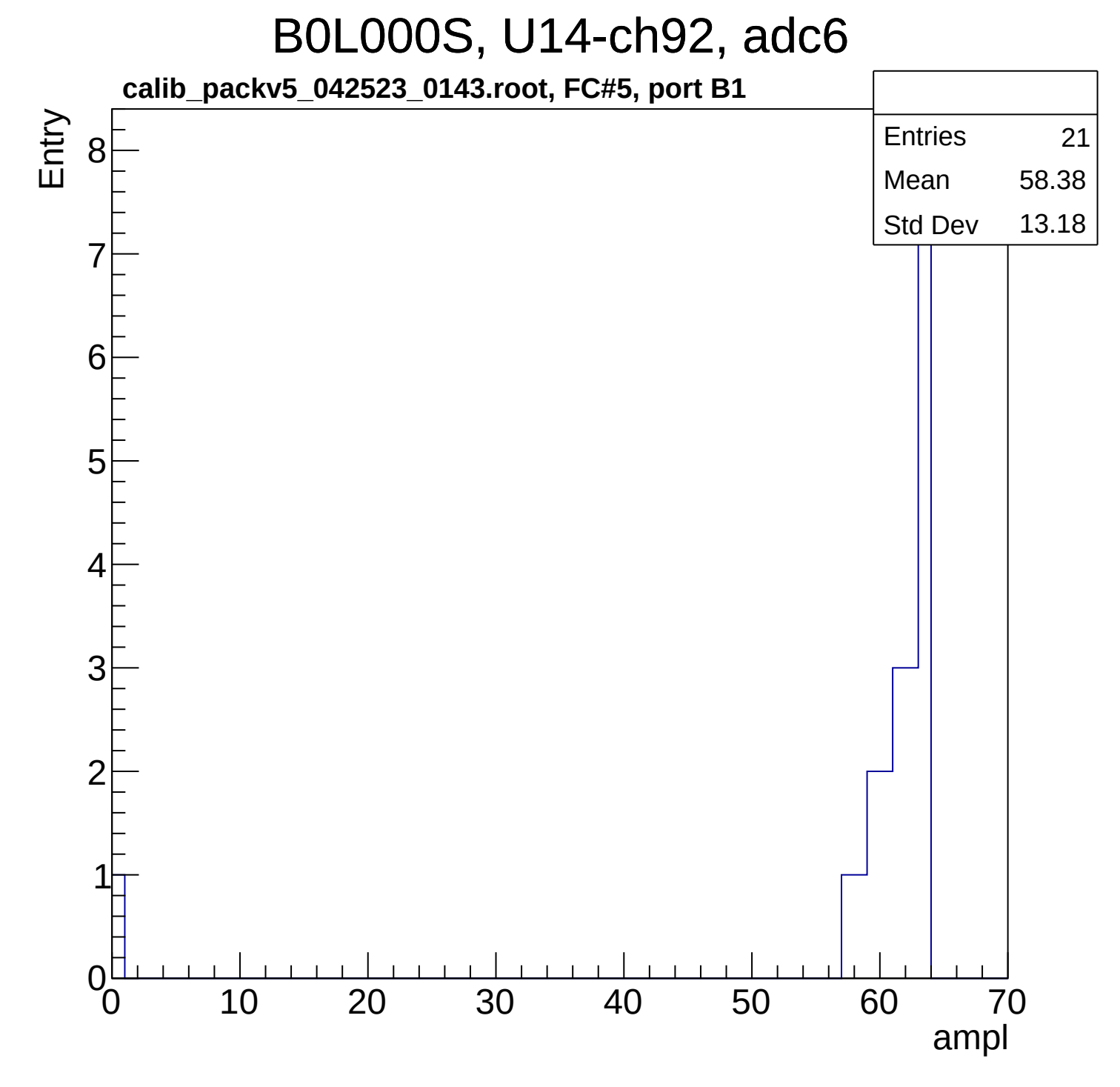
B0L000S, U14-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.38
Std Dev 13.18
Entry
ampl
0
1
2
3
4
5
6
7
8
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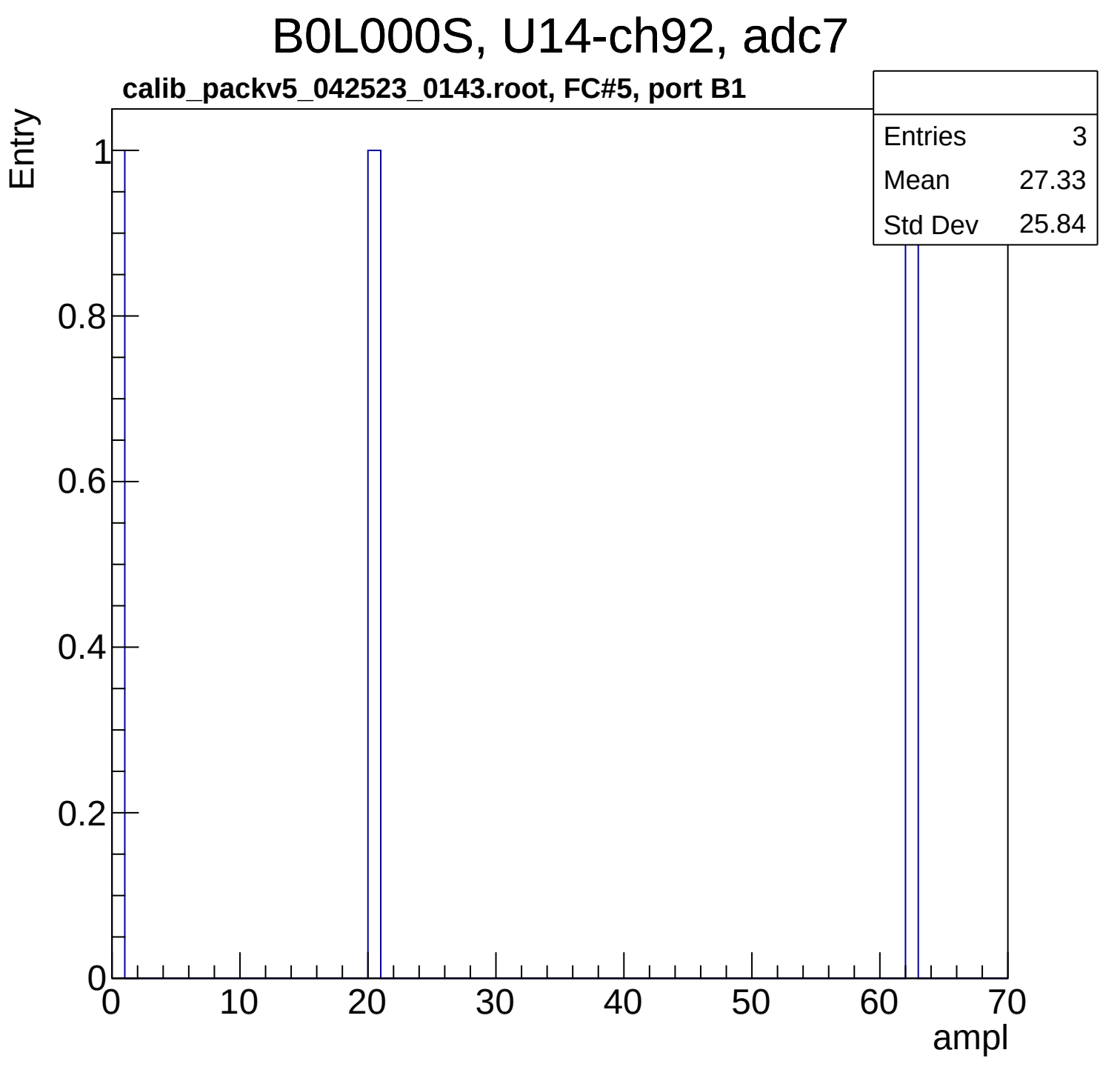
B0L000S, U14-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
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60
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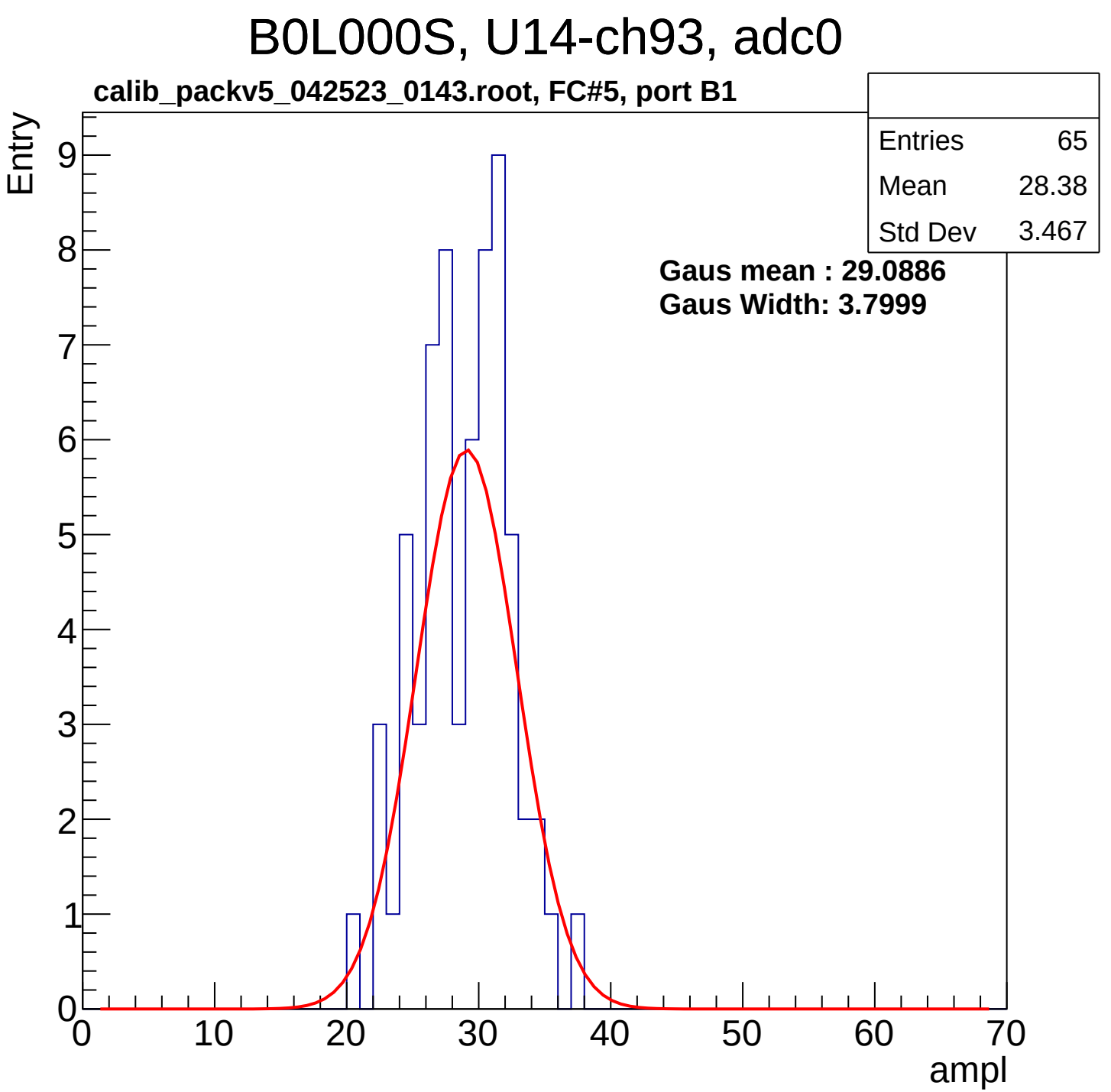
B0L000S, U14-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.38
Std Dev 3.467
Gaus mean : 29.0886
Gaus Width: 3.7999
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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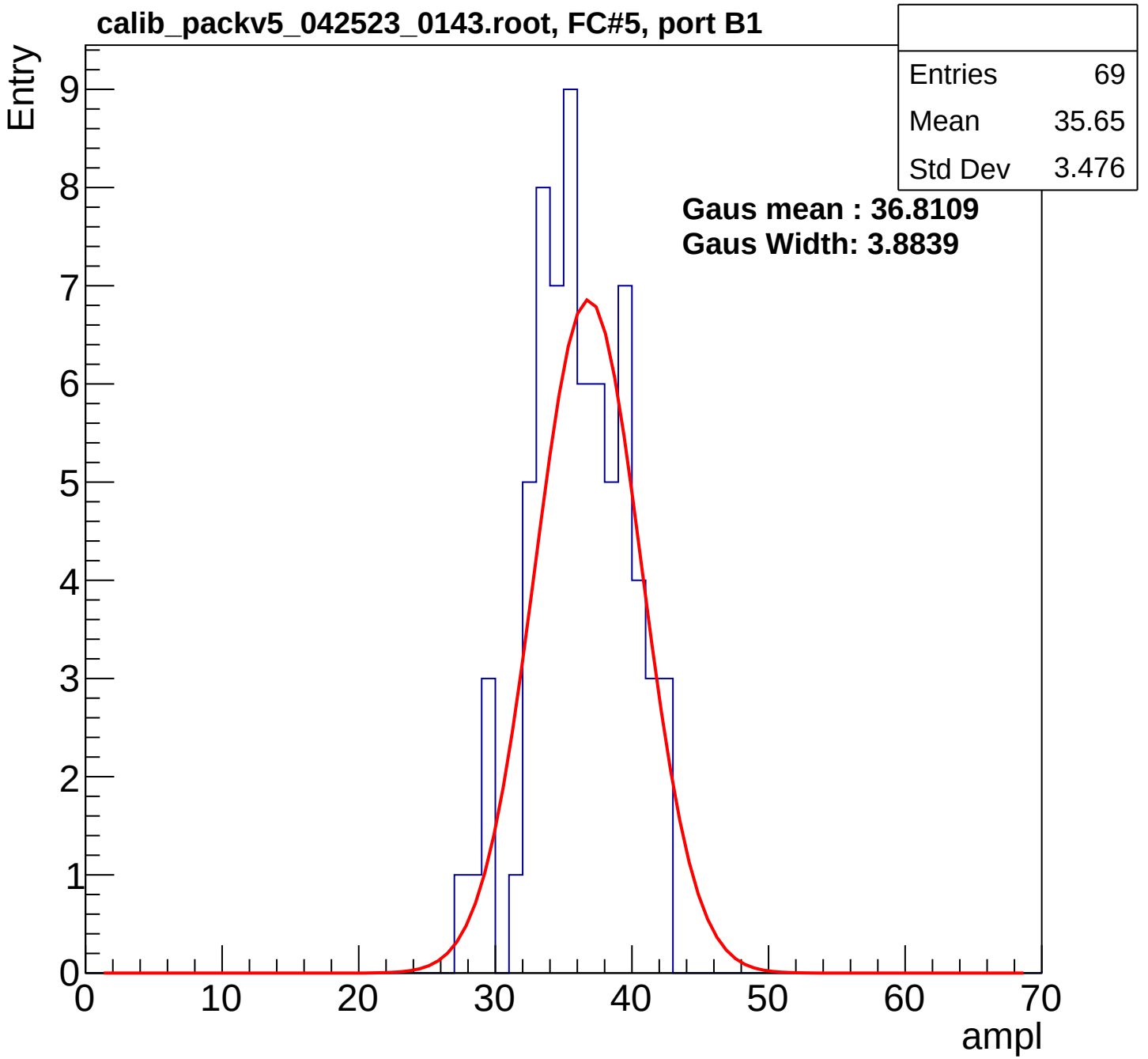
B0L000S, U14-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.65
Std Dev 3.476
Gaus mean : 36.8109
Gaus Width: 3.8839
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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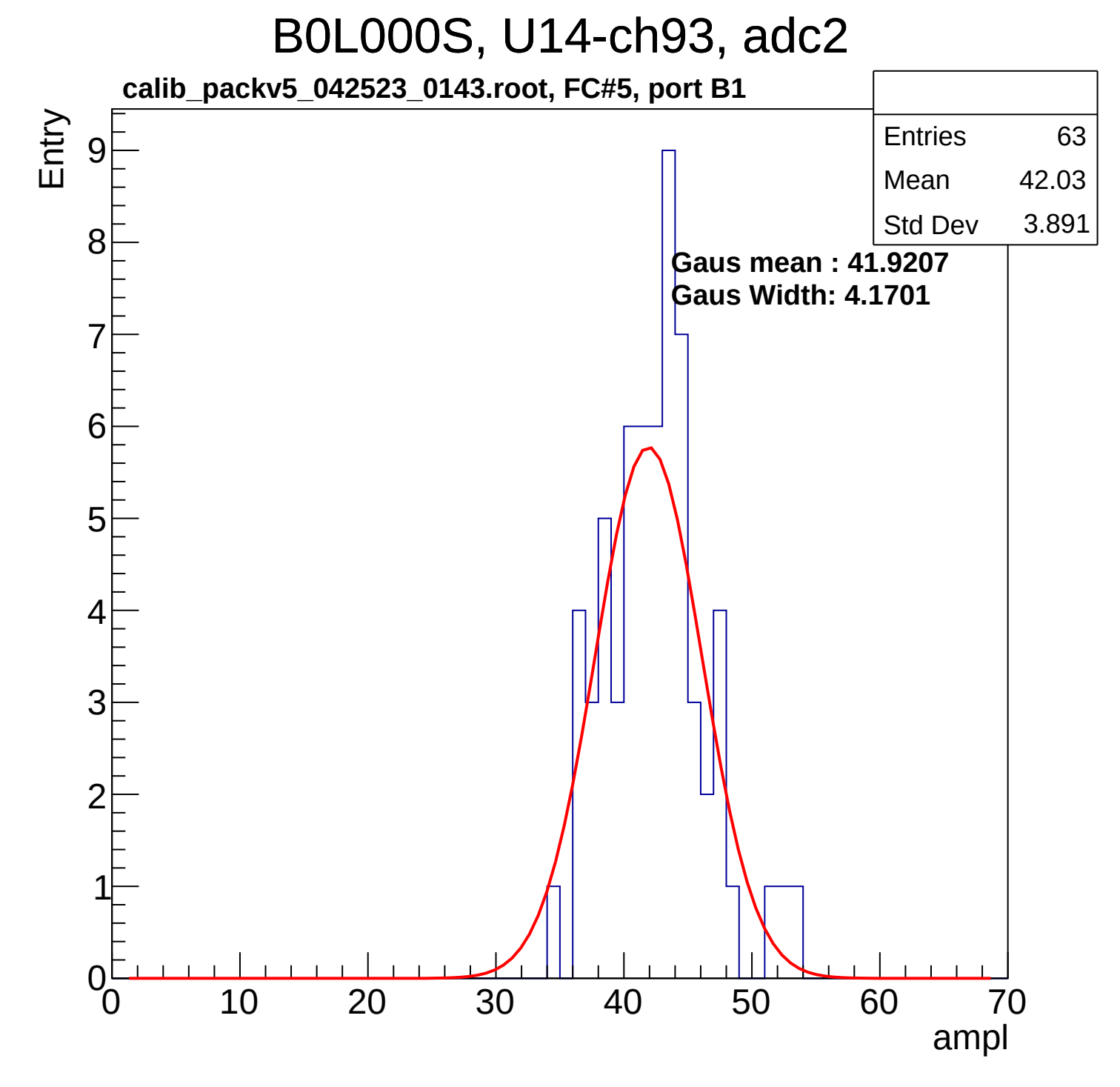
B0L000S, U14-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.03
Std Dev 3.891
Gaus mean : 41.9207
Gaus Width: 4.1701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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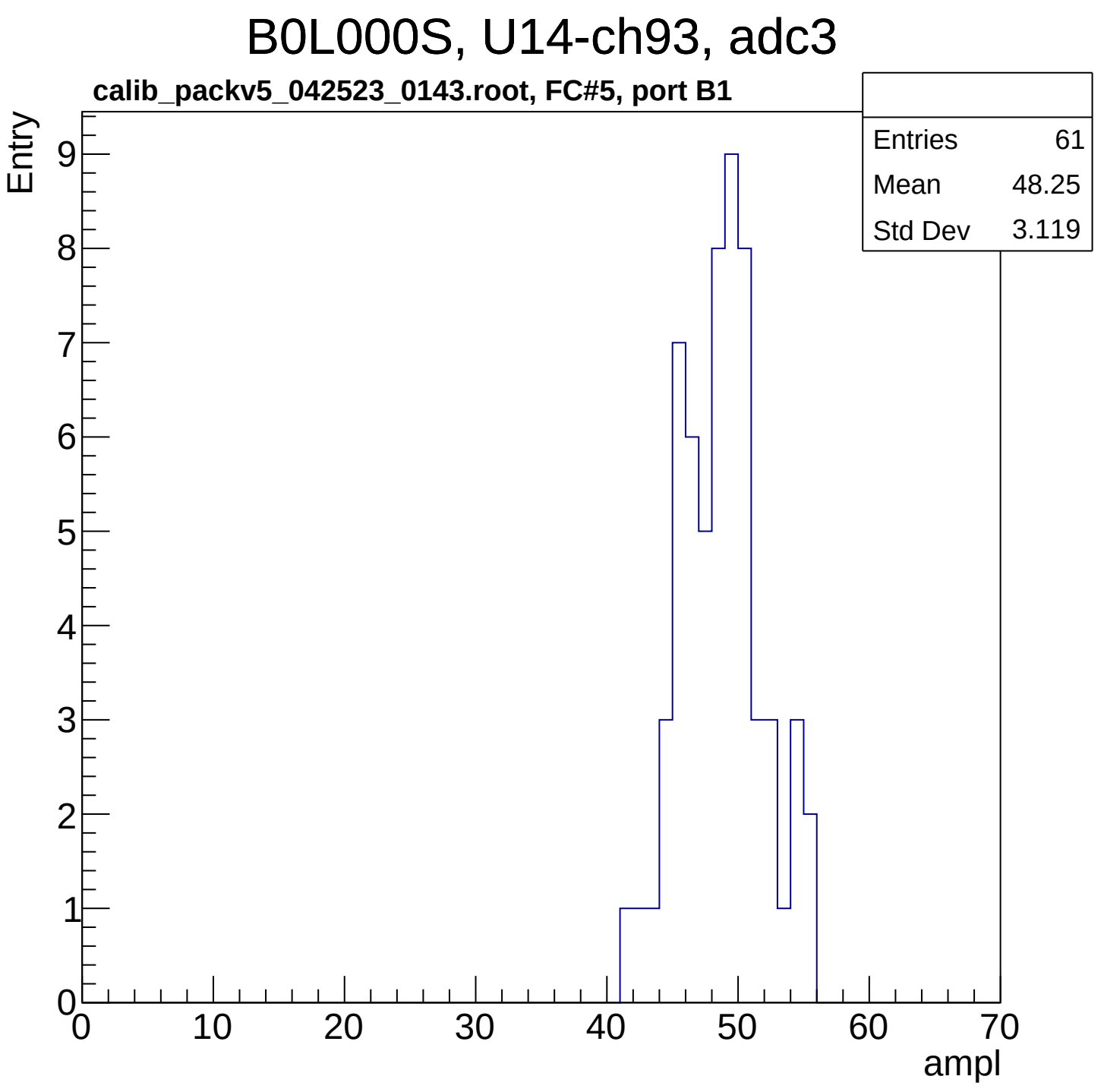
B0L000S, U14-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.25
Std Dev 3.119
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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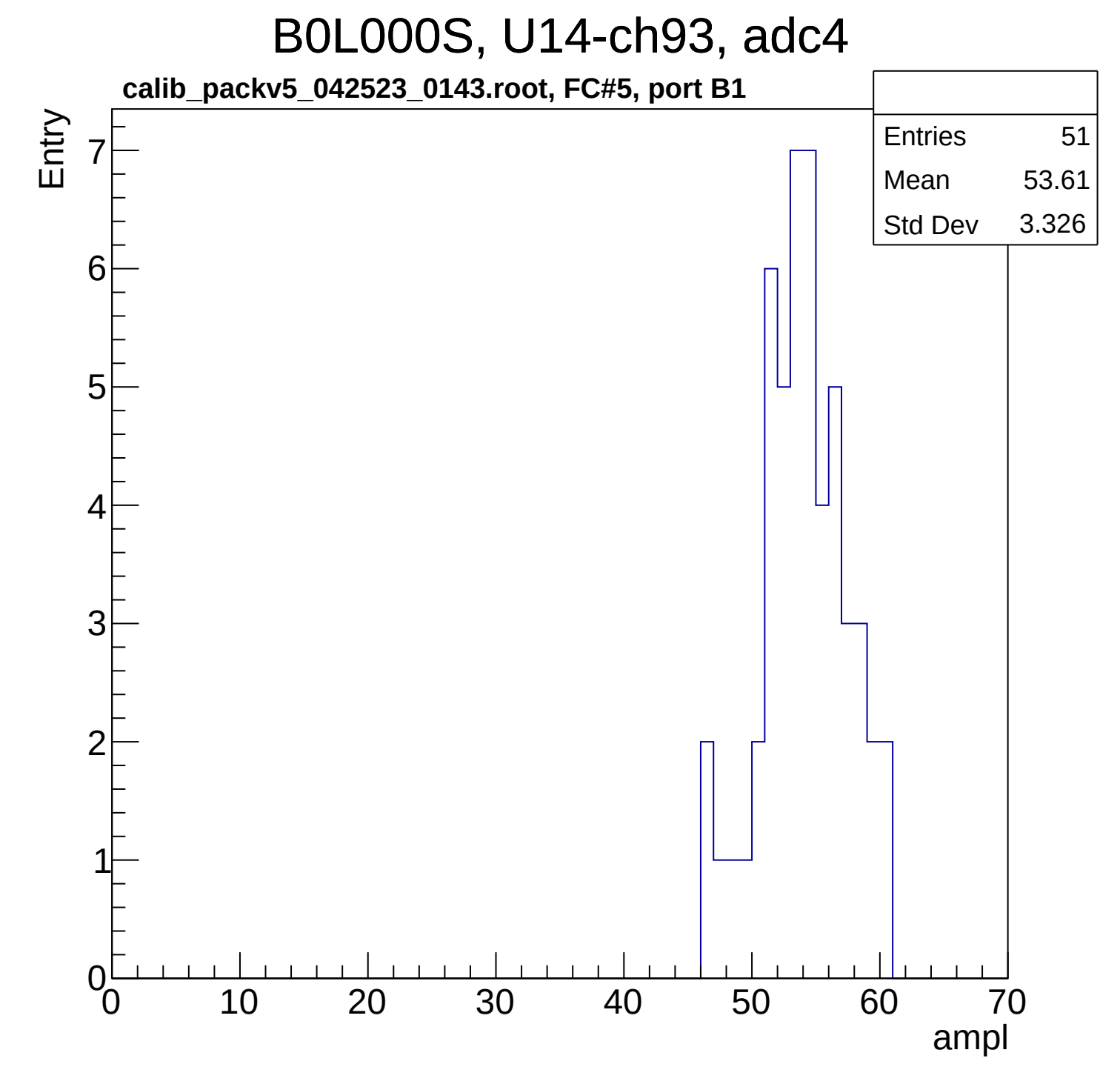
B0L000S, U14-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 53.61
Std Dev 3.326
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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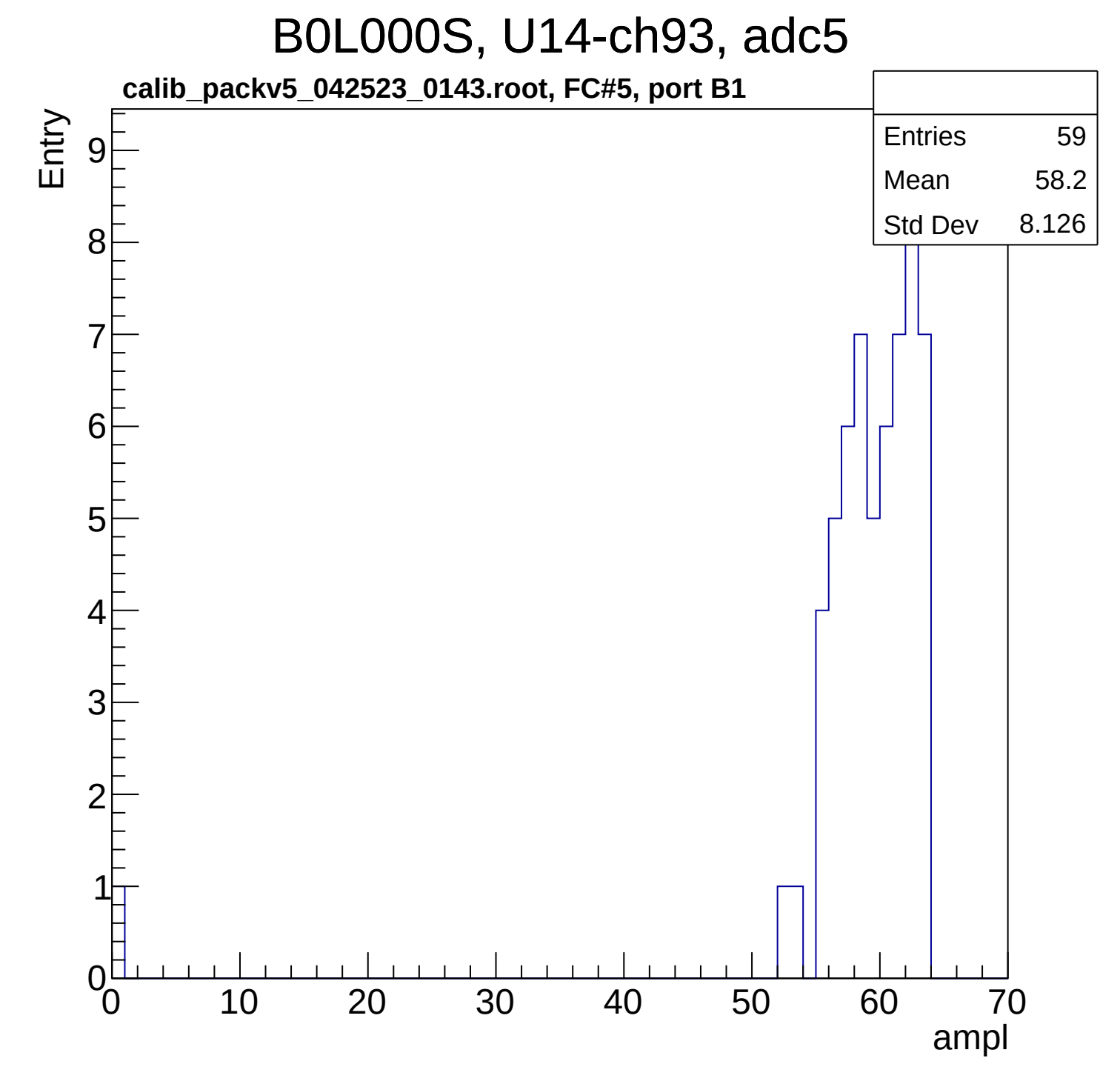
B0L000S, U14-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 58.2
Std Dev 8.126
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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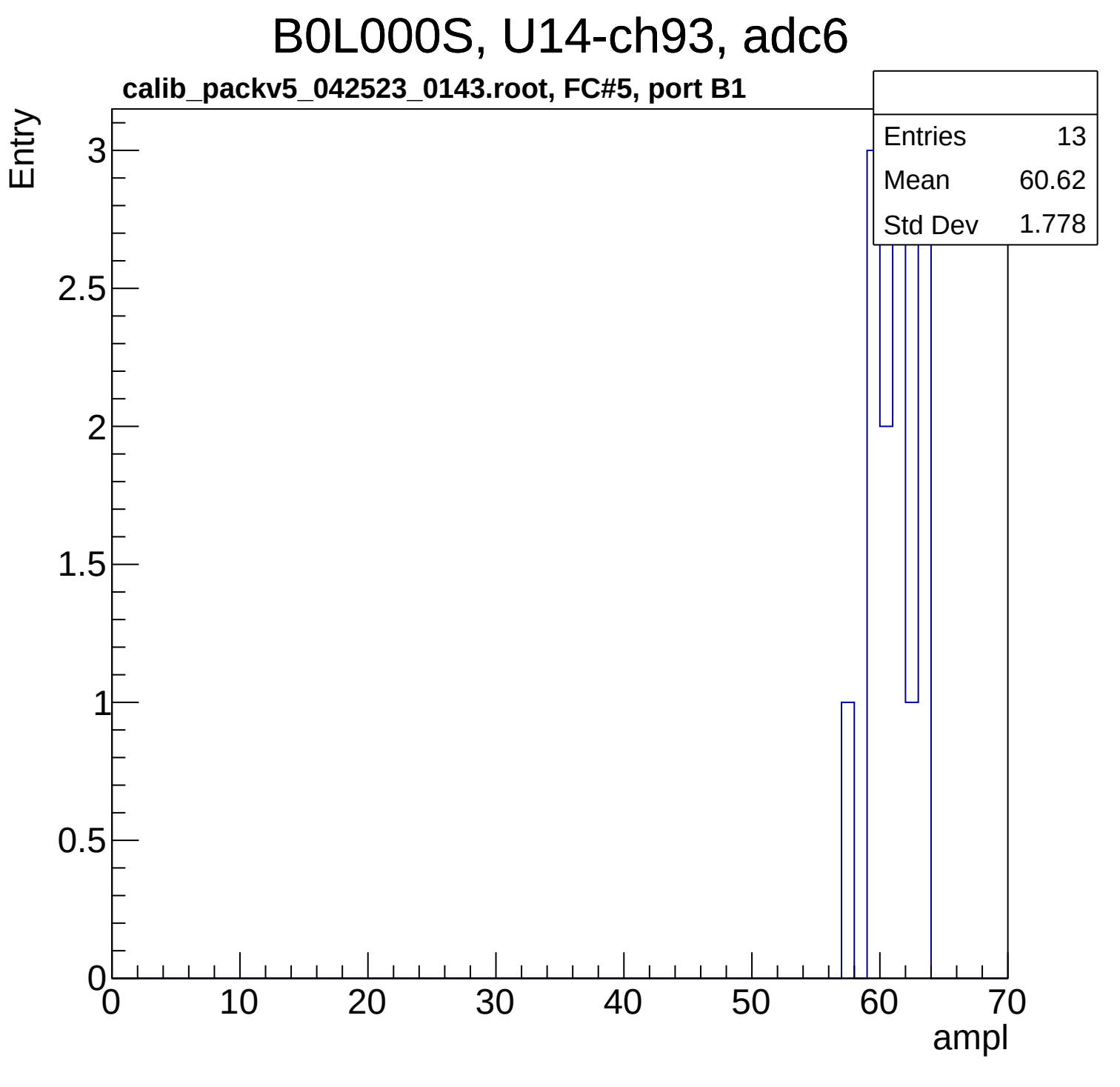
B0L000S, U14-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 60.62
Std Dev 1.778
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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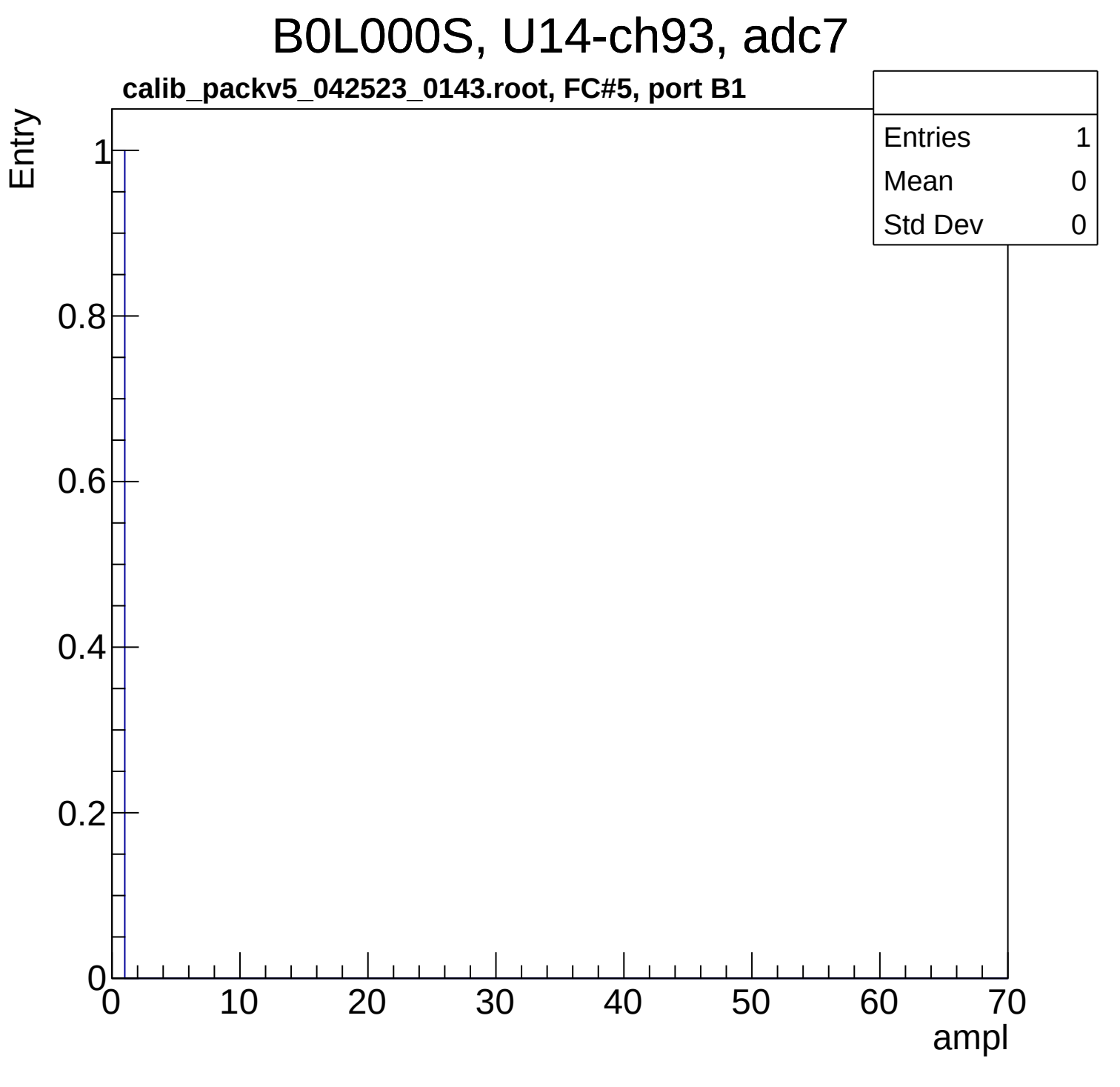
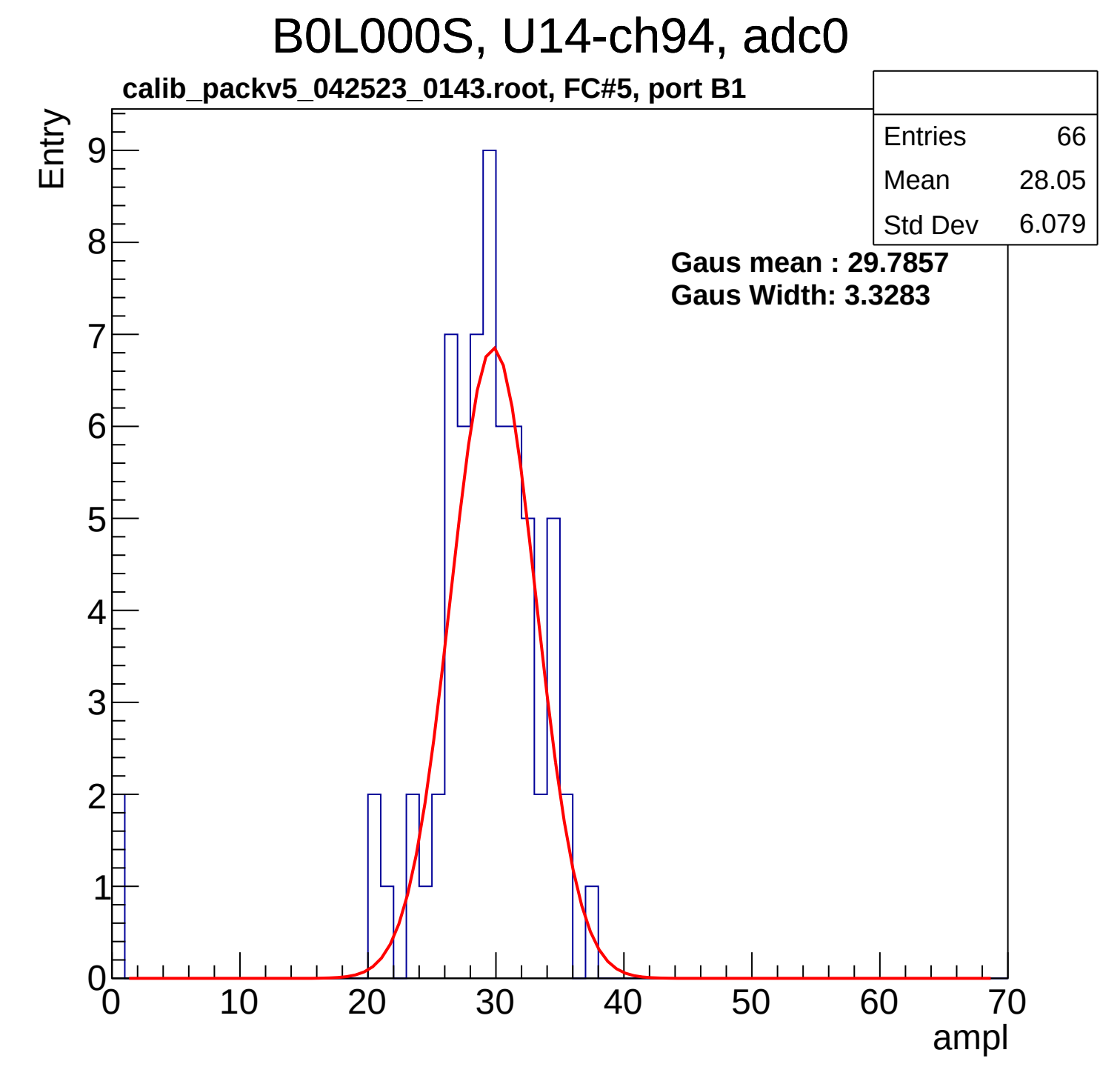
40
50
60
70


B0L000S, U14-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28.05
Std Dev 6.079
Gaus mean : 29.7857
Gaus Width: 3.3283
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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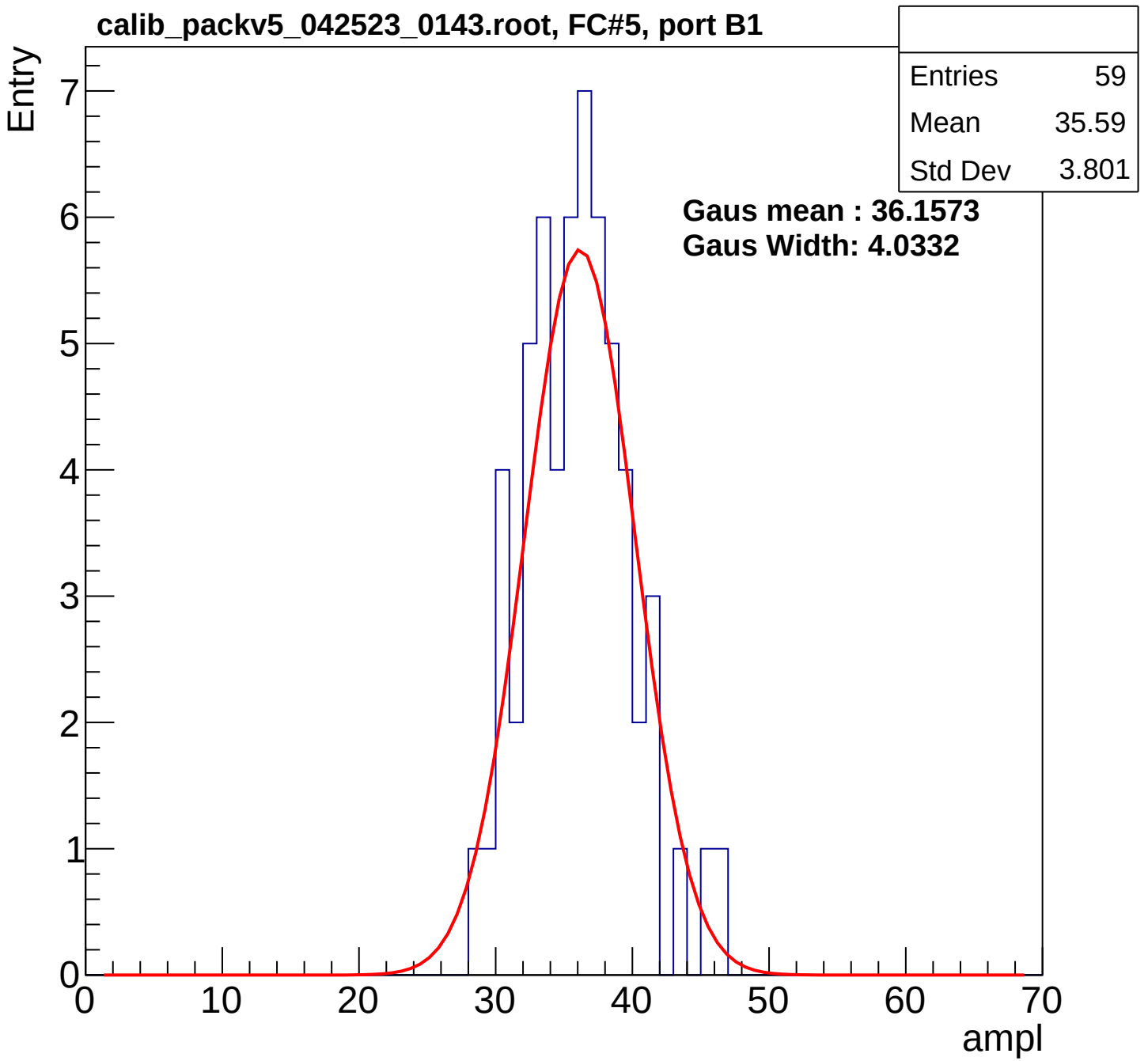
B0L000S, U14-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.59
Std Dev 3.801
Gaus mean : 36.1573
Gaus Width: 4.0332
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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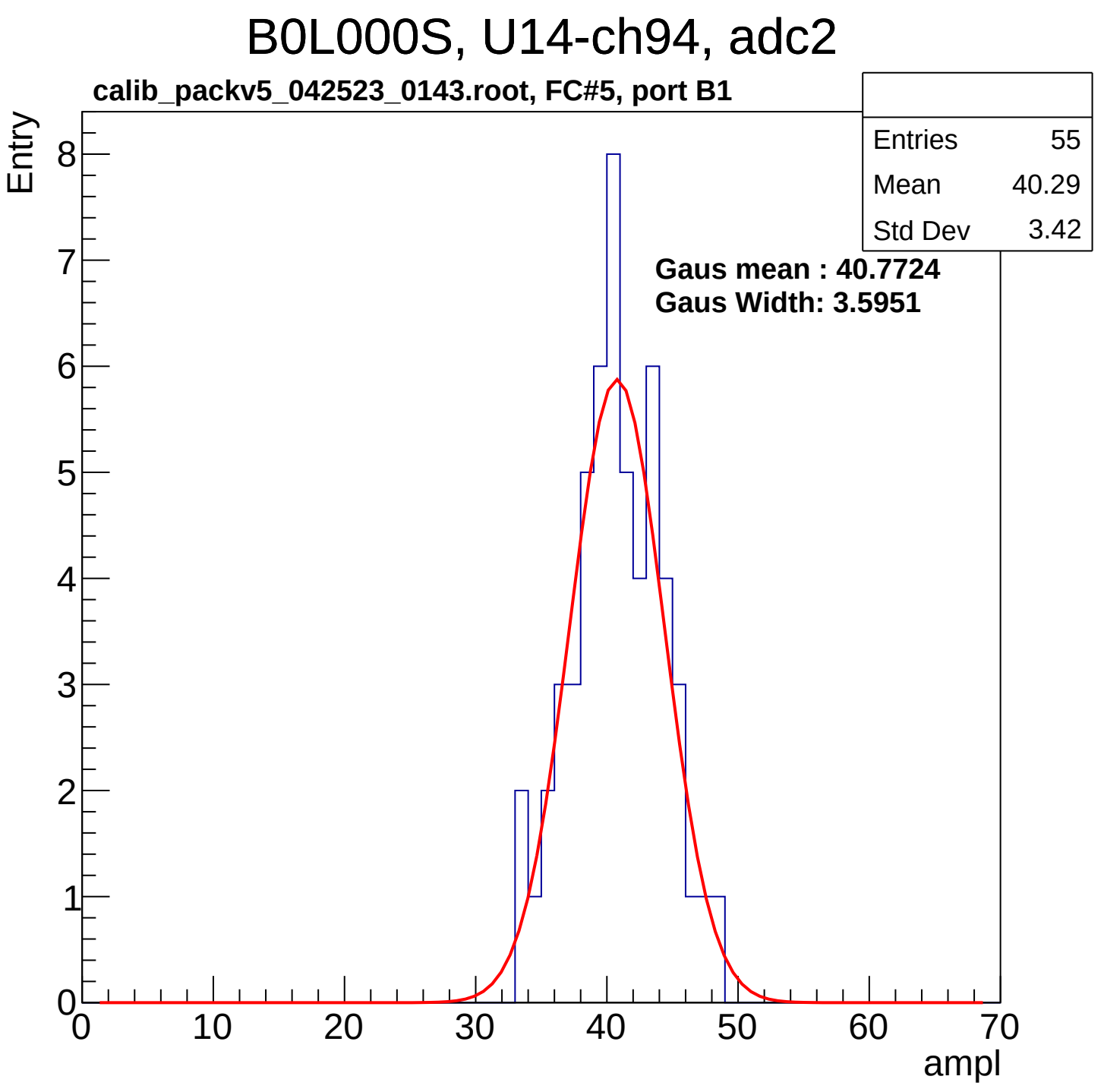
B0L000S, U14-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 40.29
Std Dev 3.42
Gaus mean : 40.7724
Gaus Width: 3.5951
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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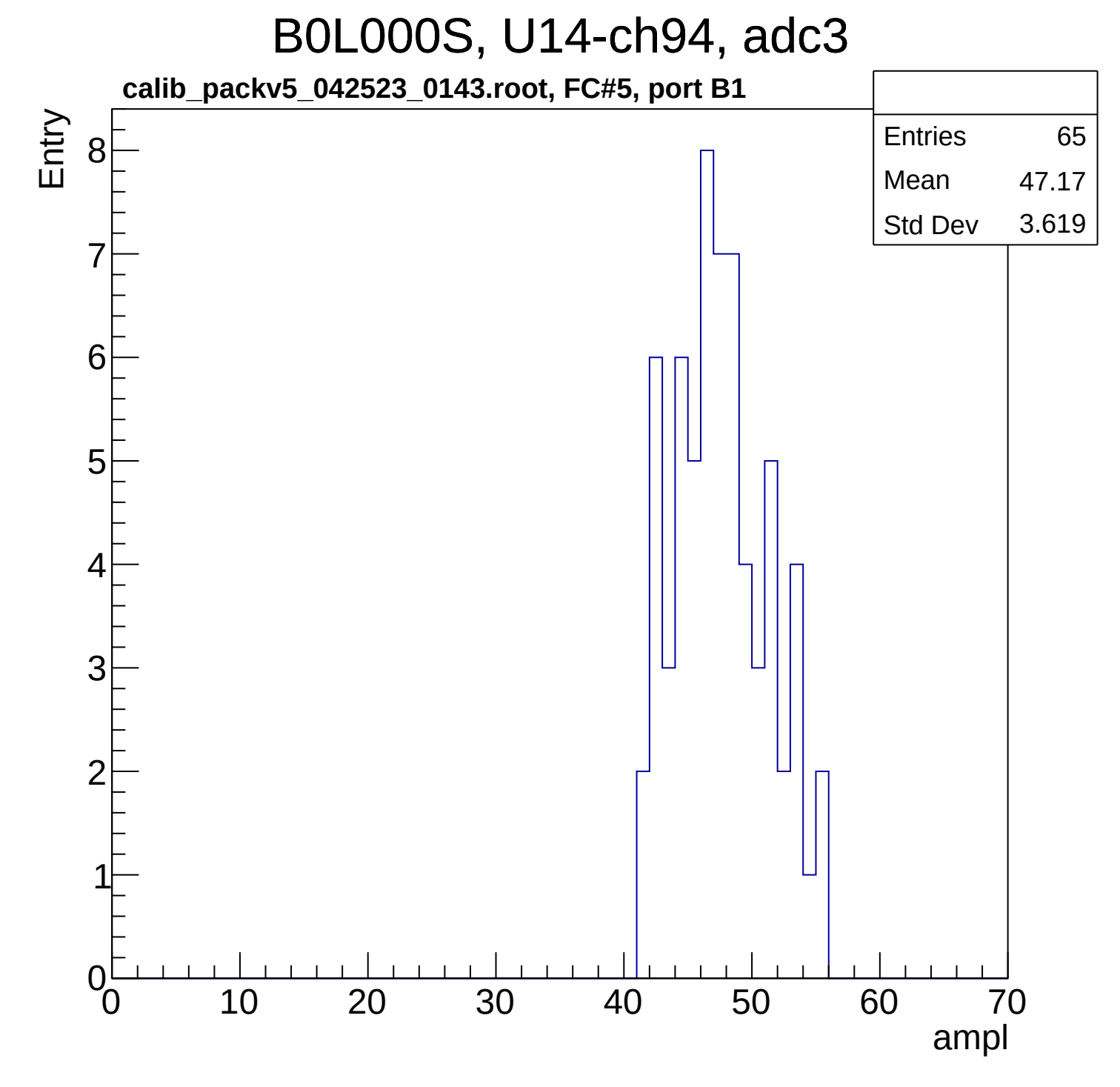
B0L000S, U14-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 47.17
Std Dev 3.619
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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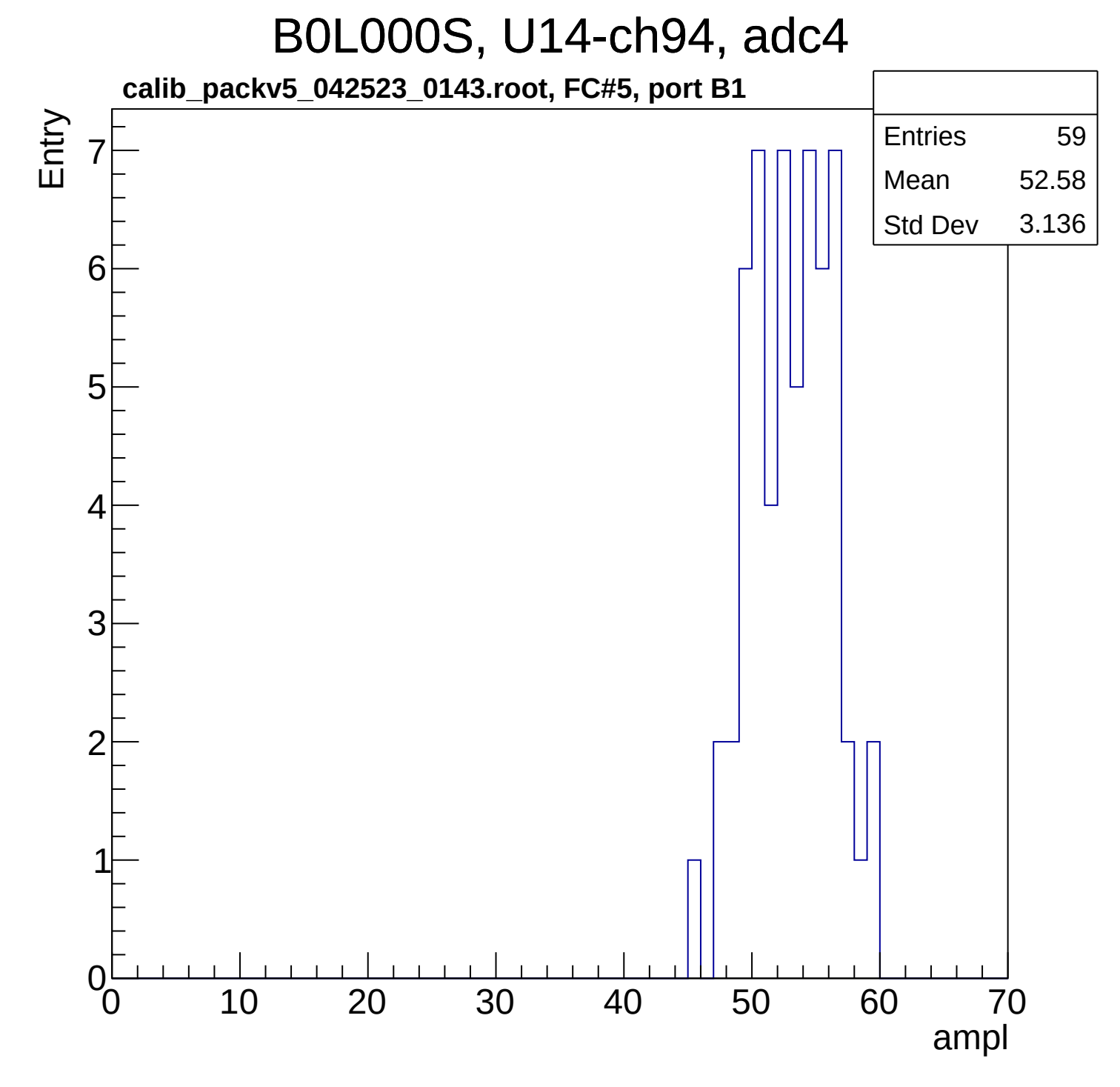
B0L000S, U14-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 52.58
Std Dev 3.136
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
30
40
50
60
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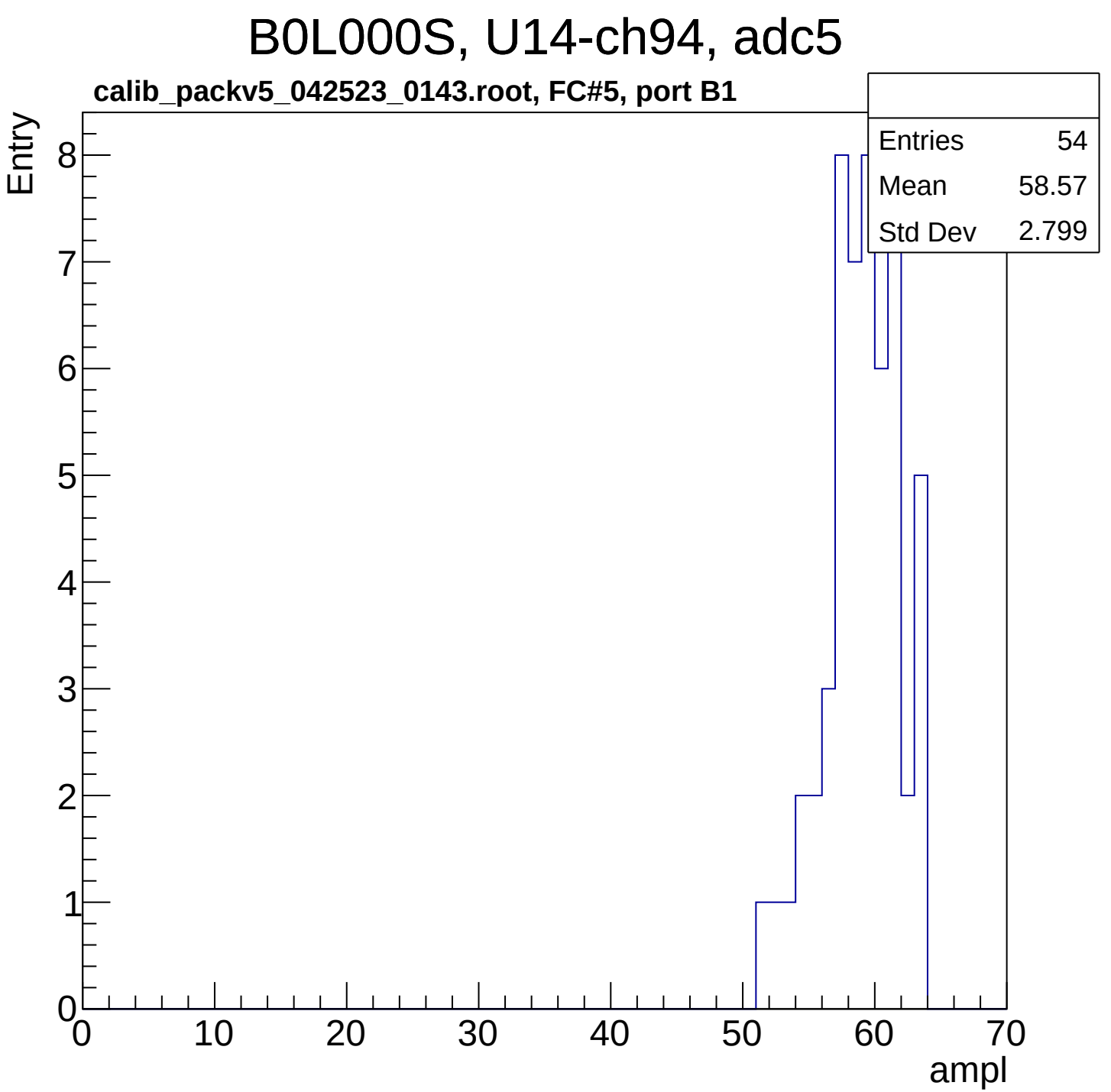
B0L000S, U14-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.57
Std Dev 2.799
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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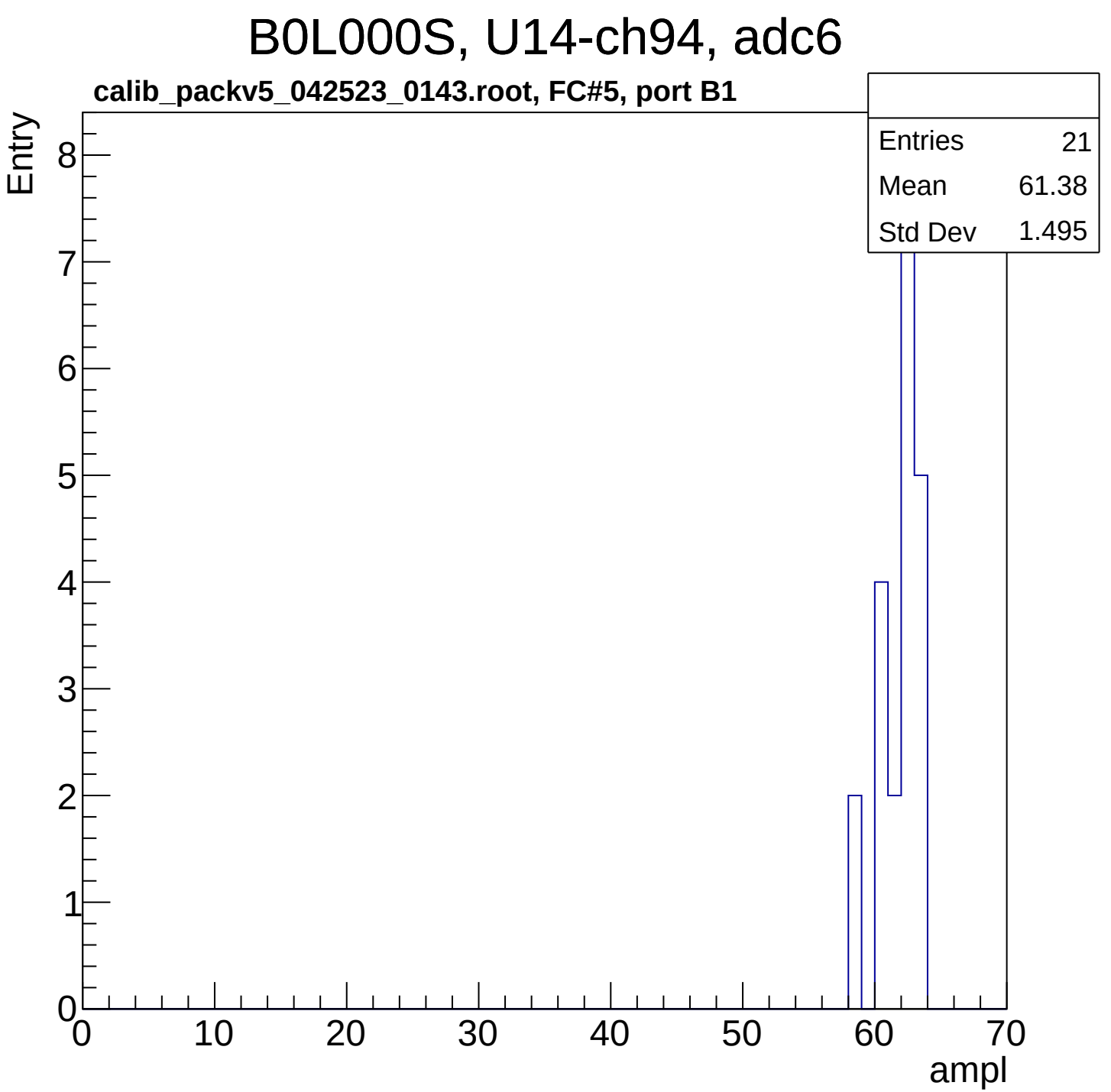
B0L000S, U14-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 61.38
Std Dev 1.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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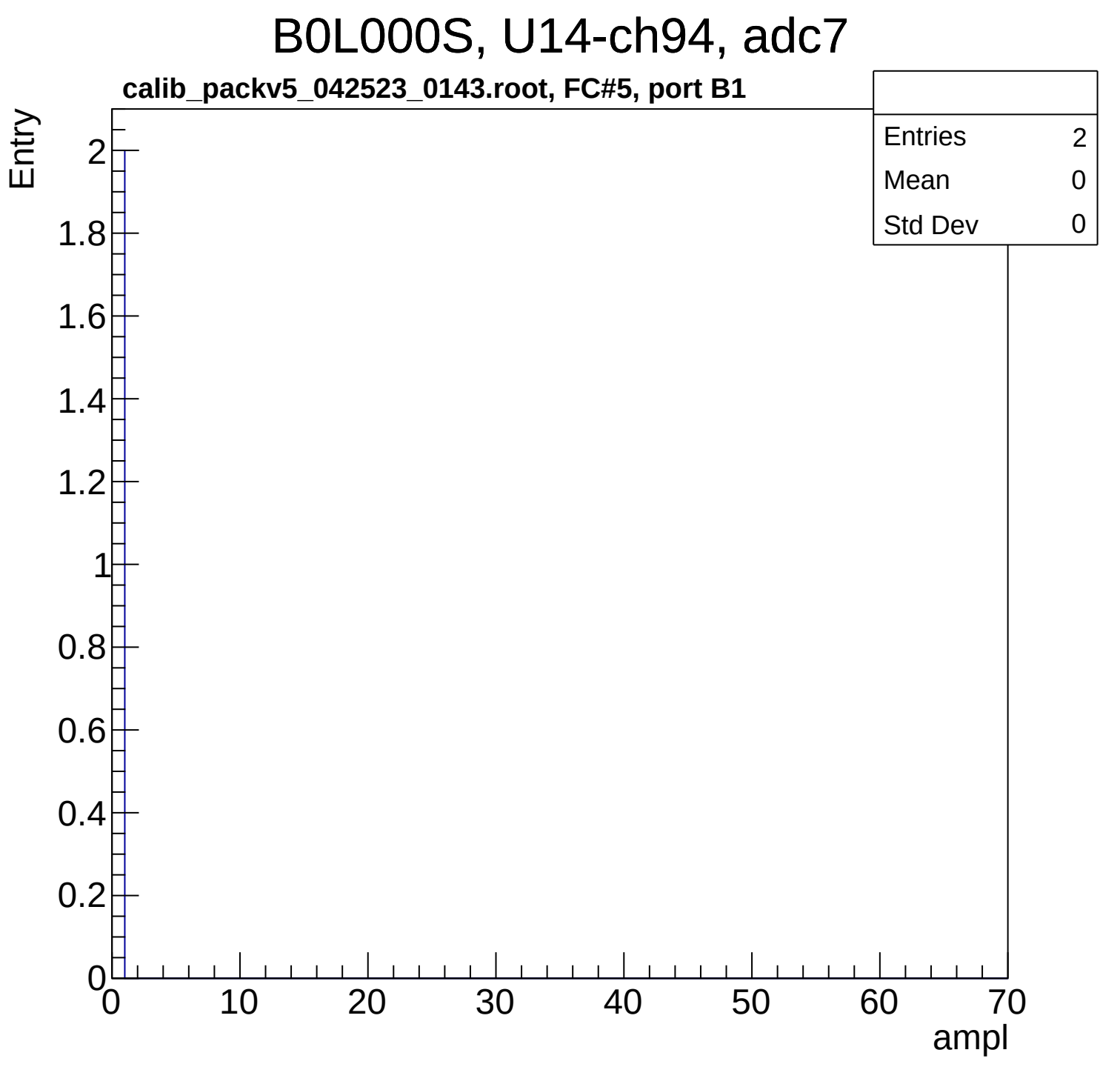
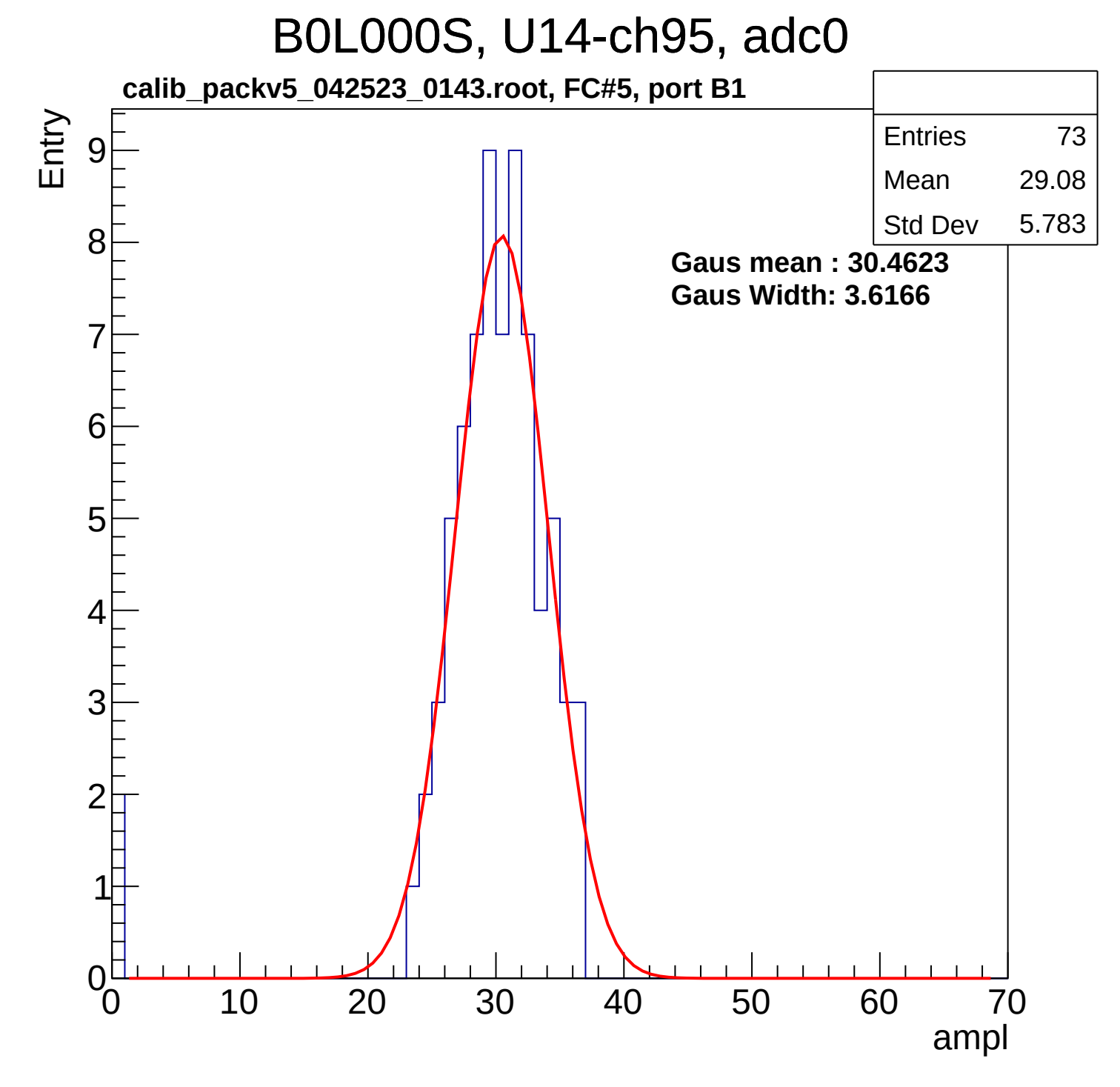

B0L000S, U14-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.08
Std Dev 5.783
Gaus mean : 30.4623
Gaus Width: 3.6166
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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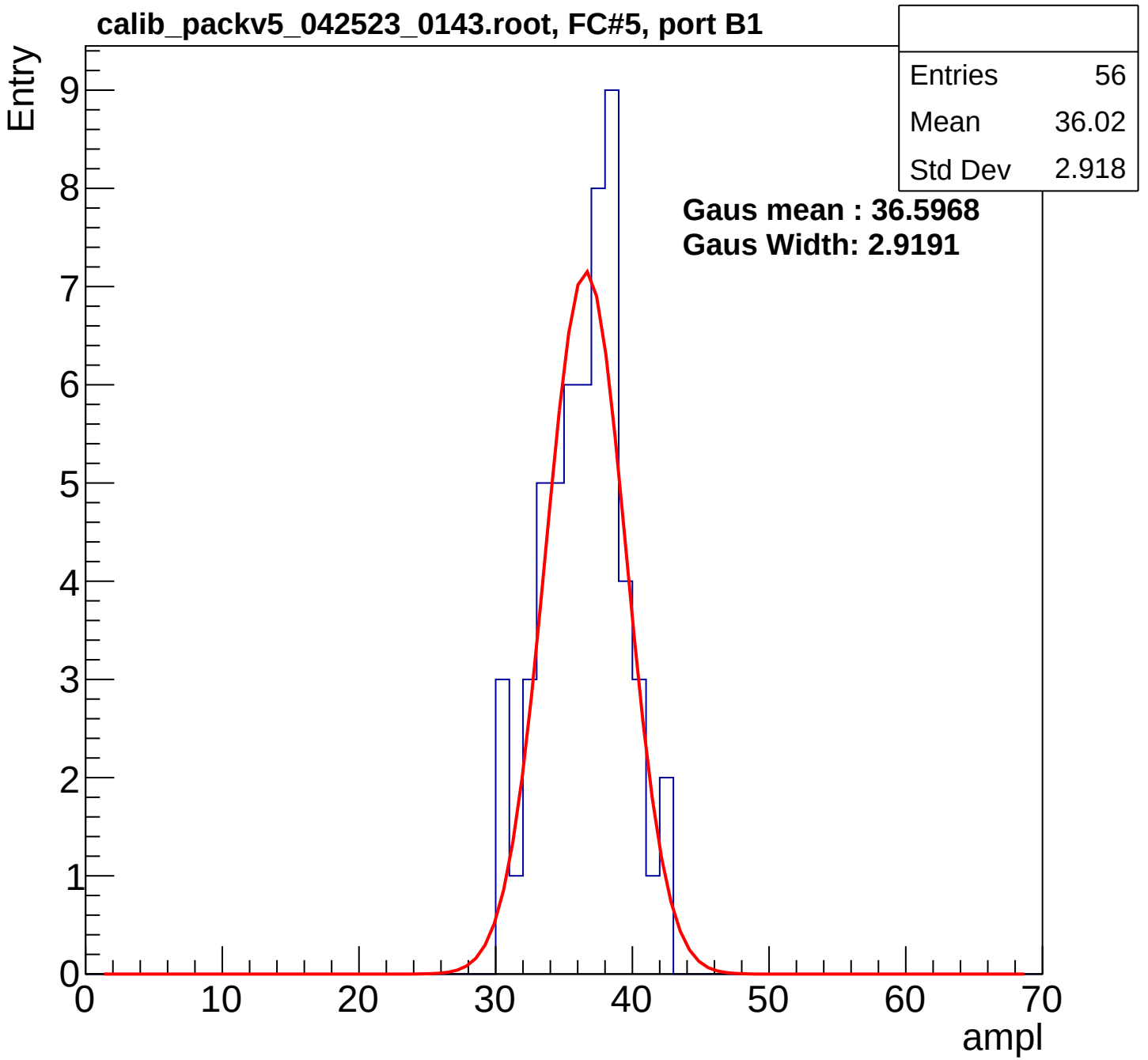
B0L000S, U14-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.02
Std Dev 2.918
Gaus mean : 36.5968
Gaus Width: 2.9191
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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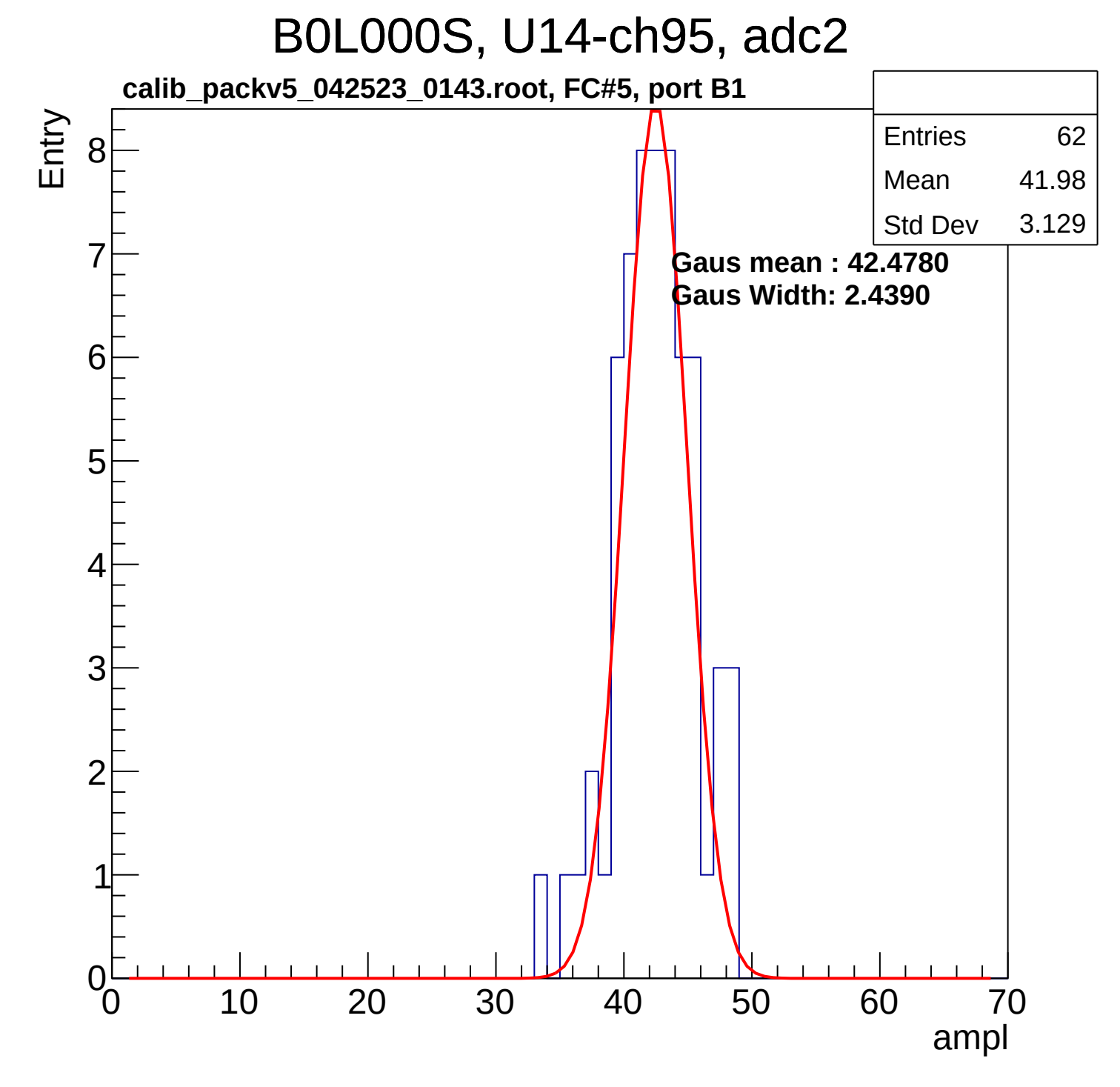
B0L000S, U14-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.98
Std Dev 3.129
Gaus mean : 42.4780
Gaus Width: 2.4390
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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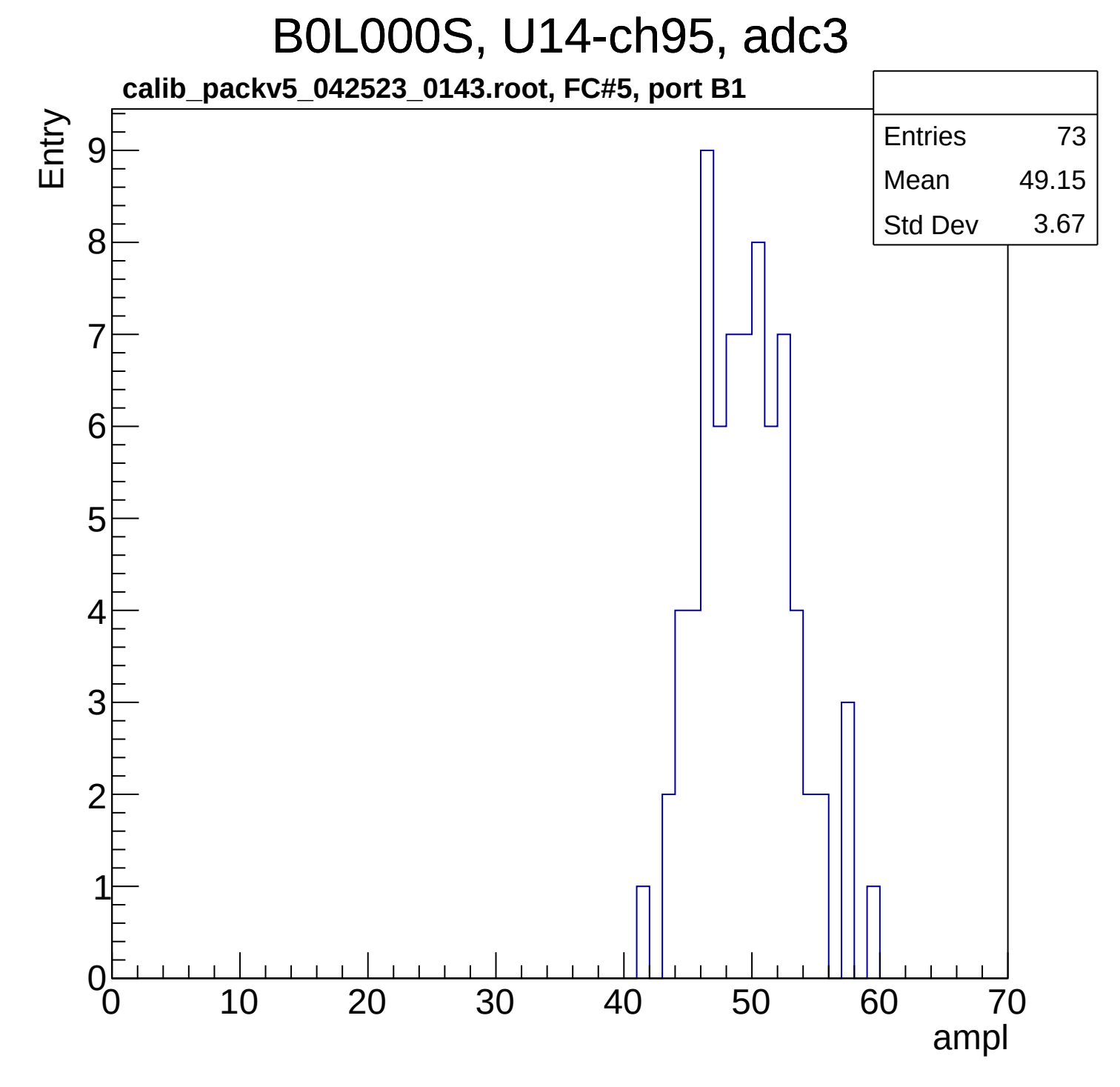
B0L000S, U14-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.15
Std Dev 3.67
Entry
ampl
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2
3
4
5
6
7
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0
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60
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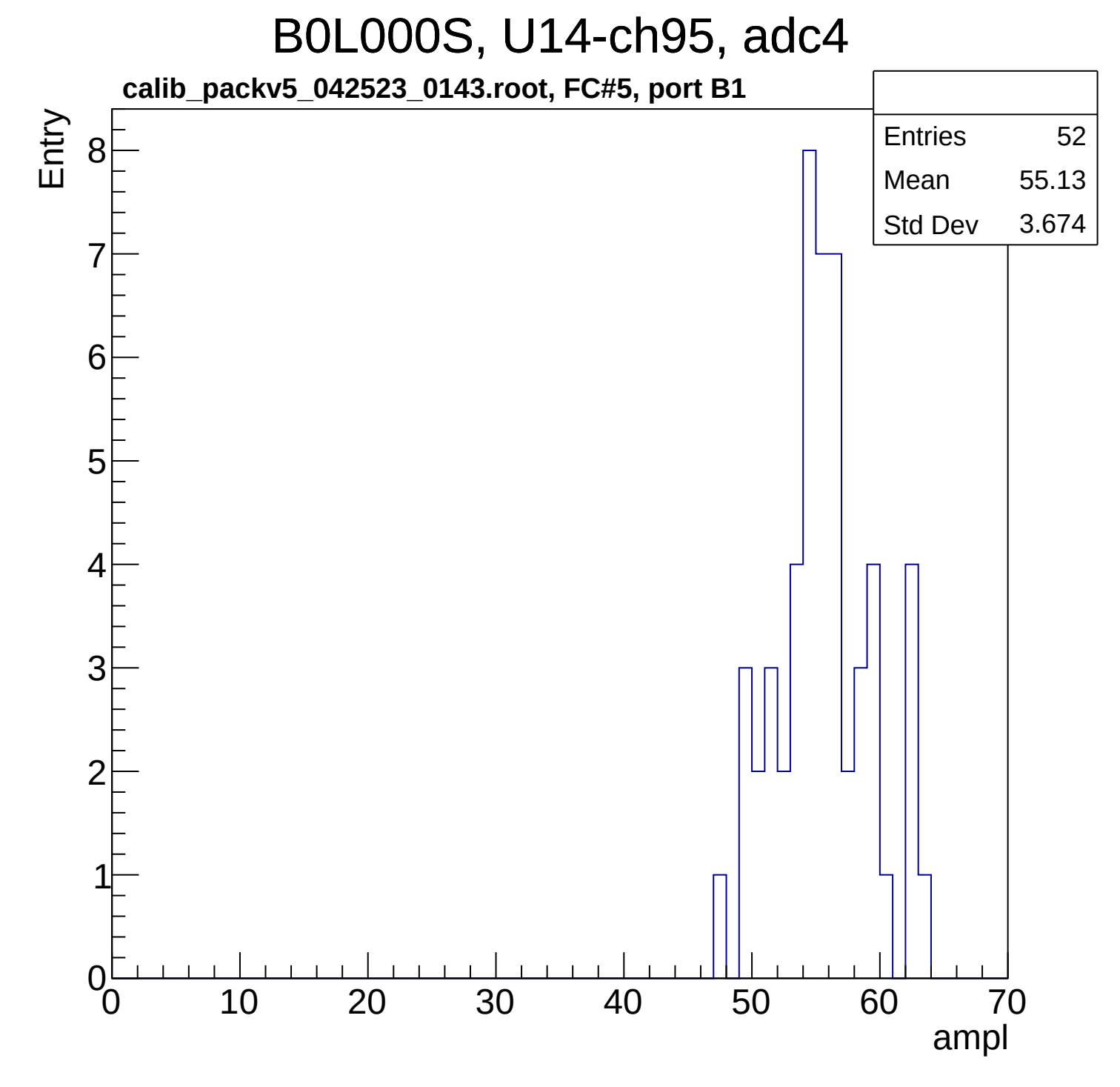
B0L000S, U14-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.13
Std Dev 3.674
Entry
ampl
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1
2
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5
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7
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0
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60
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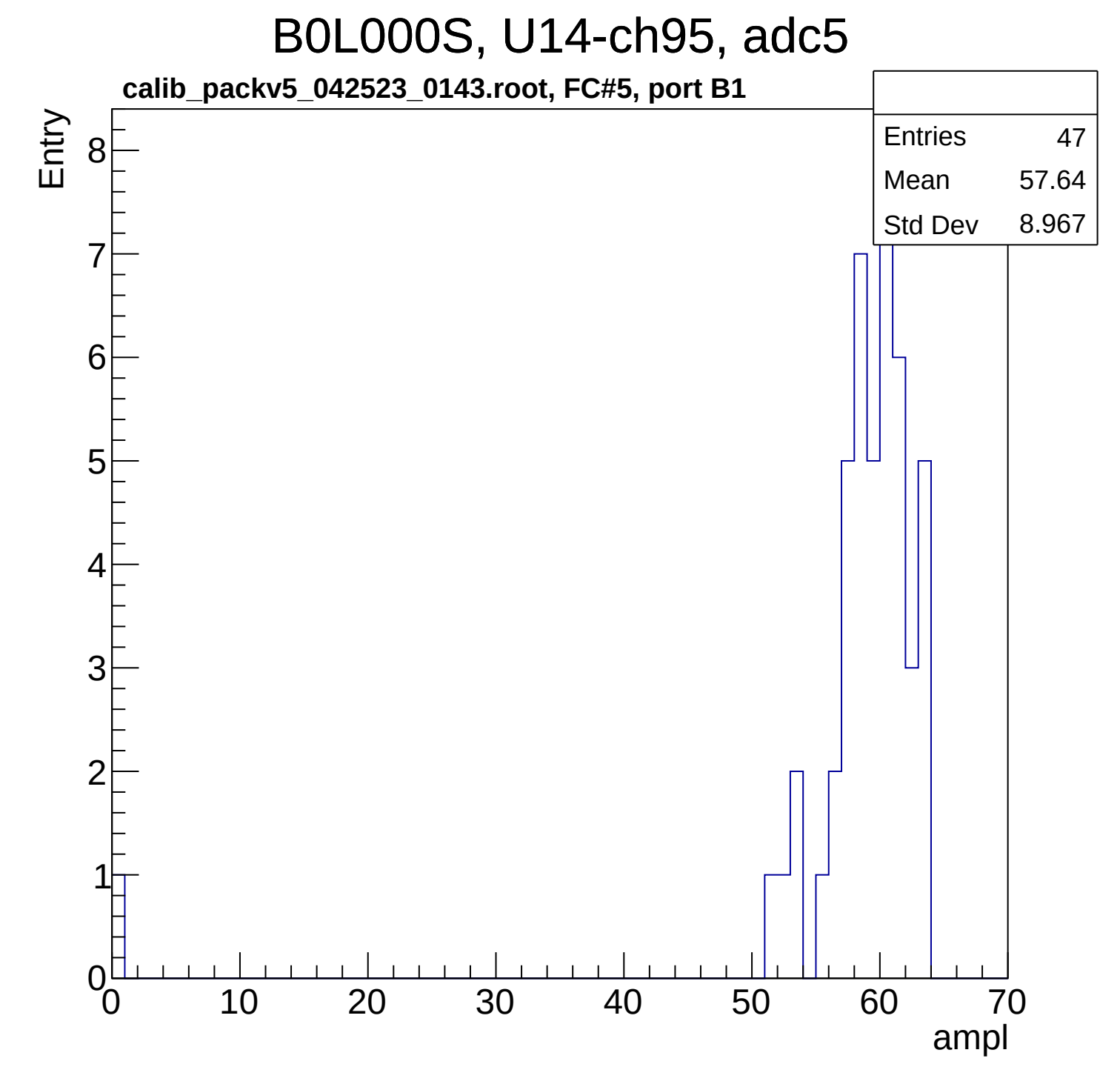
B0L000S, U14-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.64
Std Dev 8.967
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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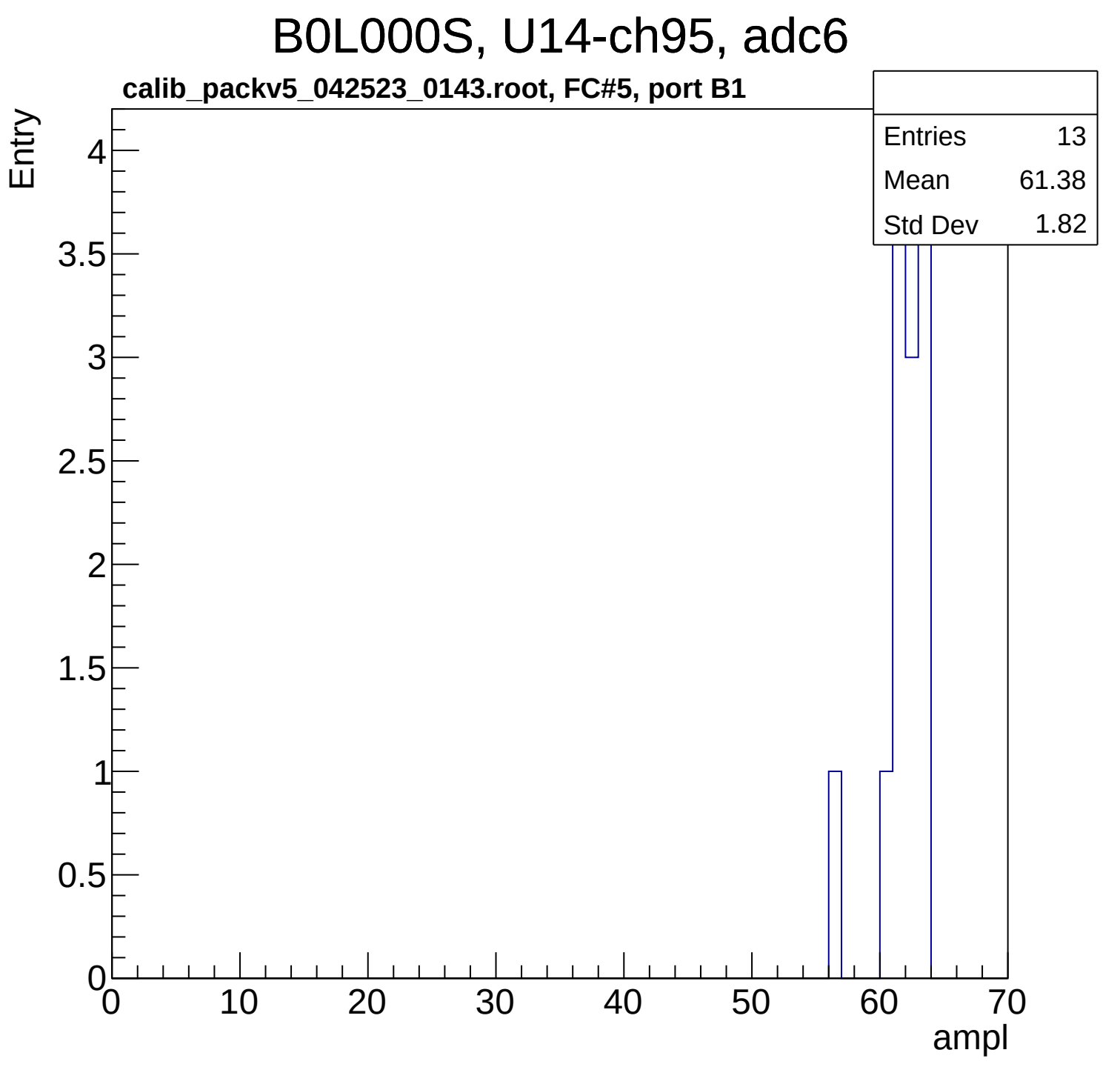
B0L000S, U14-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 61.38
Std Dev 1.82
Entry
ampl
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1.5
2
2.5
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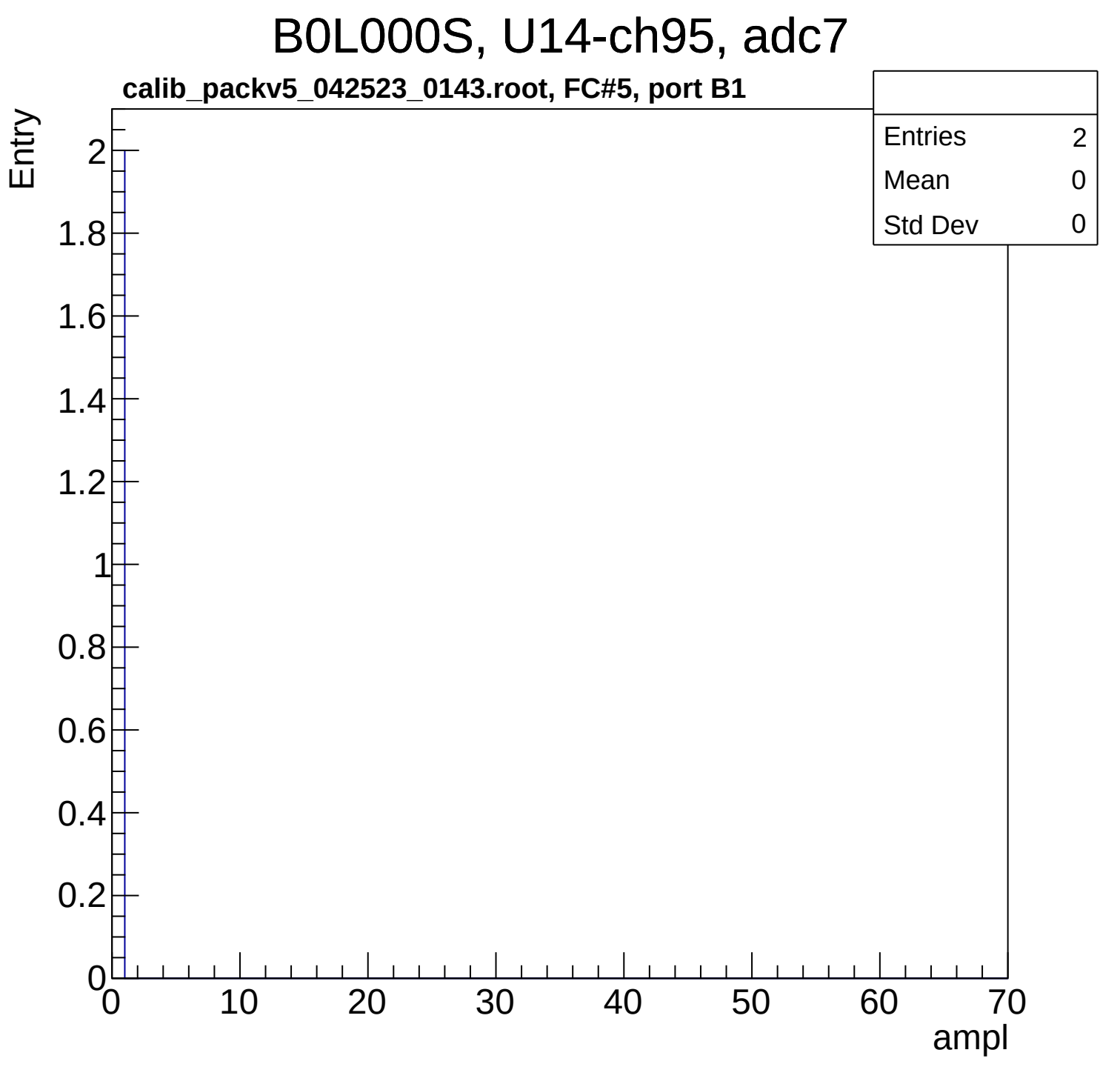
B0L000S, U14-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
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10
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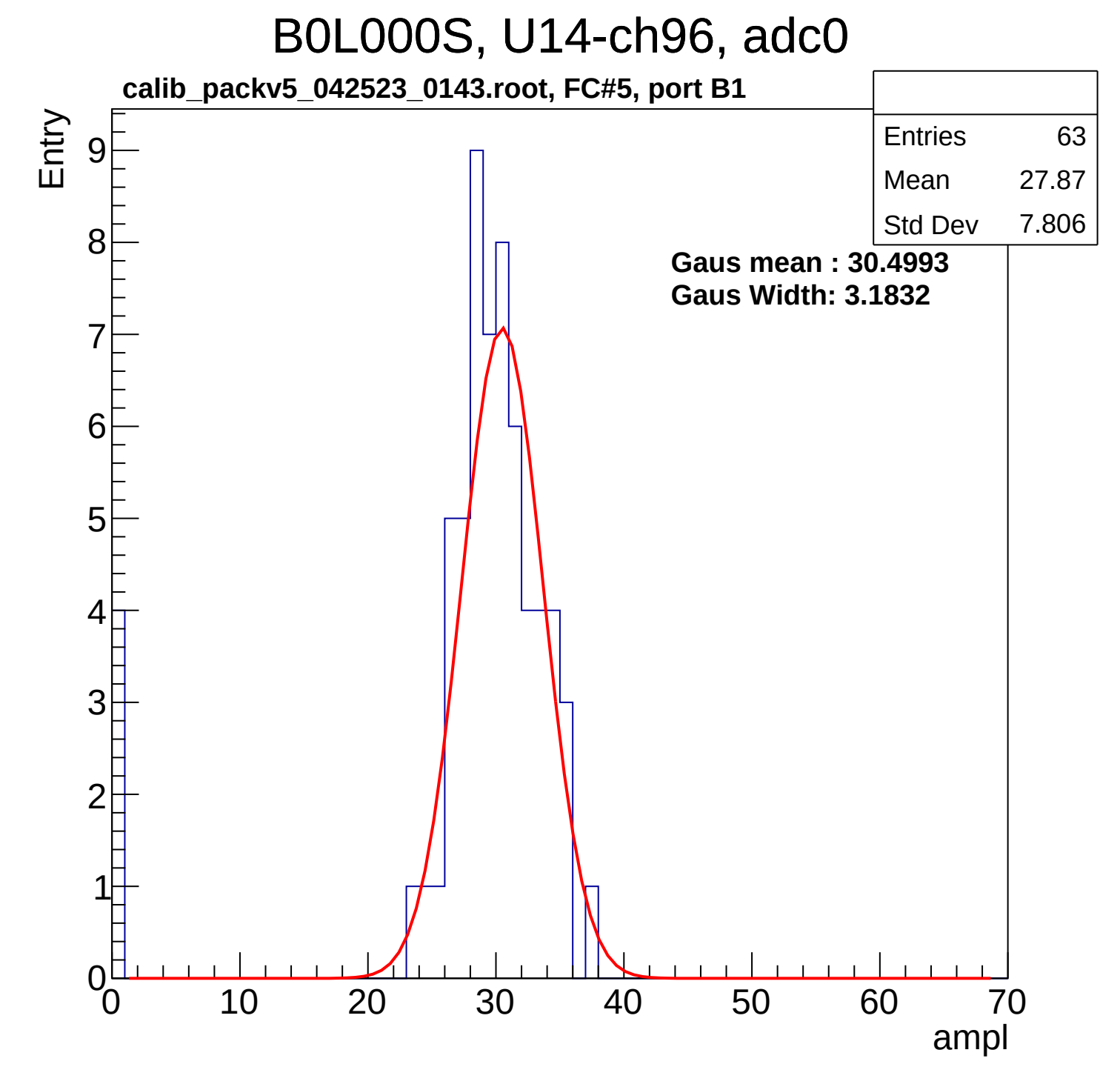
50
60
70
ampl


B0L000S, U14-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 27.87
Std Dev 7.806
Gaus mean : 30.4993
Gaus Width: 3.1832
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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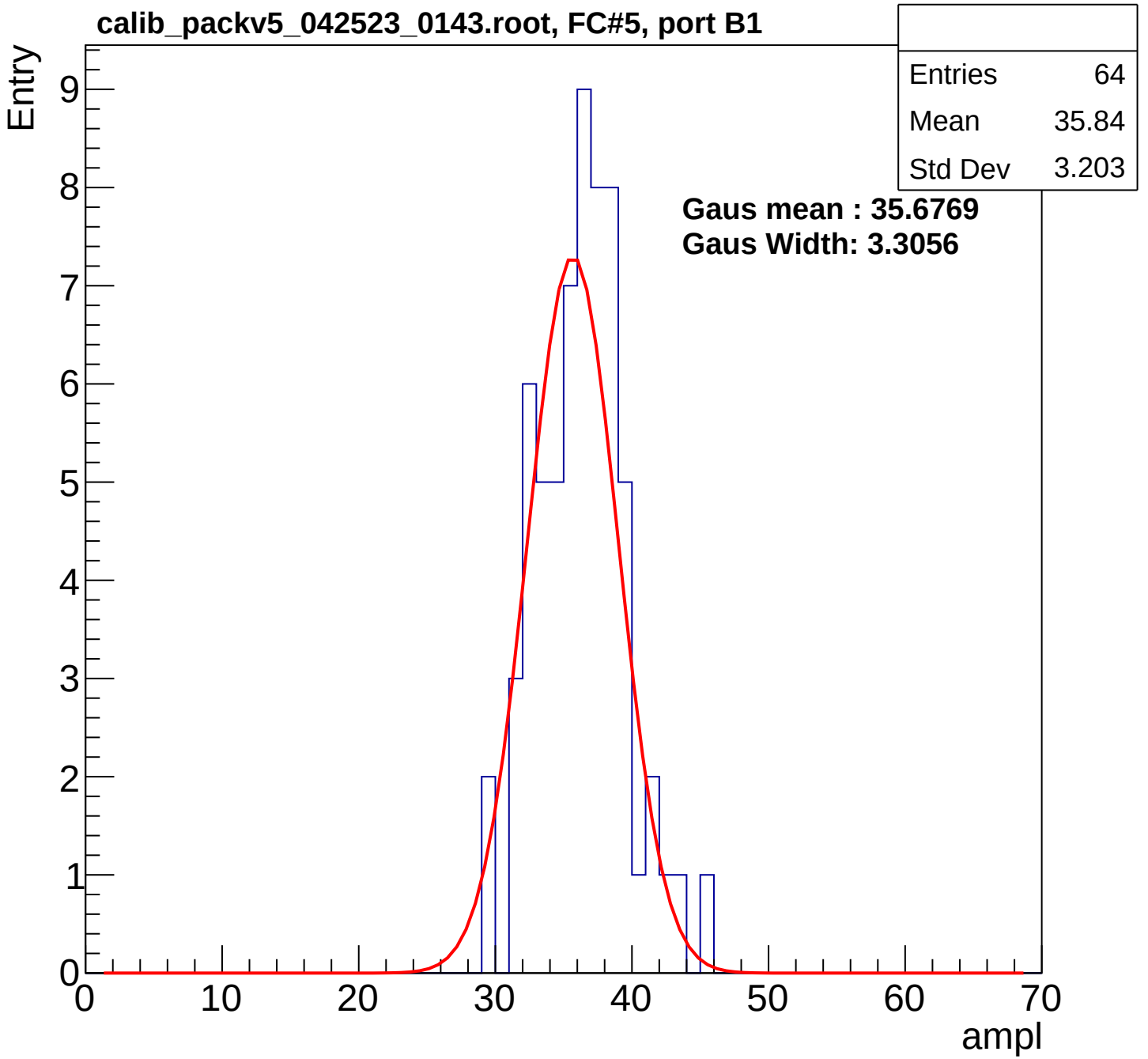
B0L000S, U14-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.84
Std Dev 3.203
Gaus mean : 35.6769
Gaus Width: 3.3056
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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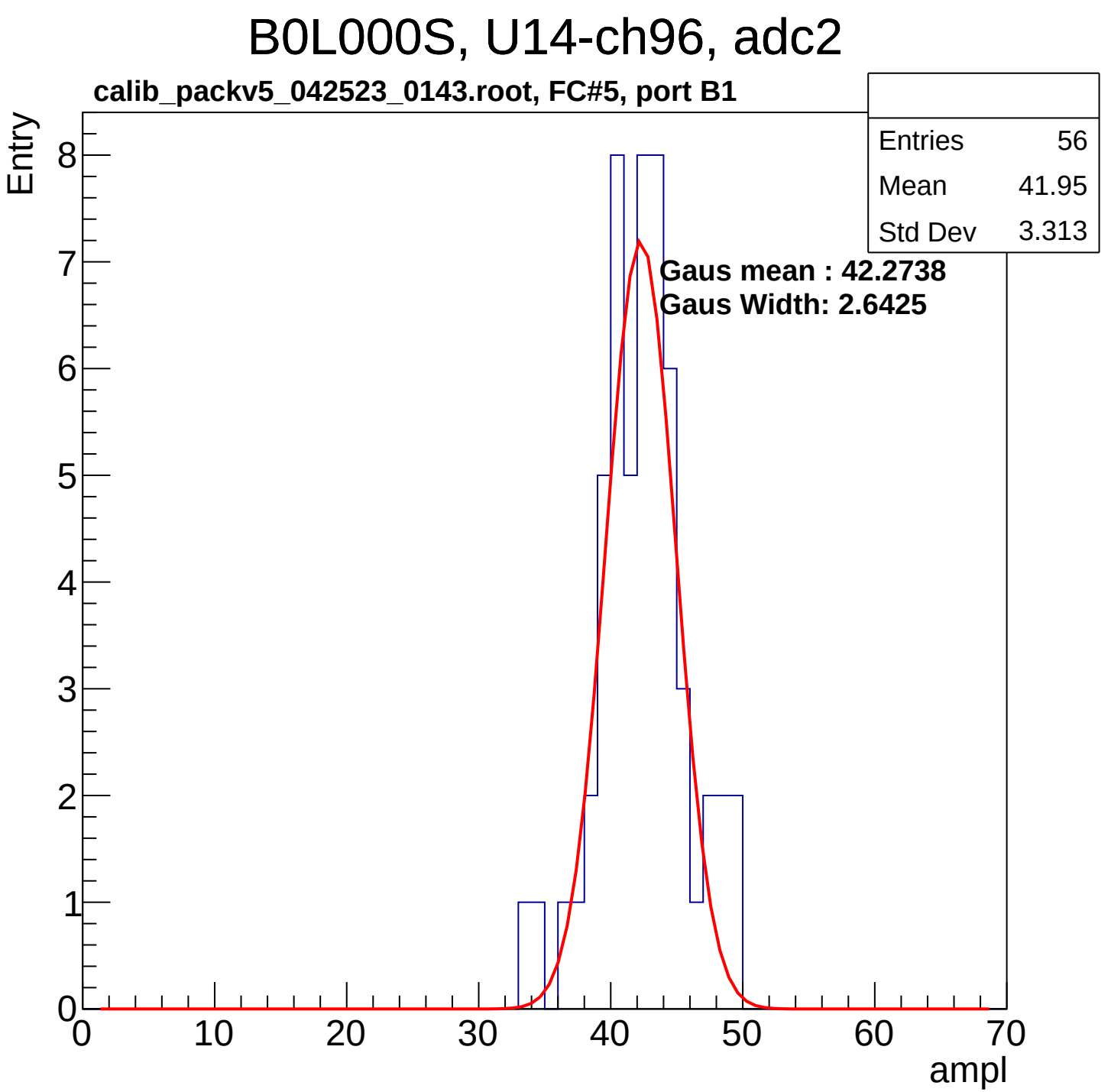
B0L000S, U14-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 41.95
Std Dev 3.313
Gaus mean : 42.2738
Gaus Width: 2.6425
Entry
ampl
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1
2
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4
5
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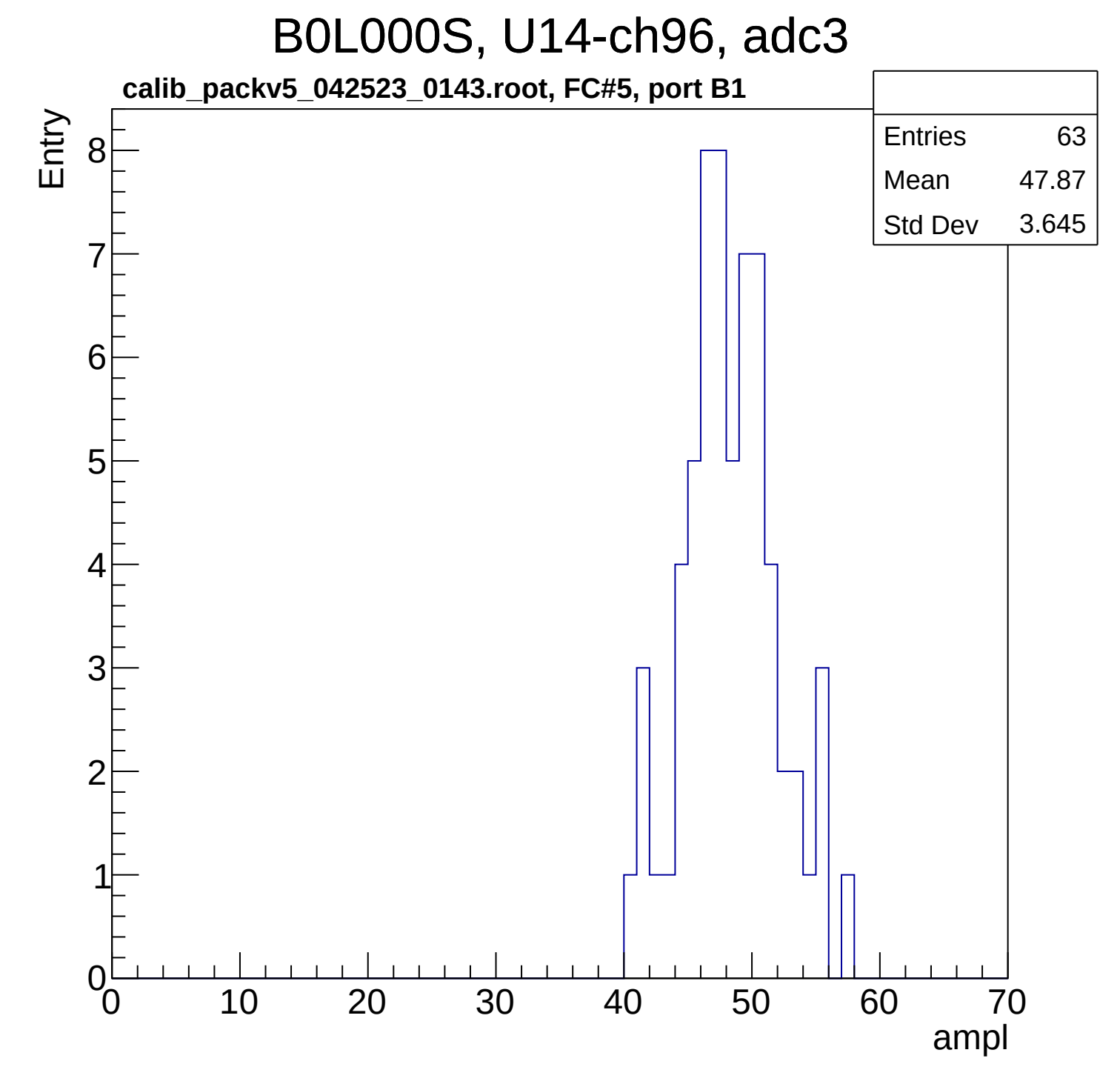
B0L000S, U14-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 47.87
Std Dev 3.645
Entry
ampl
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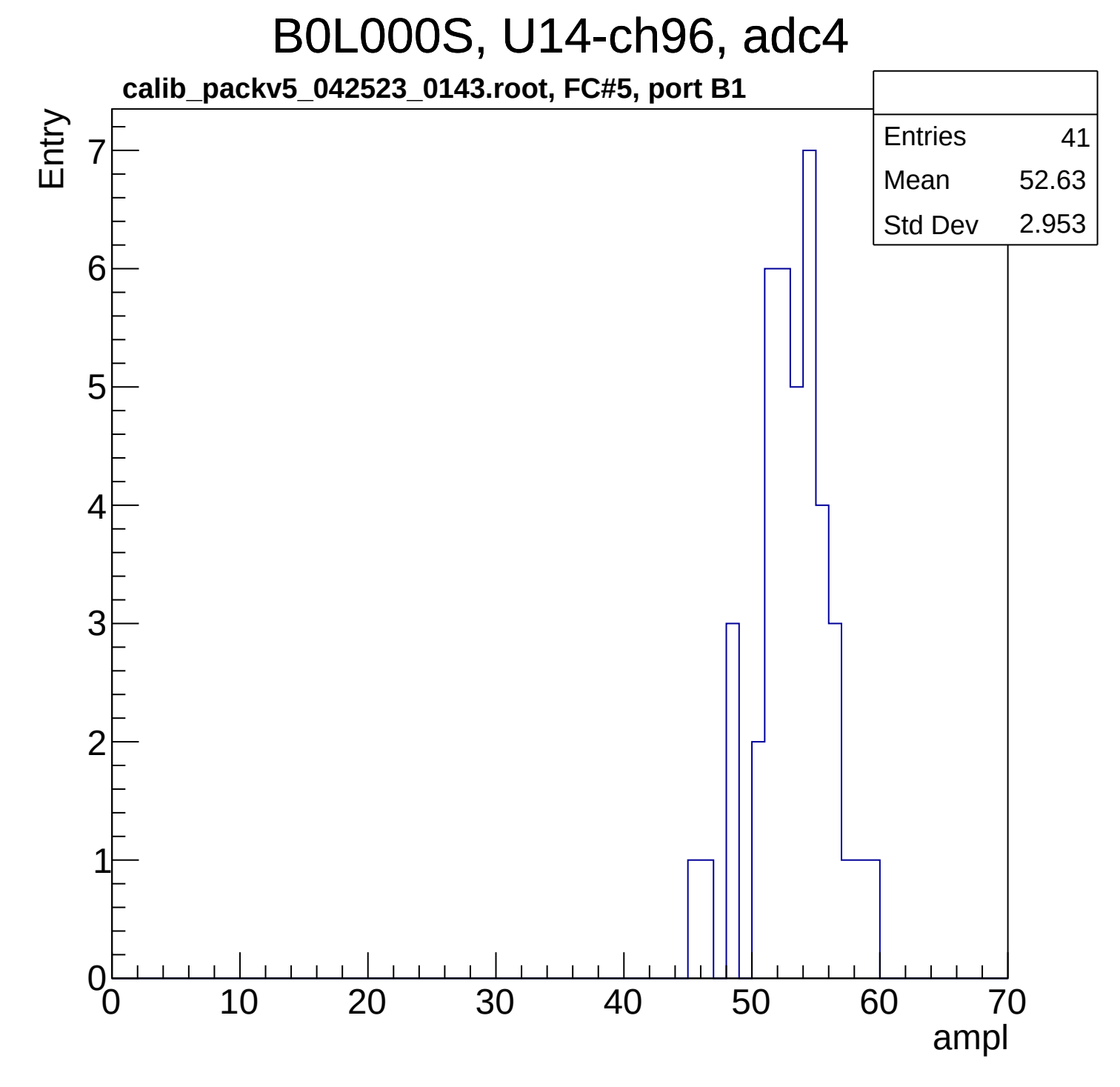
B0L000S, U14-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 52.63
Std Dev 2.953
Entry
ampl
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40
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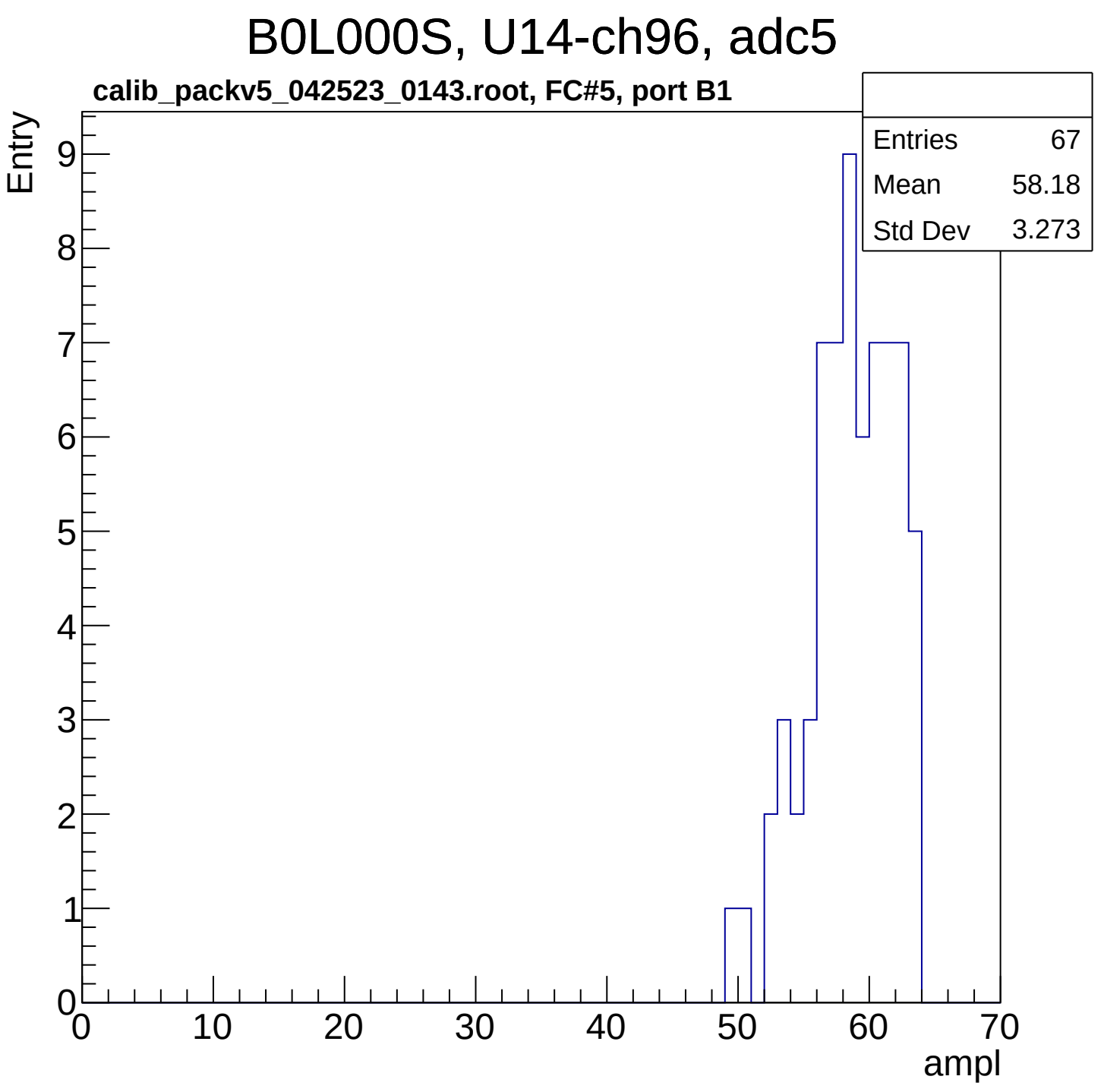
B0L000S, U14-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 58.18
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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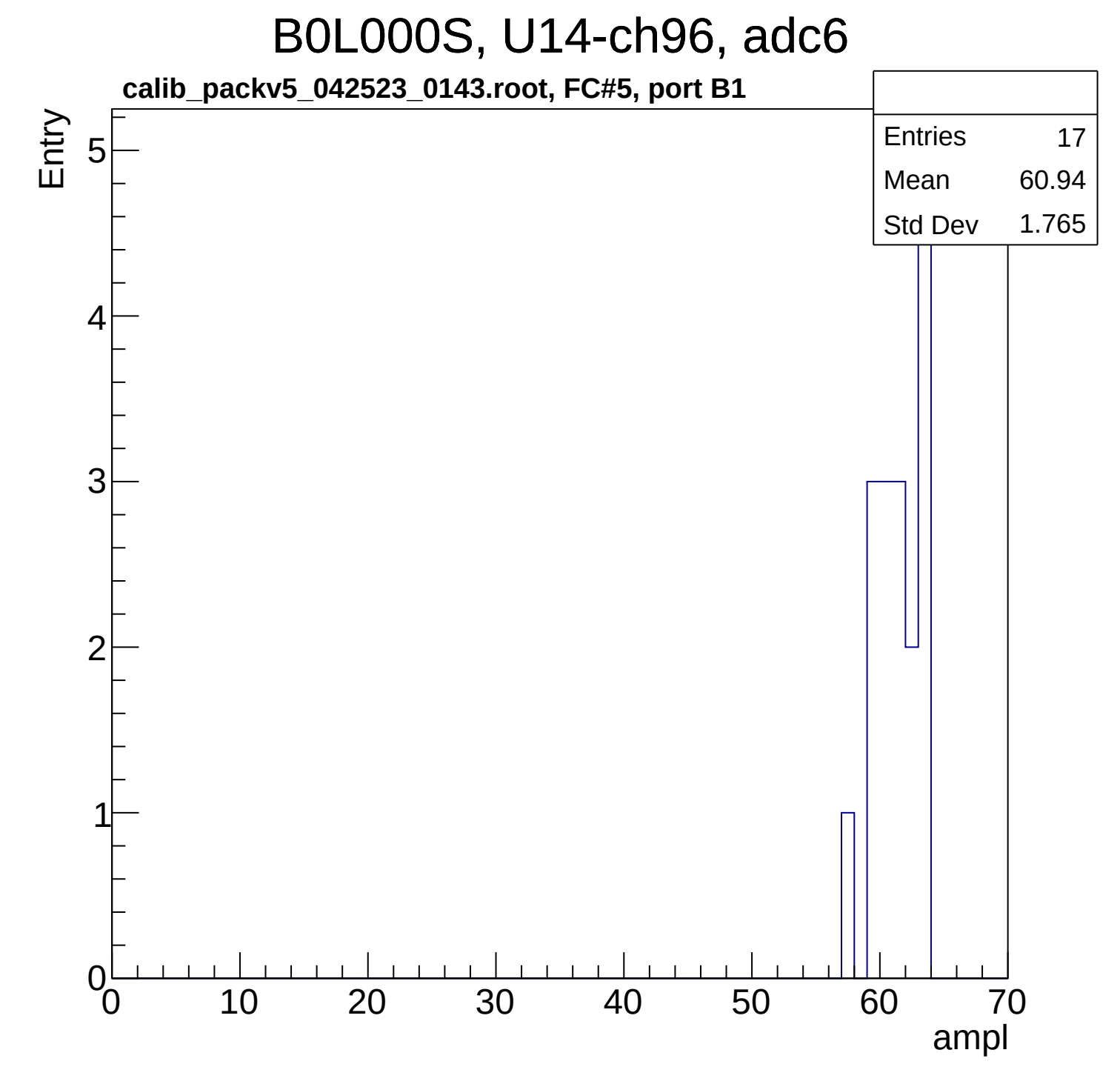
B0L000S, U14-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 60.94
Std Dev 1.765
Entry
ampl
0
1
2
3
4
5
0
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20
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40
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60
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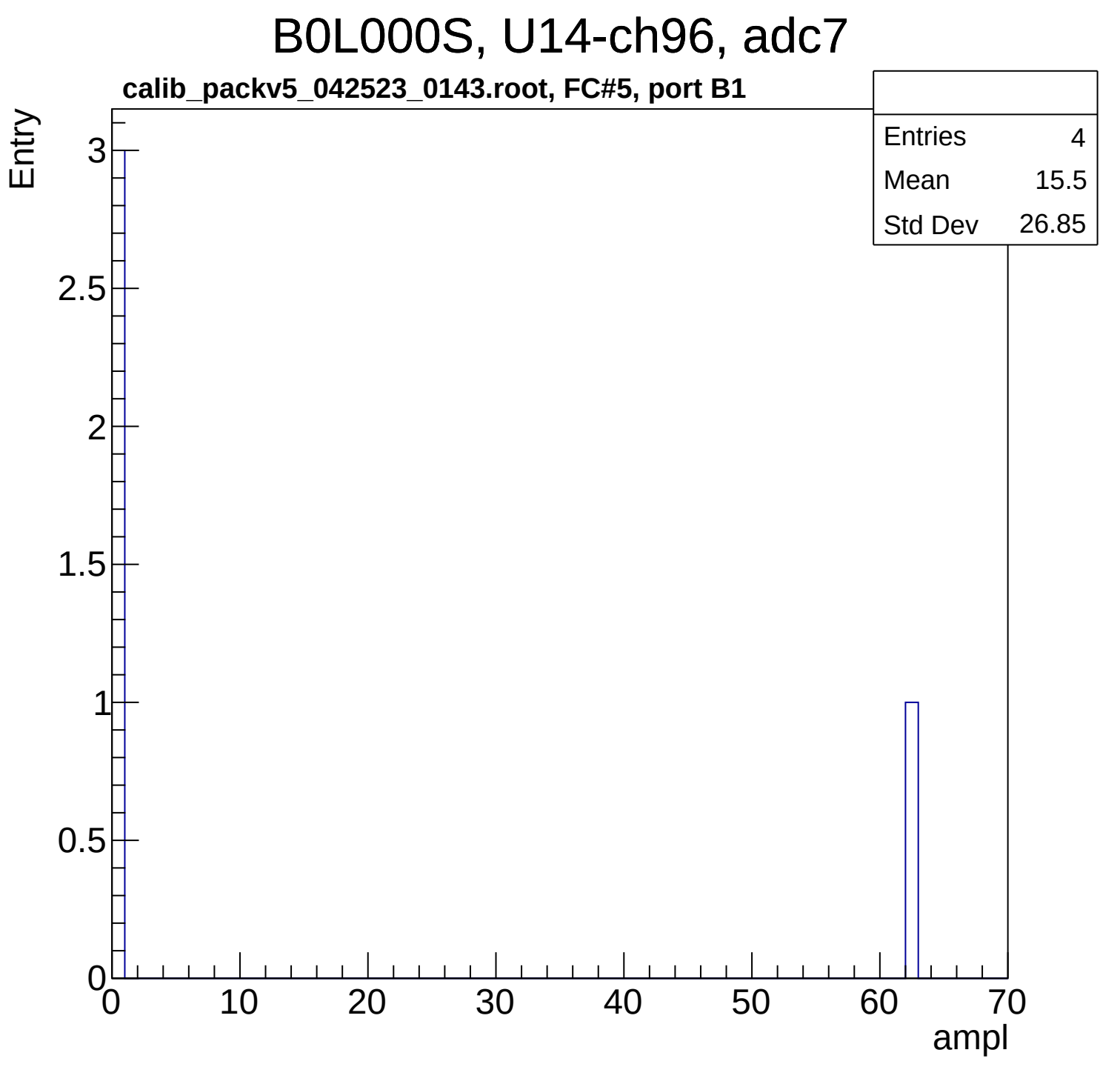
B0L000S, U14-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 15.5
Std Dev 26.85
Entry
ampl
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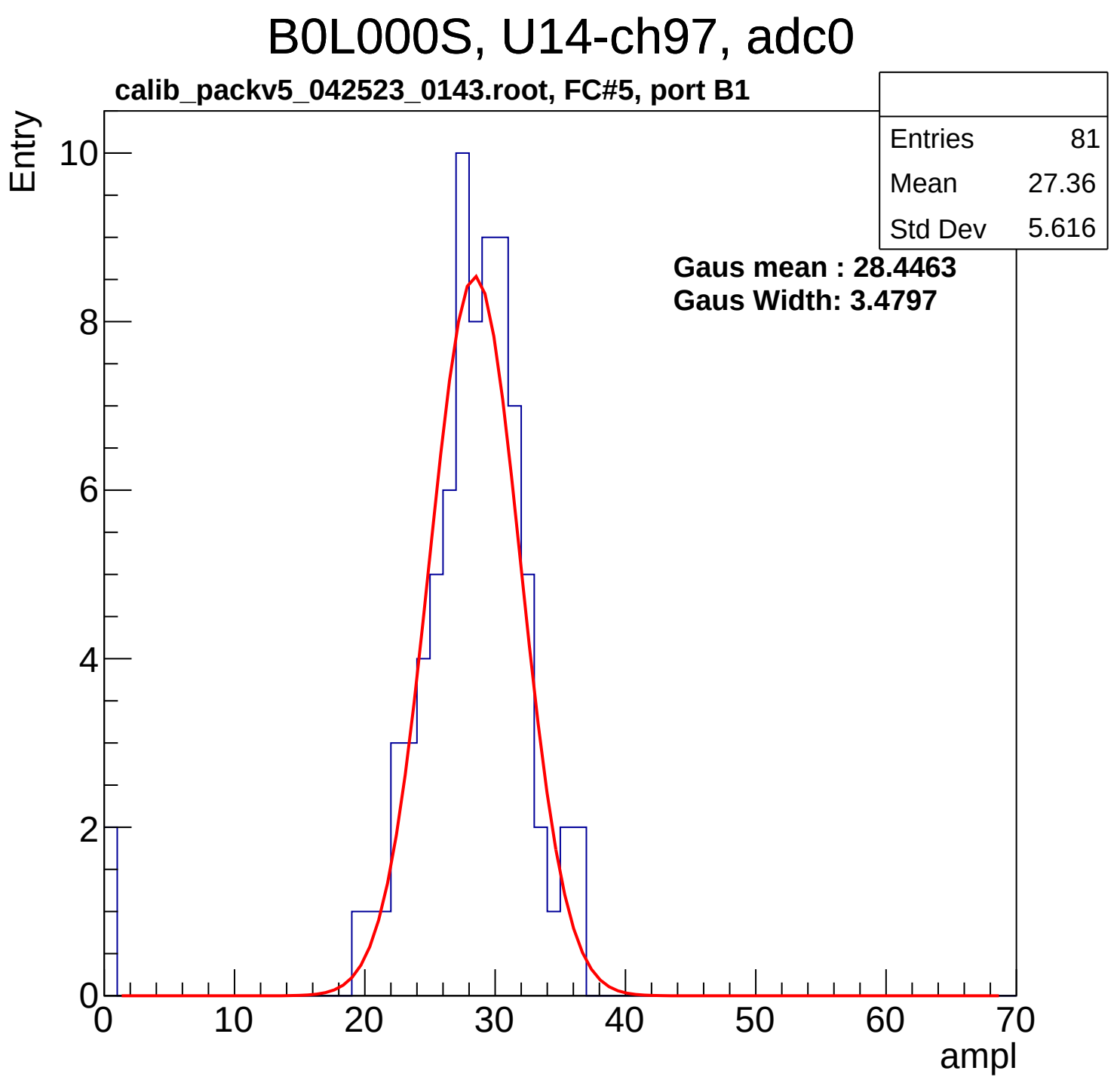
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U14-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 27.36
Std Dev 5.616
Gaus mean : 28.4463
Gaus Width: 3.4797
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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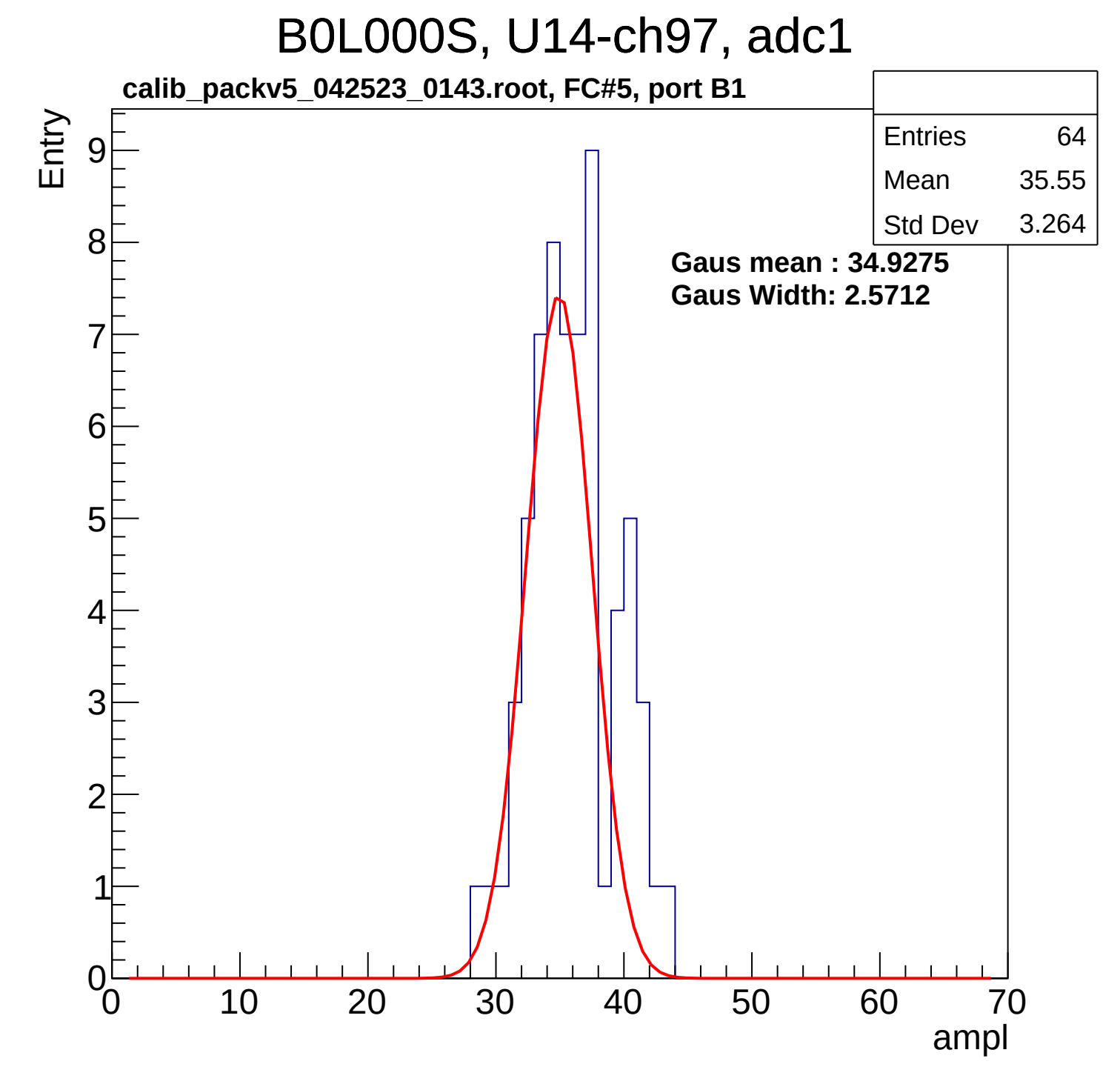
B0L000S, U14-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.55
Std Dev 3.264
Gaus mean : 34.9275
Gaus Width: 2.5712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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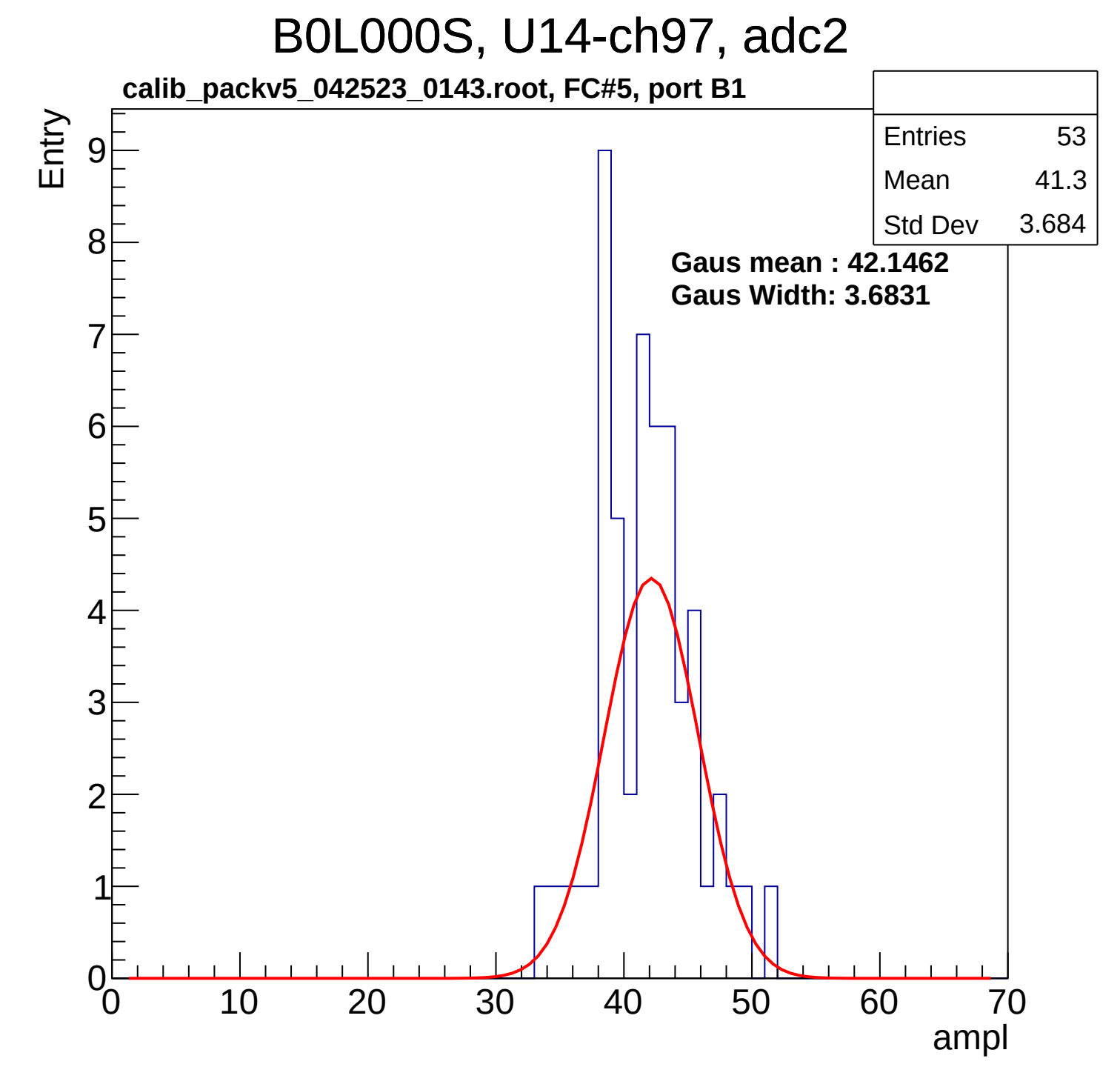
B0L000S, U14-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 41.3
Std Dev 3.684
Gaus mean : 42.1462
Gaus Width: 3.6831
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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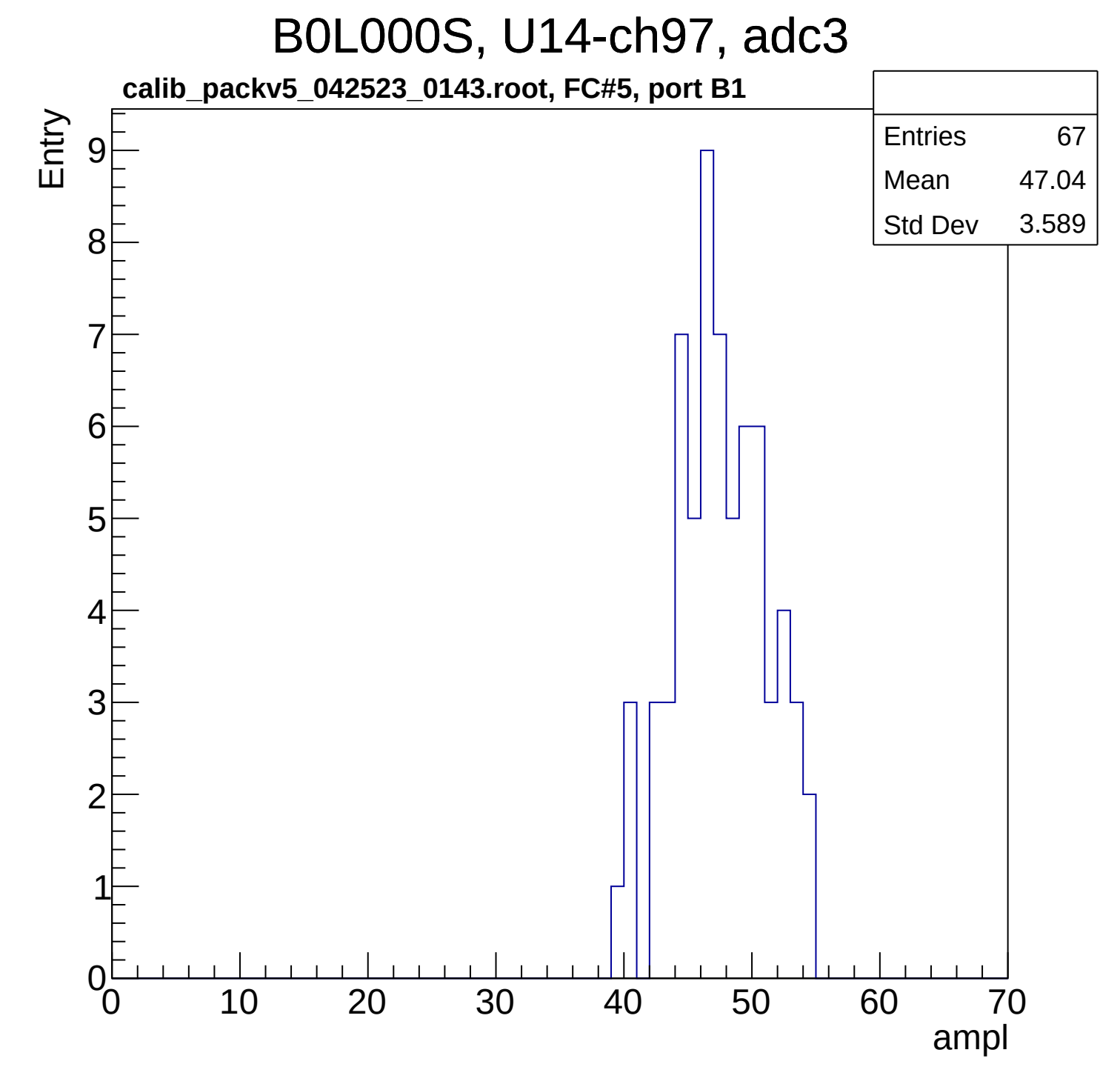
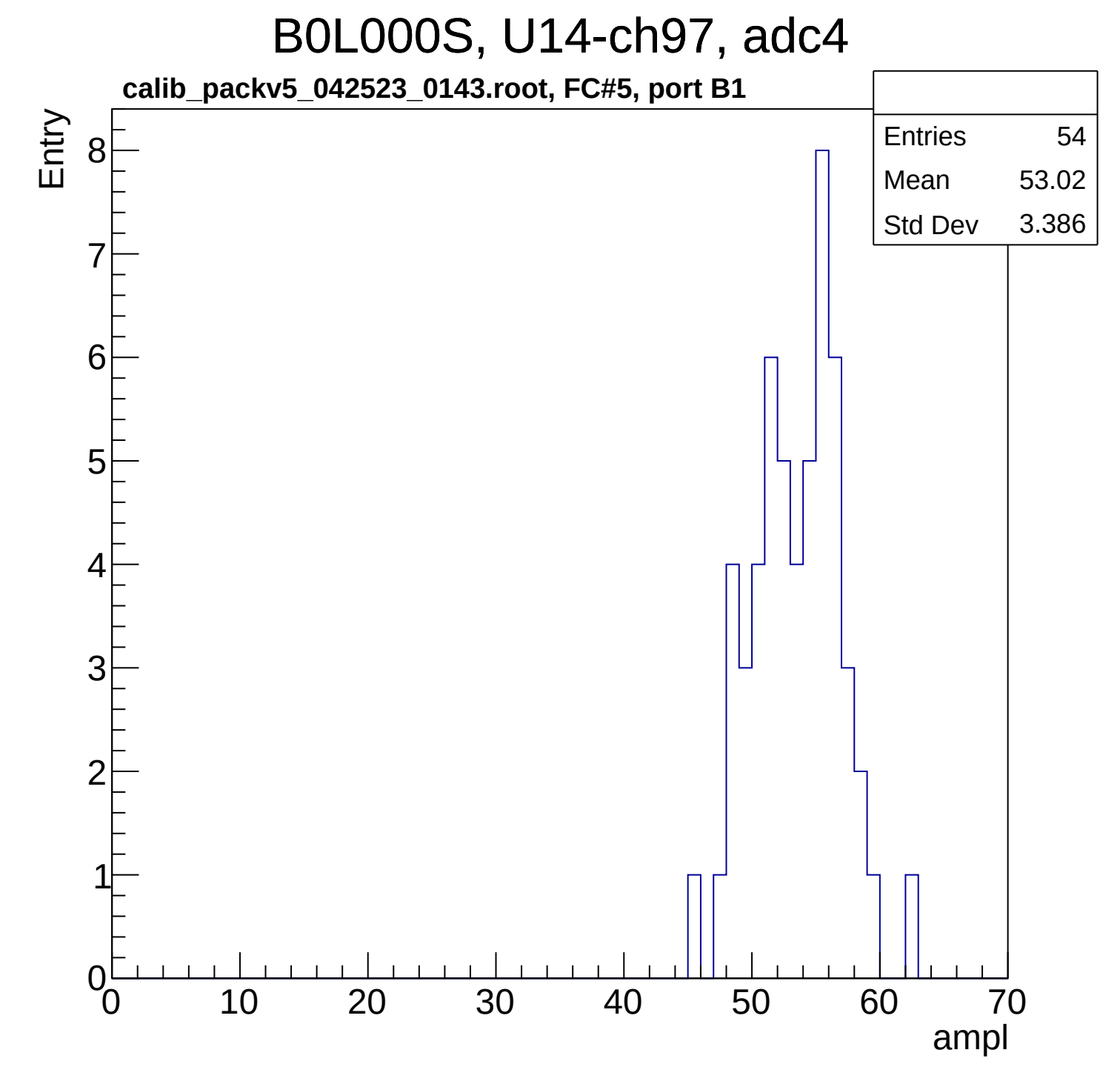

B0L000S, U14-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.04
Std Dev 3.589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.02
Std Dev 3.386
Entry
ampl
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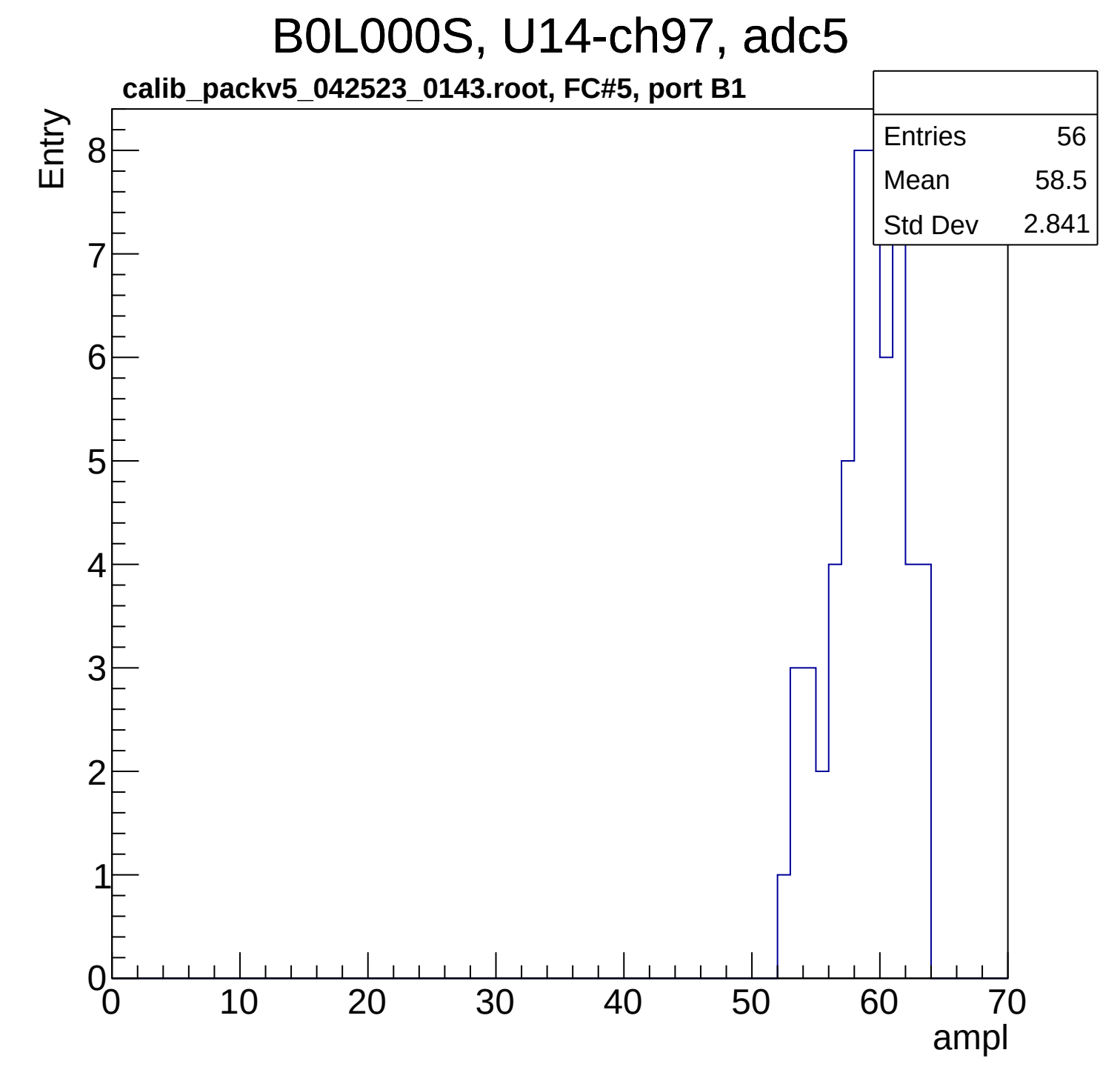
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.5
Std Dev 2.841
Entry
ampl
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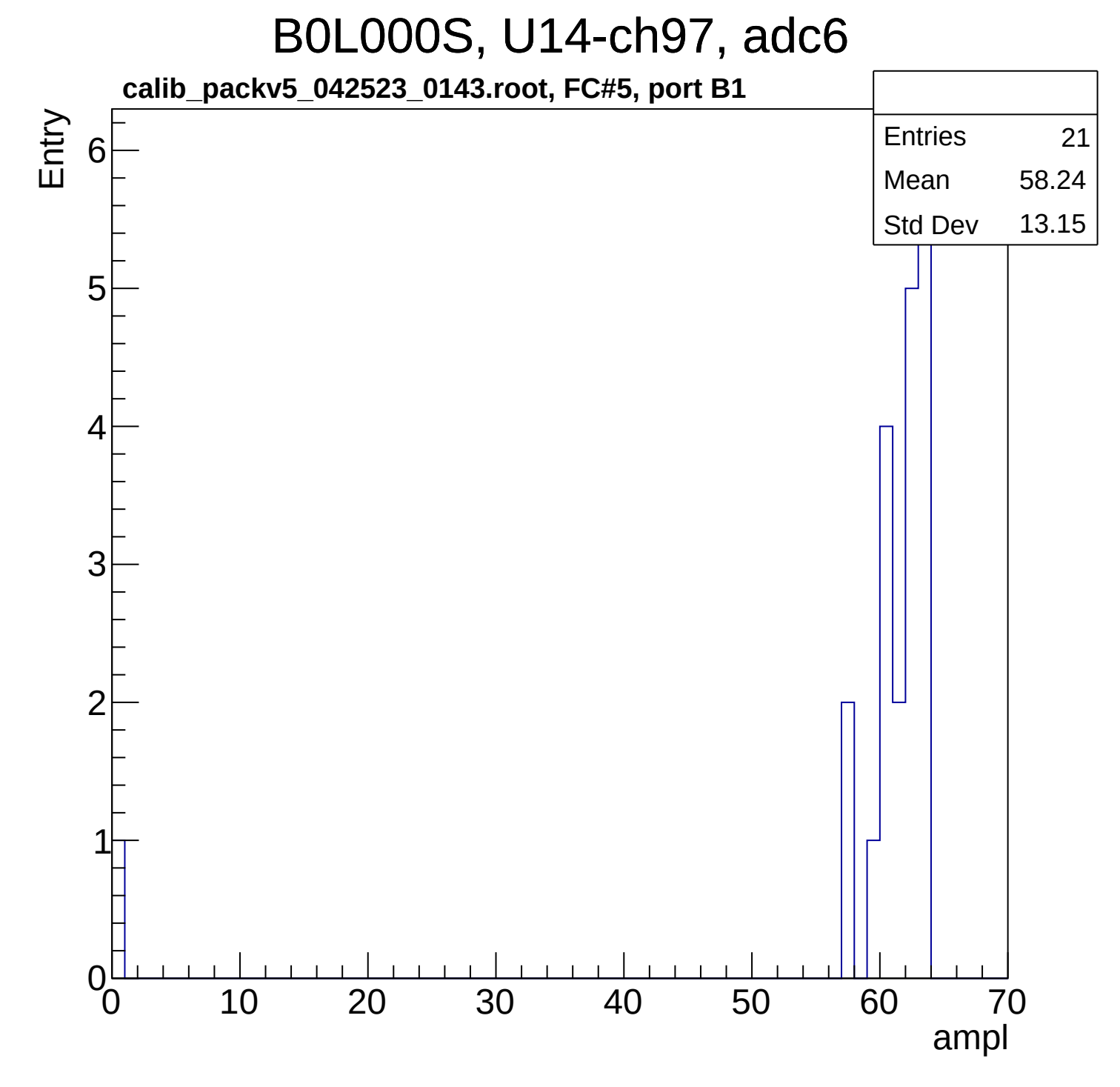
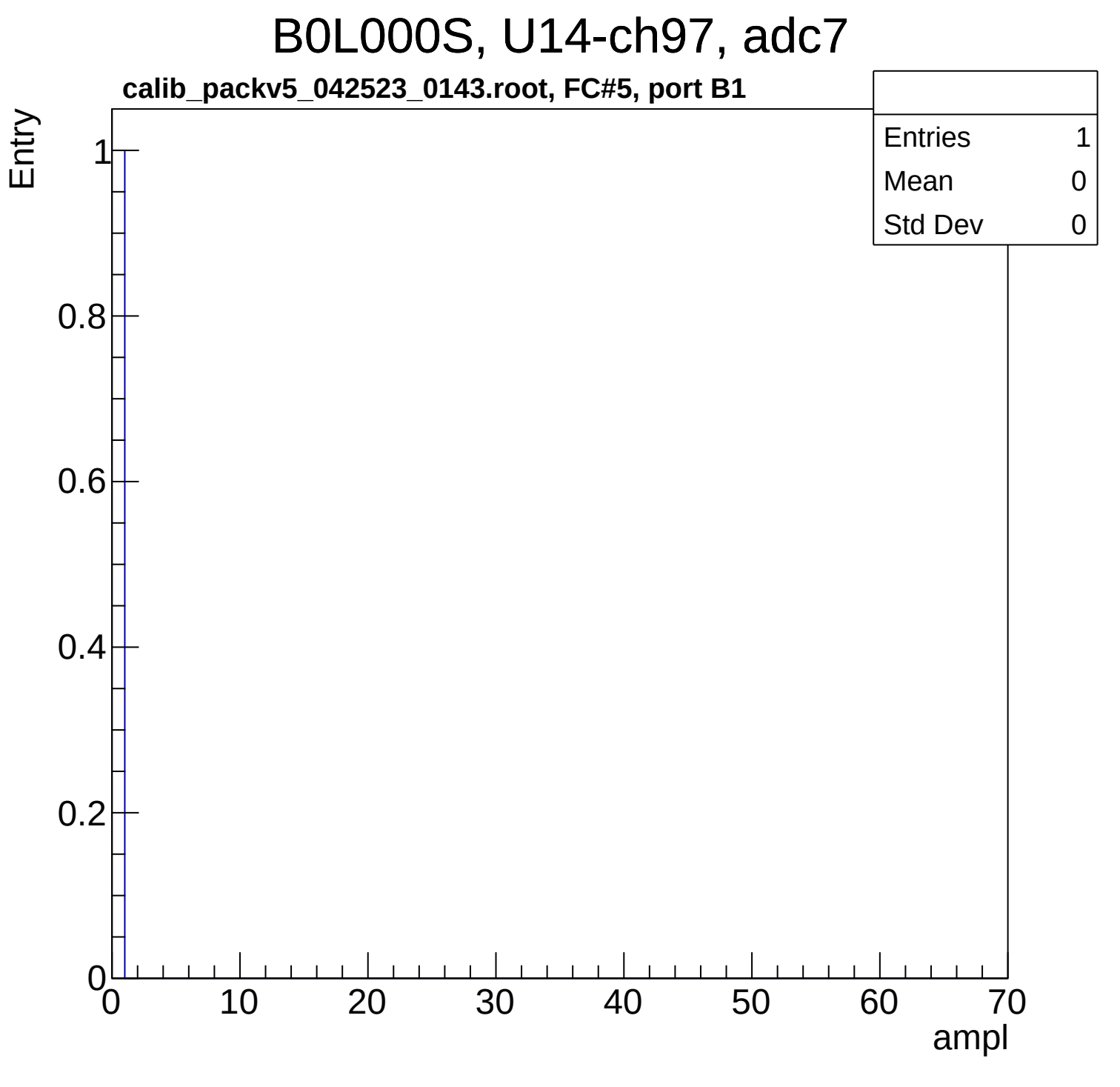
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.24
Std Dev 13.15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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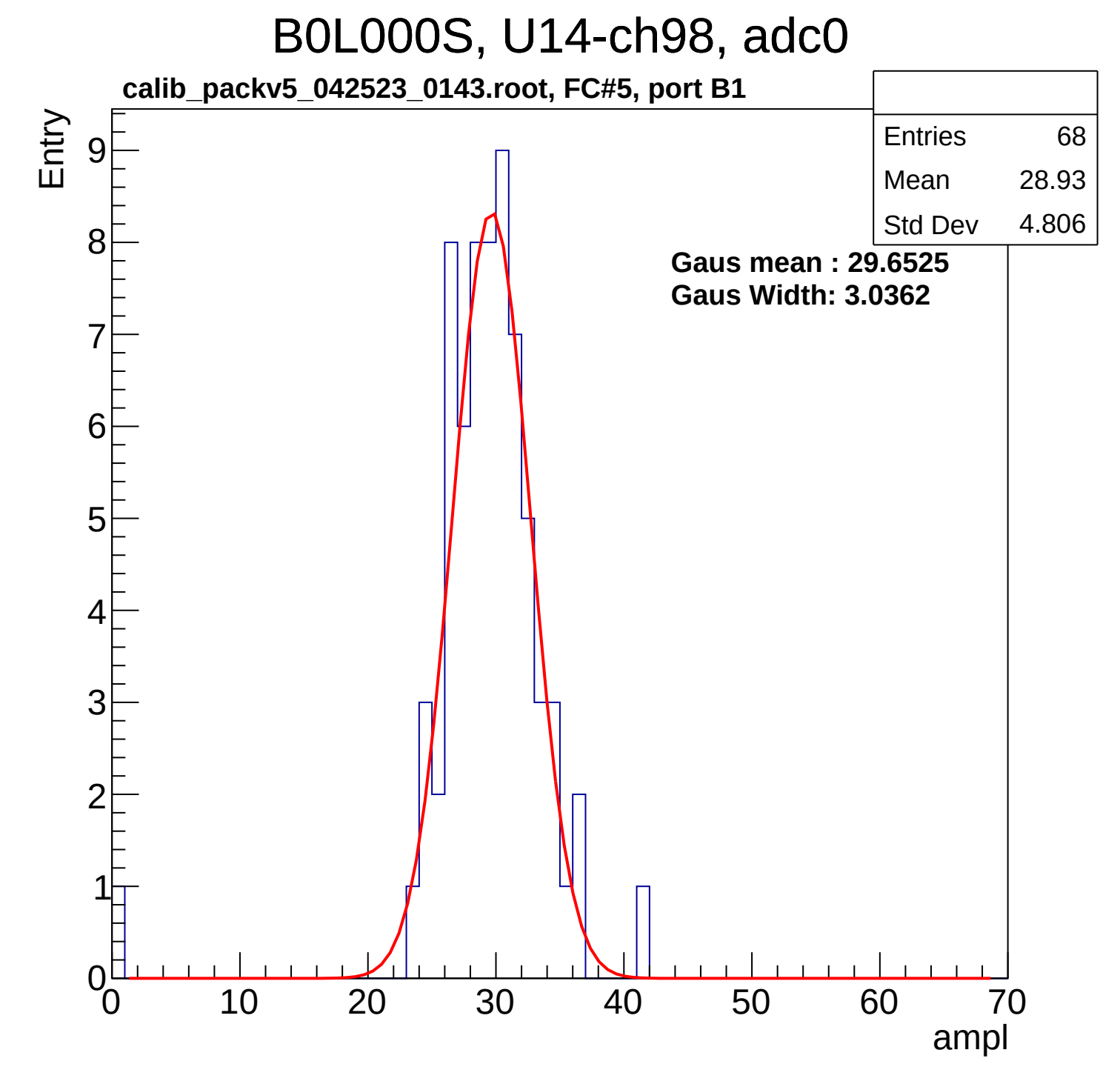
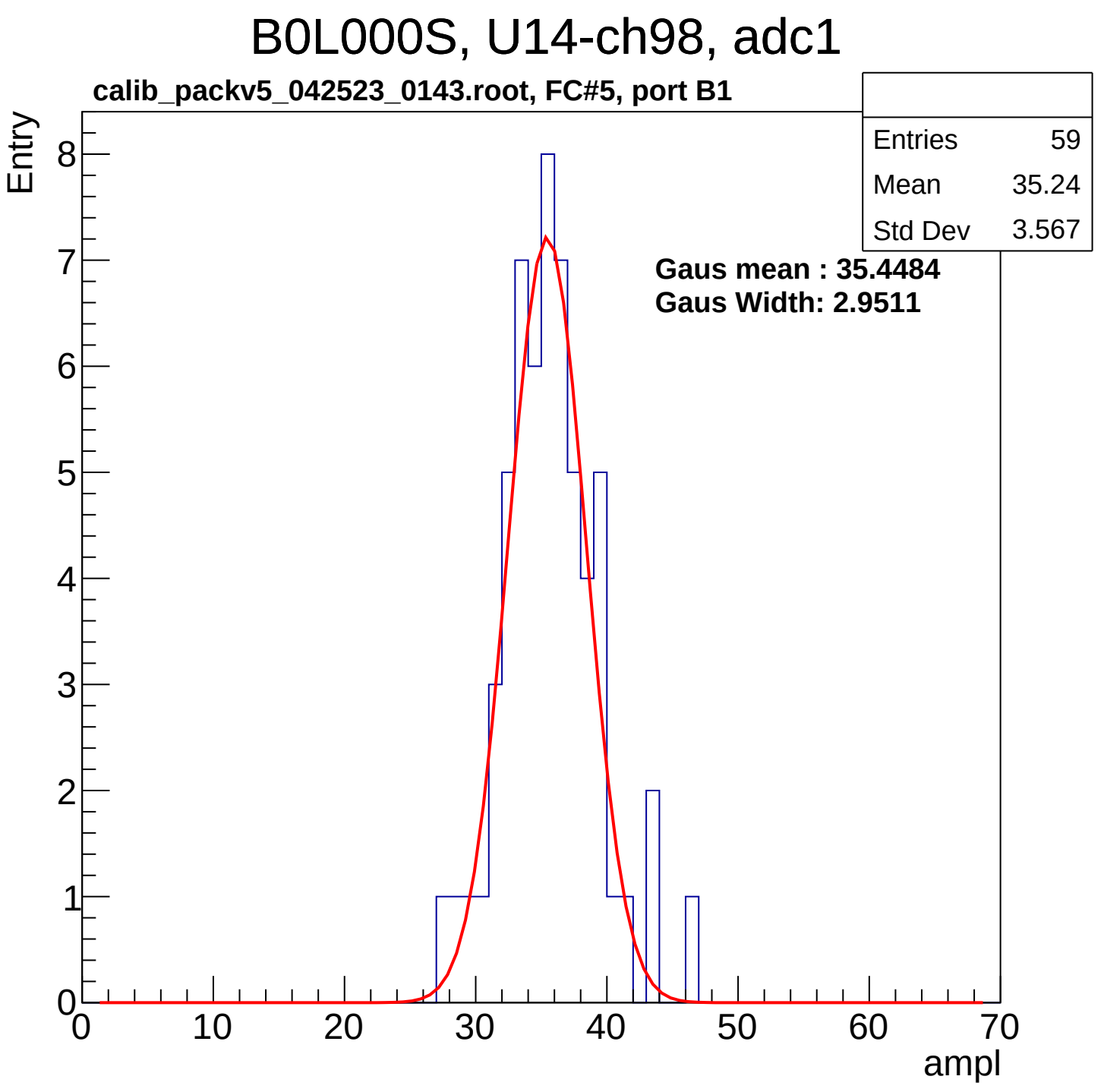
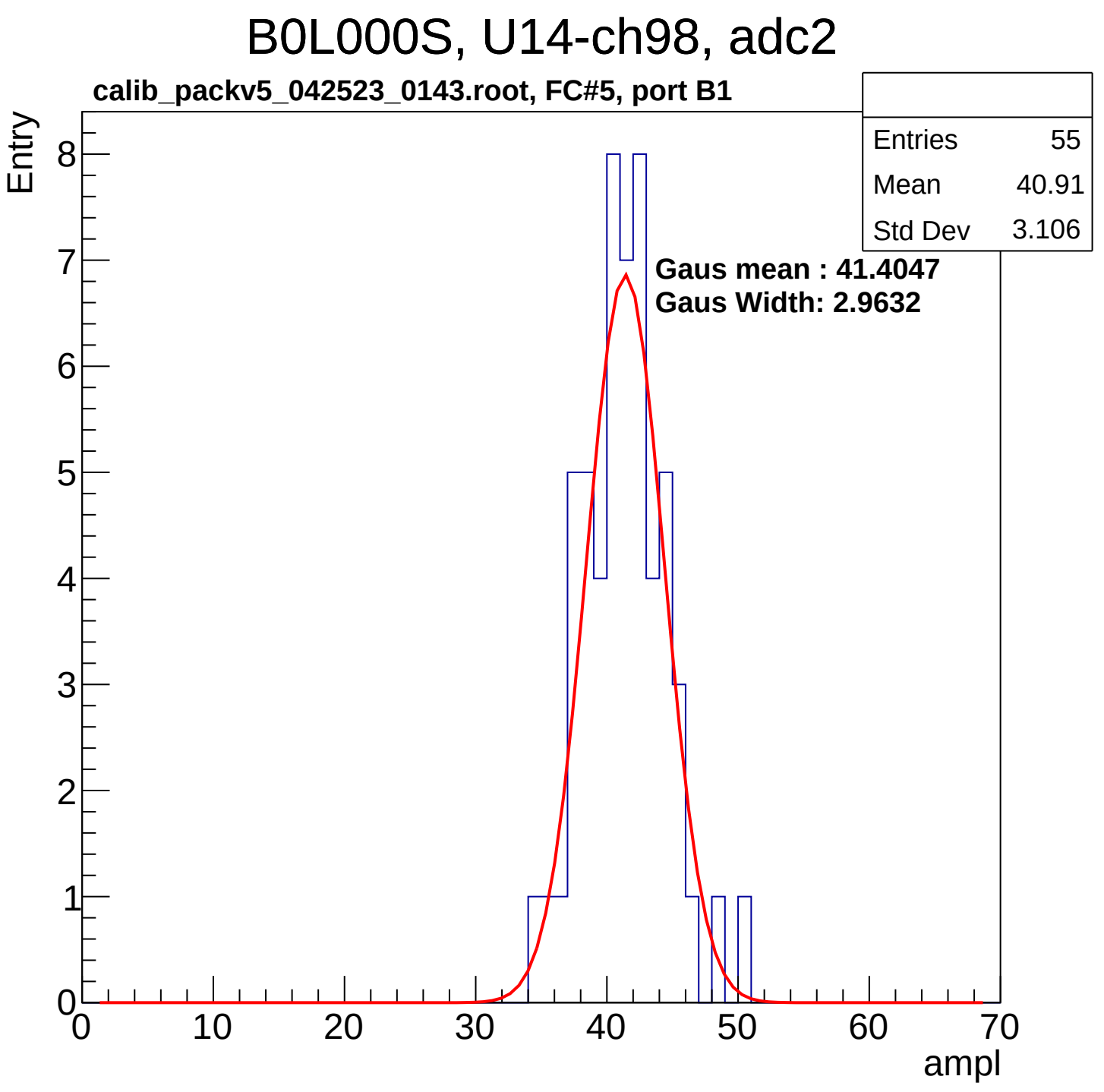
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.93
Std Dev 4.806
Gaus mean : 29.6525
Gaus Width: 3.0362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.24
Std Dev 3.567
Gaus mean : 35.4484
Gaus Width: 2.9511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 40.91
Std Dev 3.106
Gaus mean : 41.4047
Gaus Width: 2.9632
Entry
ampl
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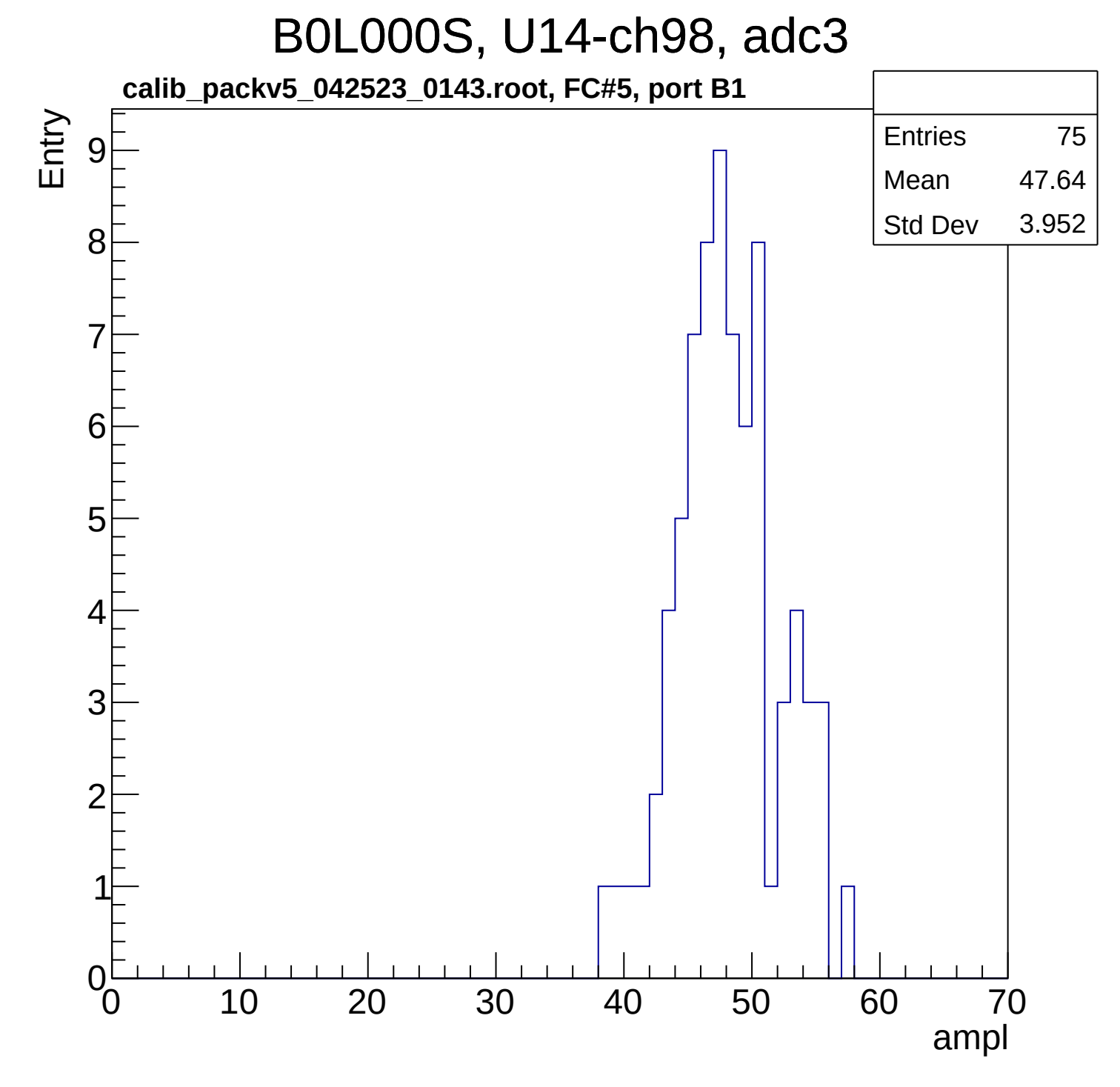
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 47.64
Std Dev 3.952
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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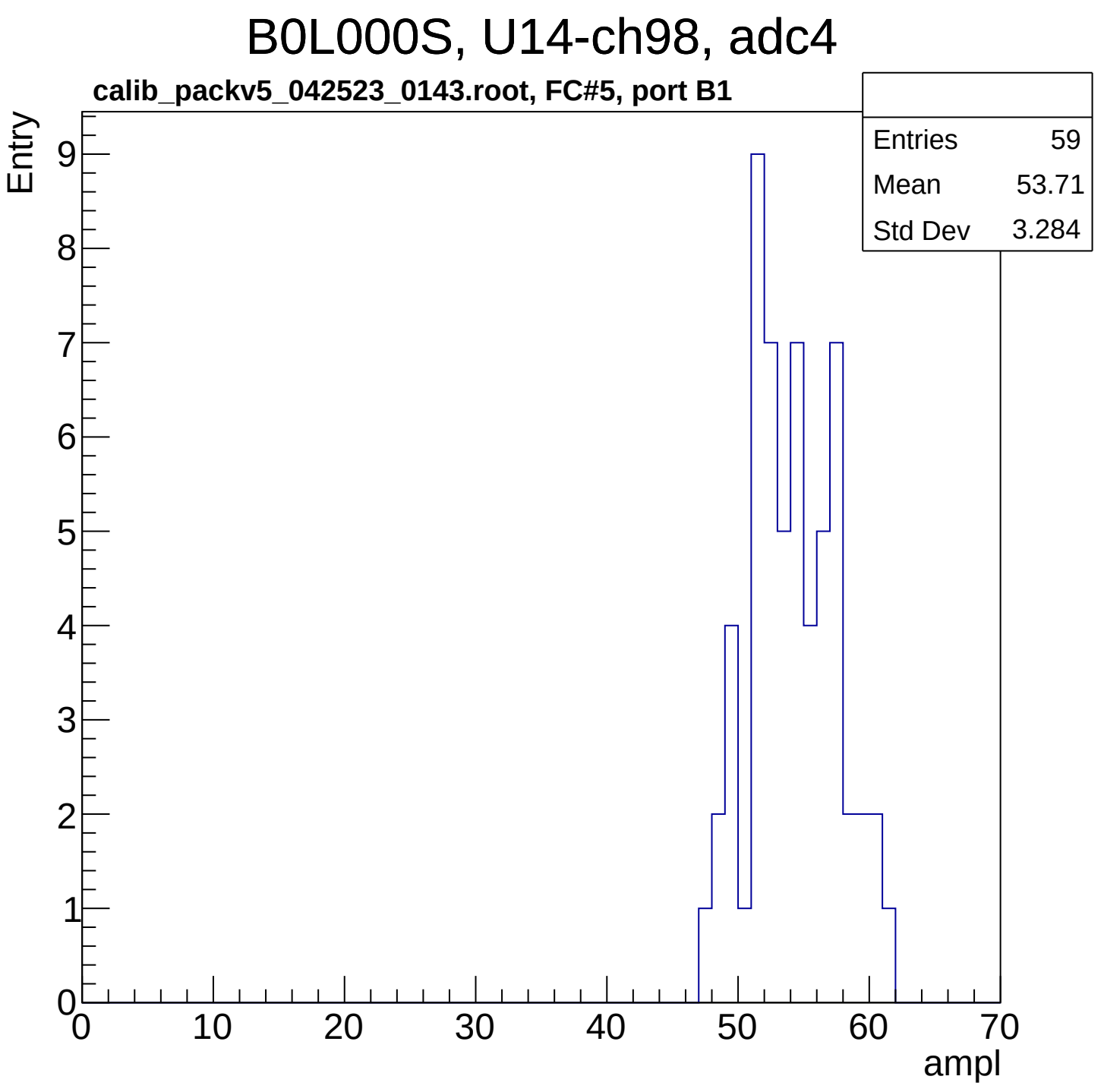
B0L000S, U14-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 53.71
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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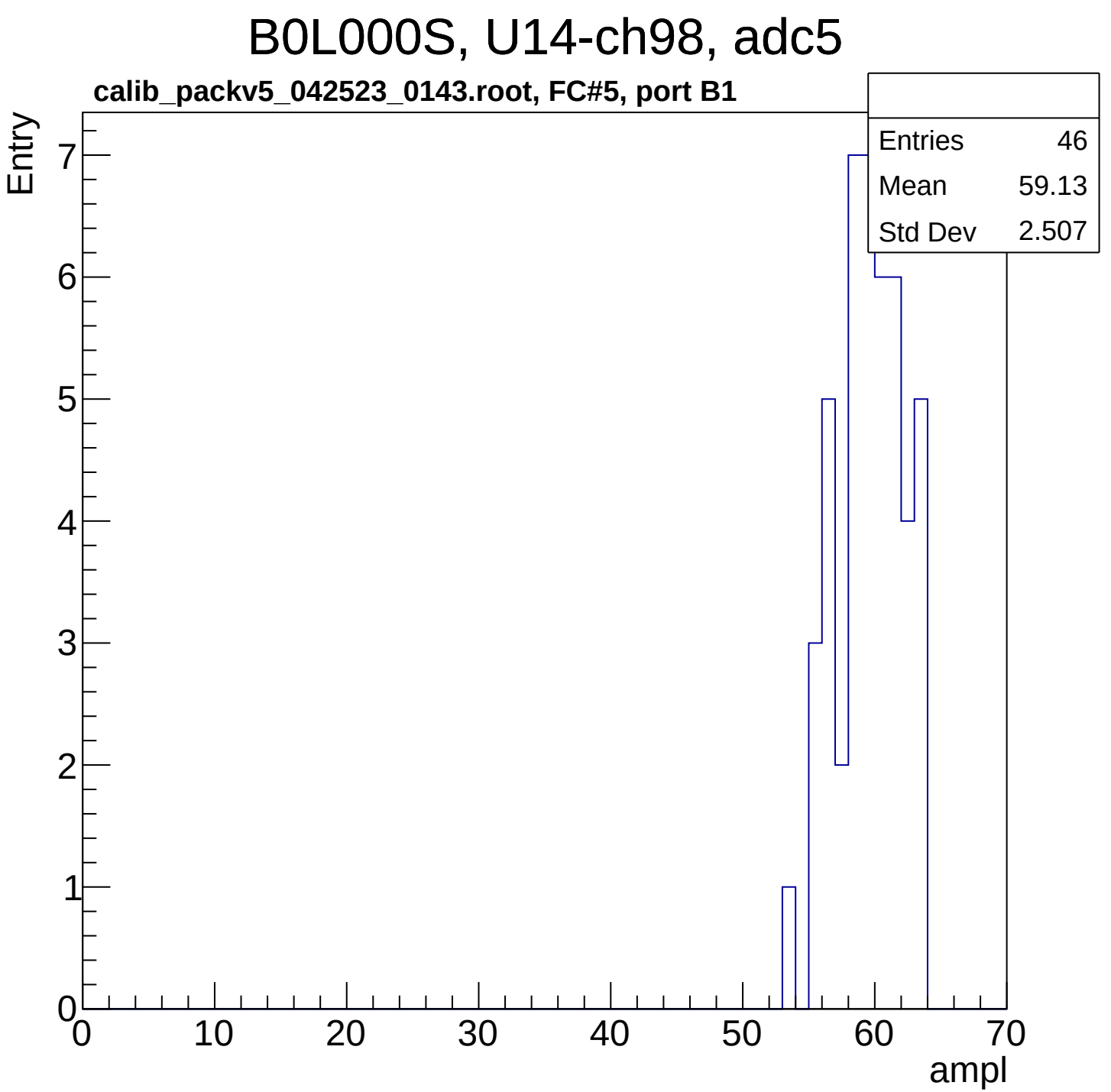
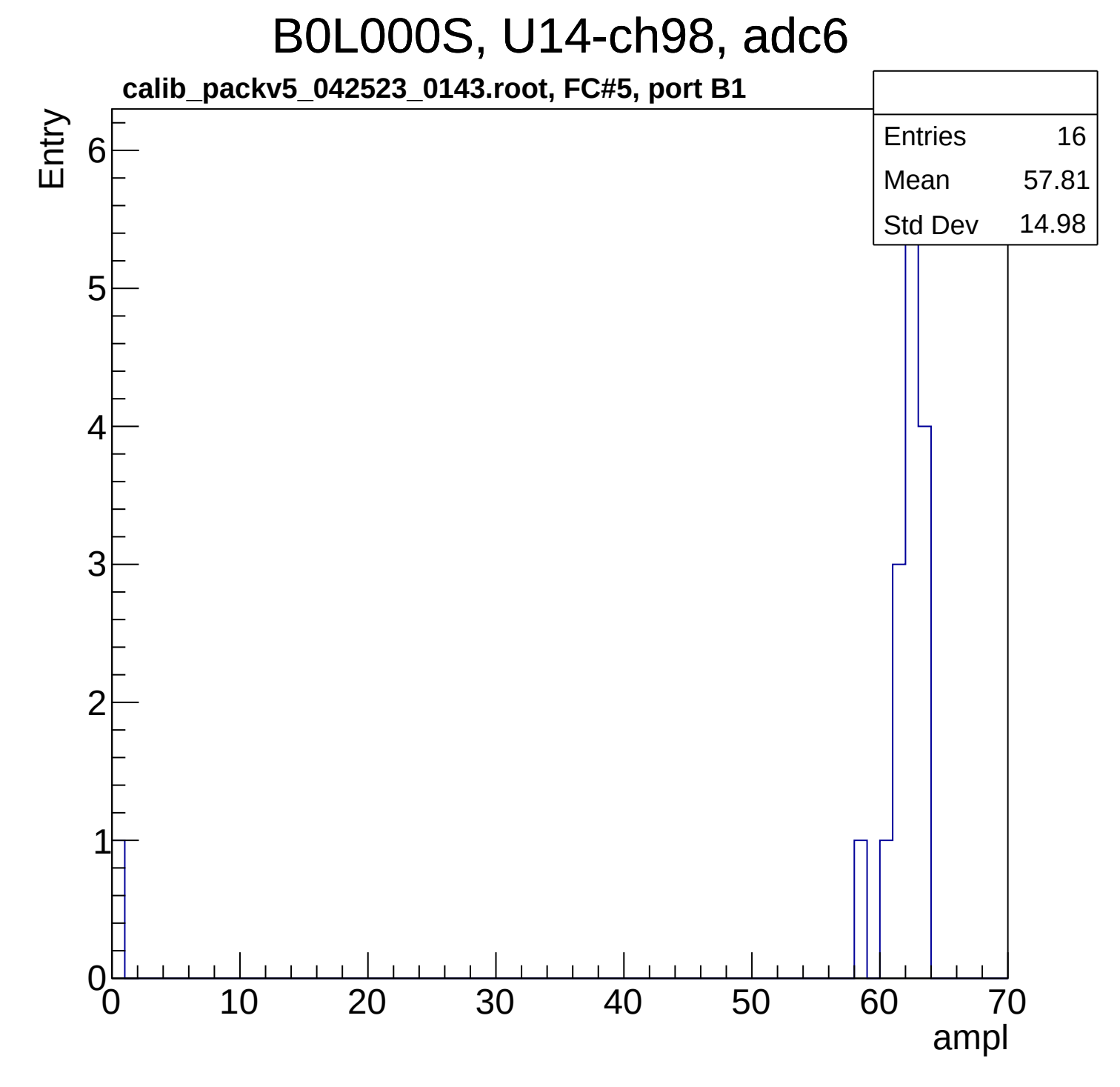
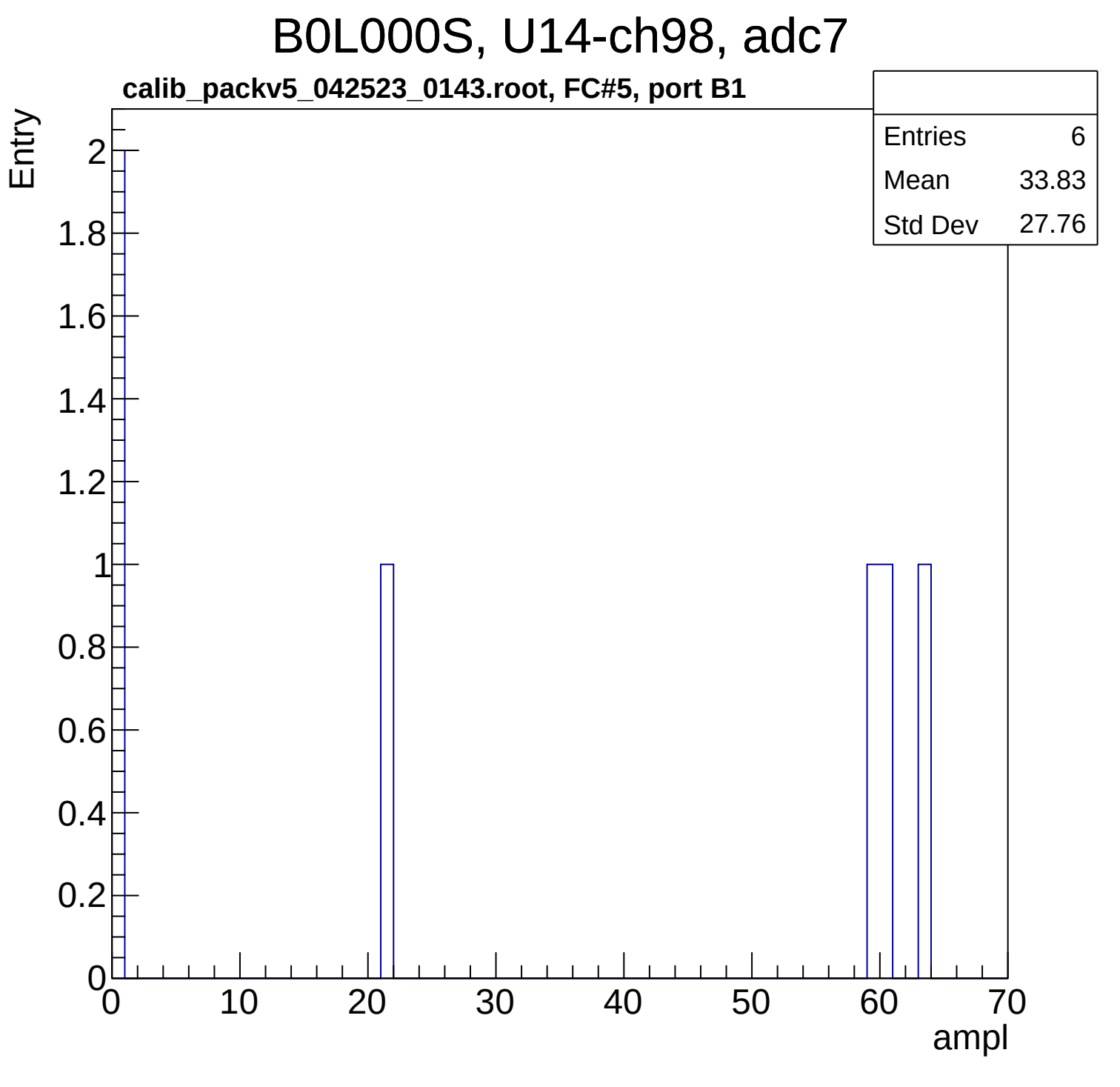
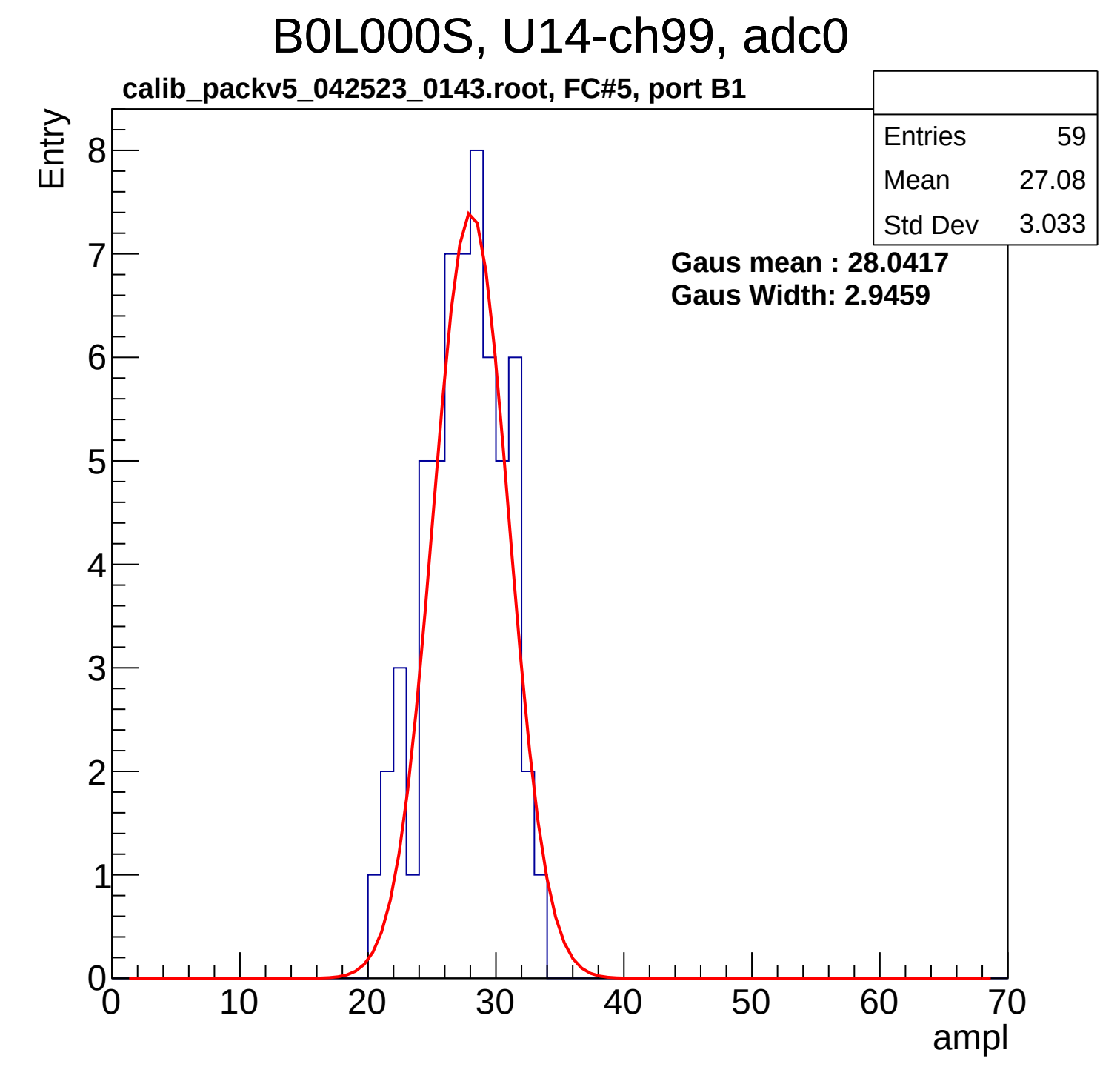

B0L000S, U14-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.13
Std Dev 2.507
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.81
Std Dev 14.98
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 33.83
Std Dev 27.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.08
Std Dev 3.033
Gaus mean : 28.0417
Gaus Width: 2.9459
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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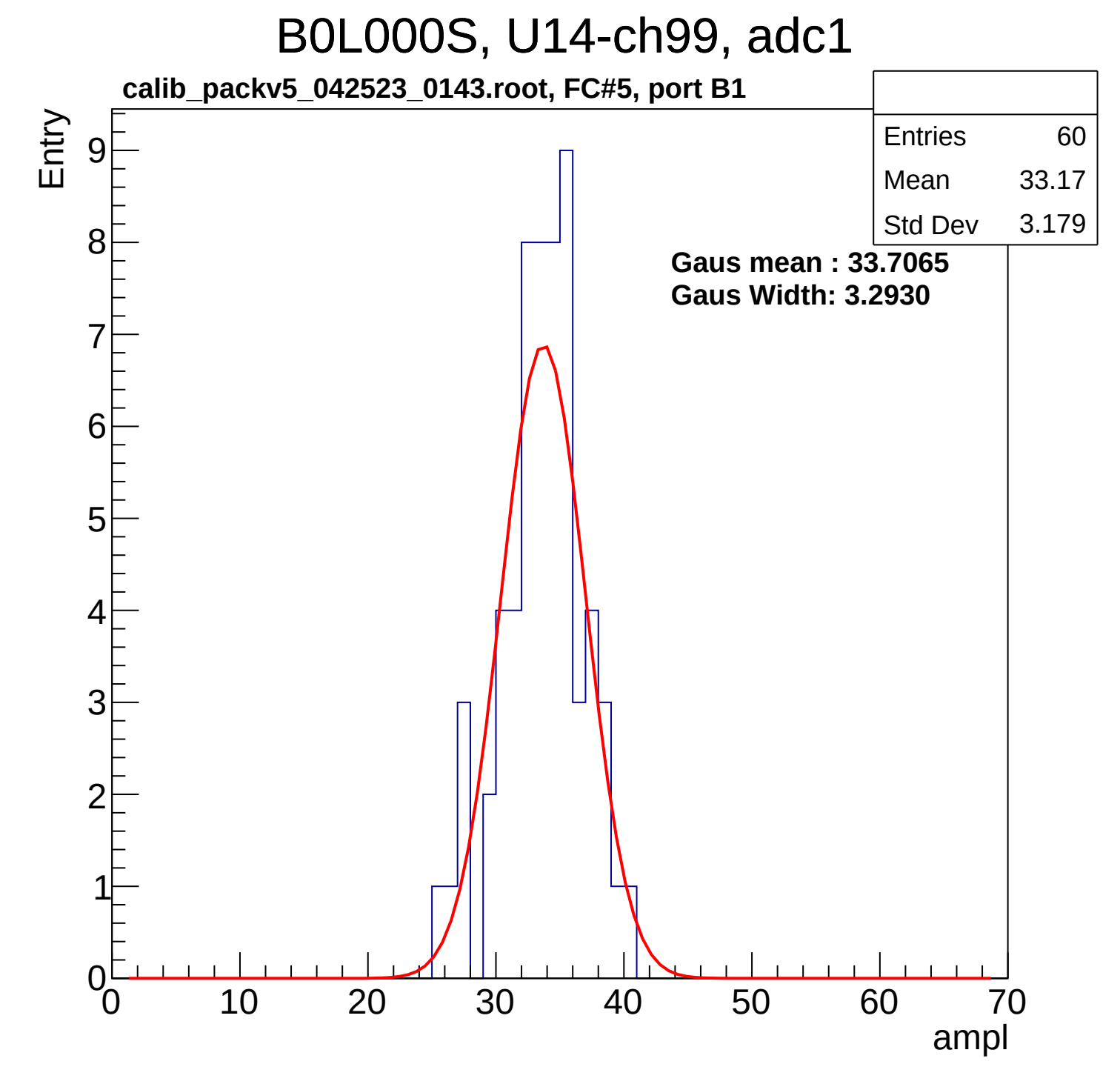
B0L000S, U14-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 33.17
Std Dev 3.179
Gaus mean : 33.7065
Gaus Width: 3.2930
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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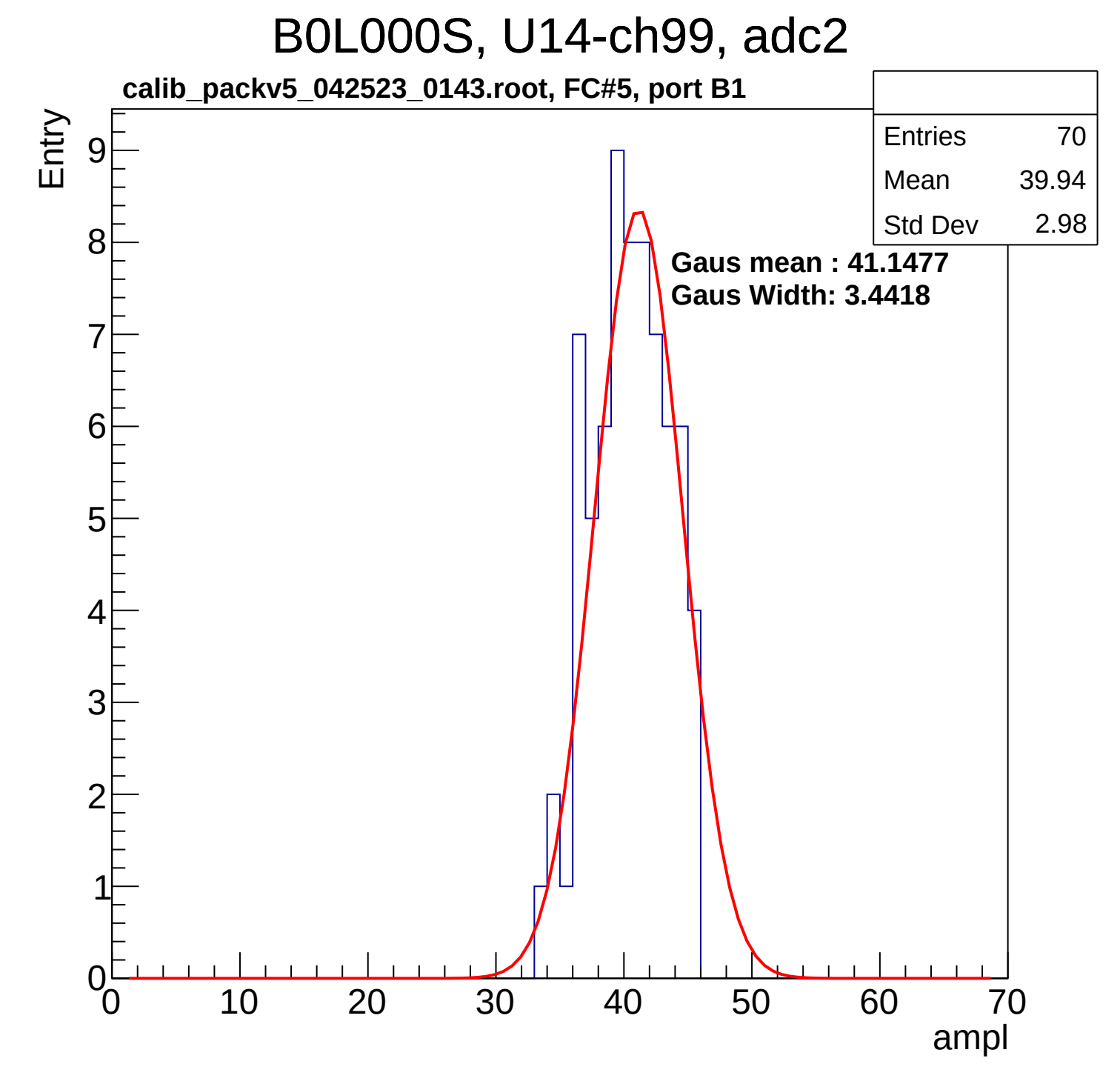
B0L000S, U14-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 39.94
Std Dev 2.98
Gaus mean : 41.1477
Gaus Width: 3.4418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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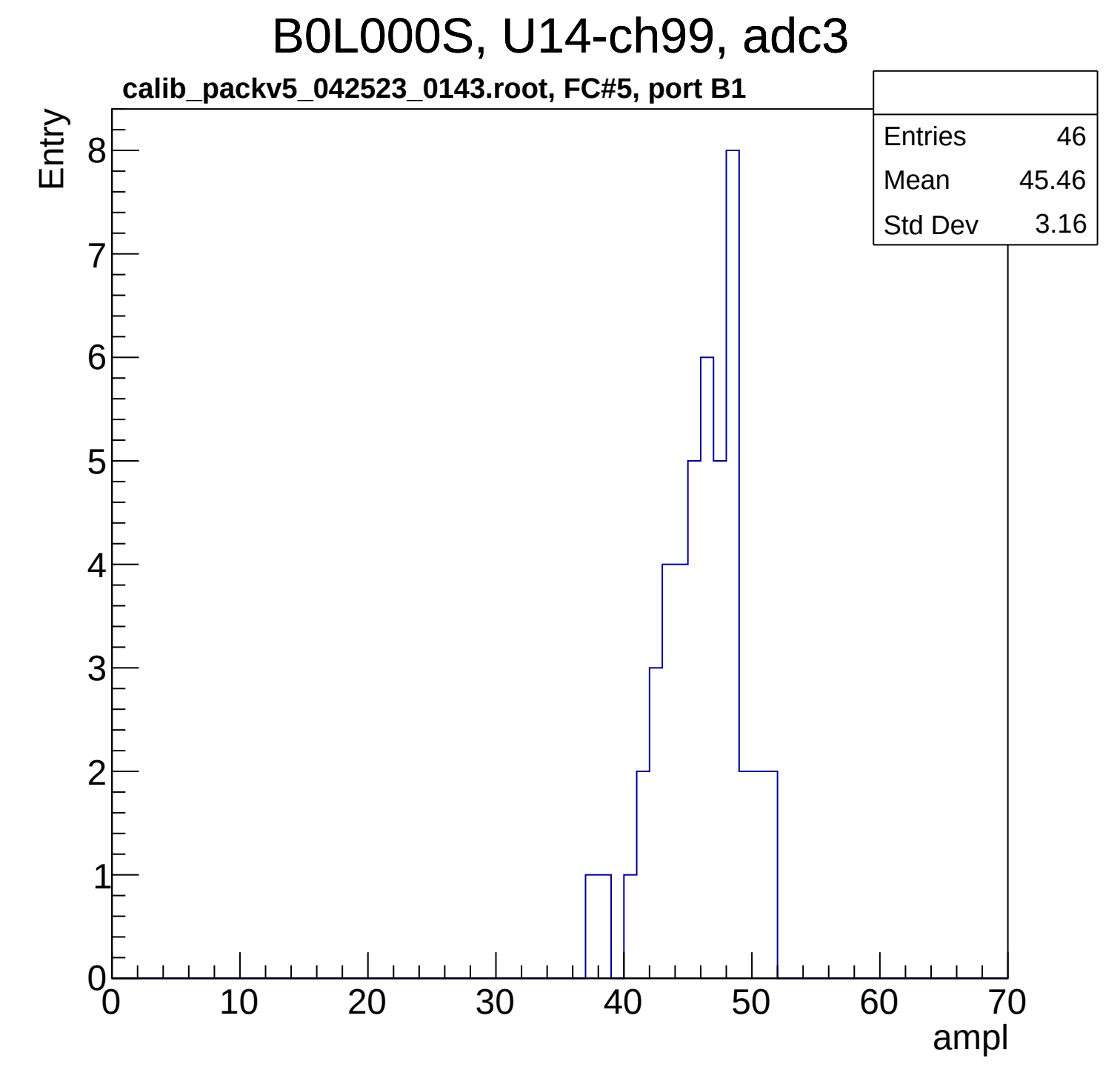
B0L000S, U14-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 45.46
Std Dev 3.16
Entry
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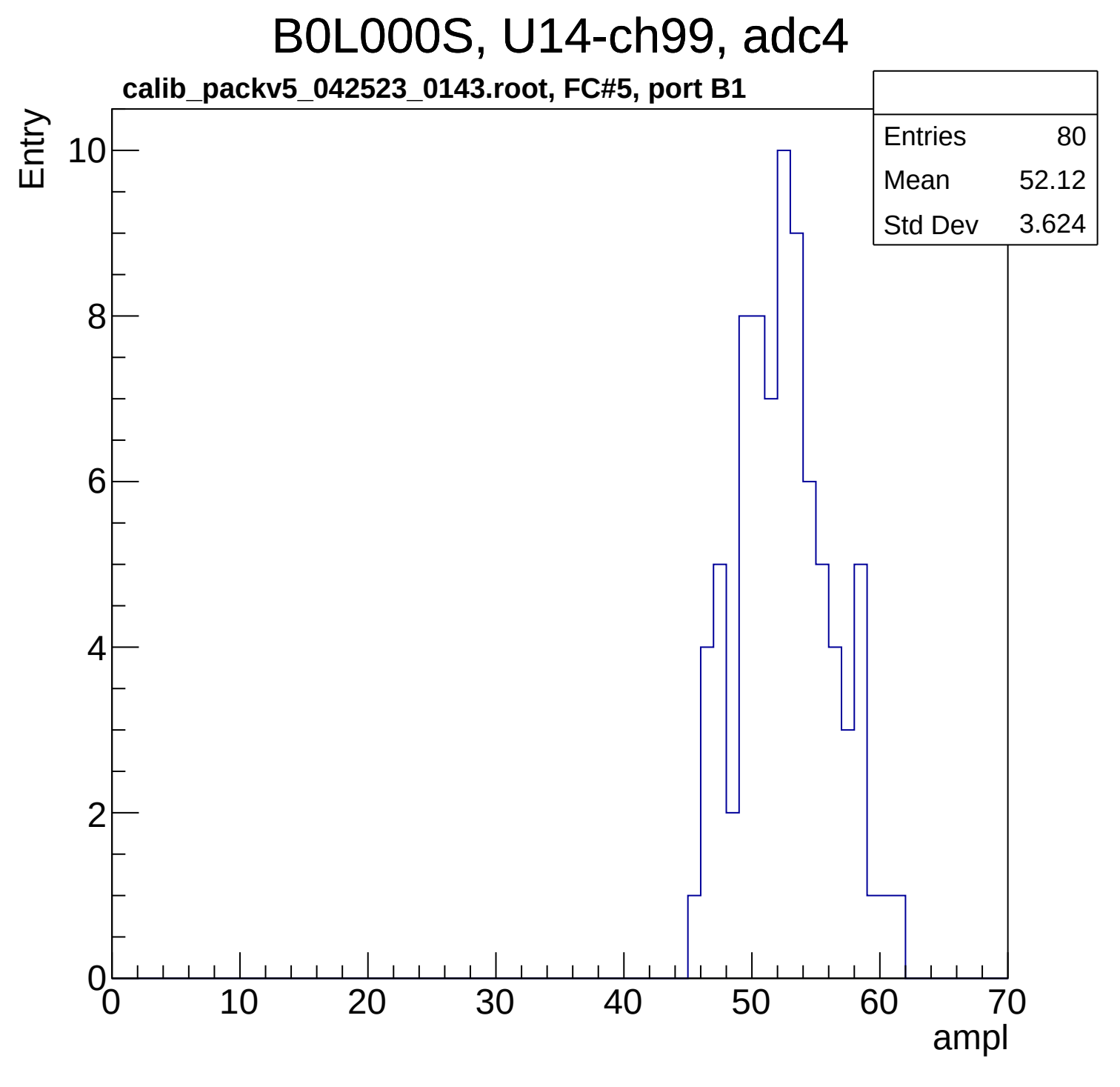
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 52.12
Std Dev 3.624
Entry
ampl
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2
4
6
8
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0
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40
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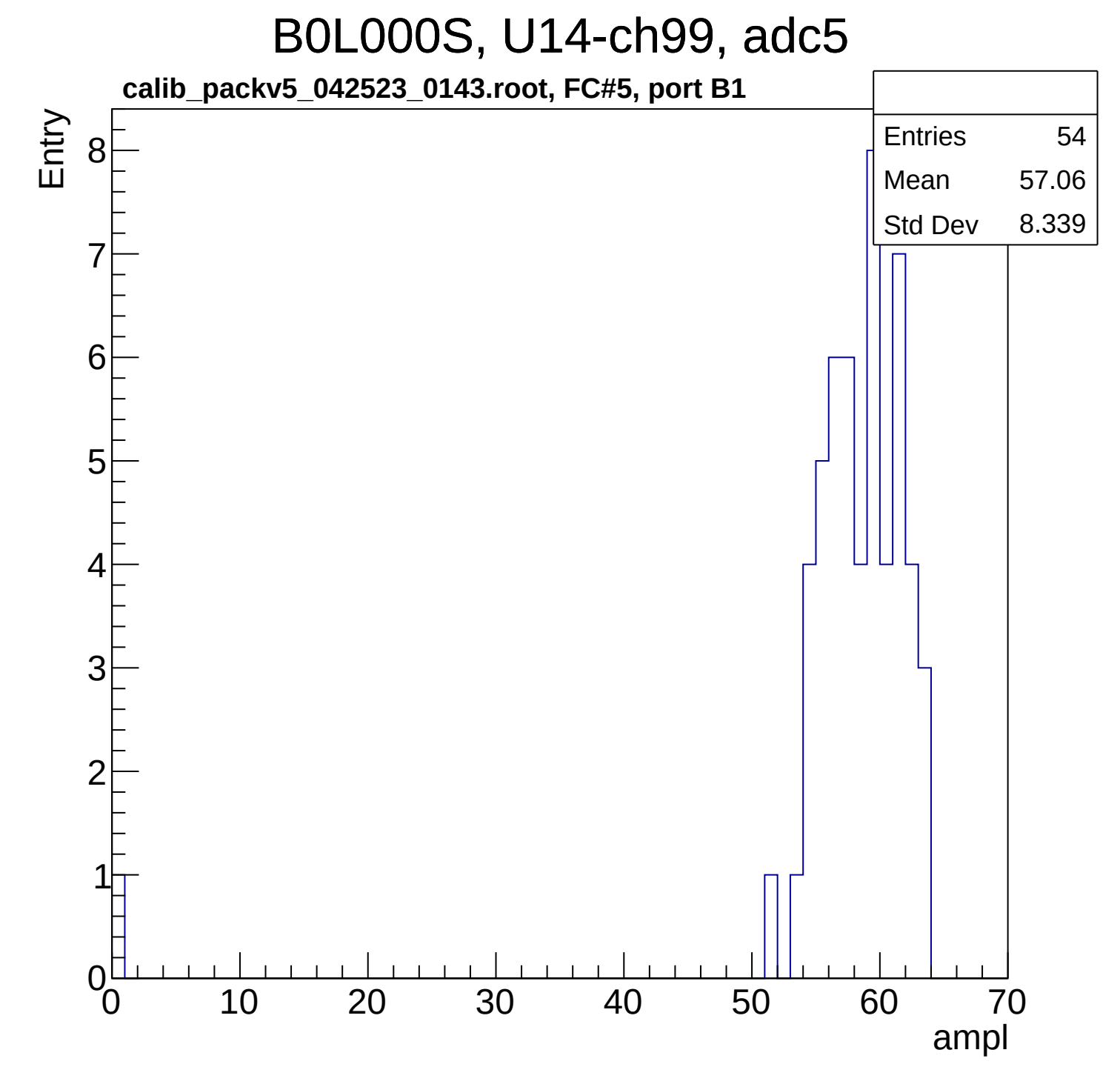
60
70


B0L000S, U14-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 57.06
Std Dev 8.339
Entry
ampl
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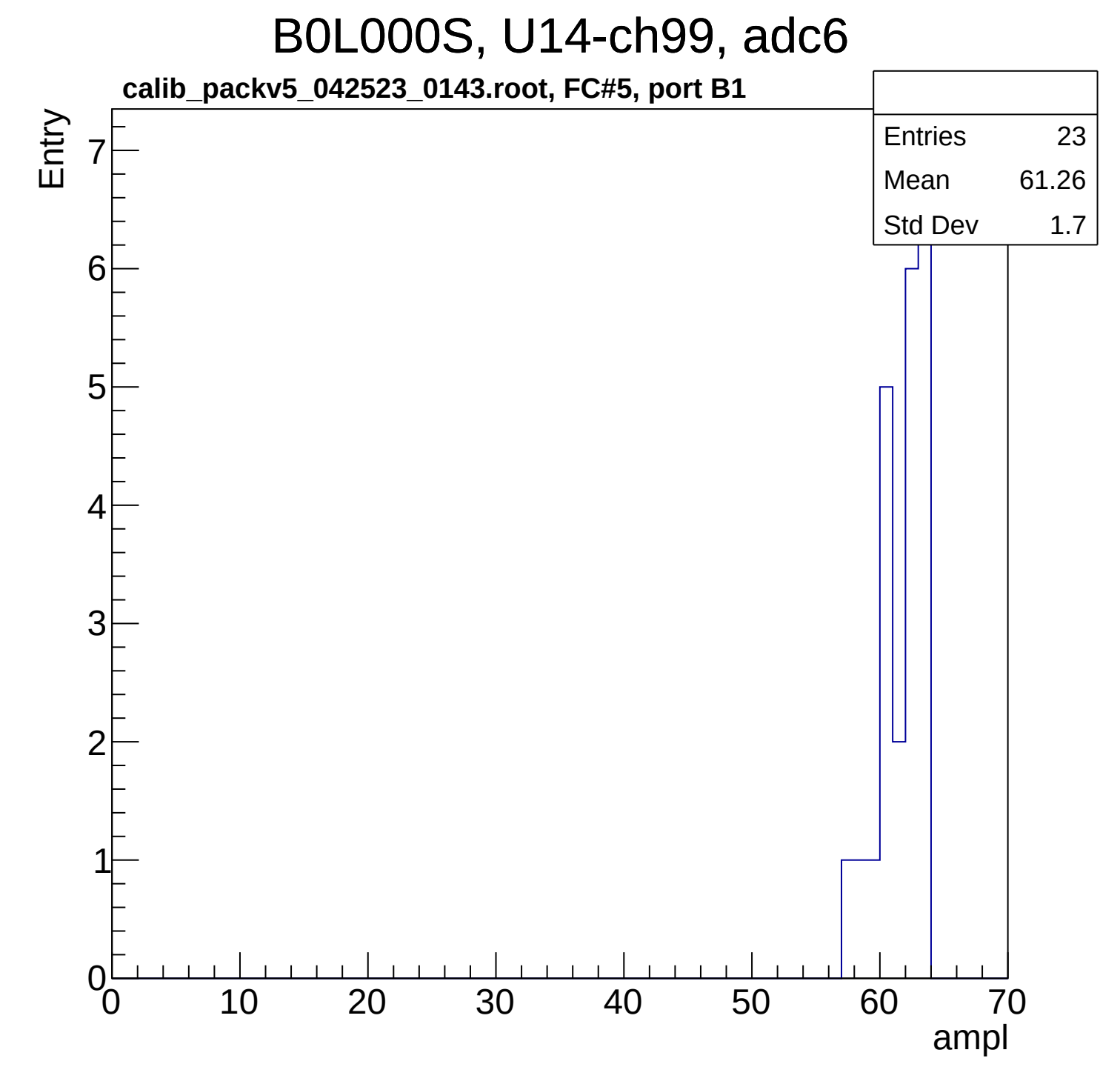
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 23
Mean 61.26
Std Dev 1.7
Entry
ampl
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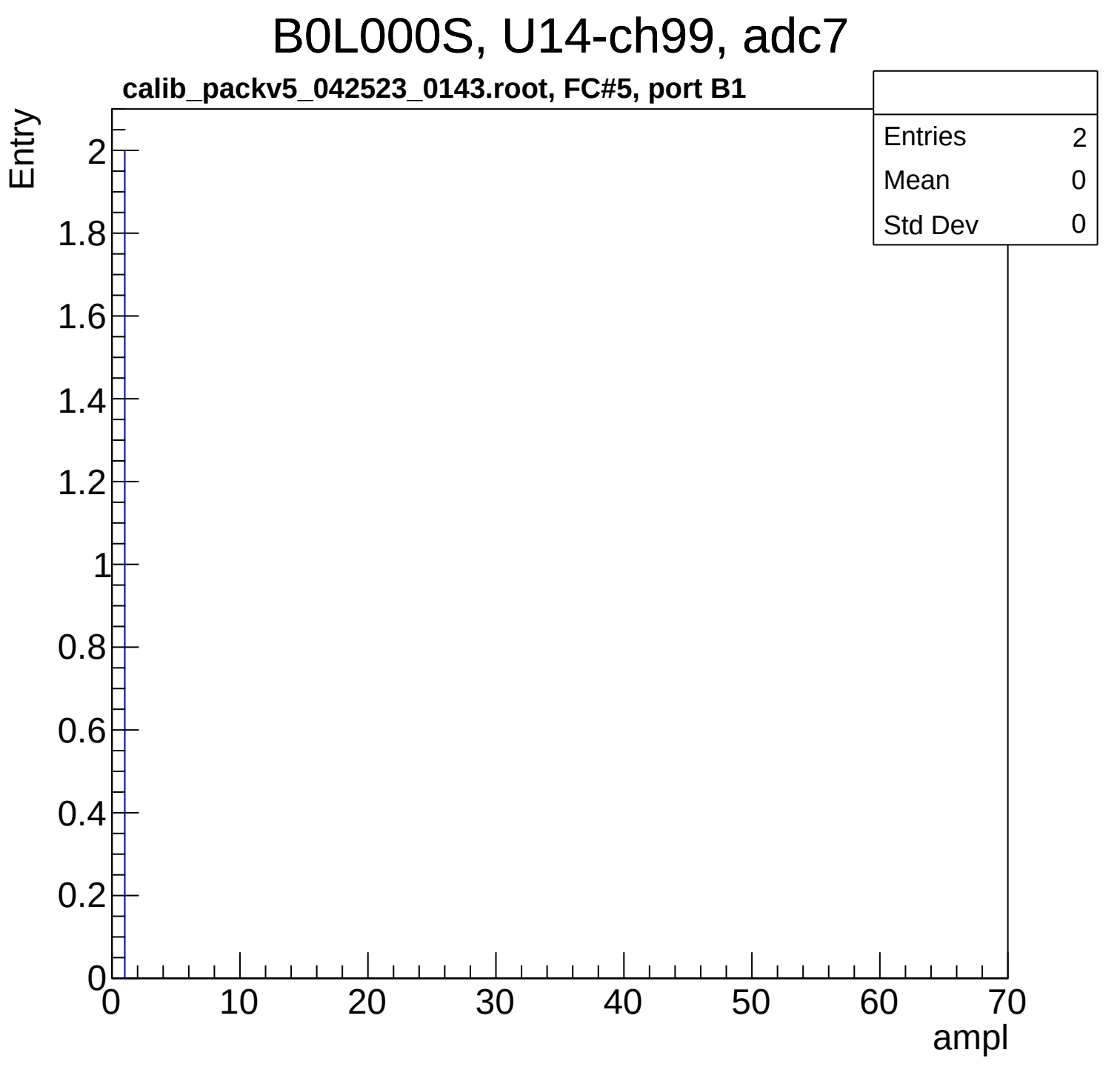
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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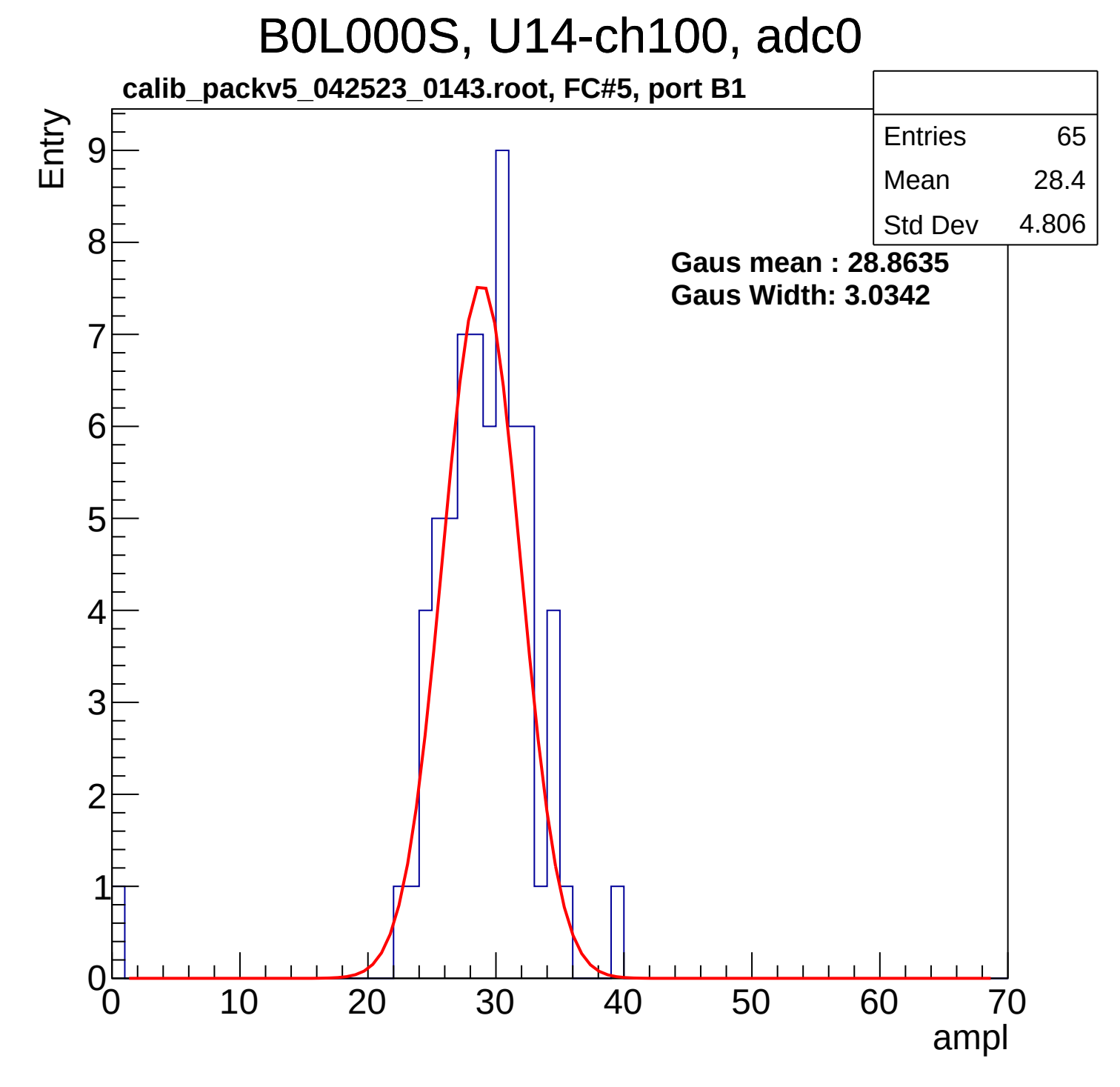
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.4
Std Dev 4.806
Gaus mean : 28.8635
Gaus Width: 3.0342
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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50
60
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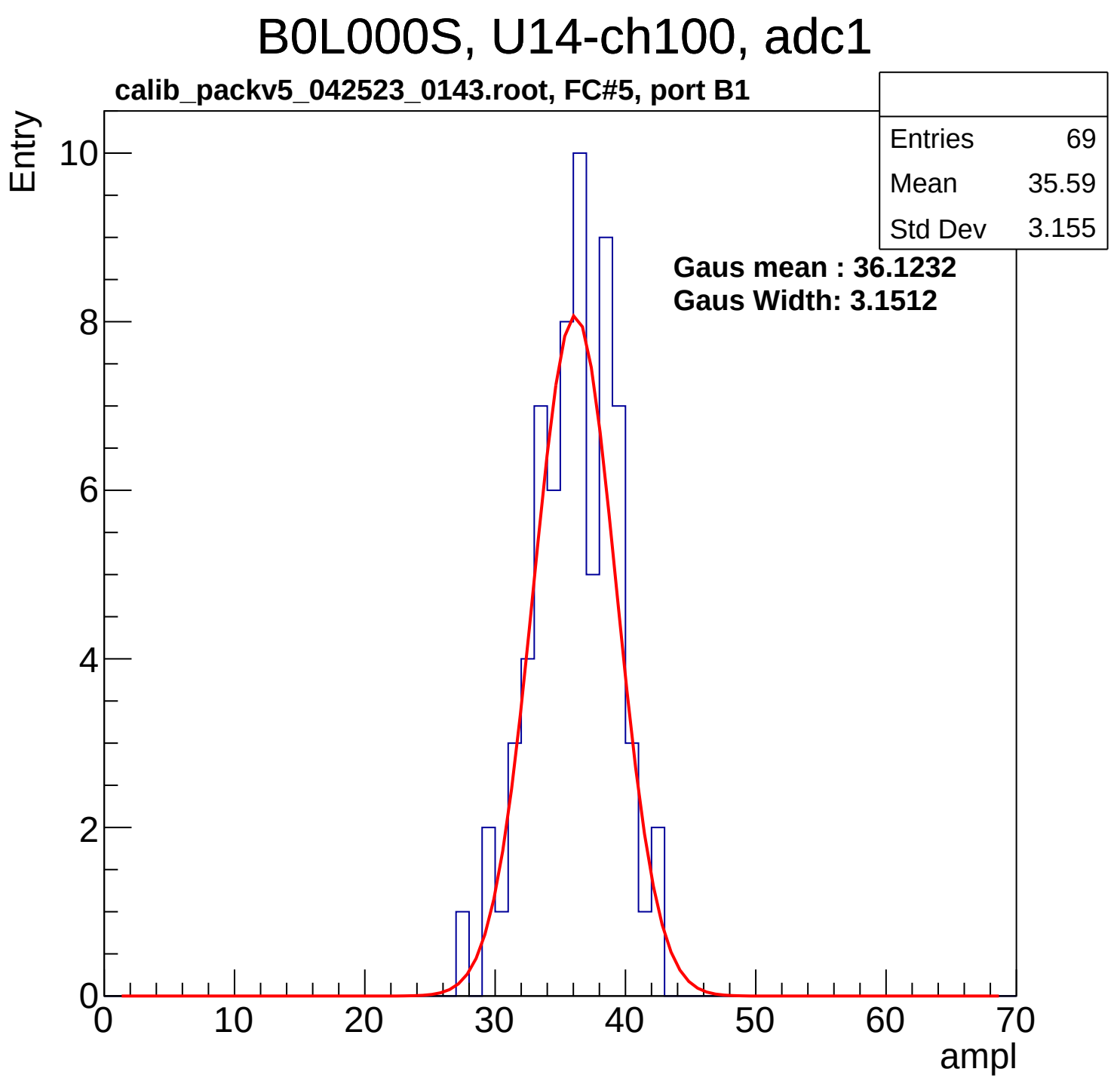
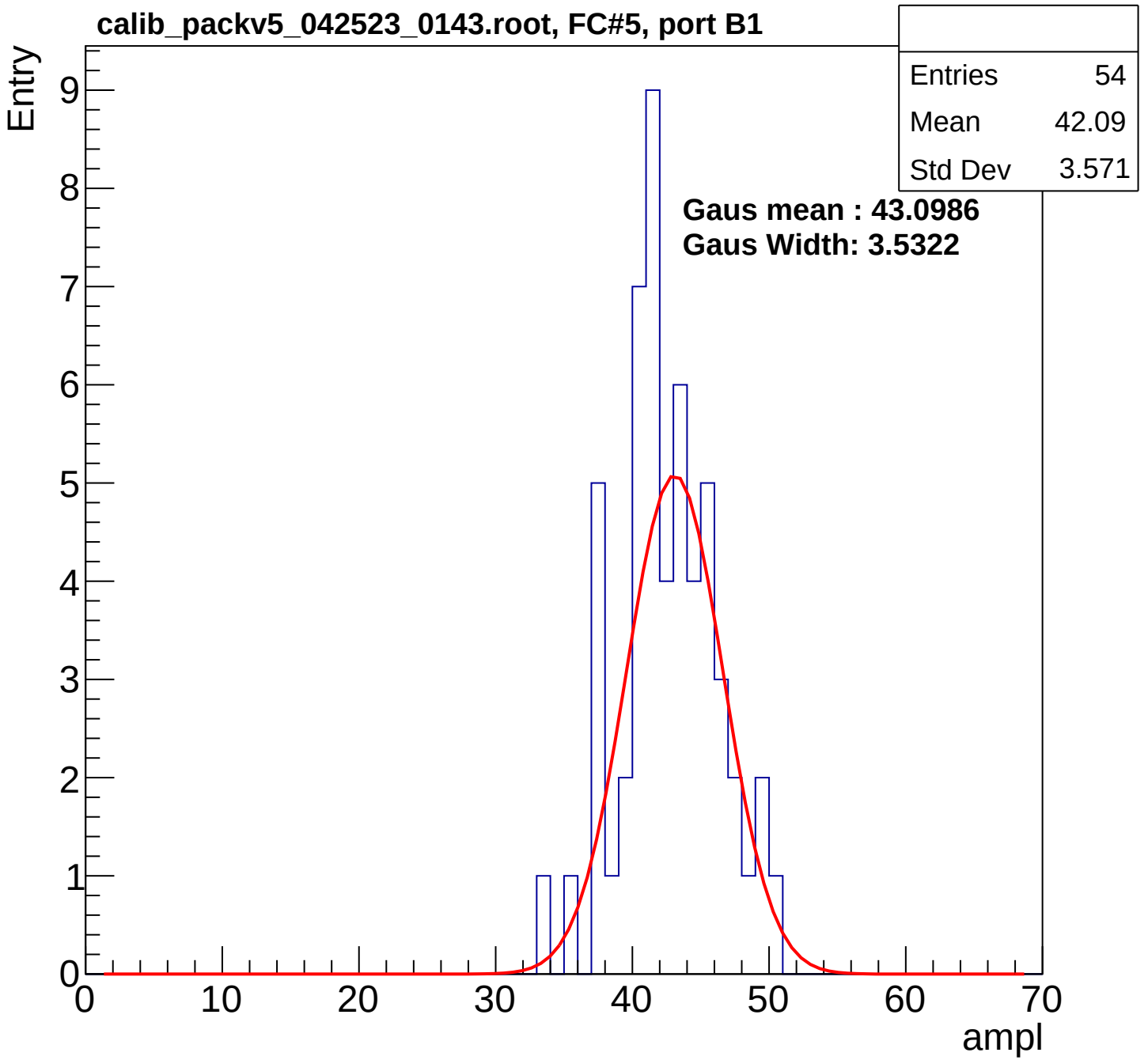

B0L000S, U14-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.59
Std Dev 3.155
Gaus mean : 36.1232
Gaus Width: 3.1512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 42.09
Std Dev 3.571
Gaus mean : 43.0986
Gaus Width: 3.5322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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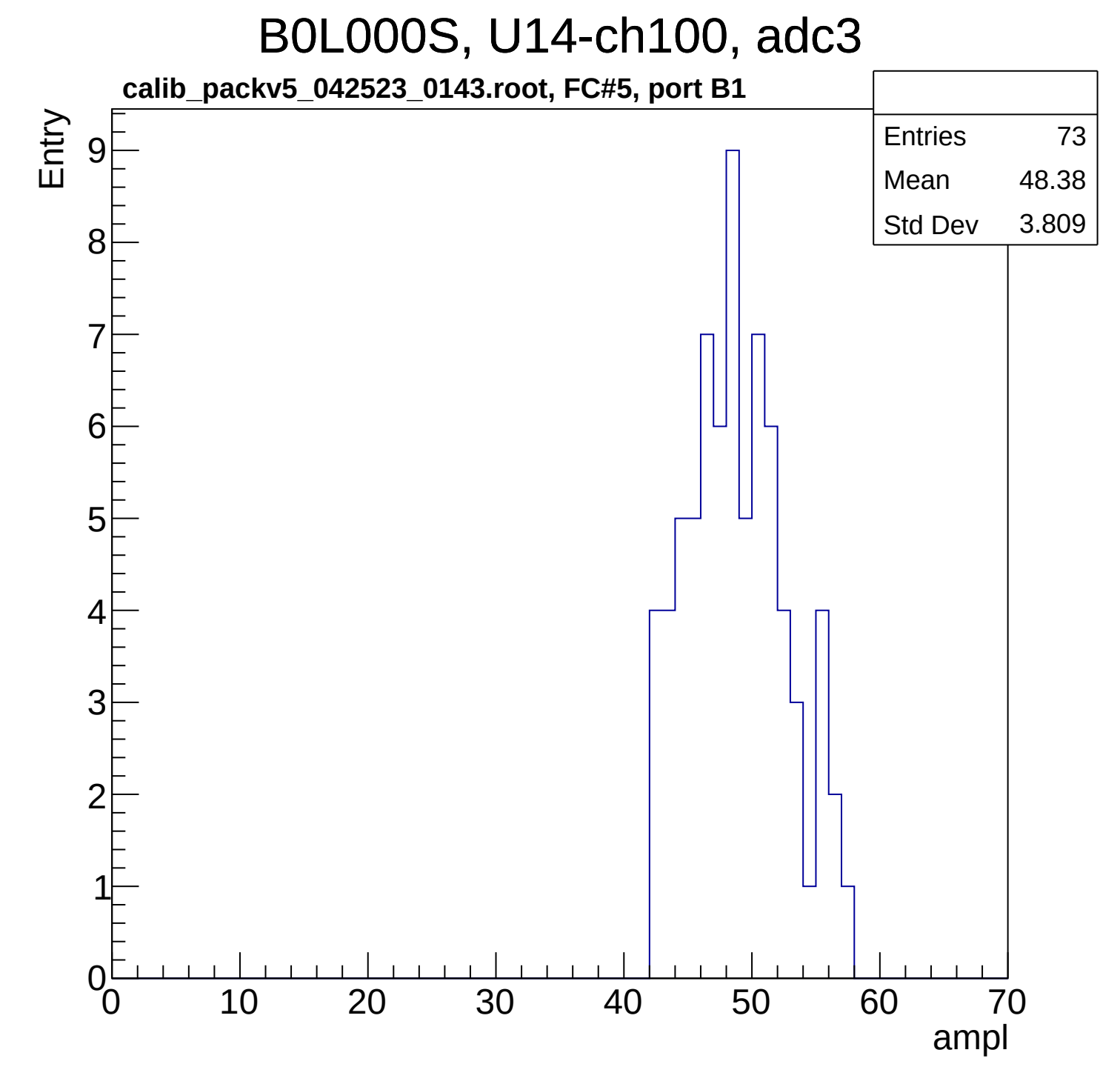
40
50
60
70


B0L000S, U14-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.38
Std Dev 3.809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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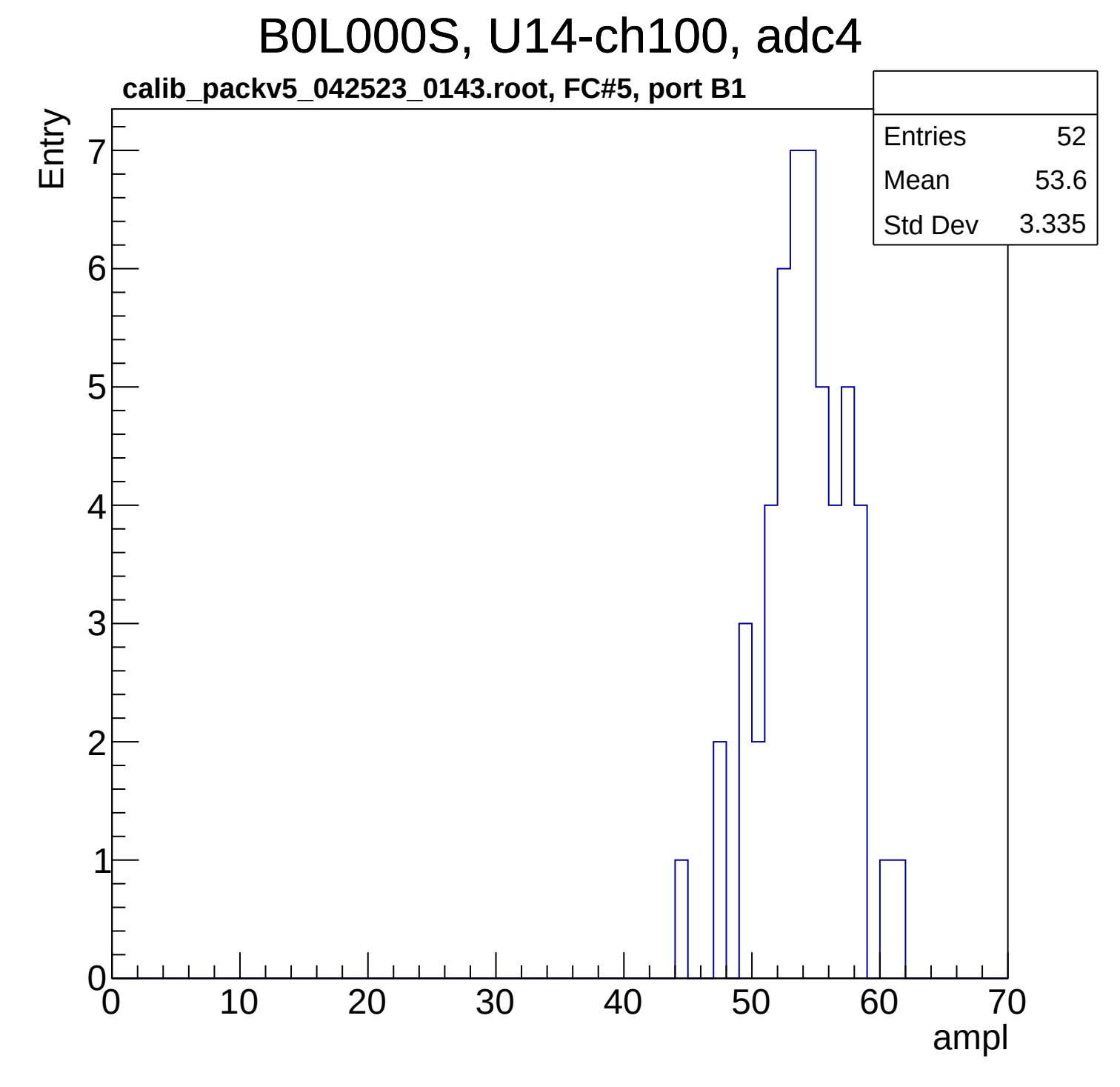
40
50
60
70


B0L000S, U14-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 53.6
Std Dev 3.335
Entry
ampl
0
1
2
3
4
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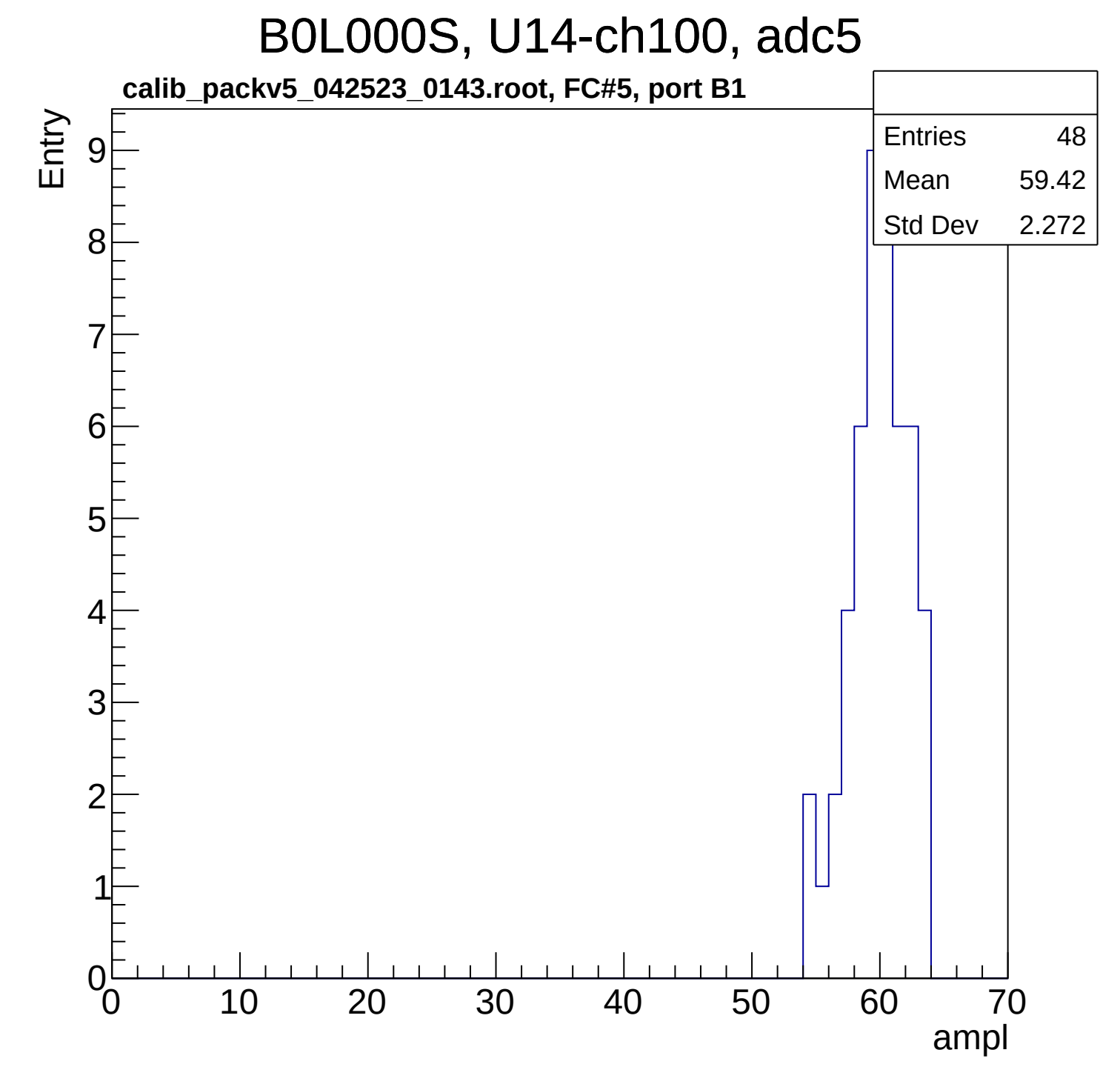
B0L000S, U14-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.42
Std Dev 2.272
Entry
ampl
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2
3
4
5
6
7
8
9
0
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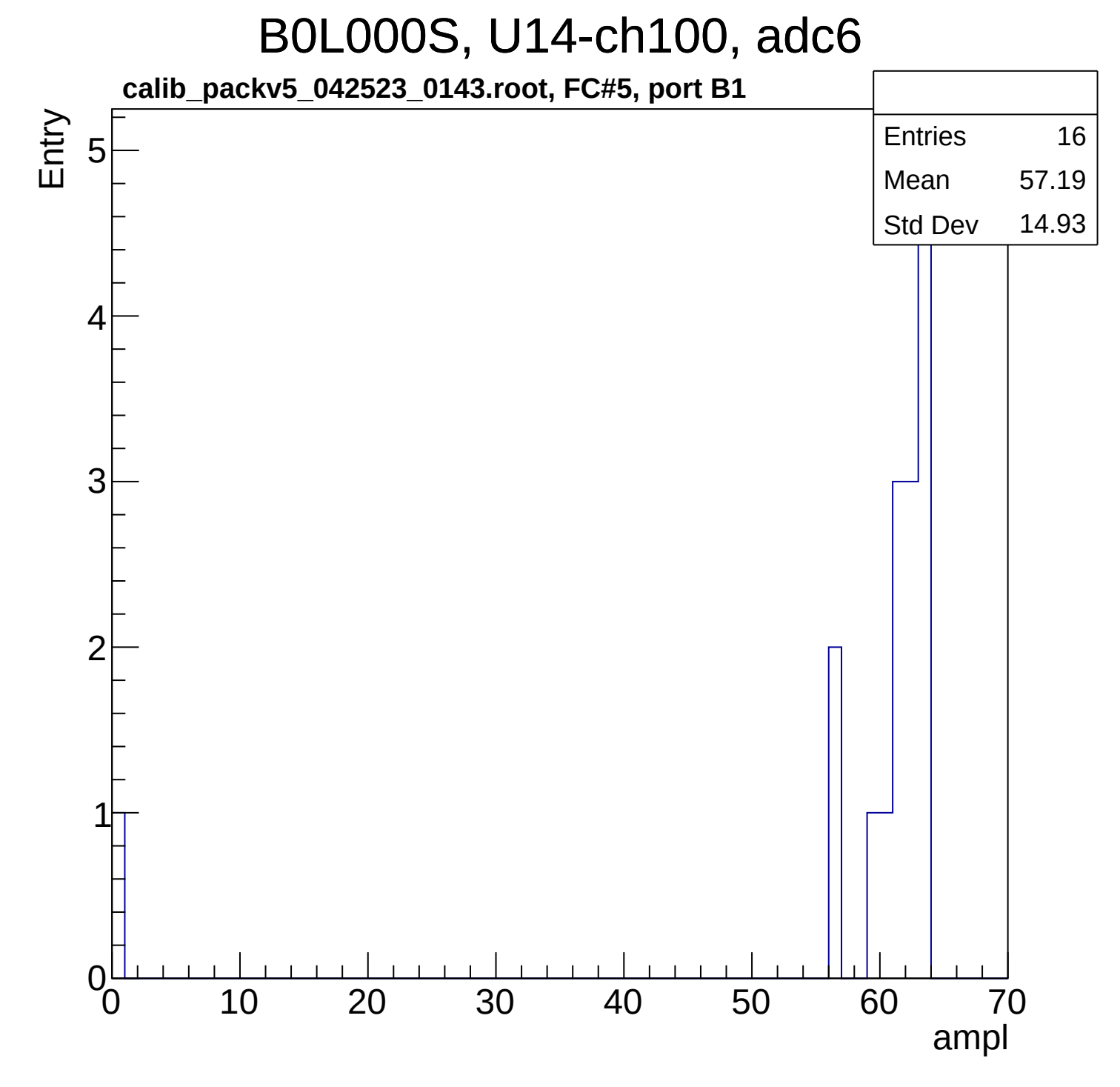
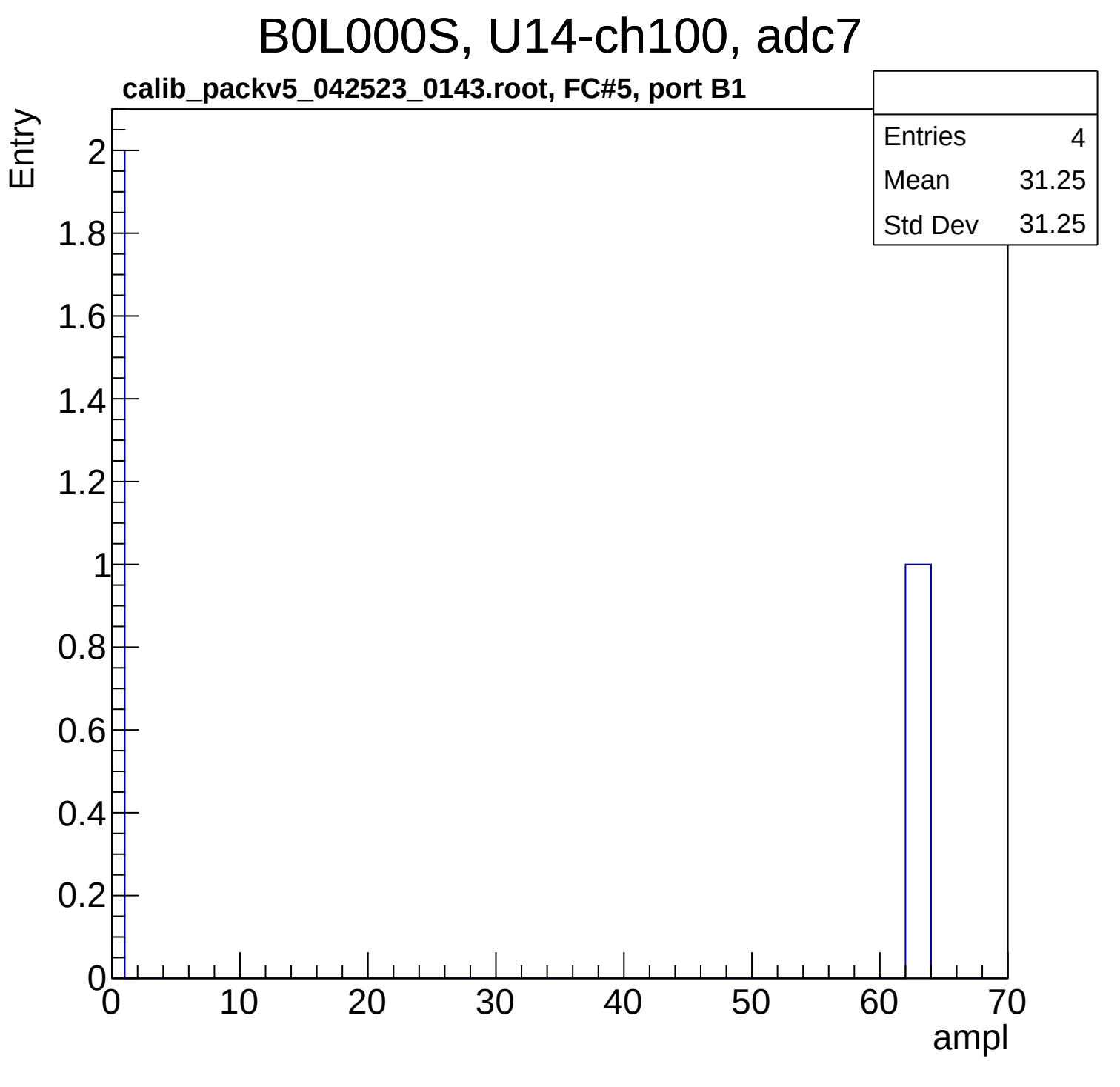

B0L000S, U14-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 57.19
Std Dev 14.93
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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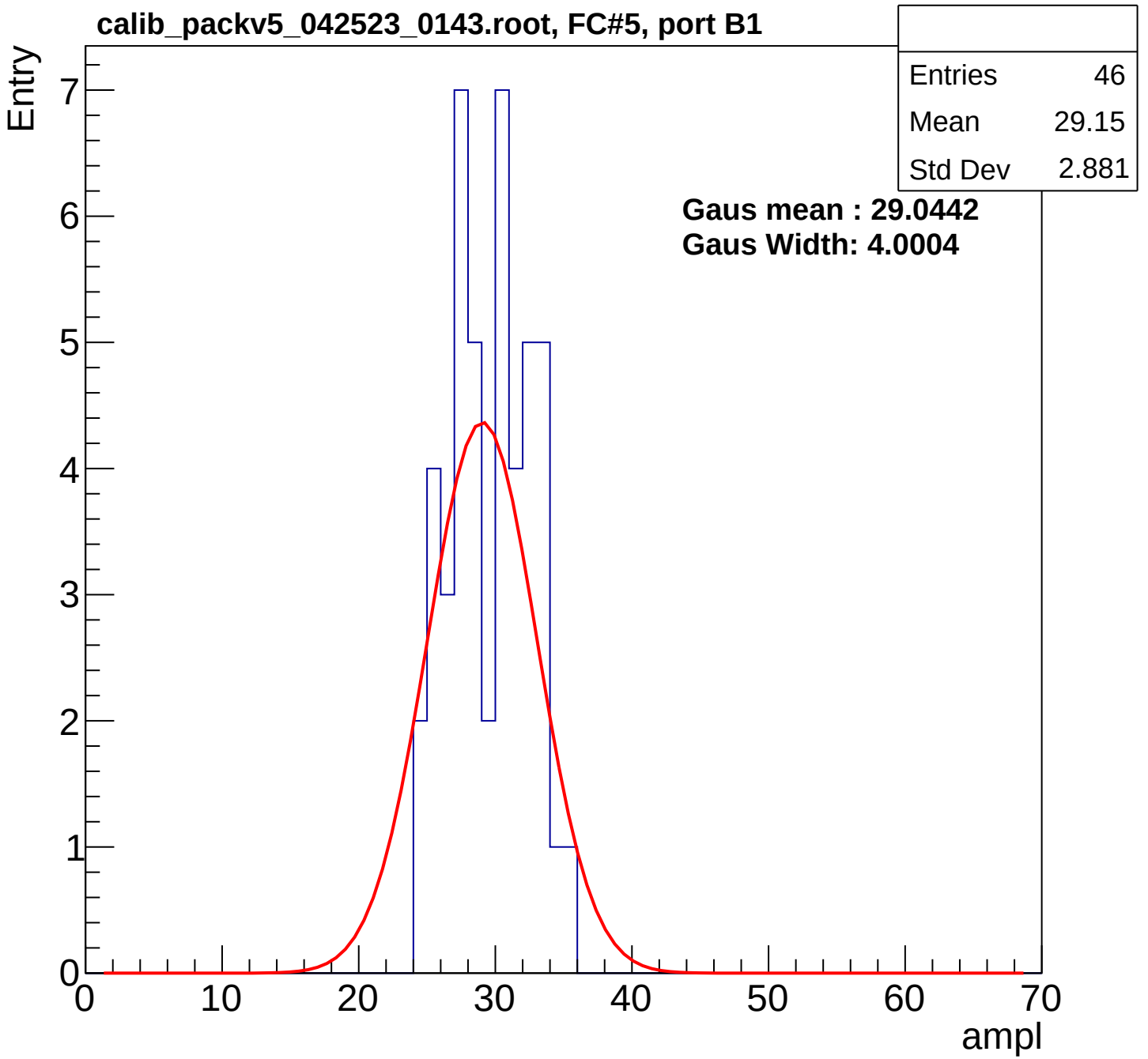
B0L000S, U14-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 29.15
Std Dev 2.881
Gaus mean : 29.0442
Gaus Width: 4.0004
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
50
60
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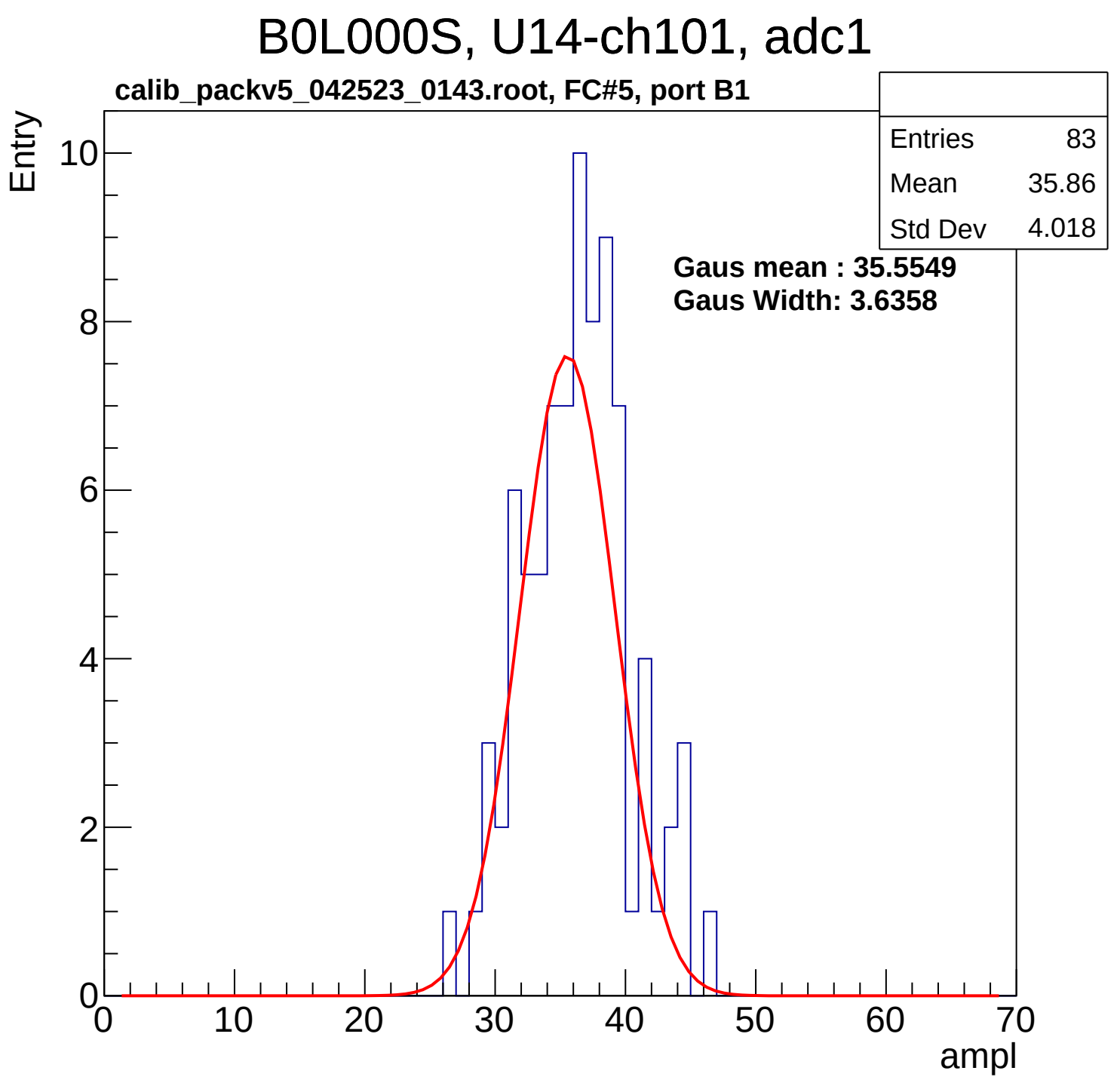
B0L000S, U14-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 35.86
Std Dev 4.018
Gaus mean : 35.5549
Gaus Width: 3.6358
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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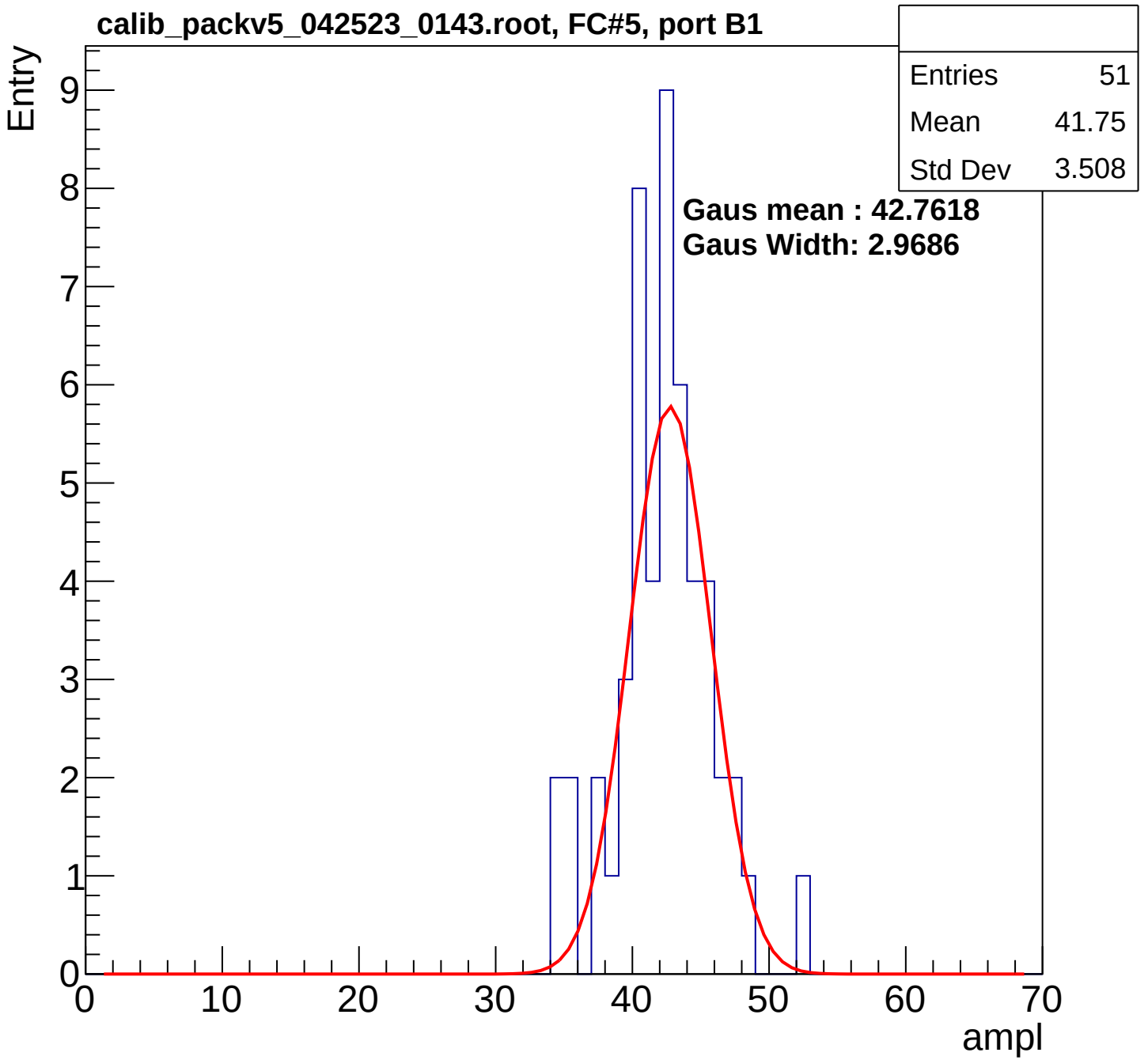
B0L000S, U14-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 41.75
Std Dev 3.508
Gaus mean : 42.7618
Gaus Width: 2.9686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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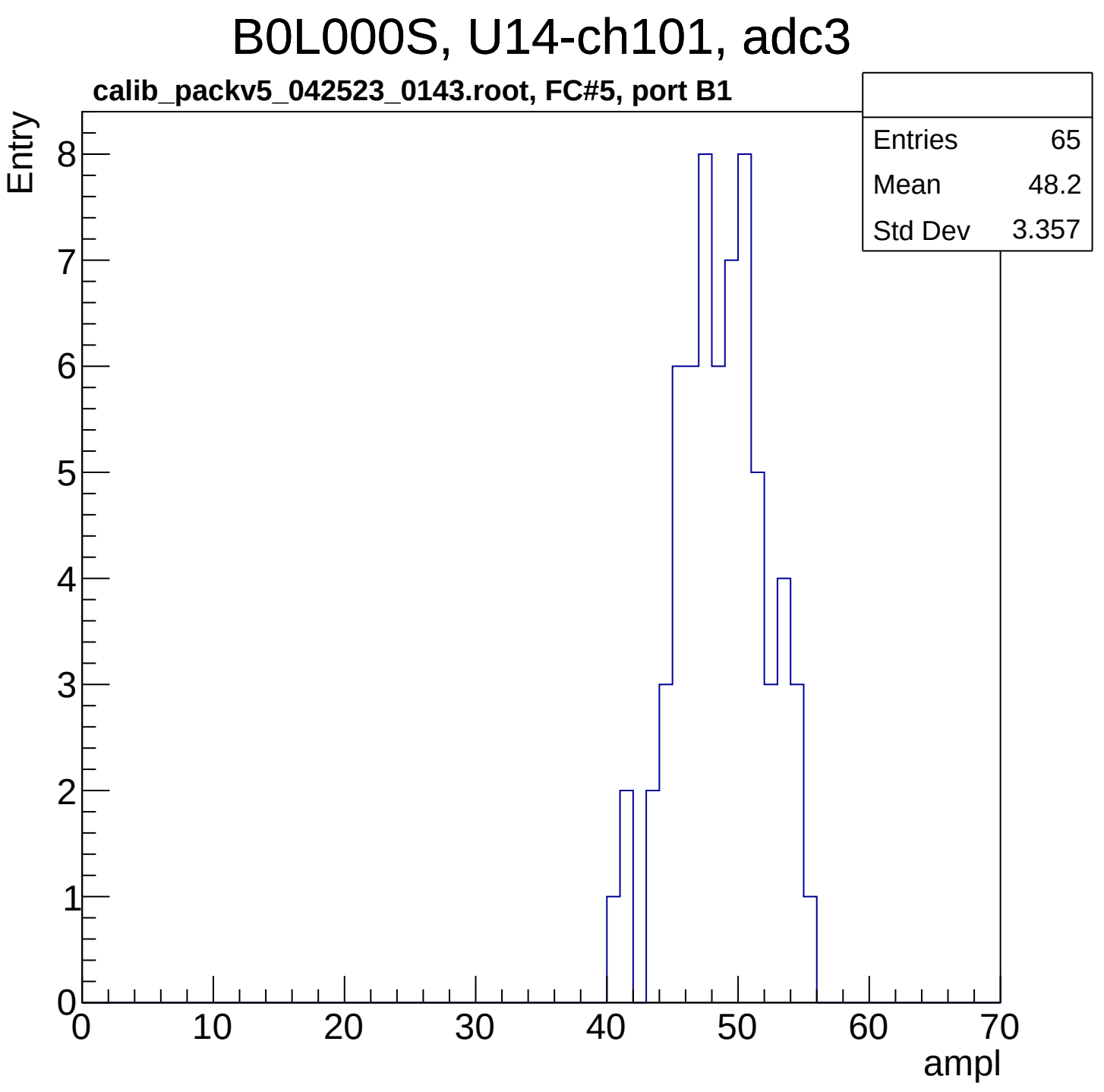
B0L000S, U14-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 48.2
Std Dev 3.357
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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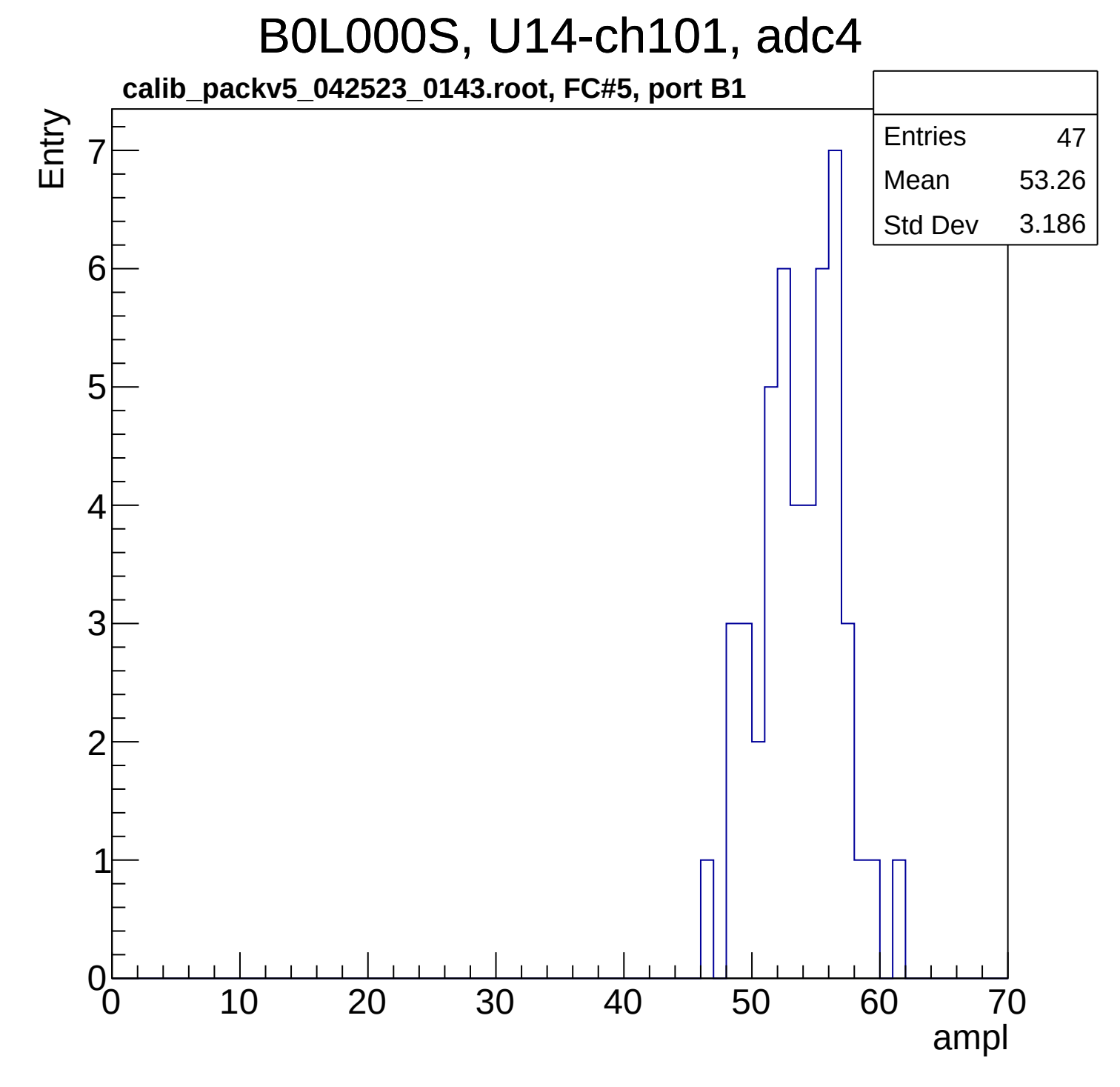
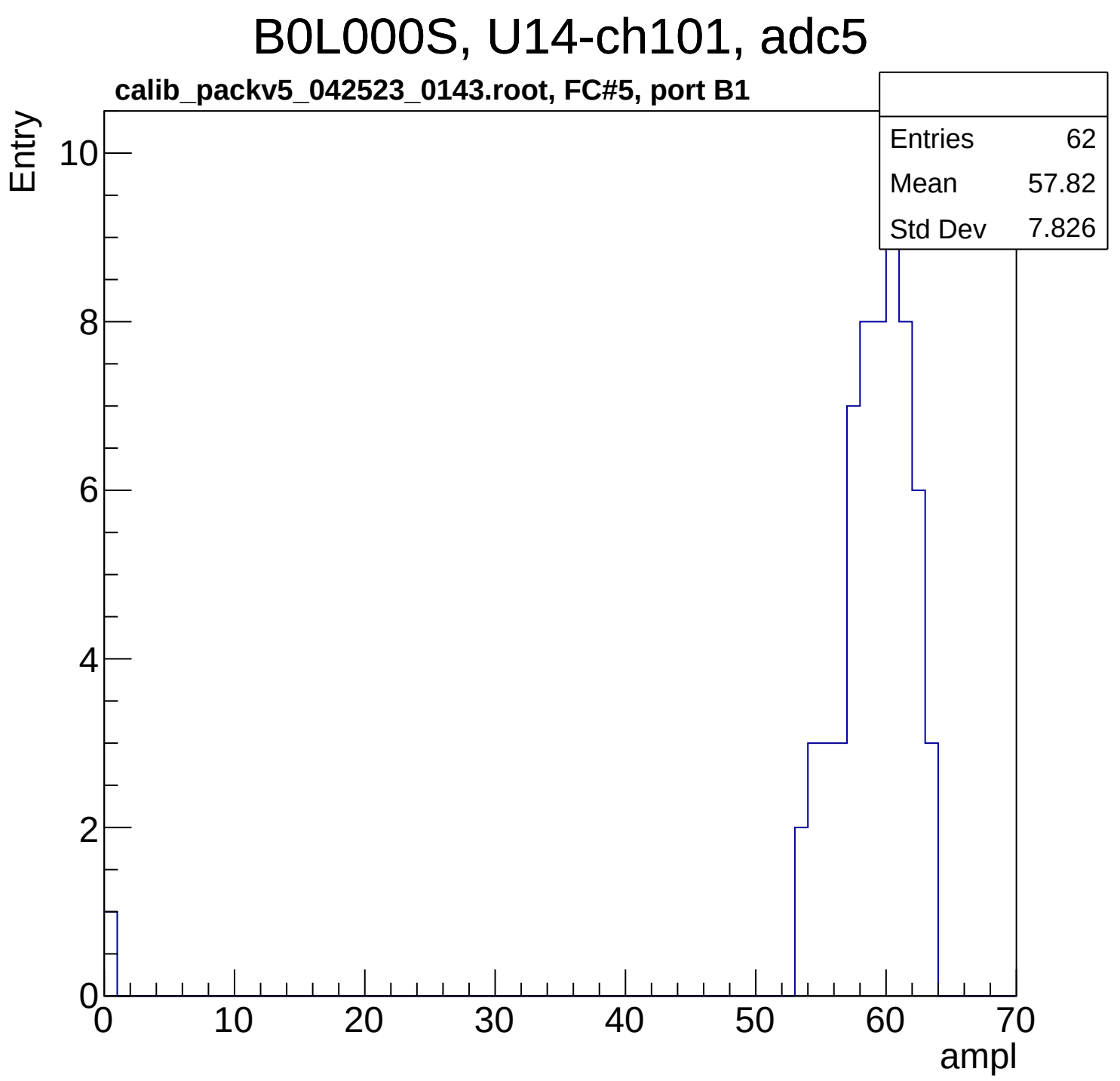

B0L000S, U14-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 53.26
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 57.82
Std Dev 7.826
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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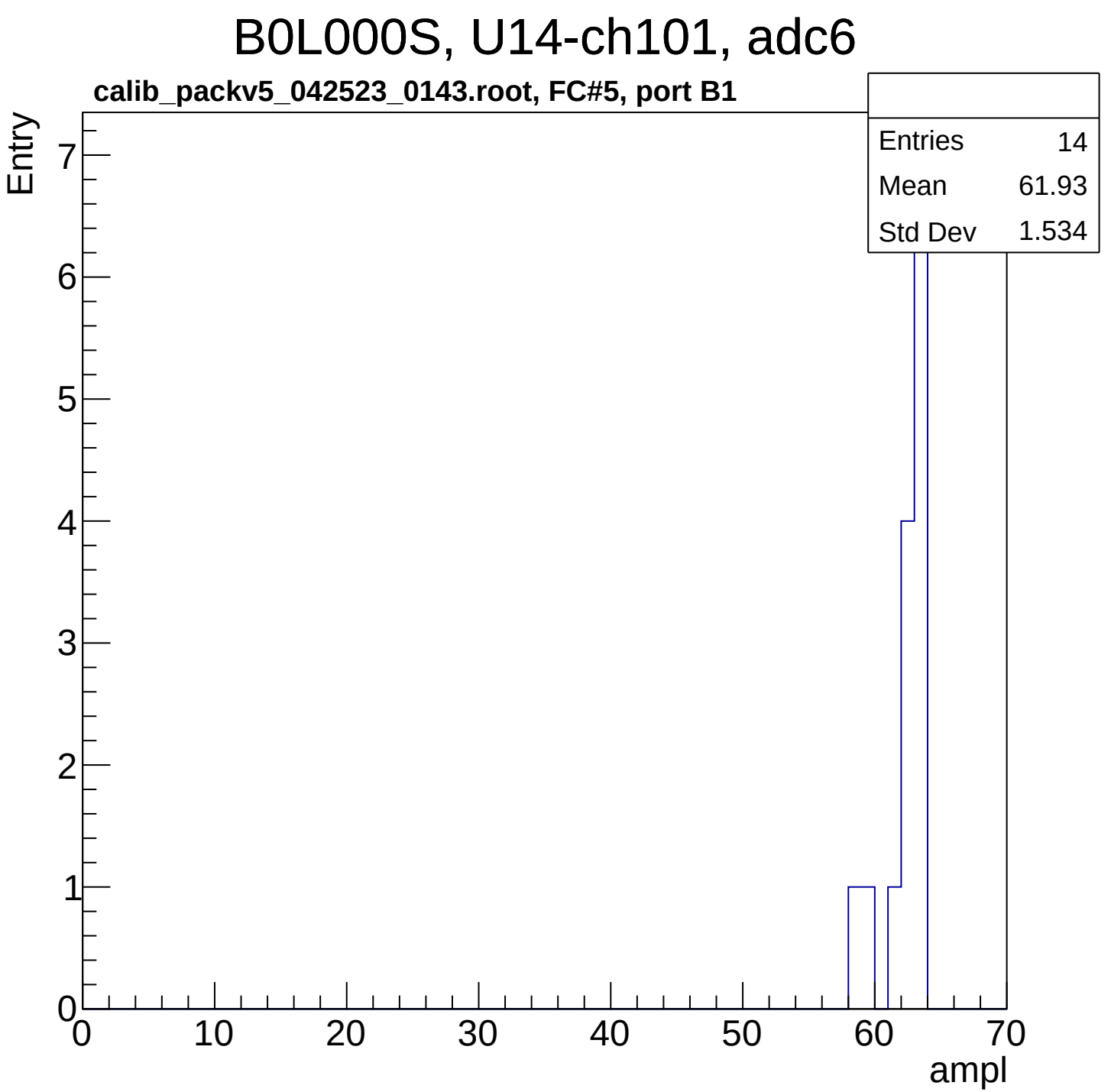
B0L000S, U14-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.93
Std Dev 1.534
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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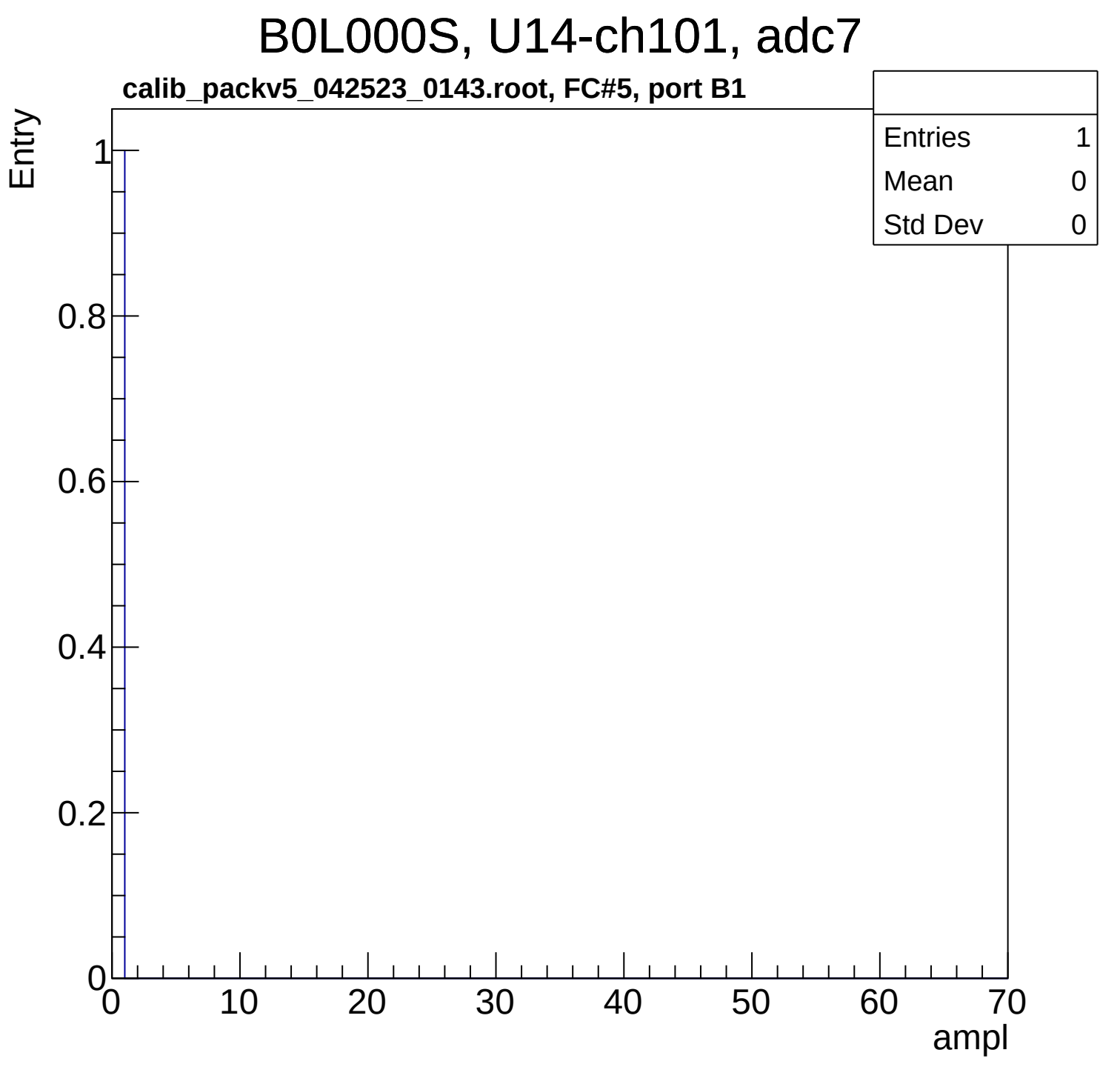
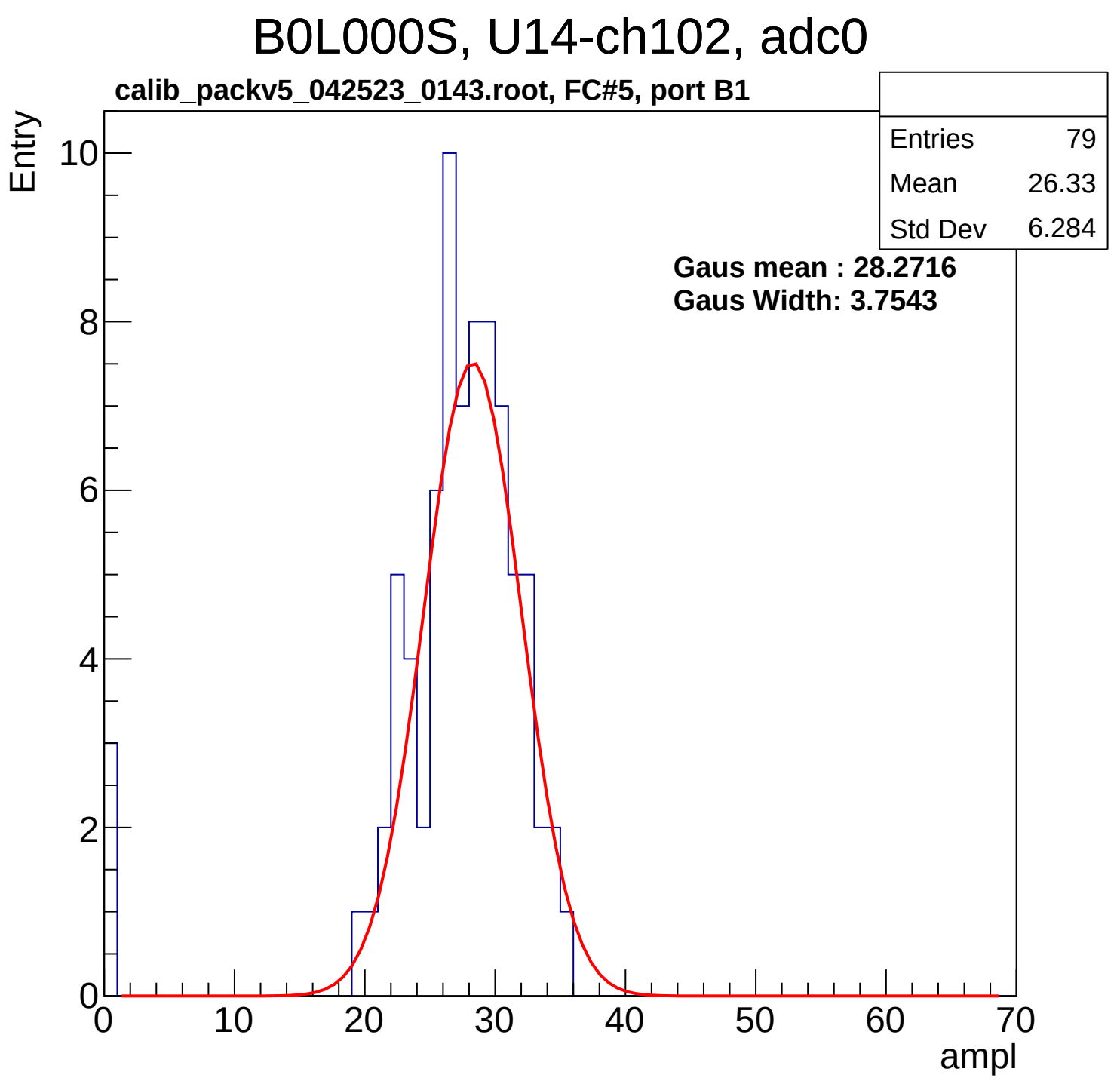
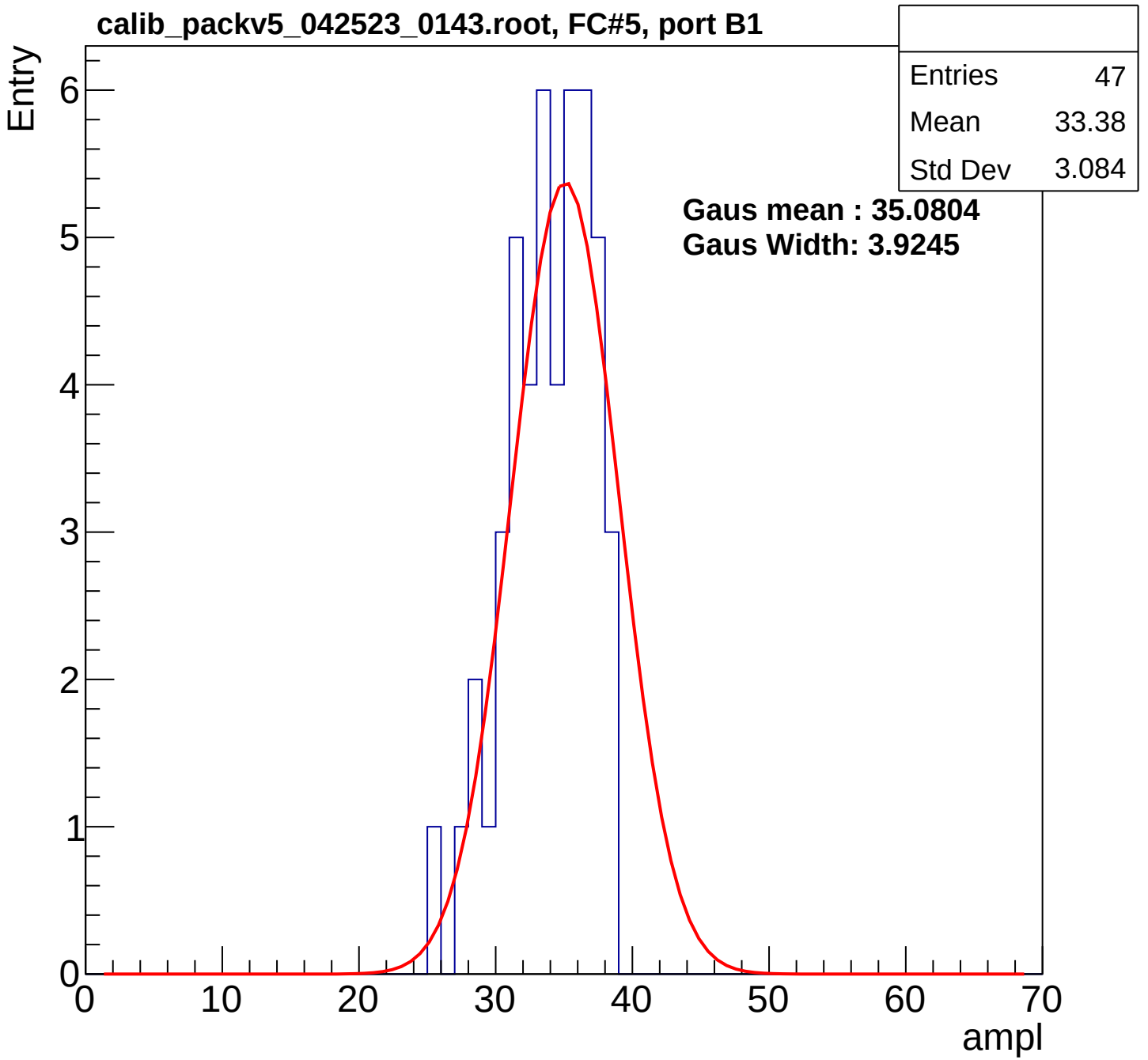

B0L000S, U14-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 26.33
Std Dev 6.284
Gaus mean : 28.2716
Gaus Width: 3.7543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 33.38
Std Dev 3.084
Gaus mean : 35.0804
Gaus Width: 3.9245
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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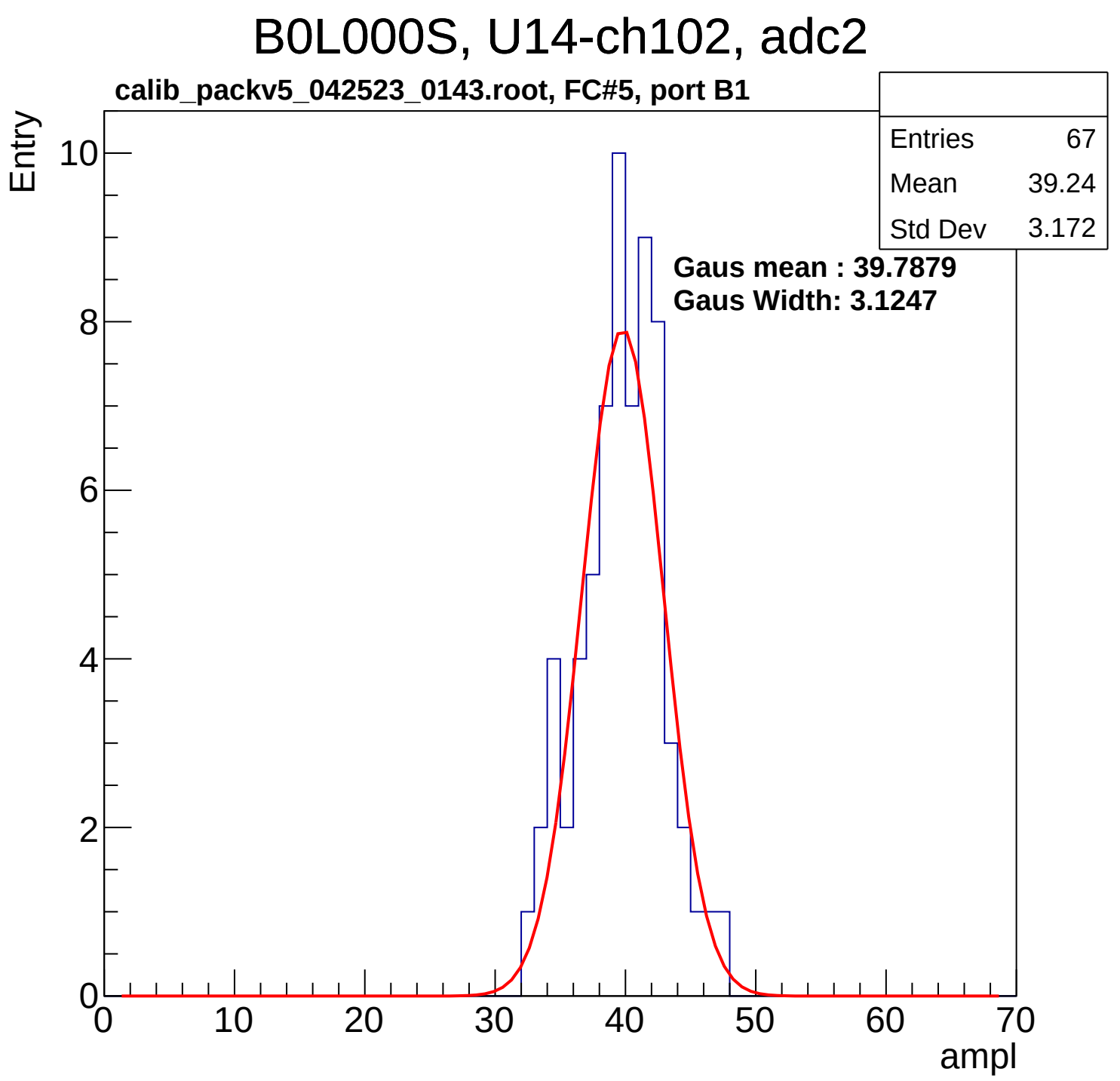
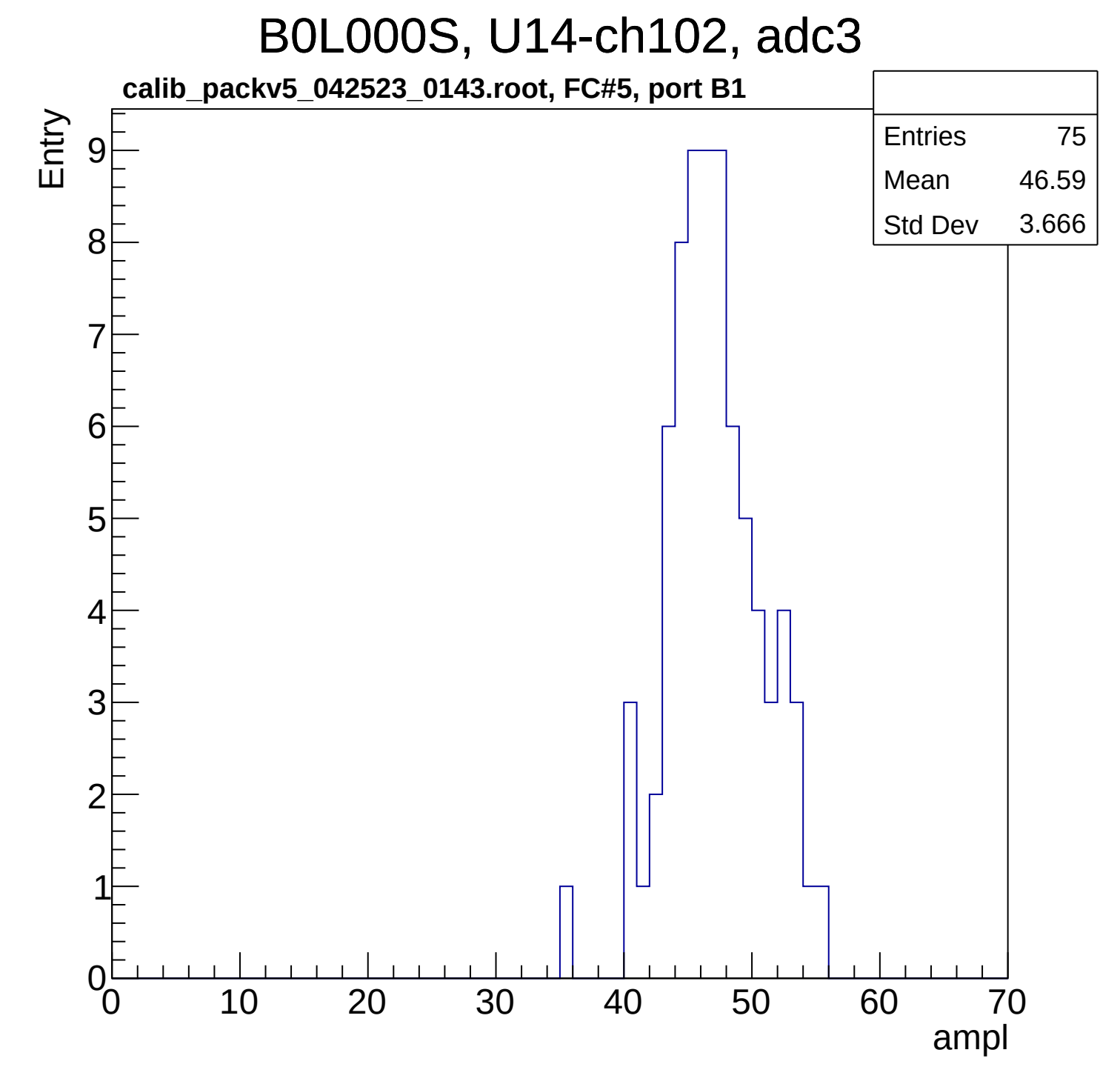
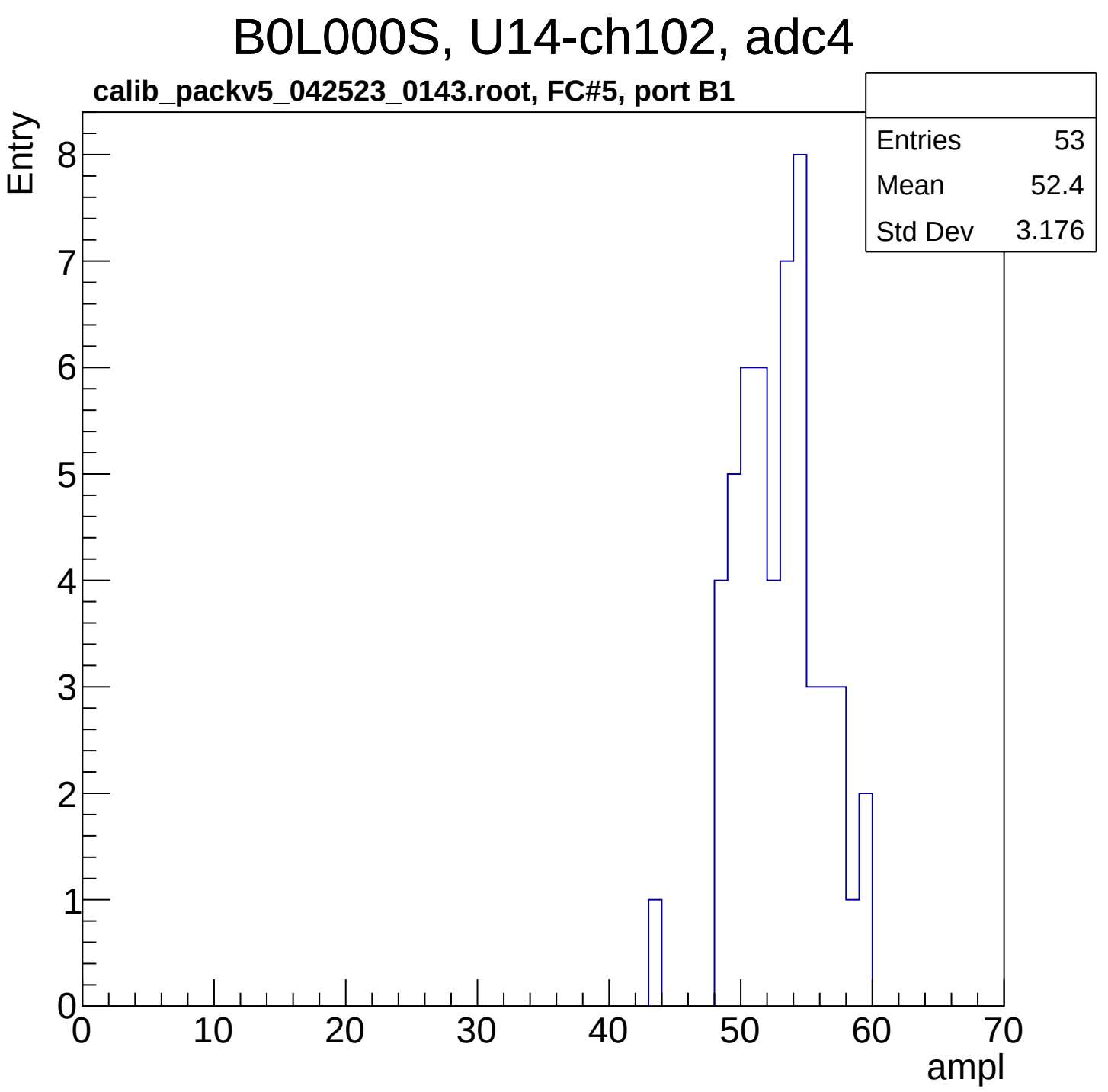

B0L000S, U14-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 39.24
Std Dev 3.172
Gaus mean : 39.7879
Gaus Width: 3.1247
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 46.59
Std Dev 3.666
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 52.4
Std Dev 3.176
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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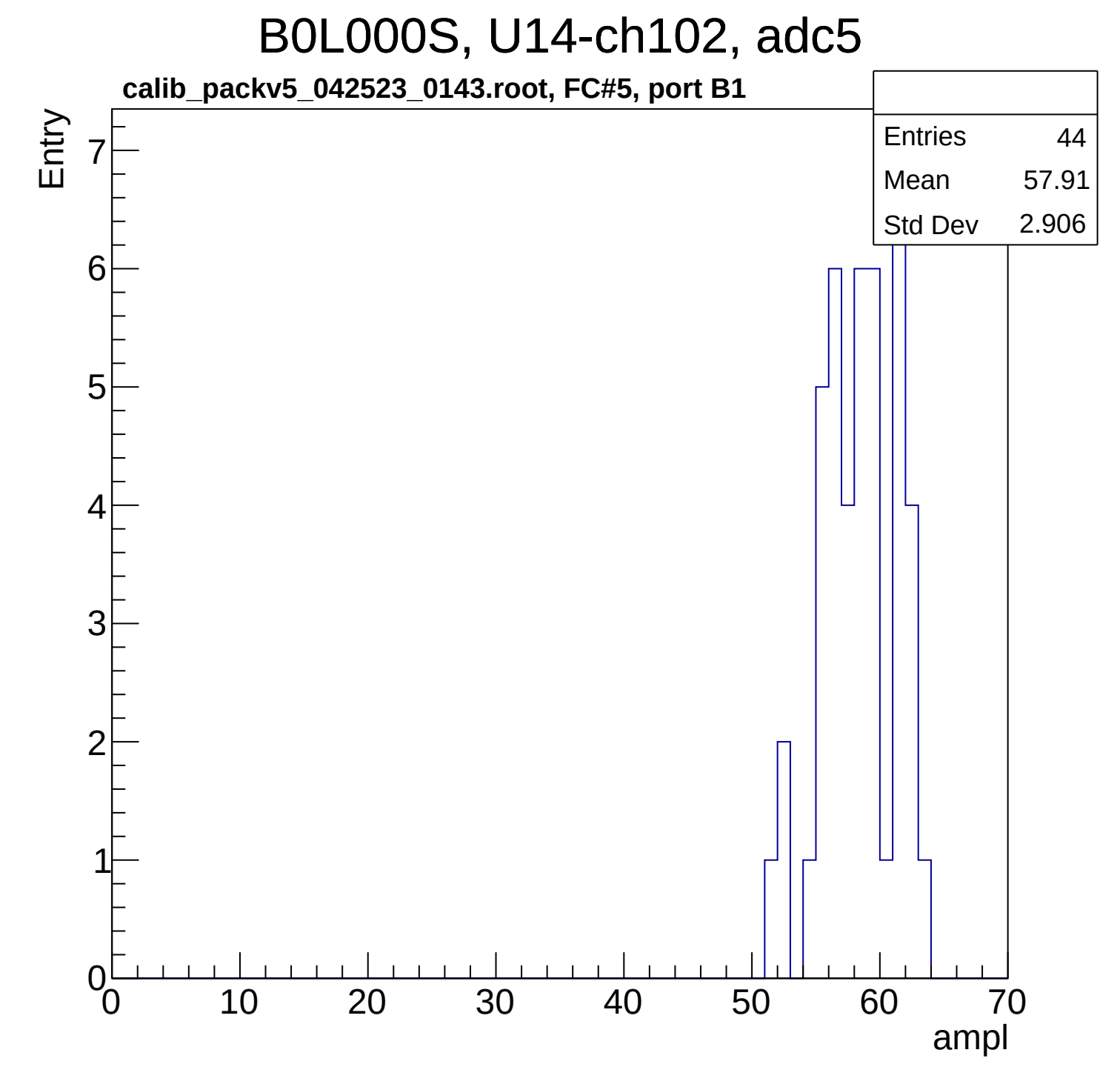
B0L000S, U14-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 57.91
Std Dev 2.906
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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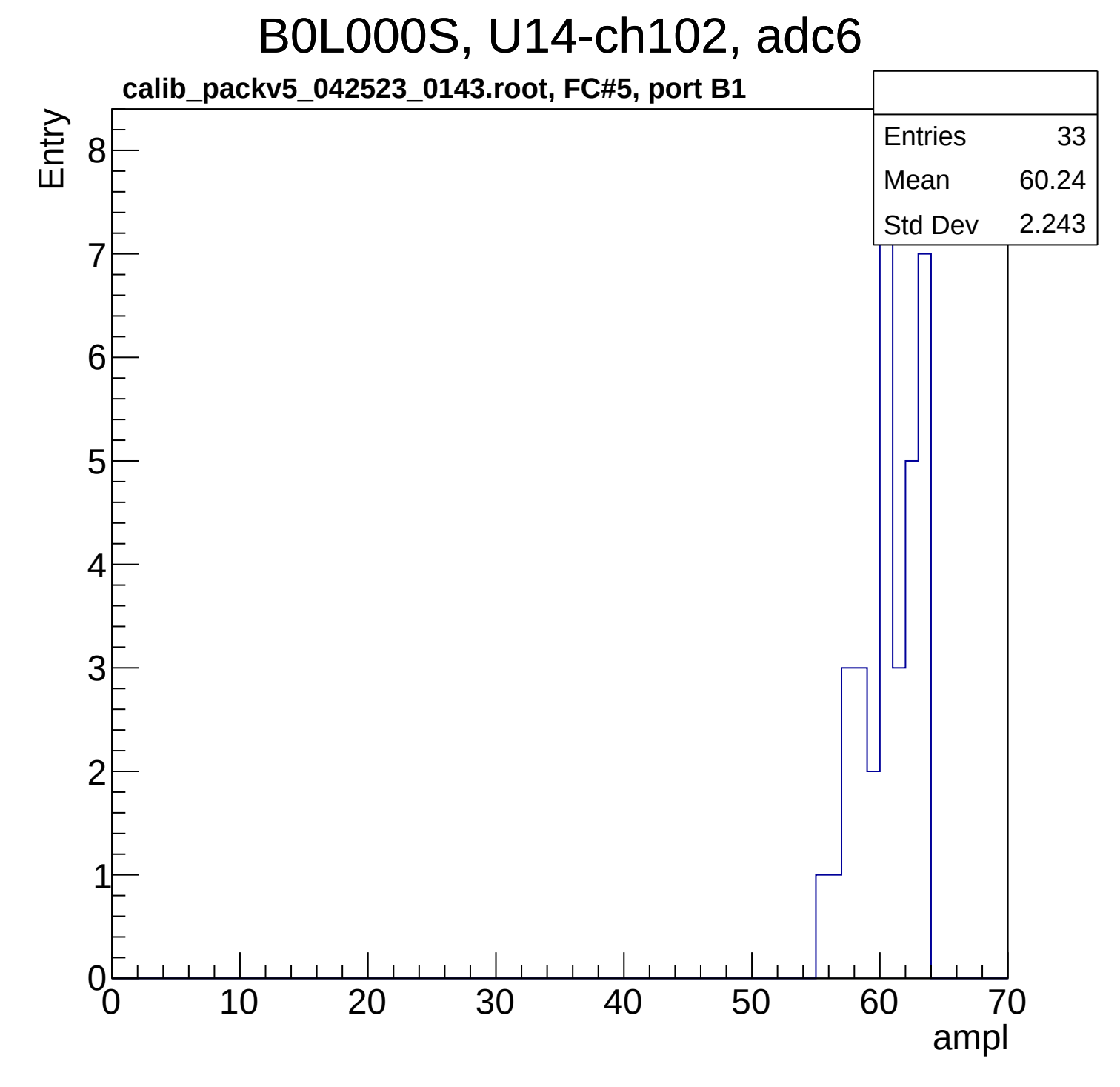
B0L000S, U14-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.24
Std Dev 2.243
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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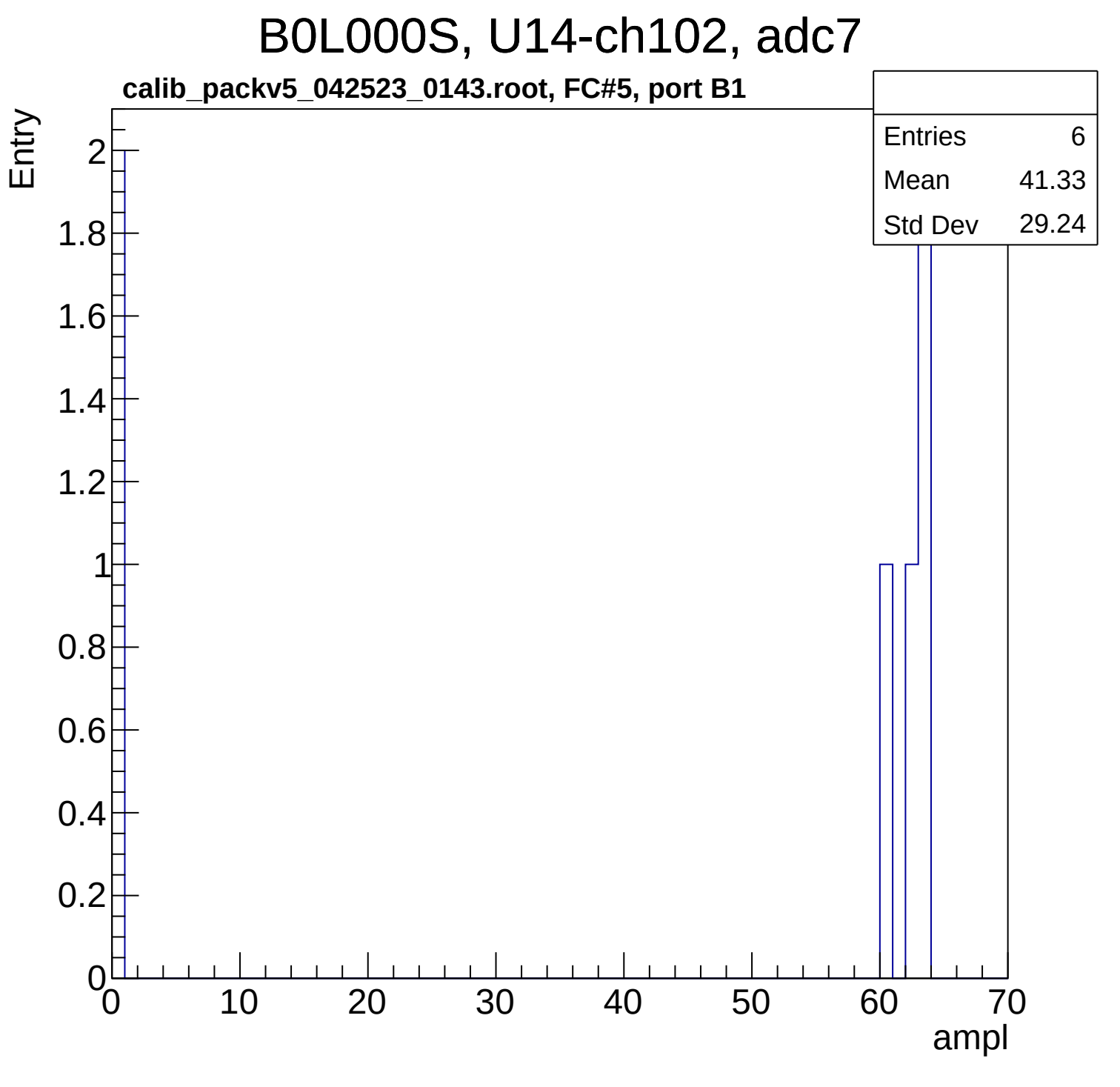
B0L000S, U14-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 41.33
Std Dev 29.24
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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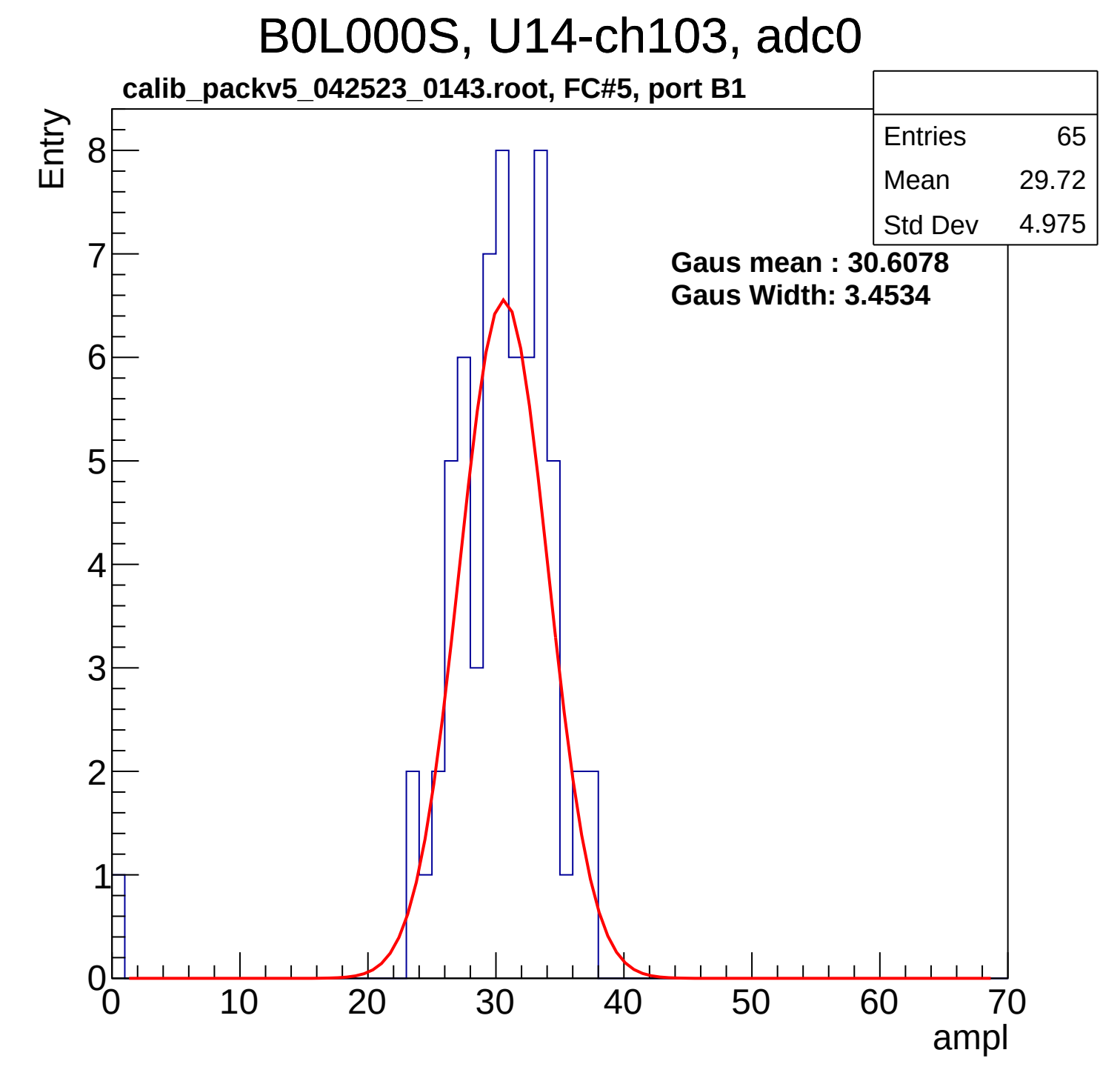
B0L000S, U14-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.72
Std Dev 4.975
Gaus mean : 30.6078
Gaus Width: 3.4534
Entry
ampl
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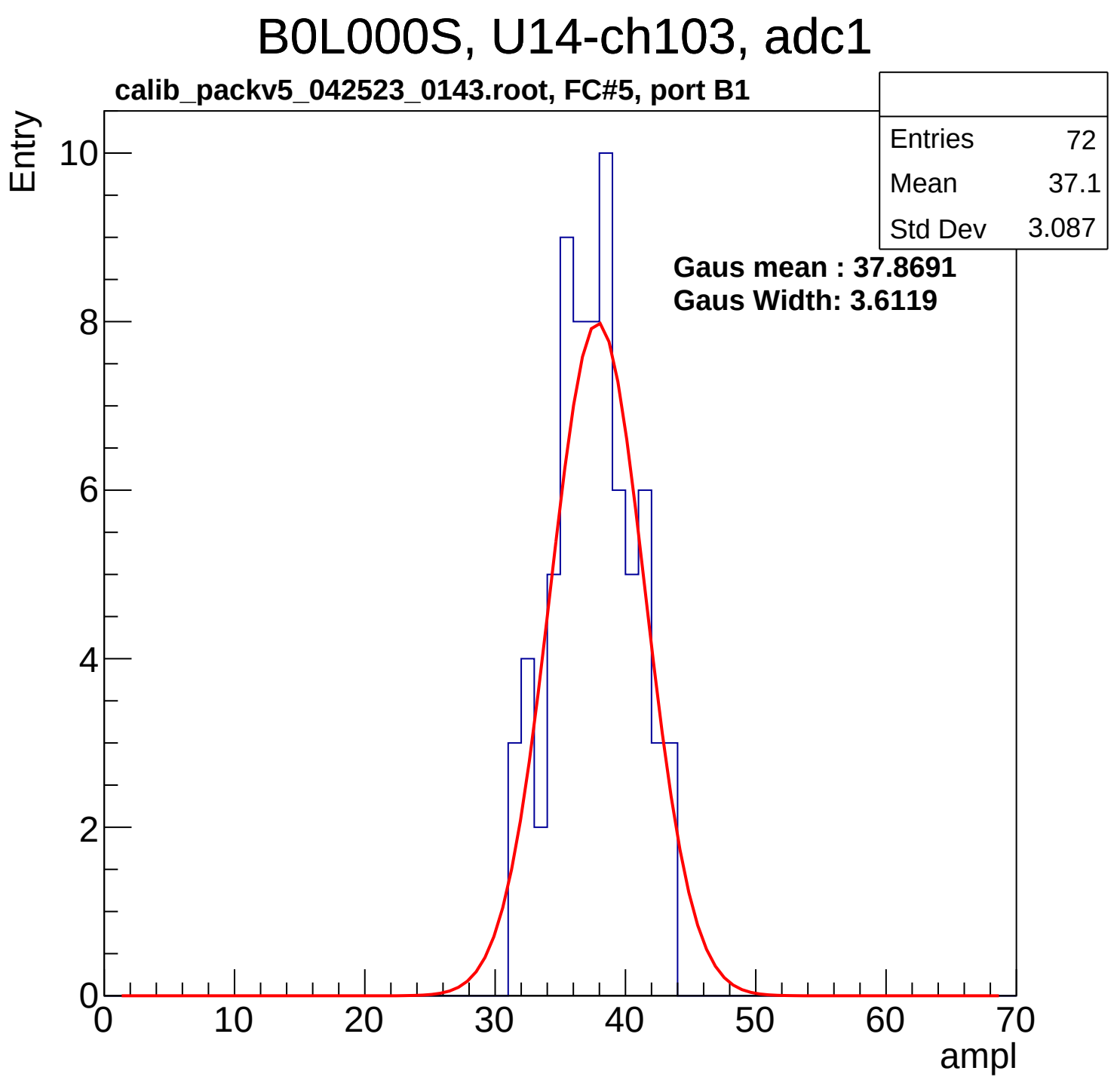
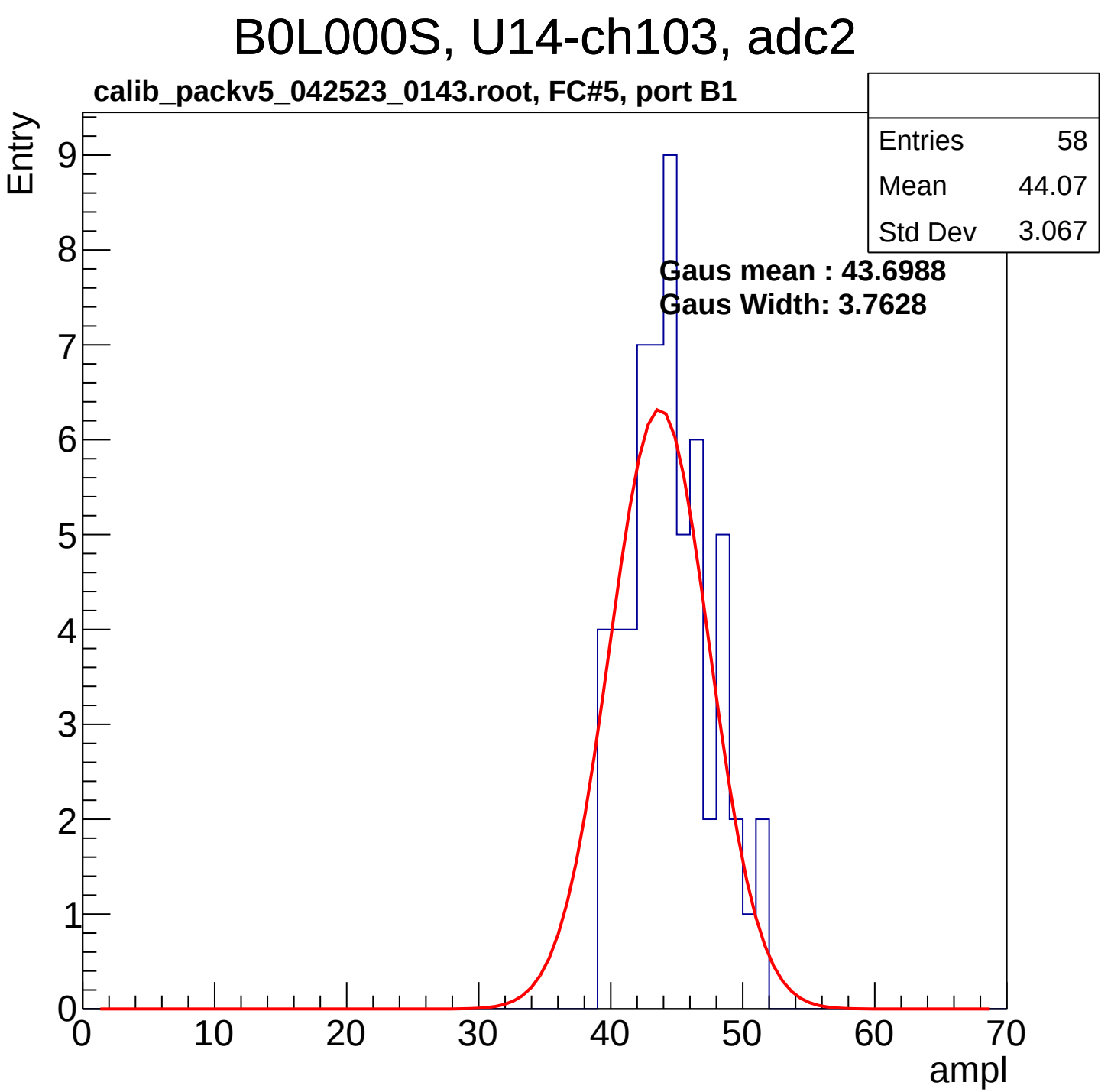
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.1
Std Dev 3.087
Gaus mean : 37.8691
Gaus Width: 3.6119
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 44.07
Std Dev 3.067
Gaus mean : 43.6988
Gaus Width: 3.7628
Entry
ampl
0
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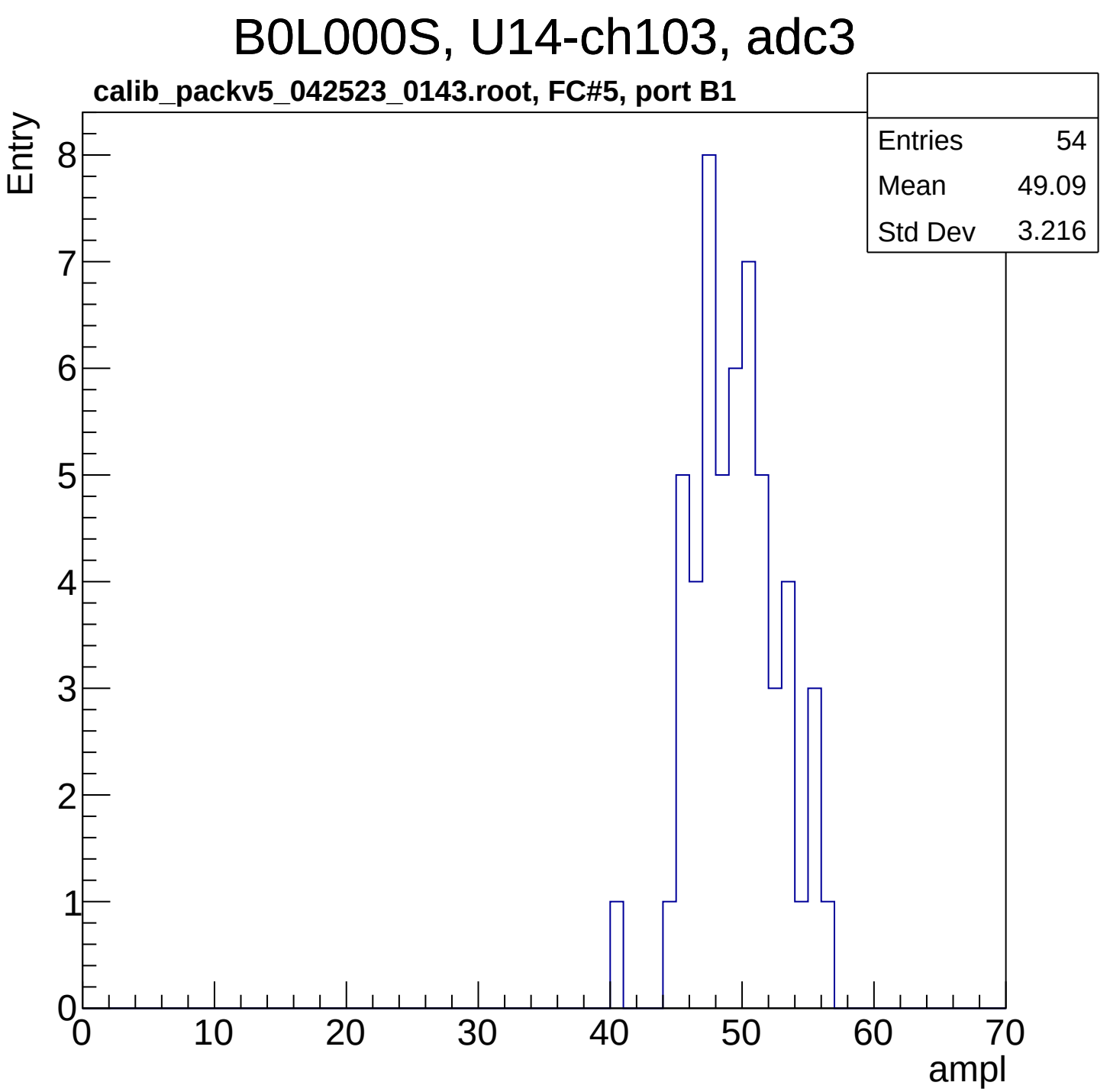
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.09
Std Dev 3.216
Entry
ampl
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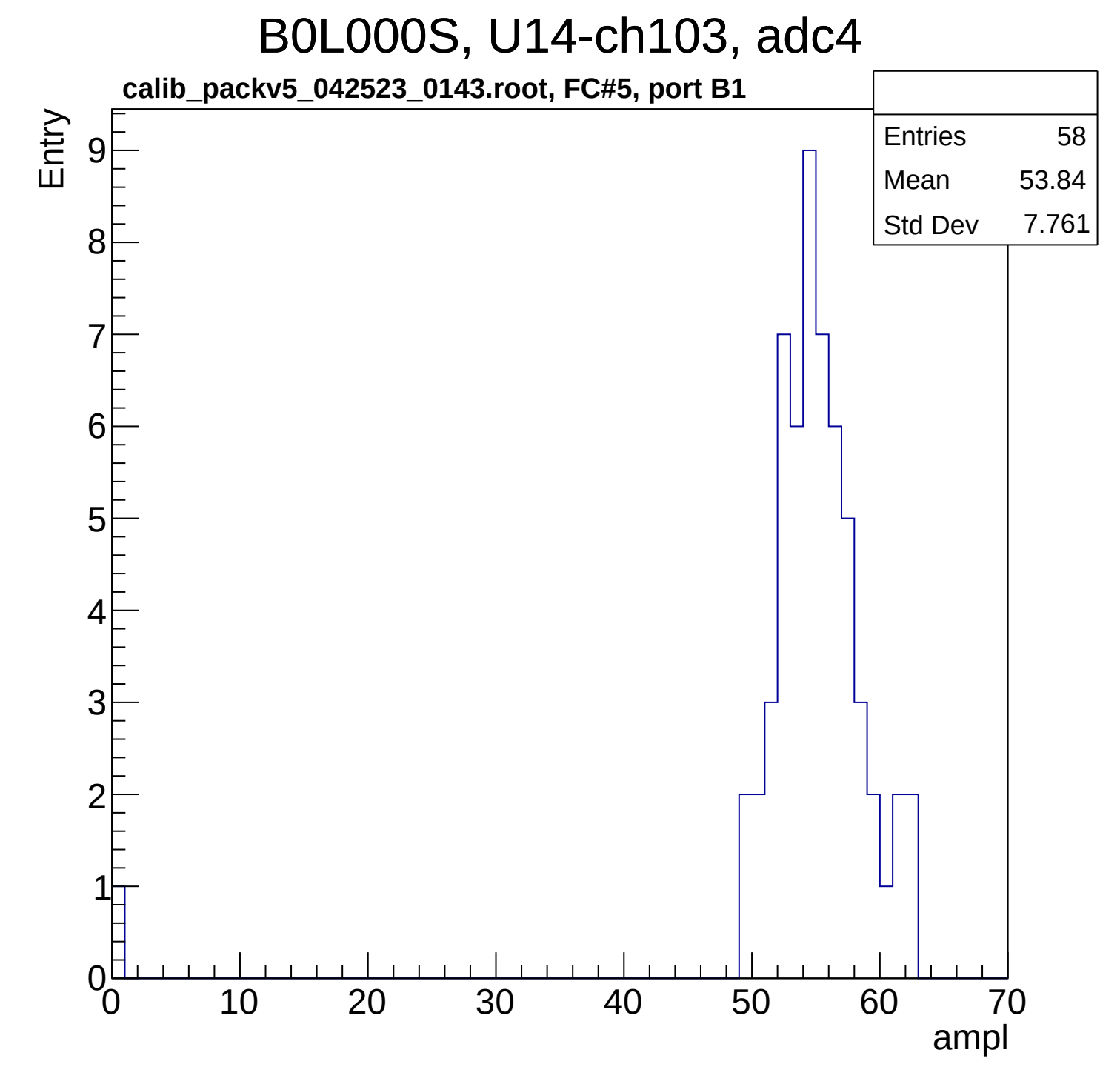
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 53.84
Std Dev 7.761
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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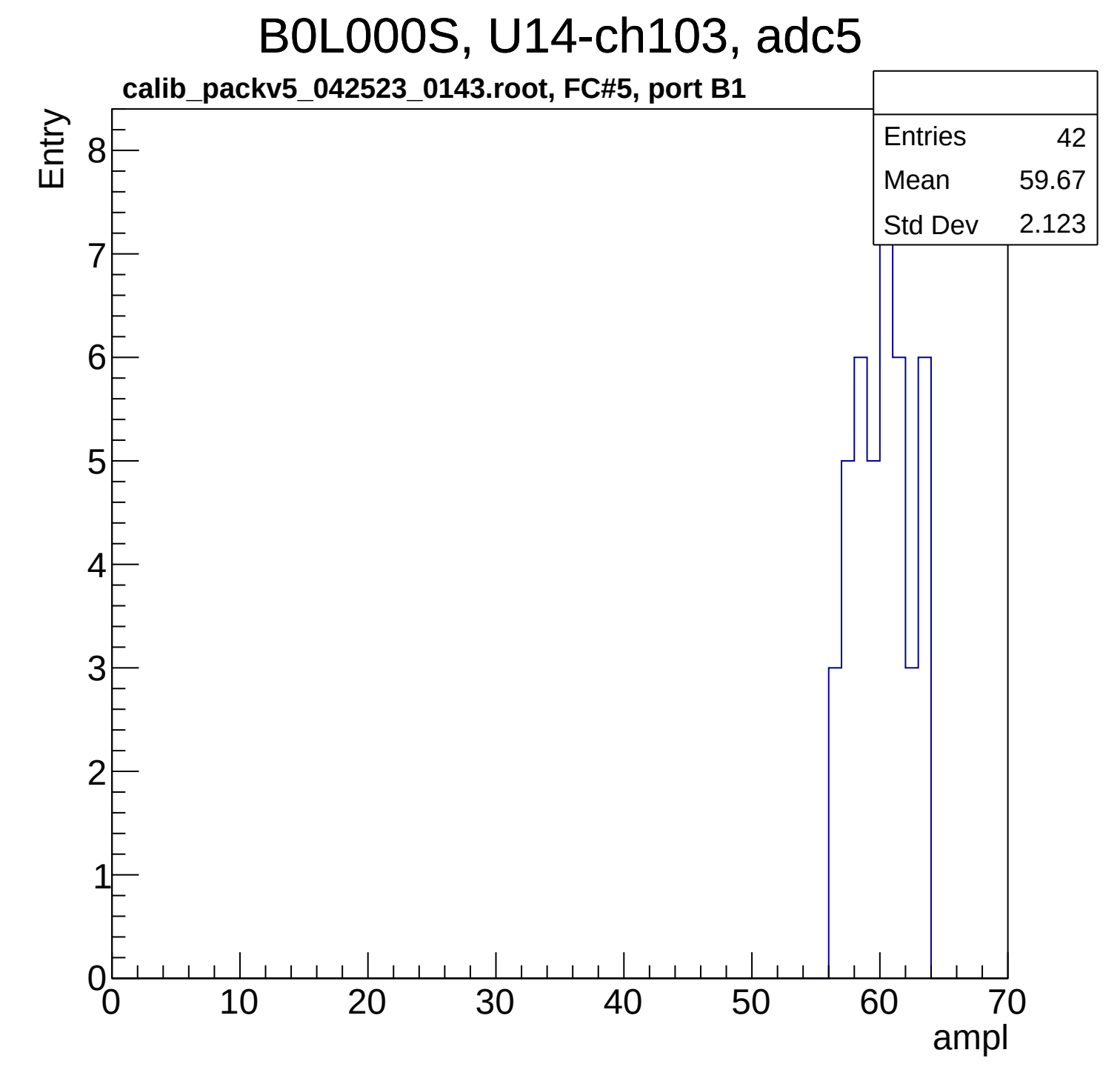
B0L000S, U14-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 59.67
Std Dev 2.123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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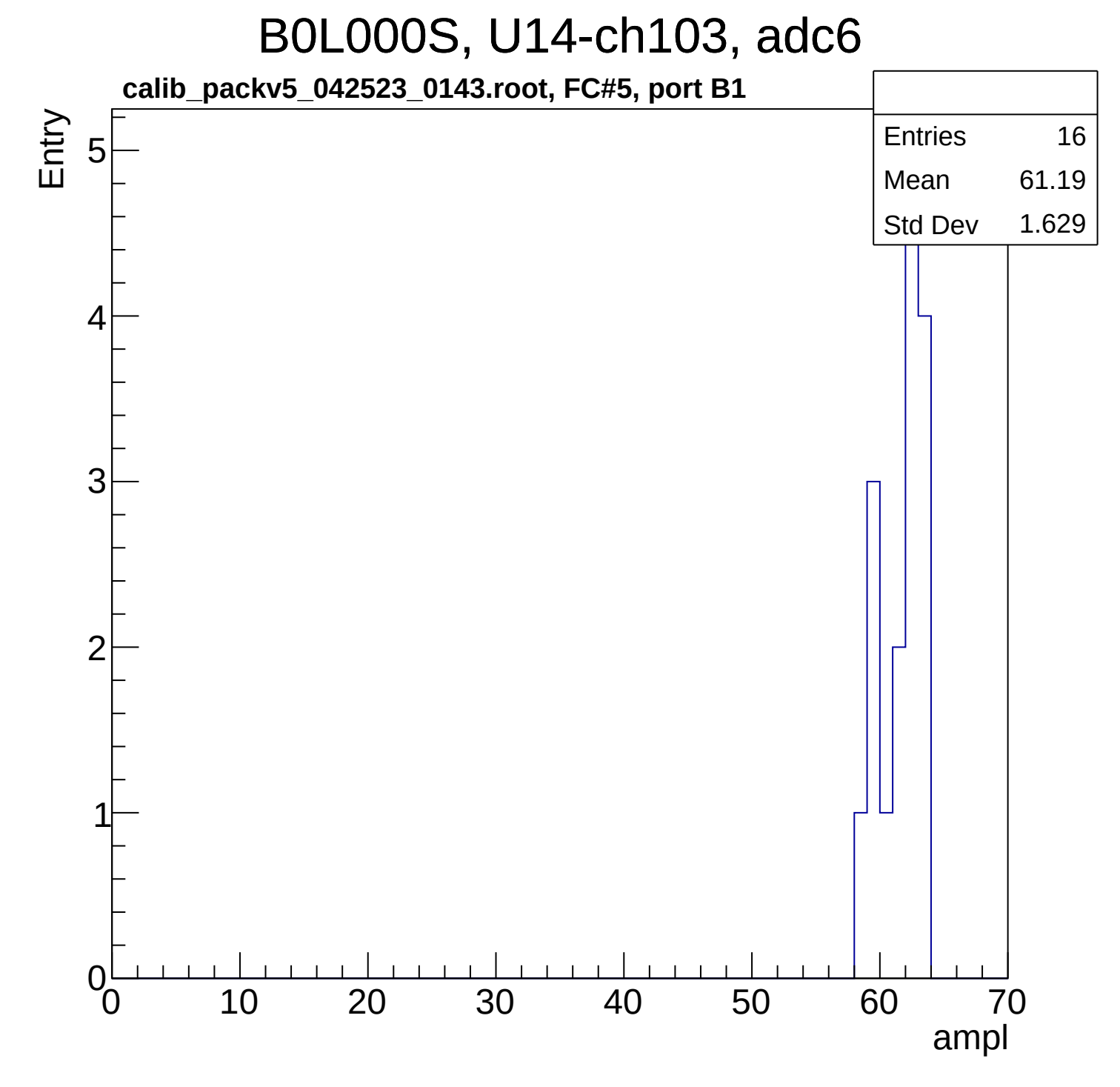
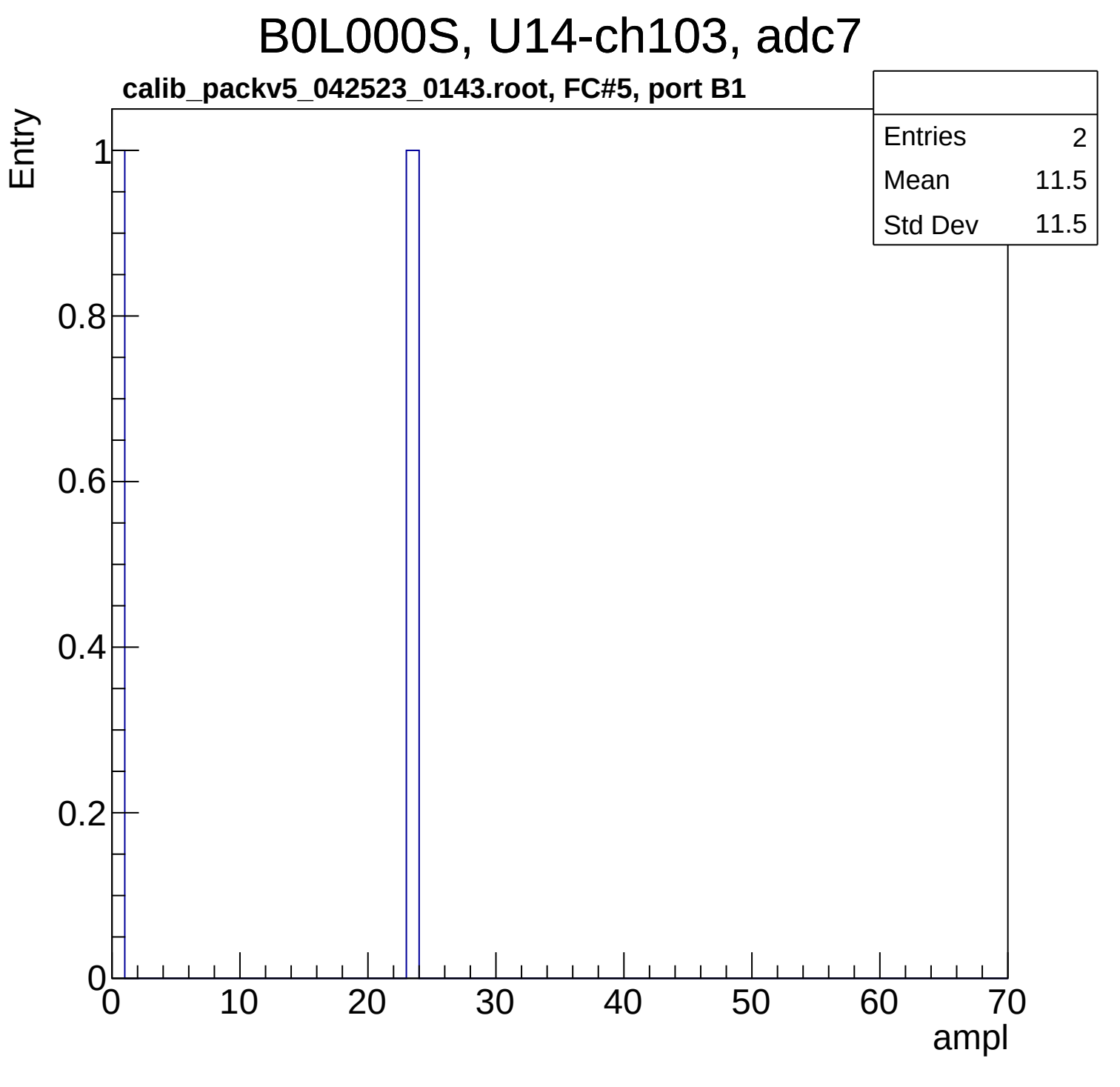

B0L000S, U14-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 16
Mean 61.19
Std Dev 1.629
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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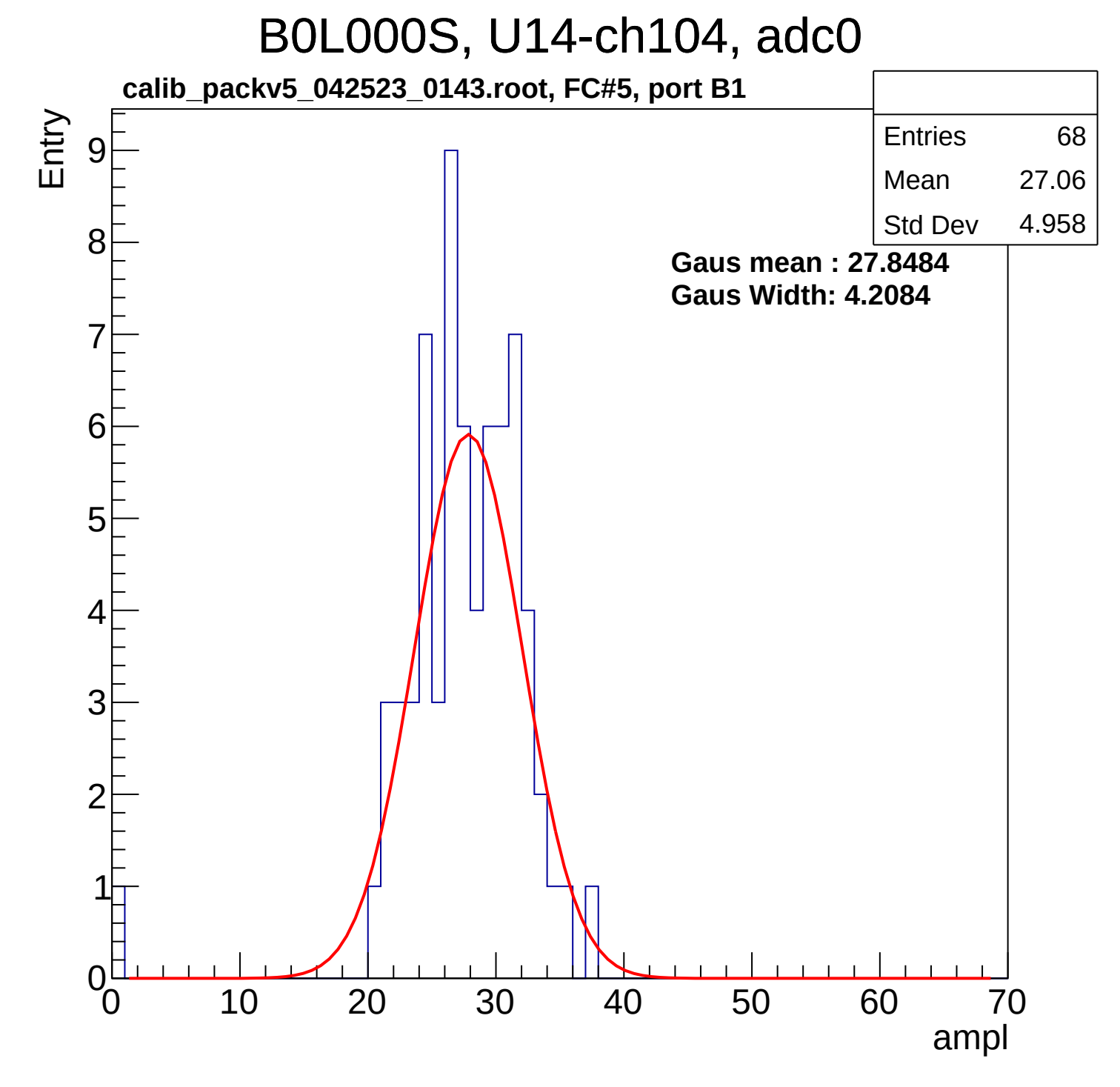
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 27.06
Std Dev 4.958
Gaus mean : 27.8484
Gaus Width: 4.2084
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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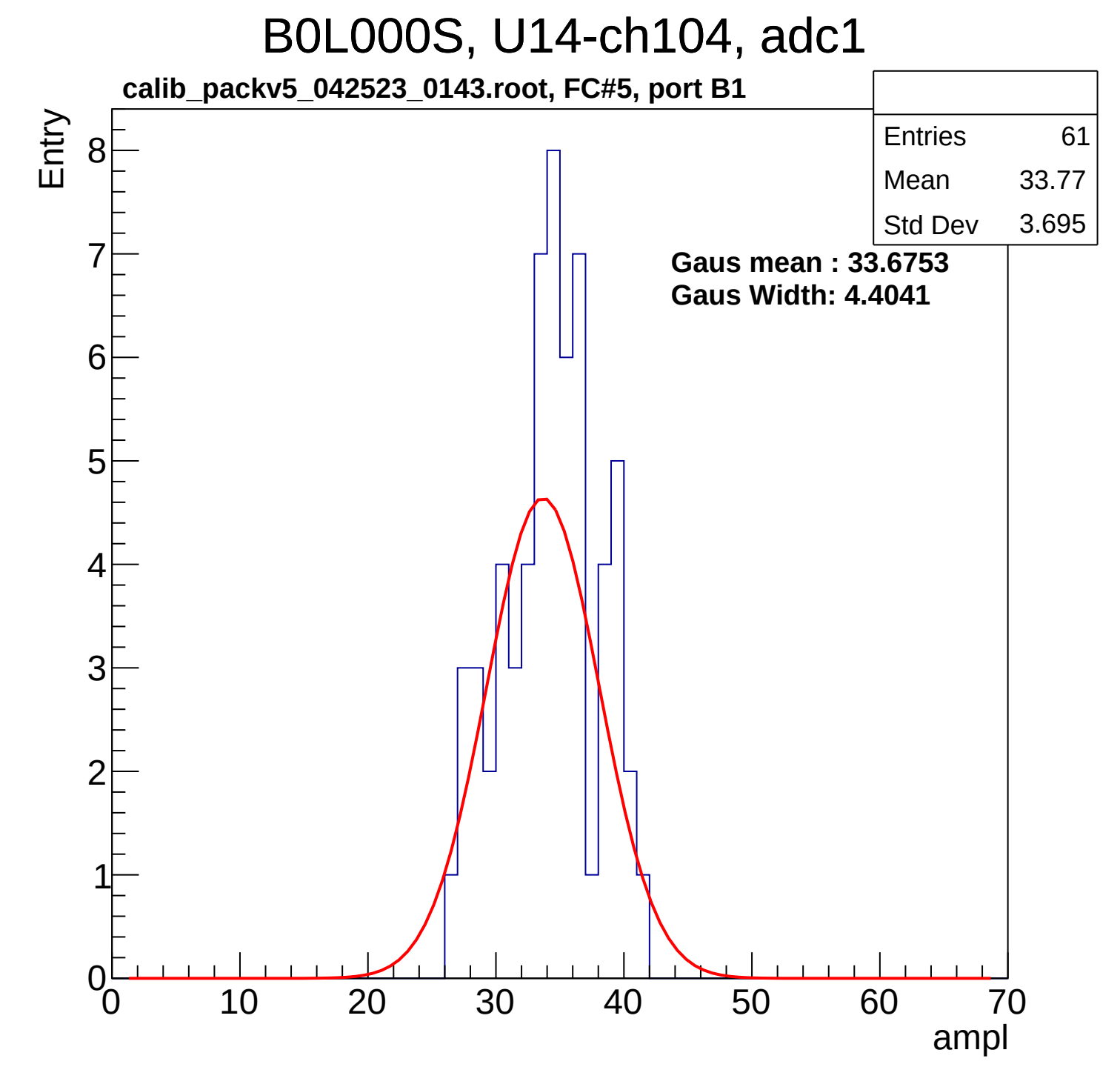
B0L000S, U14-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 33.77
Std Dev 3.695
Gaus mean : 33.6753
Gaus Width: 4.4041
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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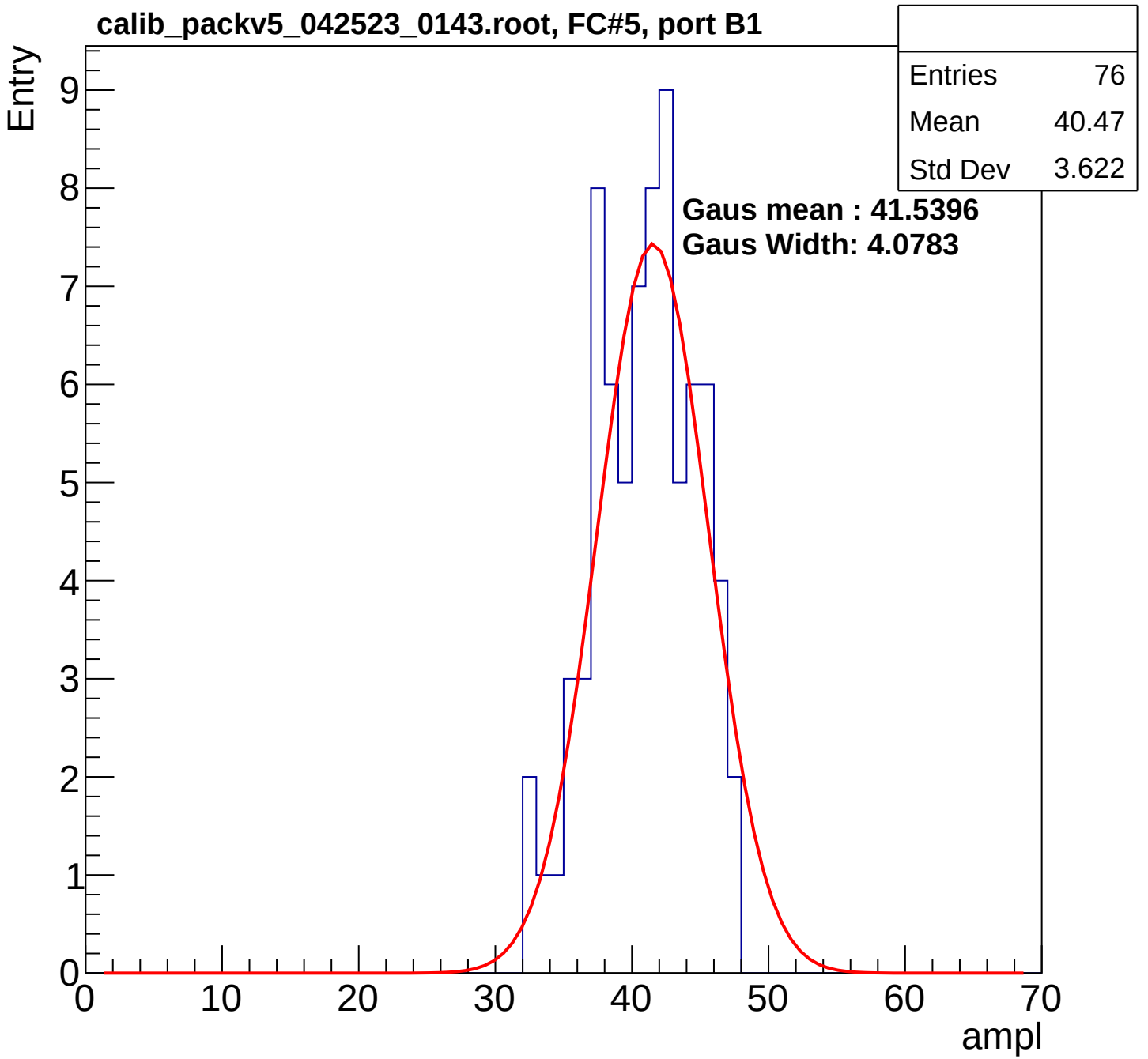
B0L000S, U14-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 40.47
Std Dev 3.622
Gaus mean : 41.5396
Gaus Width: 4.0783
Entry
ampl
0
1
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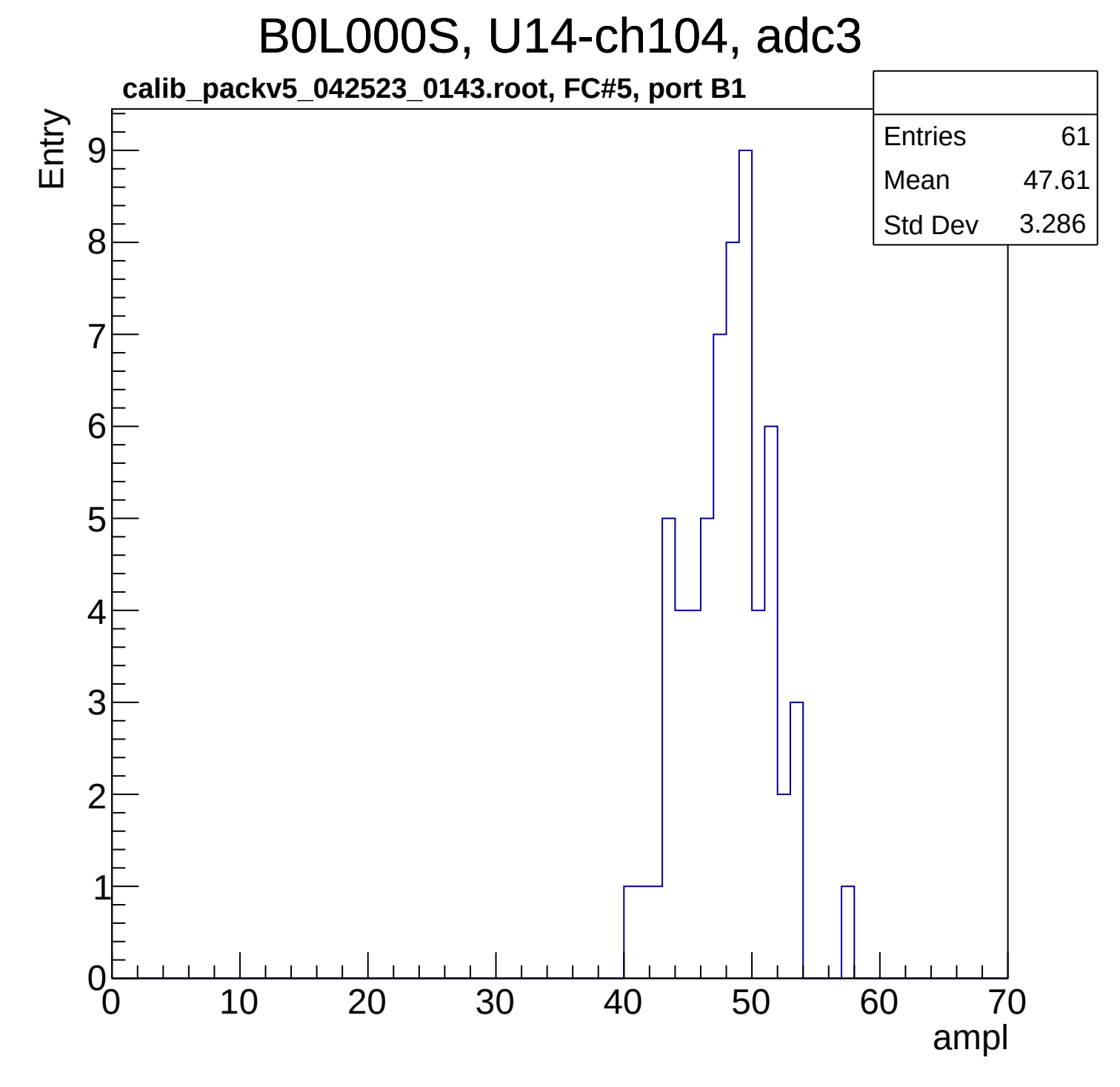
B0L000S, U14-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.61
Std Dev 3.286
Entry
ampl
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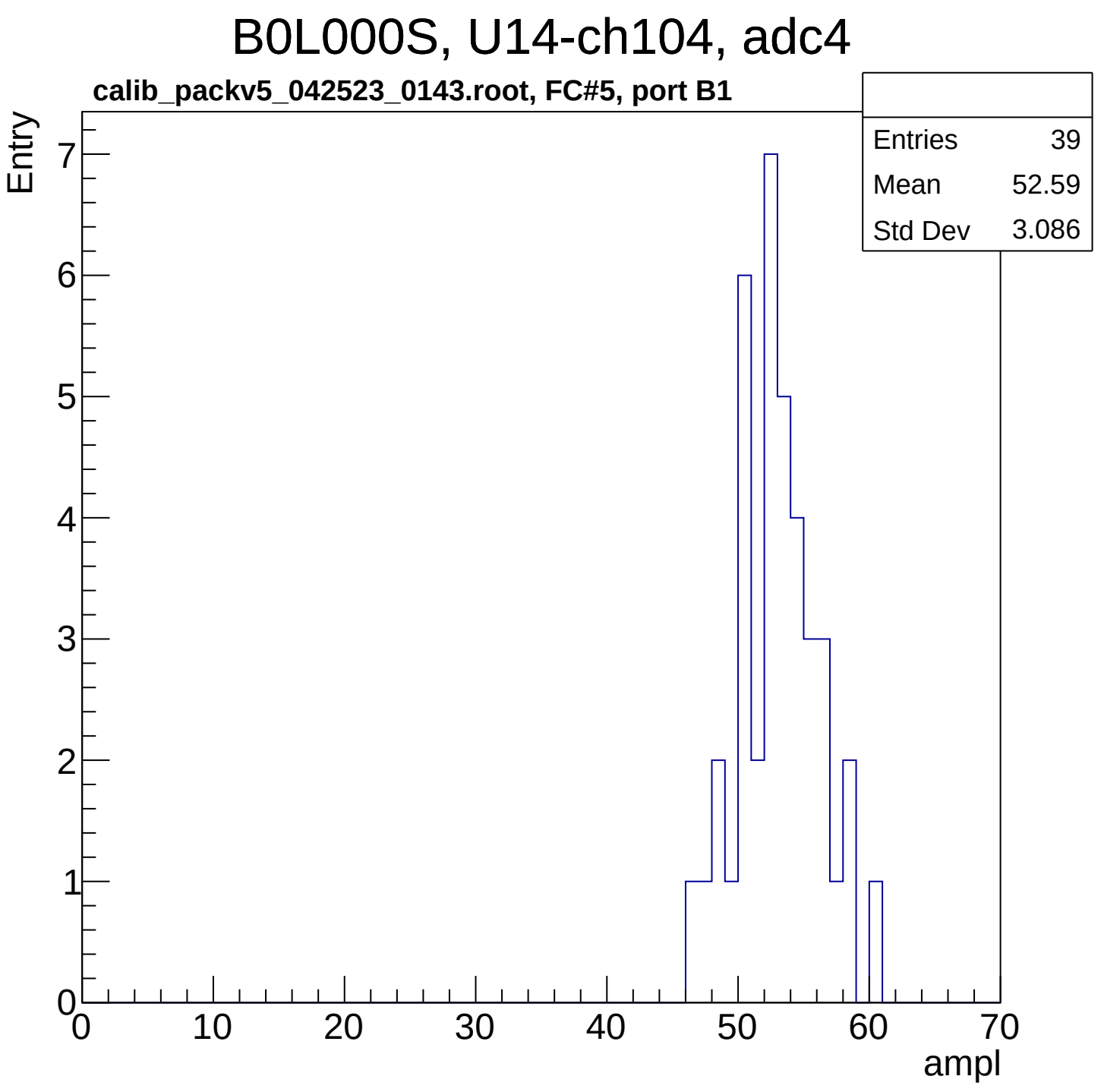
B0L000S, U14-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 52.59
Std Dev 3.086
Entry
ampl
0
1
2
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4
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0
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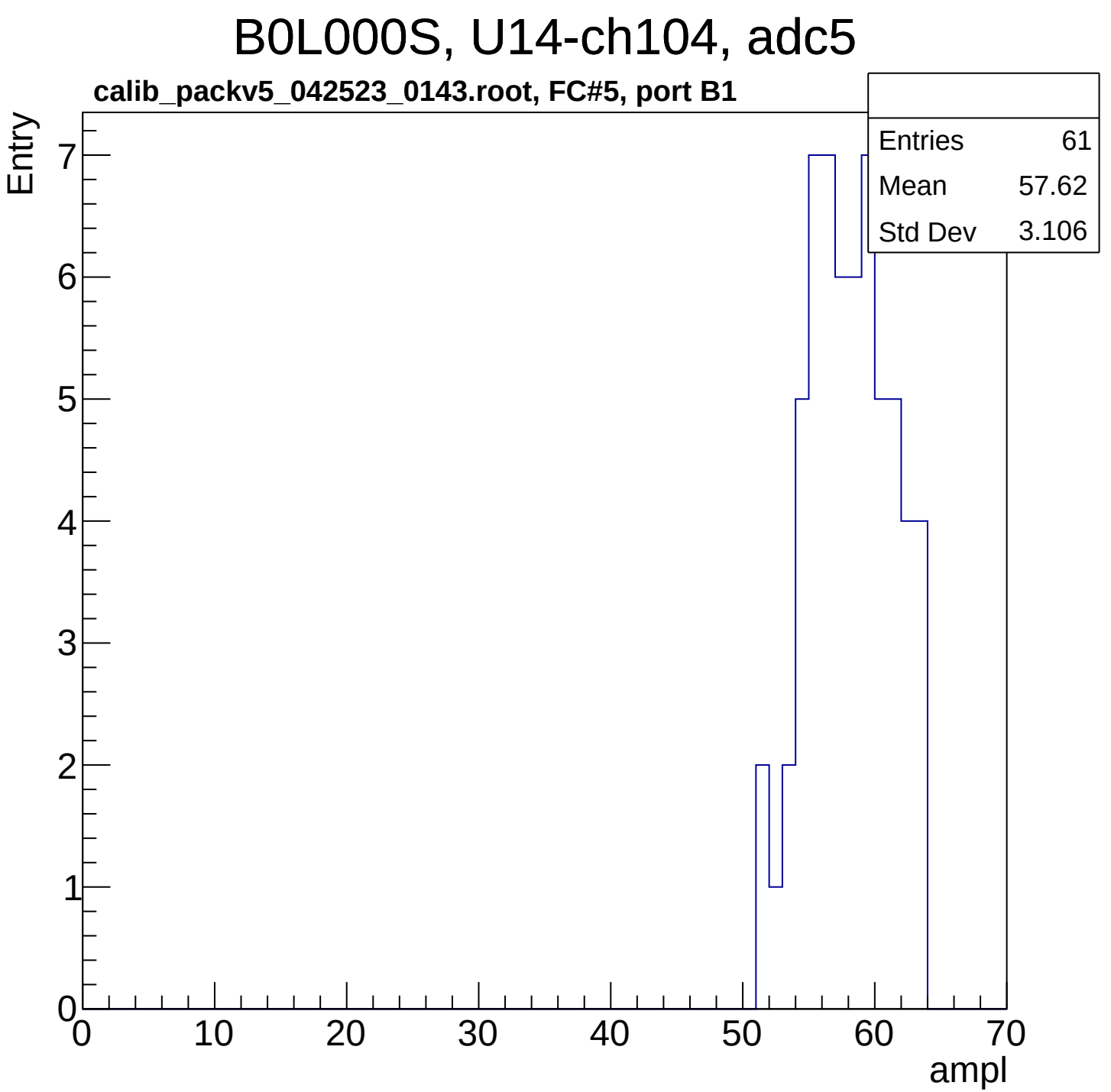
B0L000S, U14-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.62
Std Dev 3.106
Entry
ampl
0
1
2
3
4
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6
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0
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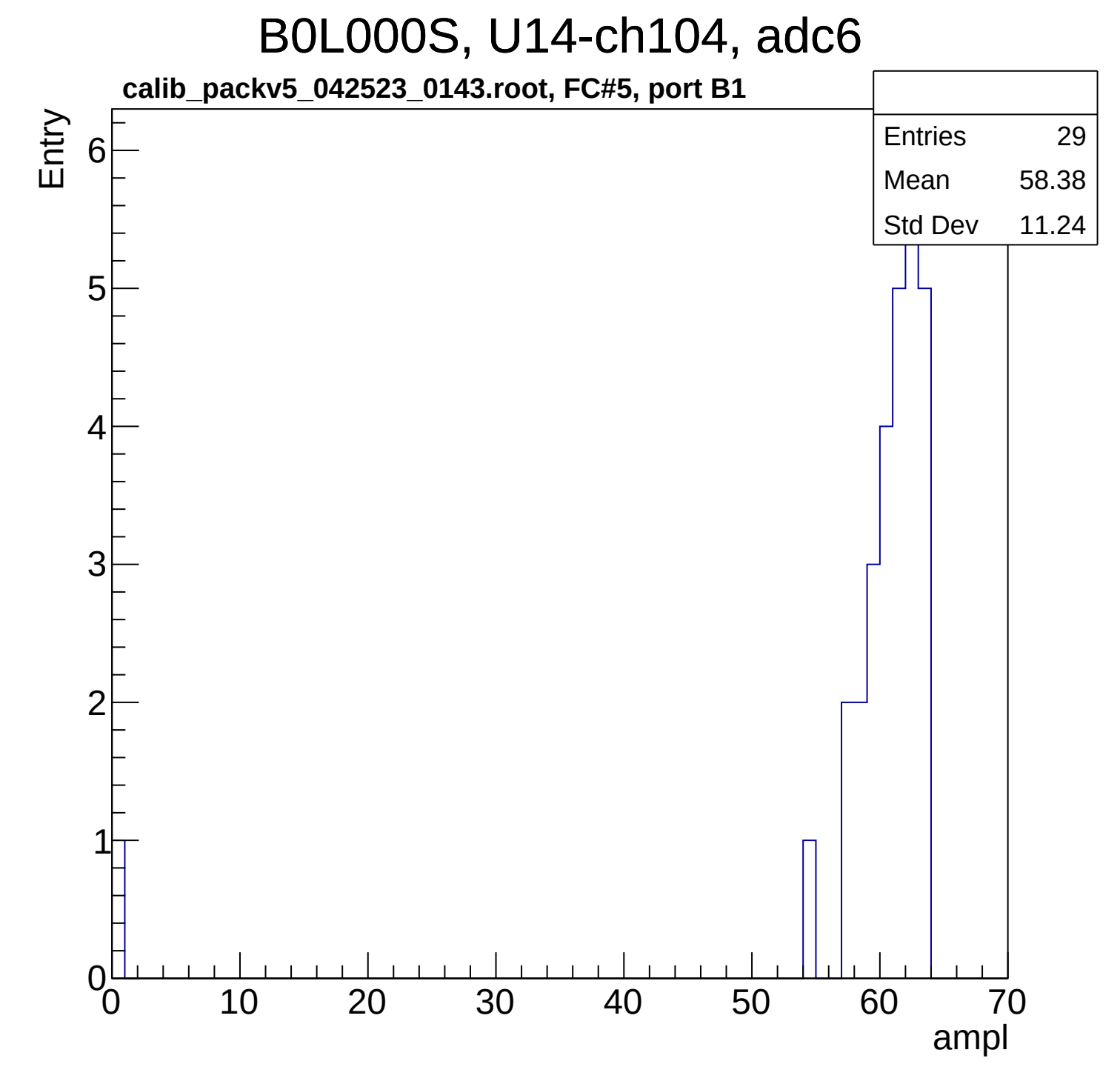
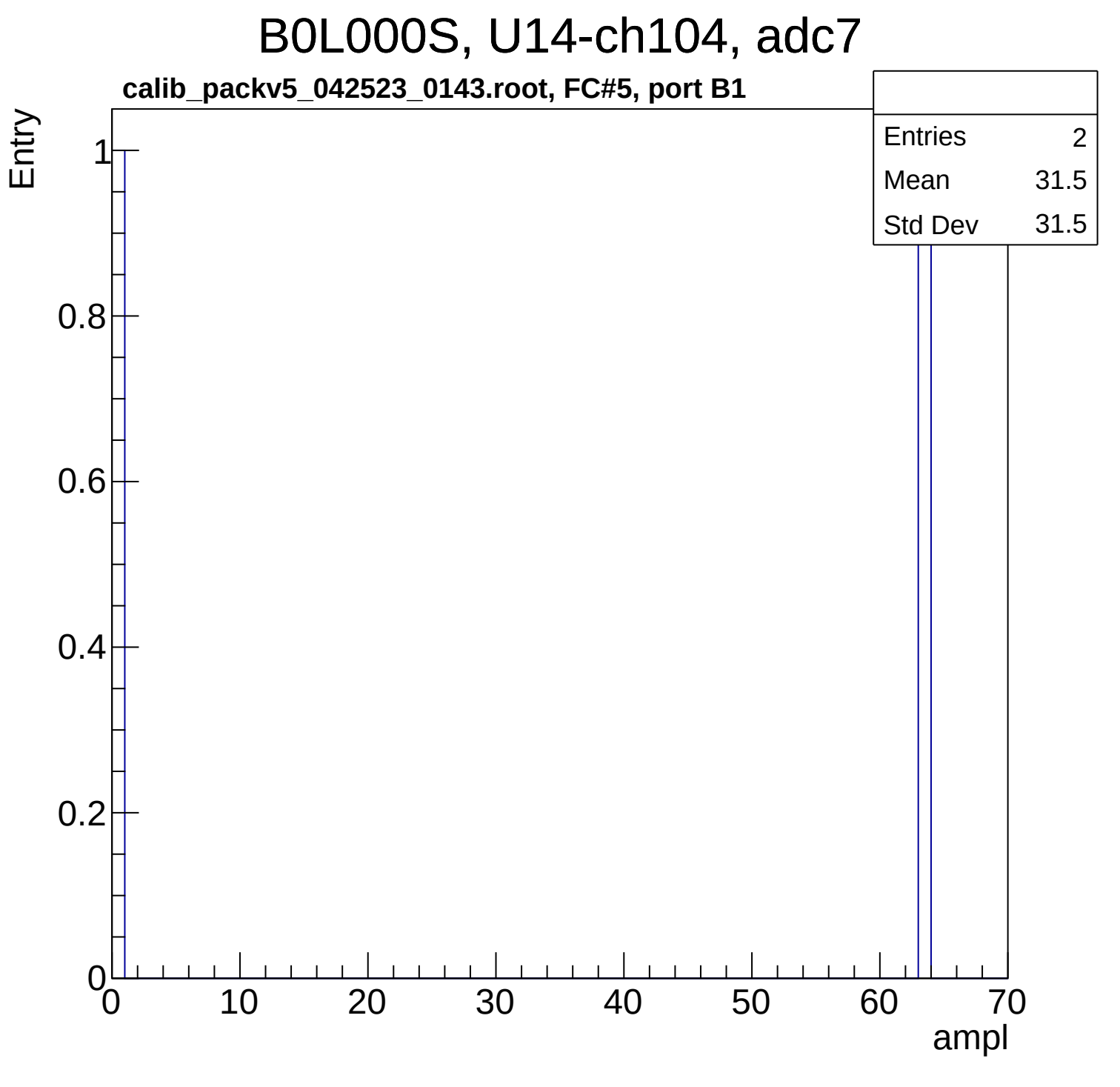

B0L000S, U14-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58.38
Std Dev 11.24
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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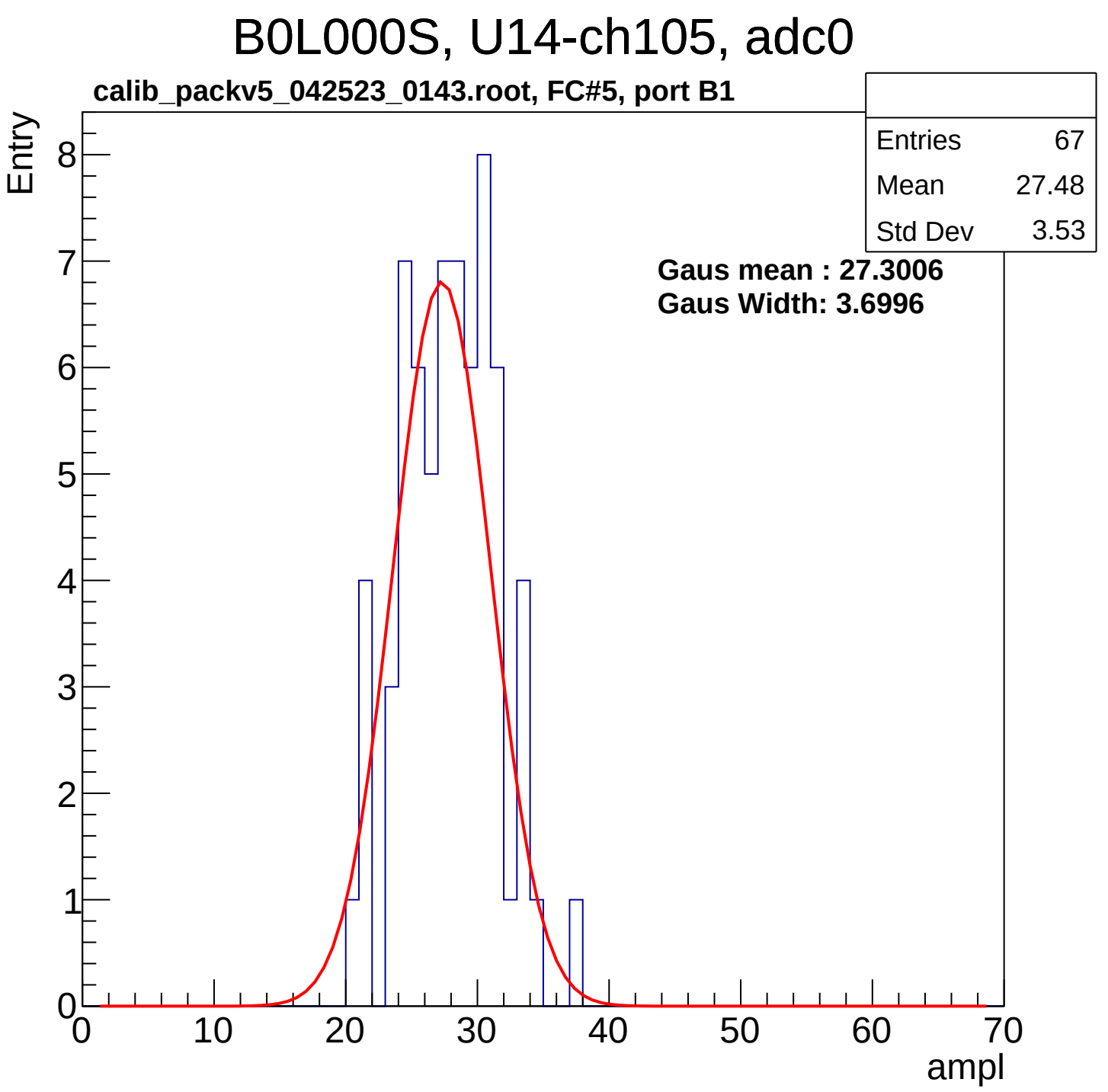
50
60
70
ampl


B0L000S, U14-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 27.48
Std Dev 3.53
Gaus mean : 27.3006
Gaus Width: 3.6996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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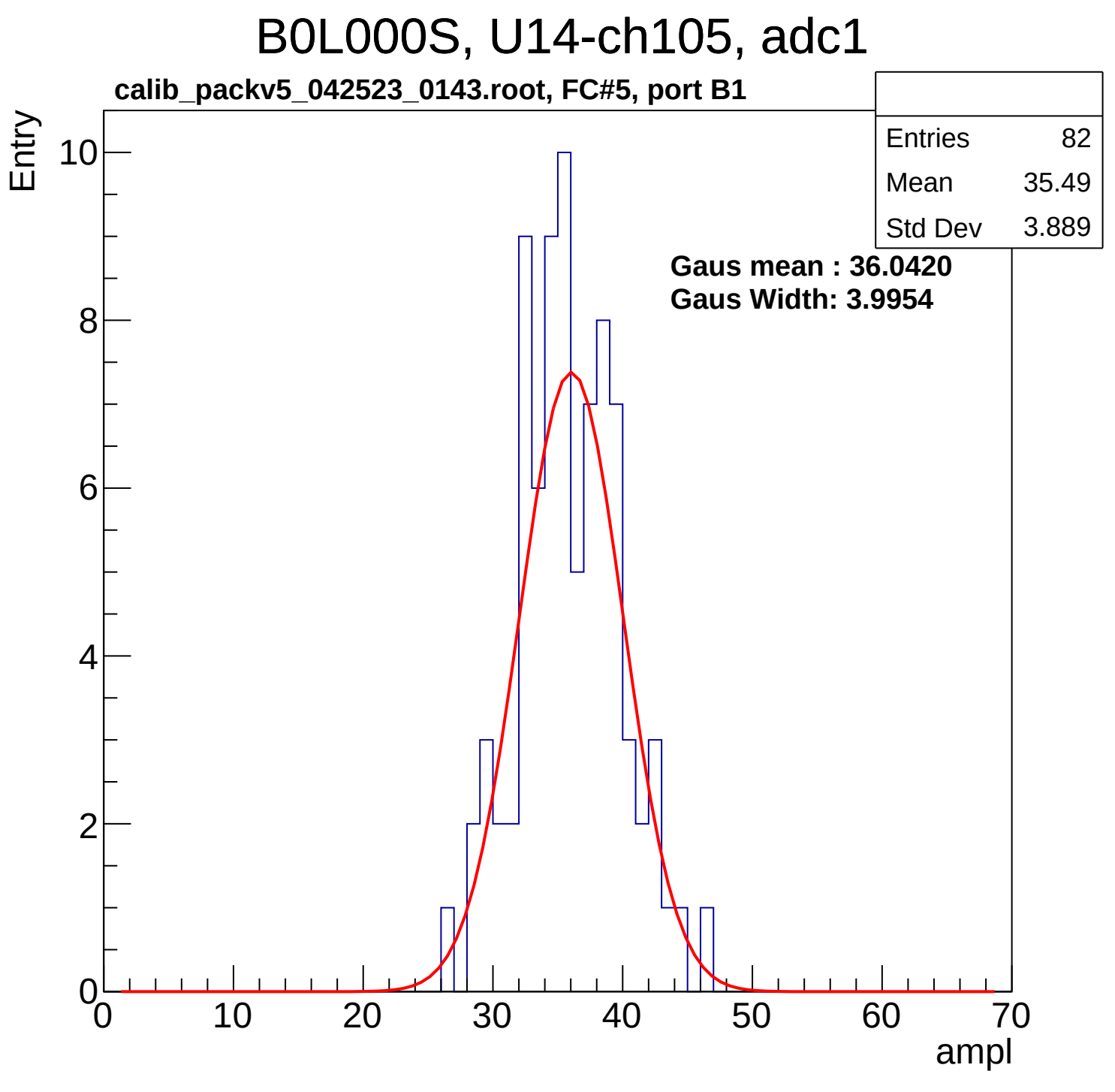
B0L000S, U14-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 35.49
Std Dev 3.889
Gaus mean : 36.0420
Gaus Width: 3.9954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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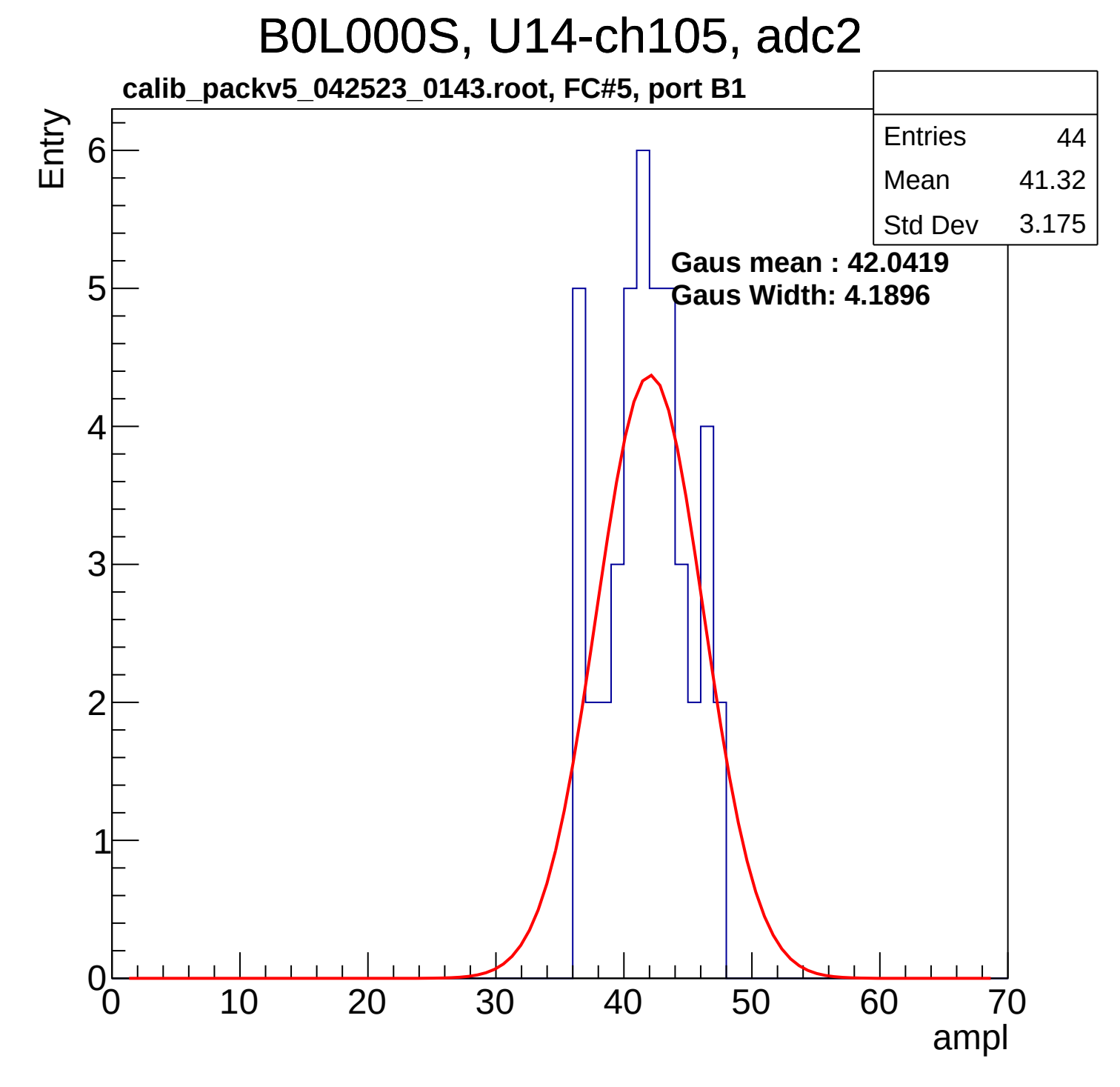
B0L000S, U14-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 41.32
Std Dev 3.175
Gaus mean : 42.0419
Gaus Width: 4.1896
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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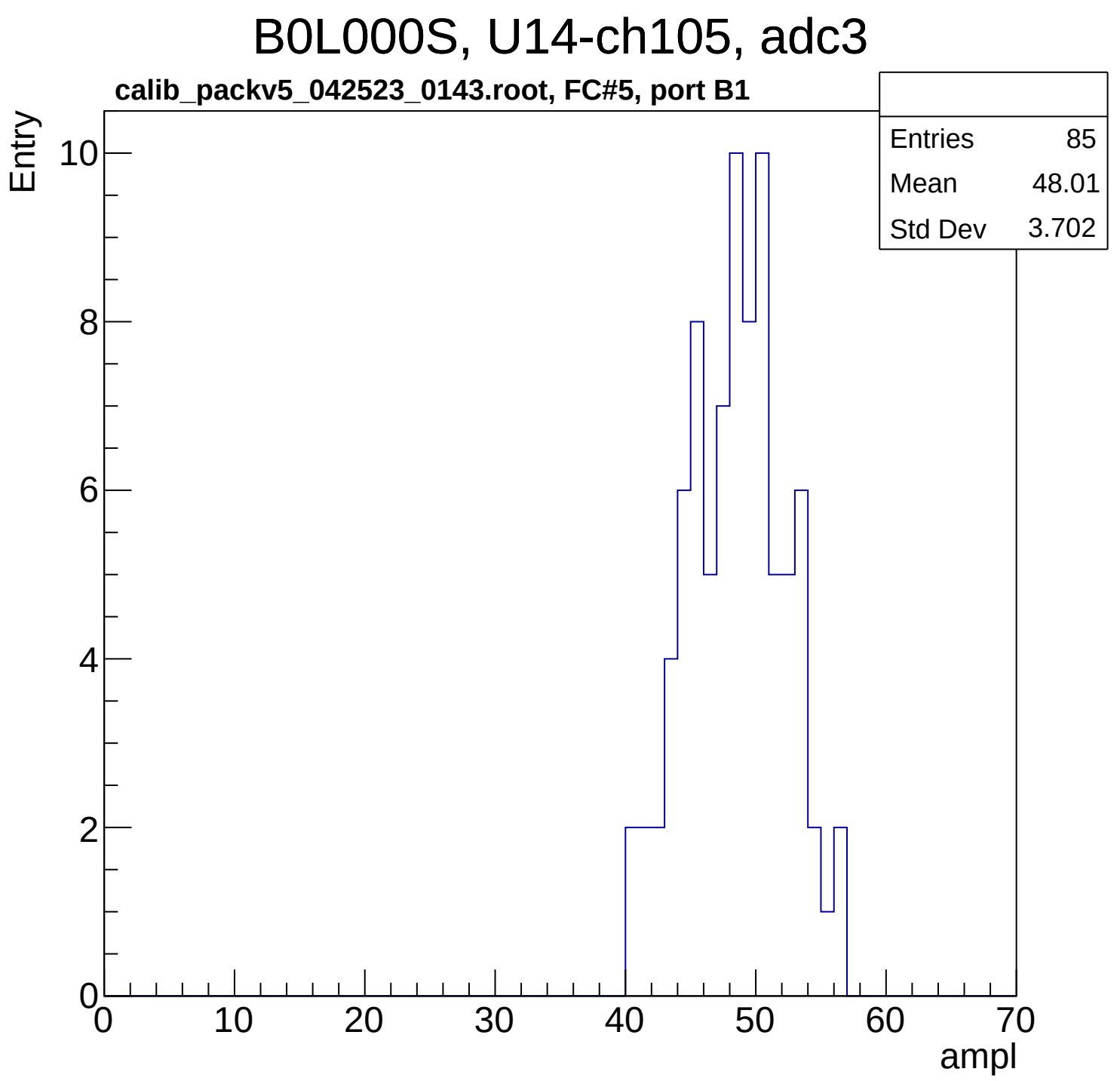
B0L000S, U14-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 48.01
Std Dev 3.702
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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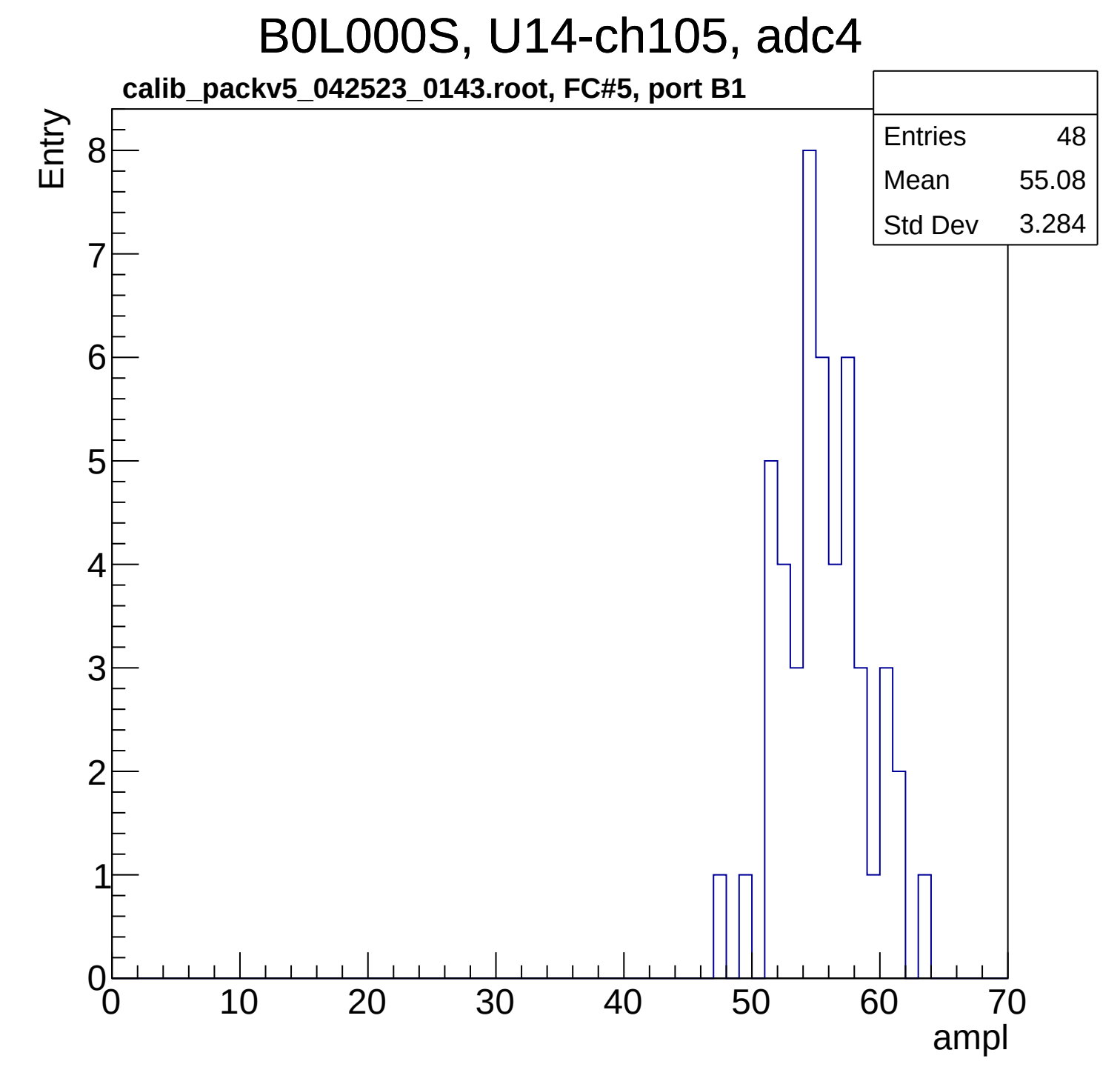
B0L000S, U14-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.08
Std Dev 3.284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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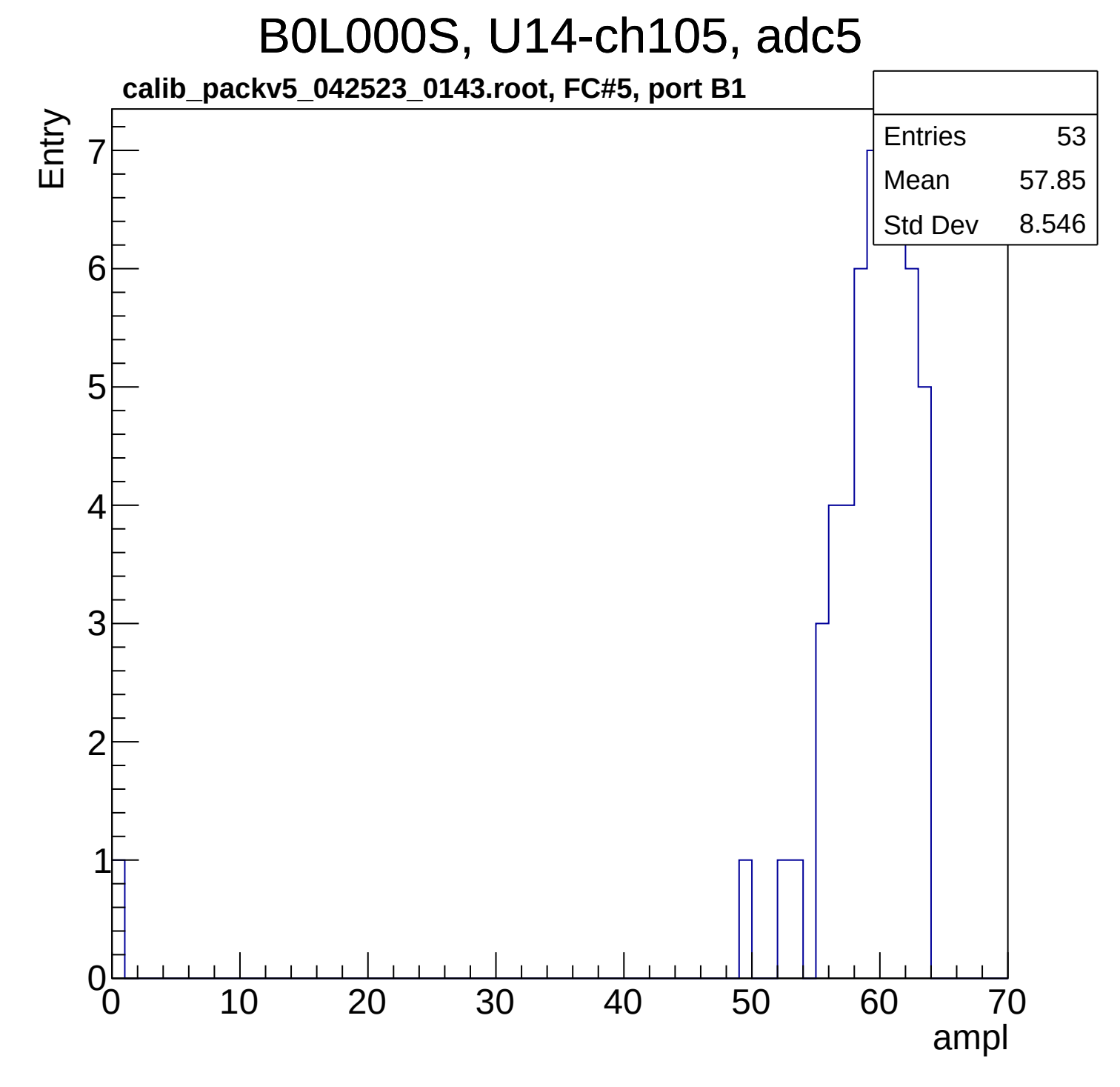
B0L000S, U14-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.85
Std Dev 8.546
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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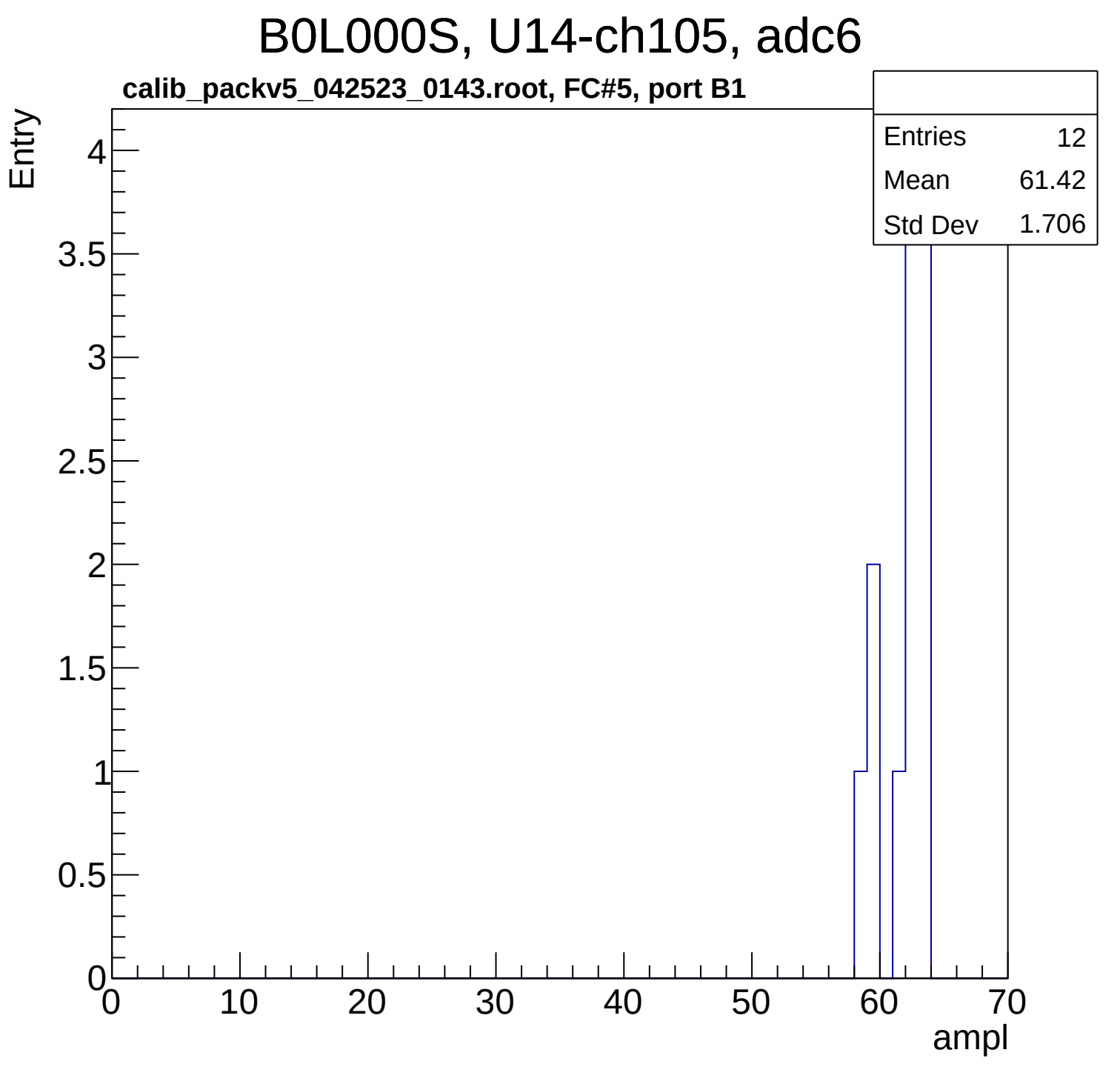
B0L000S, U14-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.42
Std Dev 1.706
Entry
ampl
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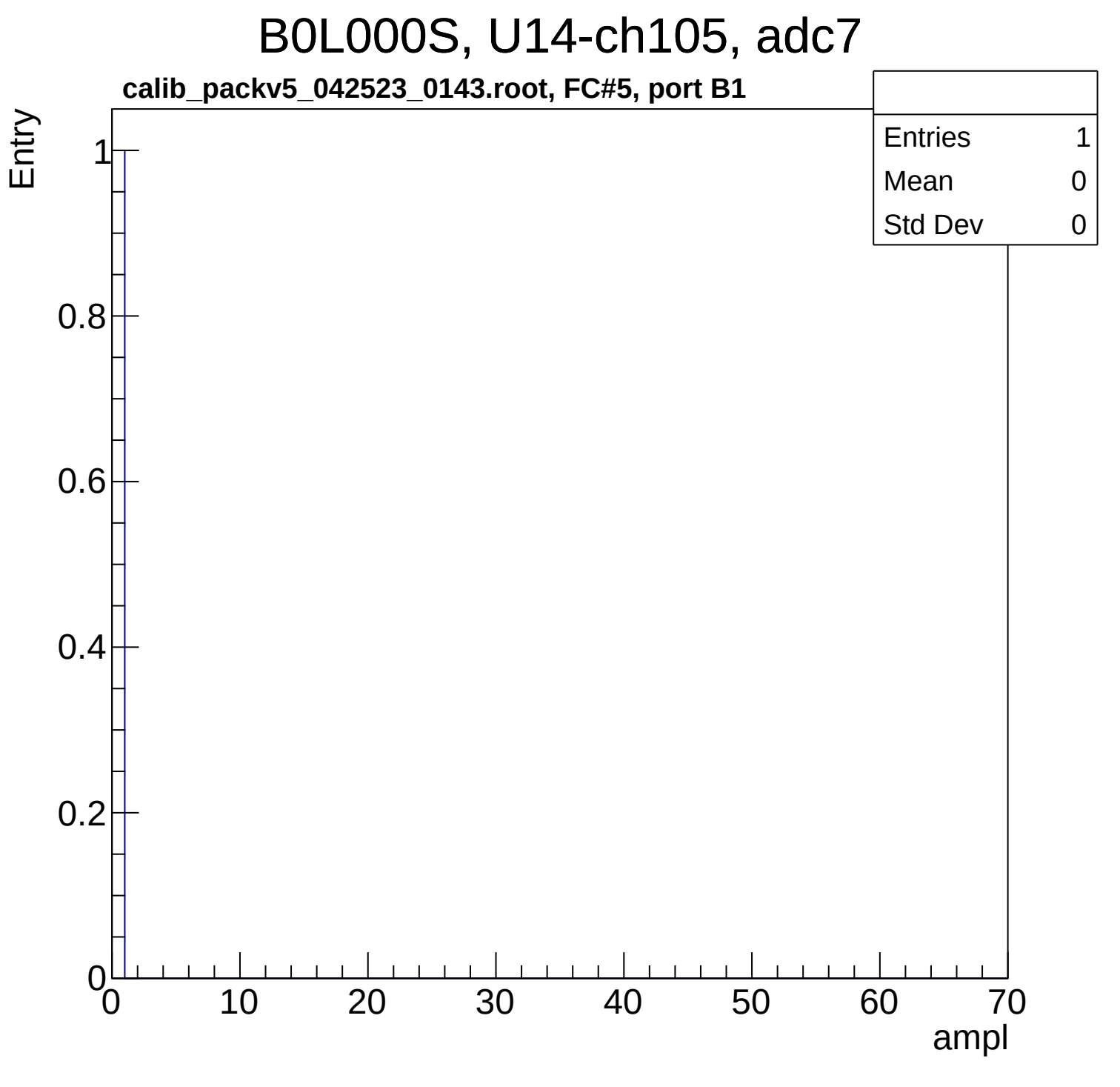
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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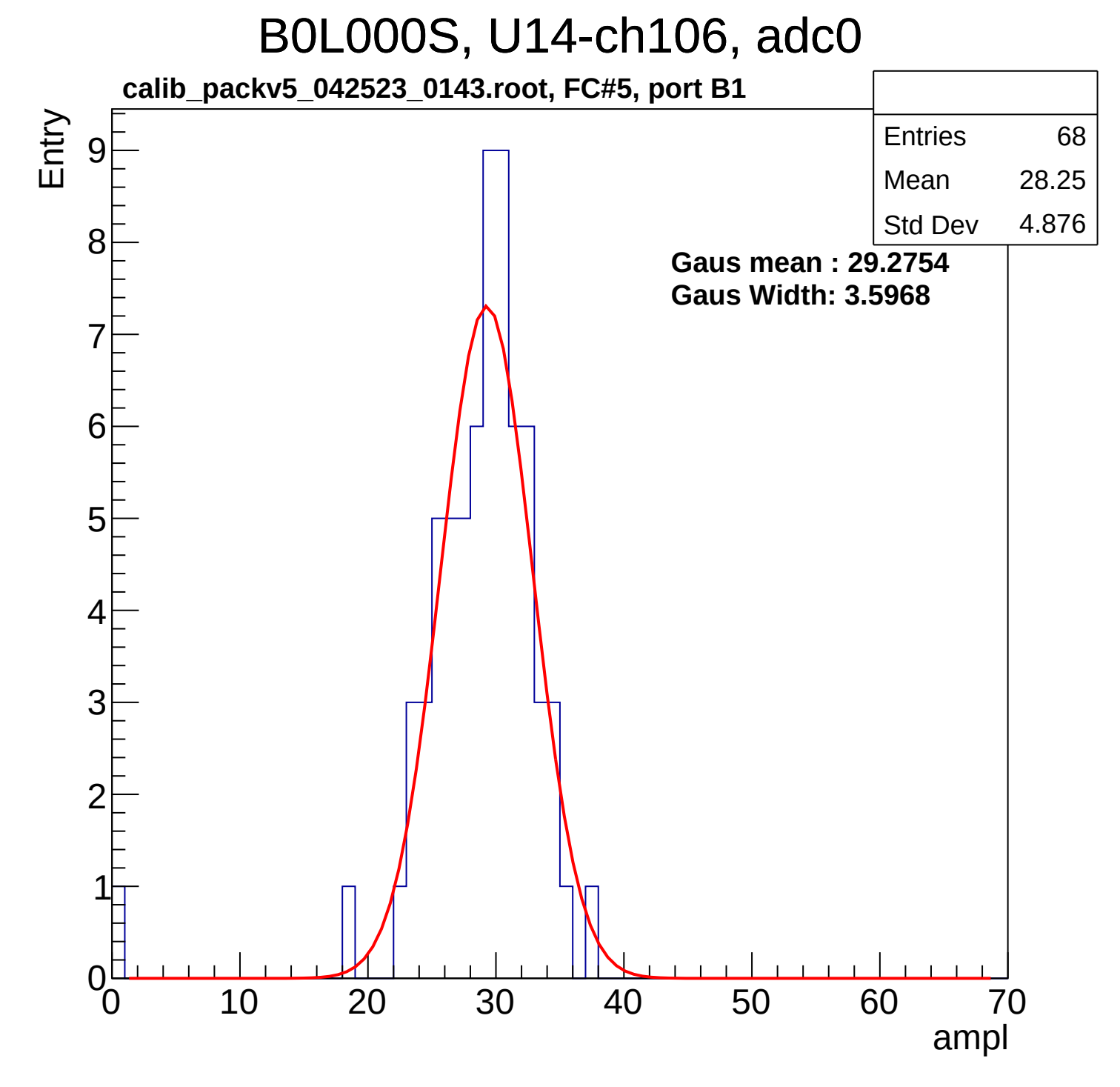
50
60
70
ampl


B0L000S, U14-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 28.25
Std Dev 4.876
Gaus mean : 29.2754
Gaus Width: 3.5968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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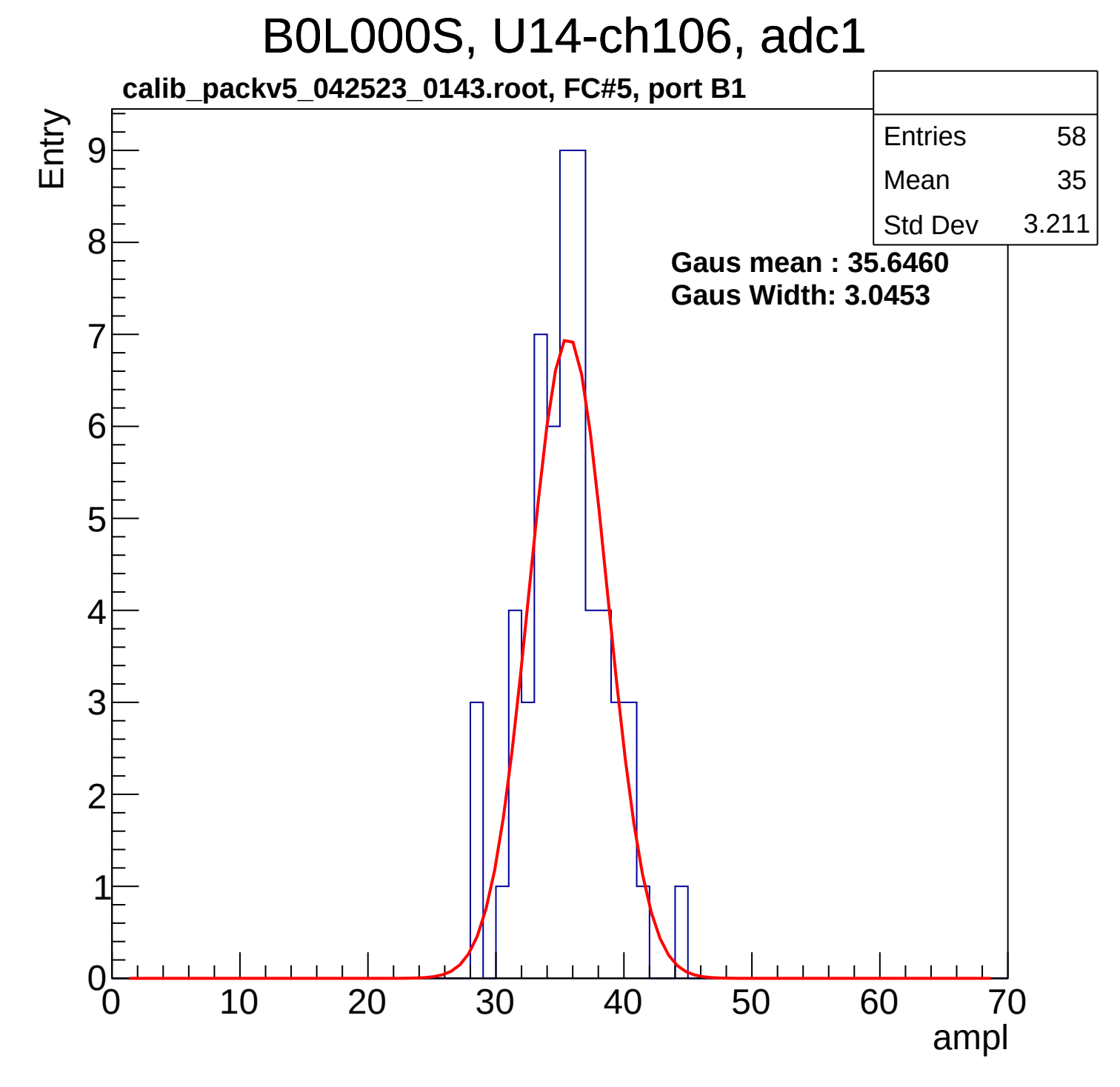
B0L000S, U14-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35
Std Dev 3.211
Gaus mean : 35.6460
Gaus Width: 3.0453
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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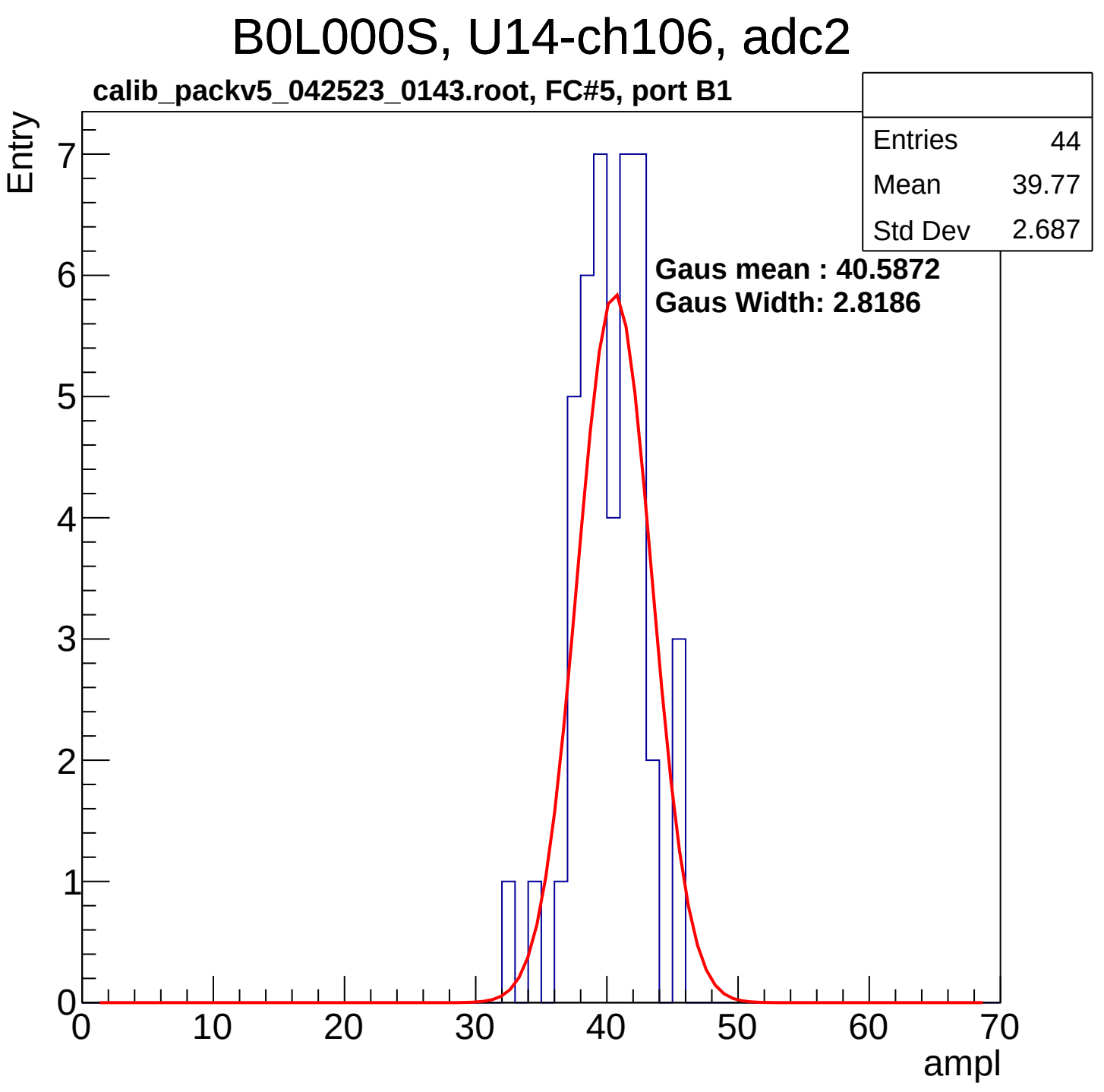
B0L000S, U14-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 39.77
Std Dev 2.687
Gaus mean : 40.5872
Gaus Width: 2.8186
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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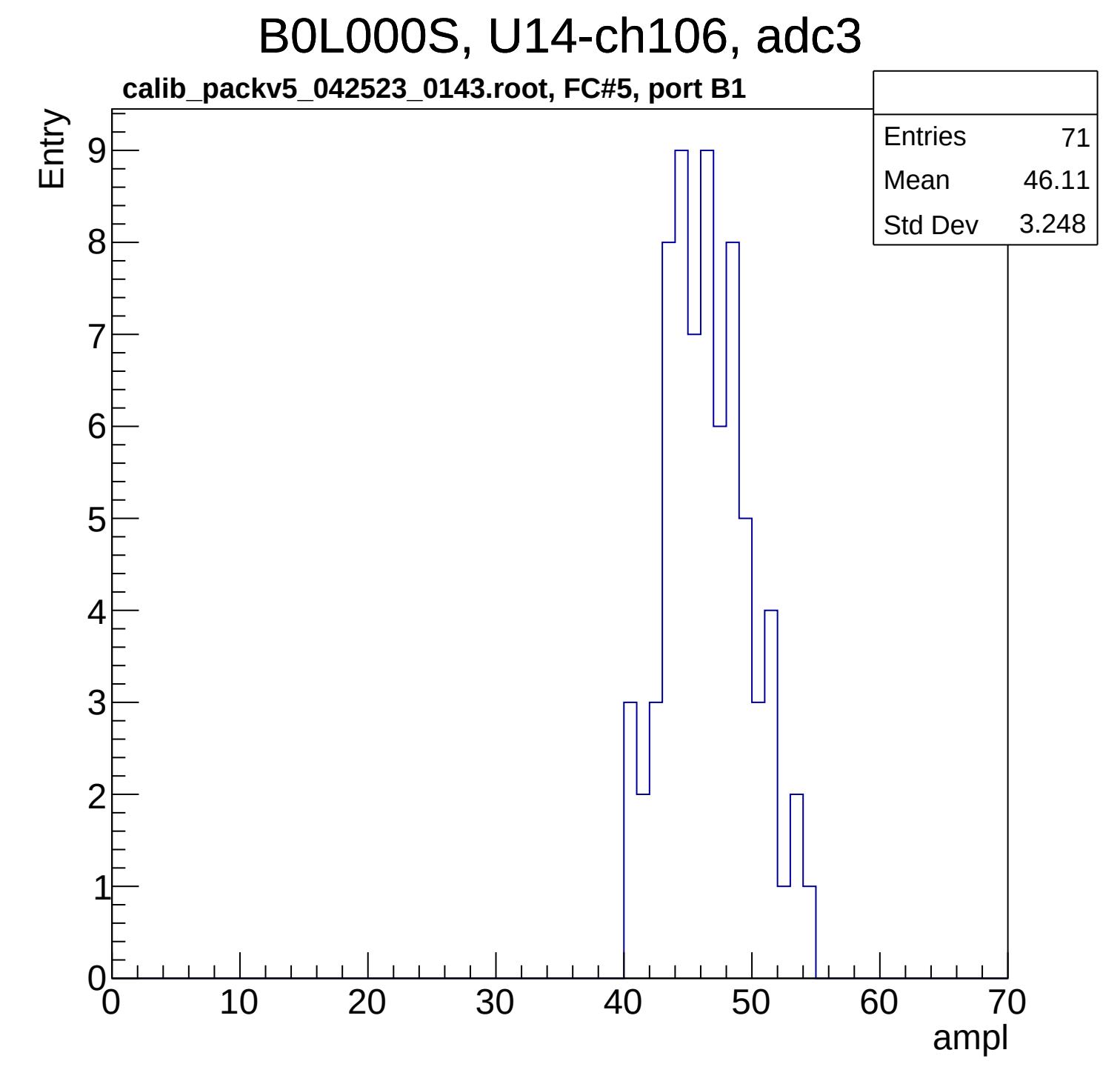
B0L000S, U14-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 46.11
Std Dev 3.248
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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60
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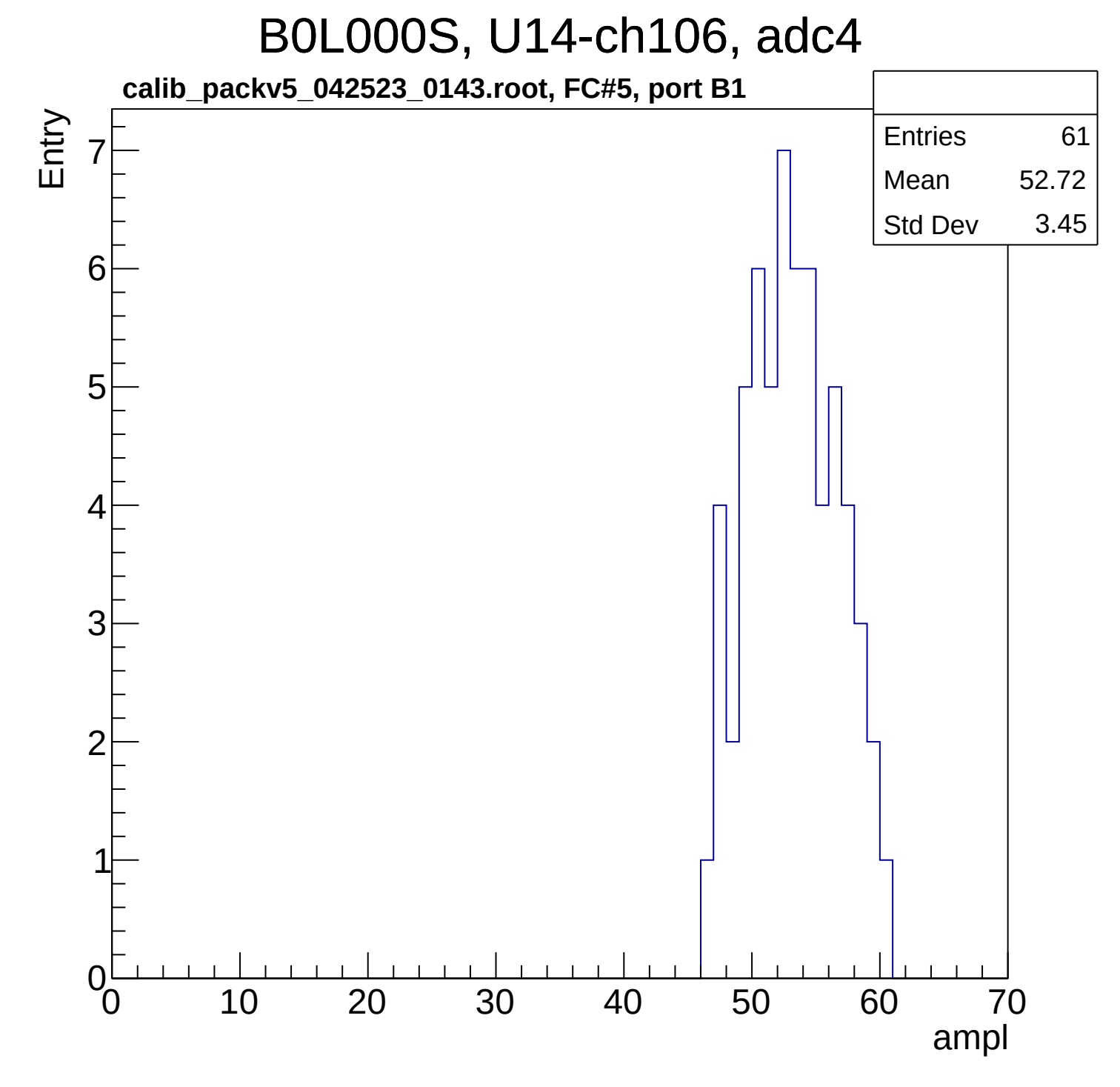
B0L000S, U14-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 52.72
Std Dev 3.45
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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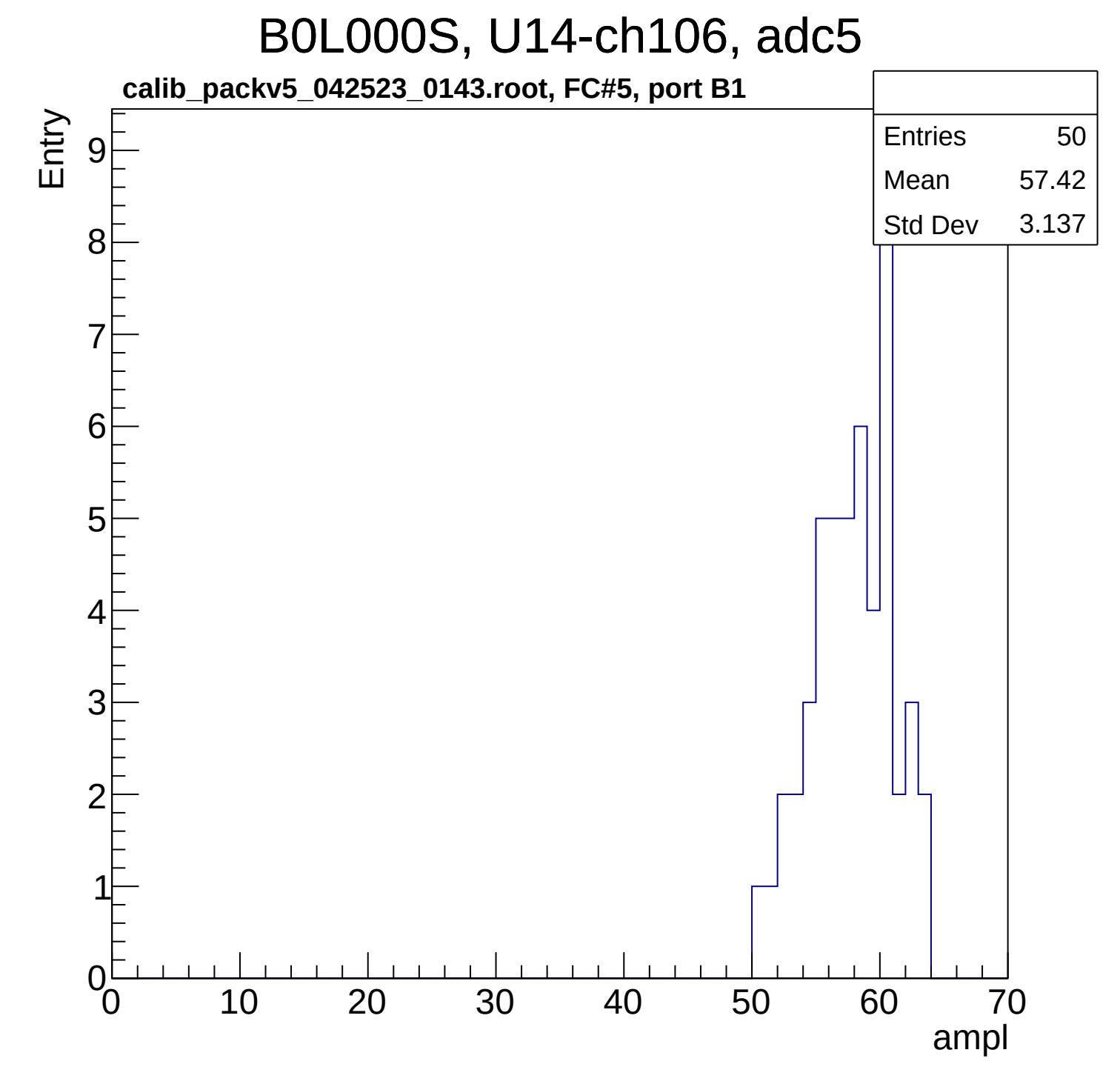
B0L000S, U14-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 57.42
Std Dev 3.137
Entry
ampl
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2
3
4
5
6
7
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0
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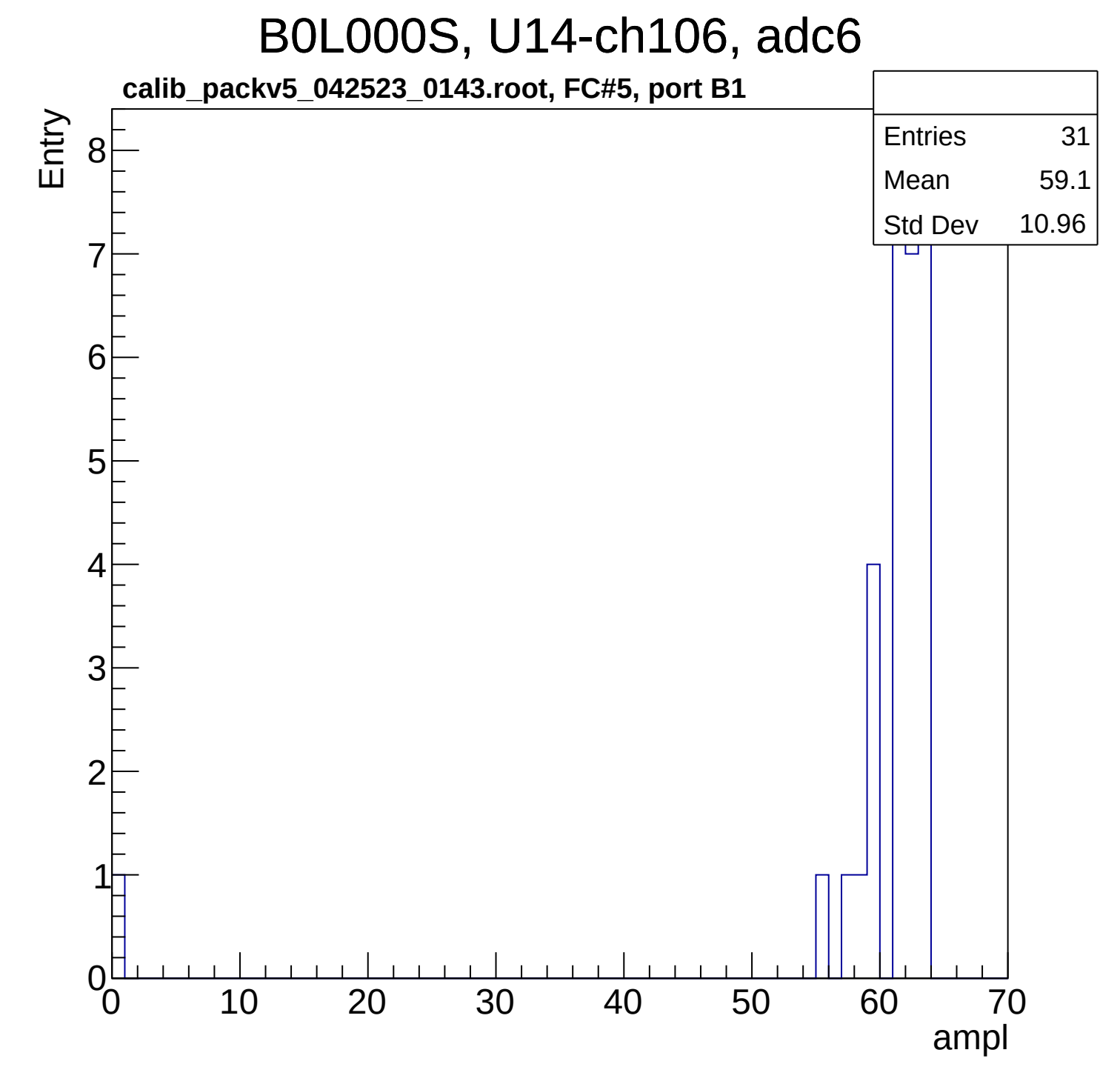
B0L000S, U14-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.1
Std Dev 10.96
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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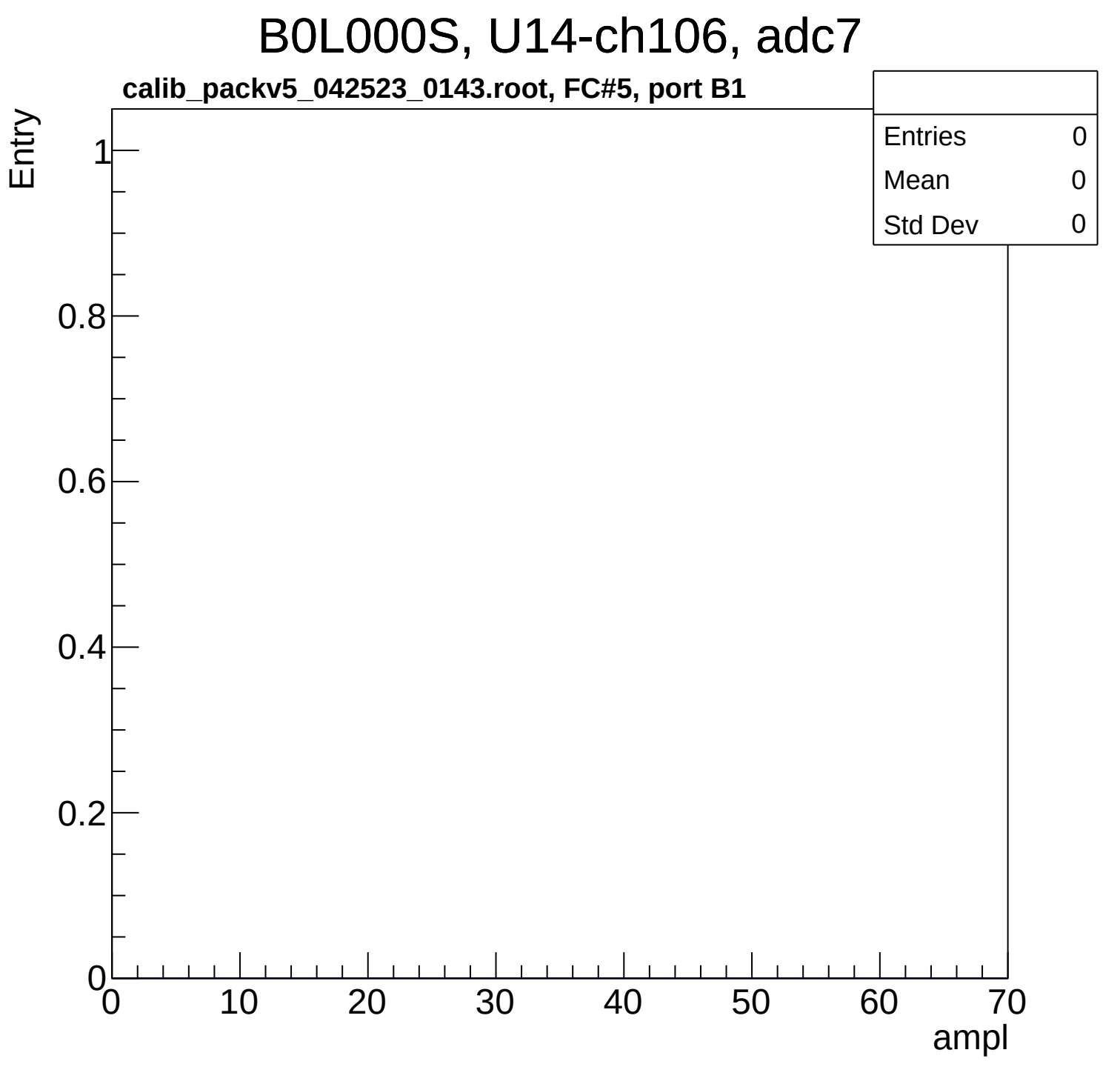
B0L000S, U14-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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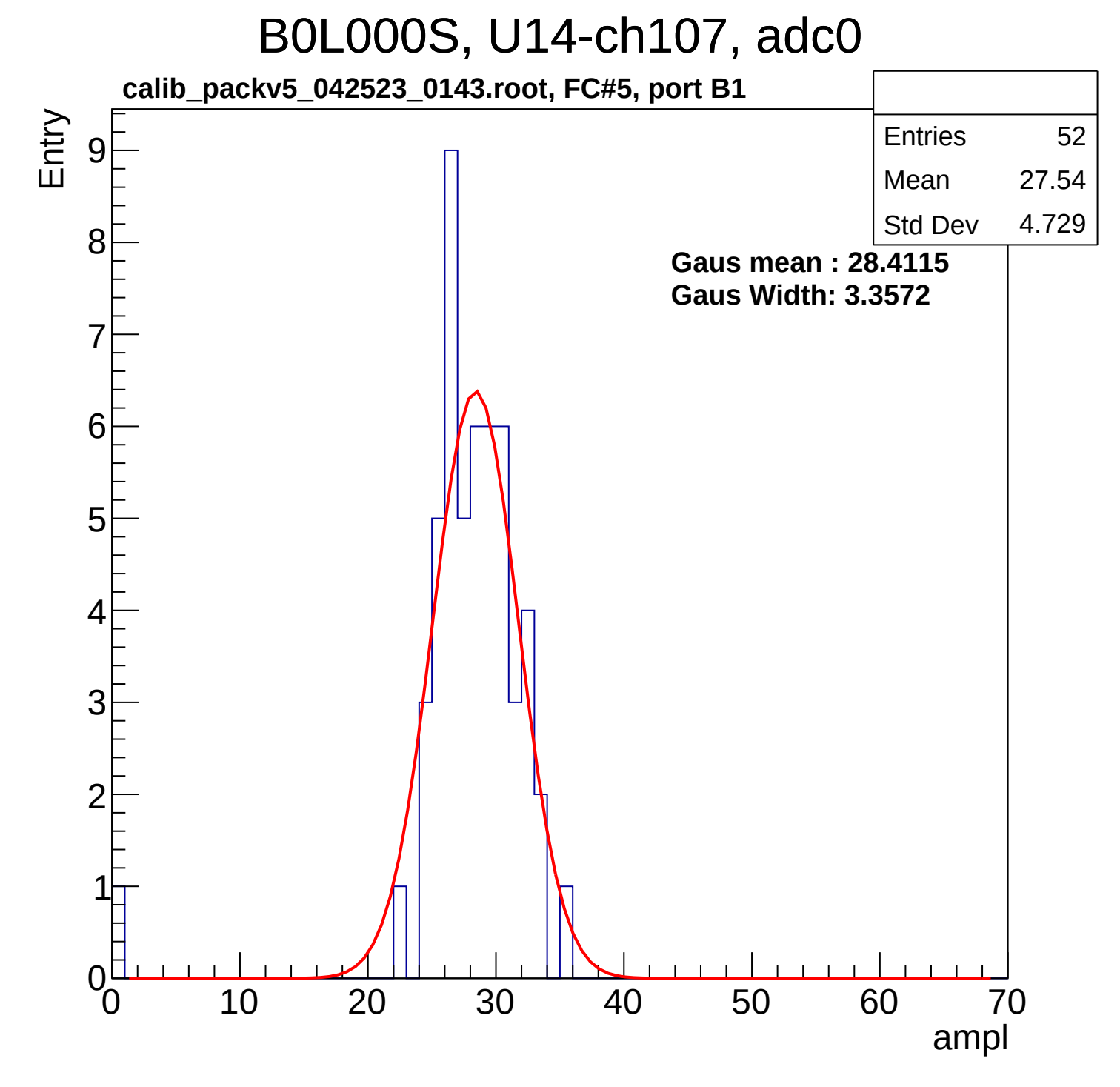
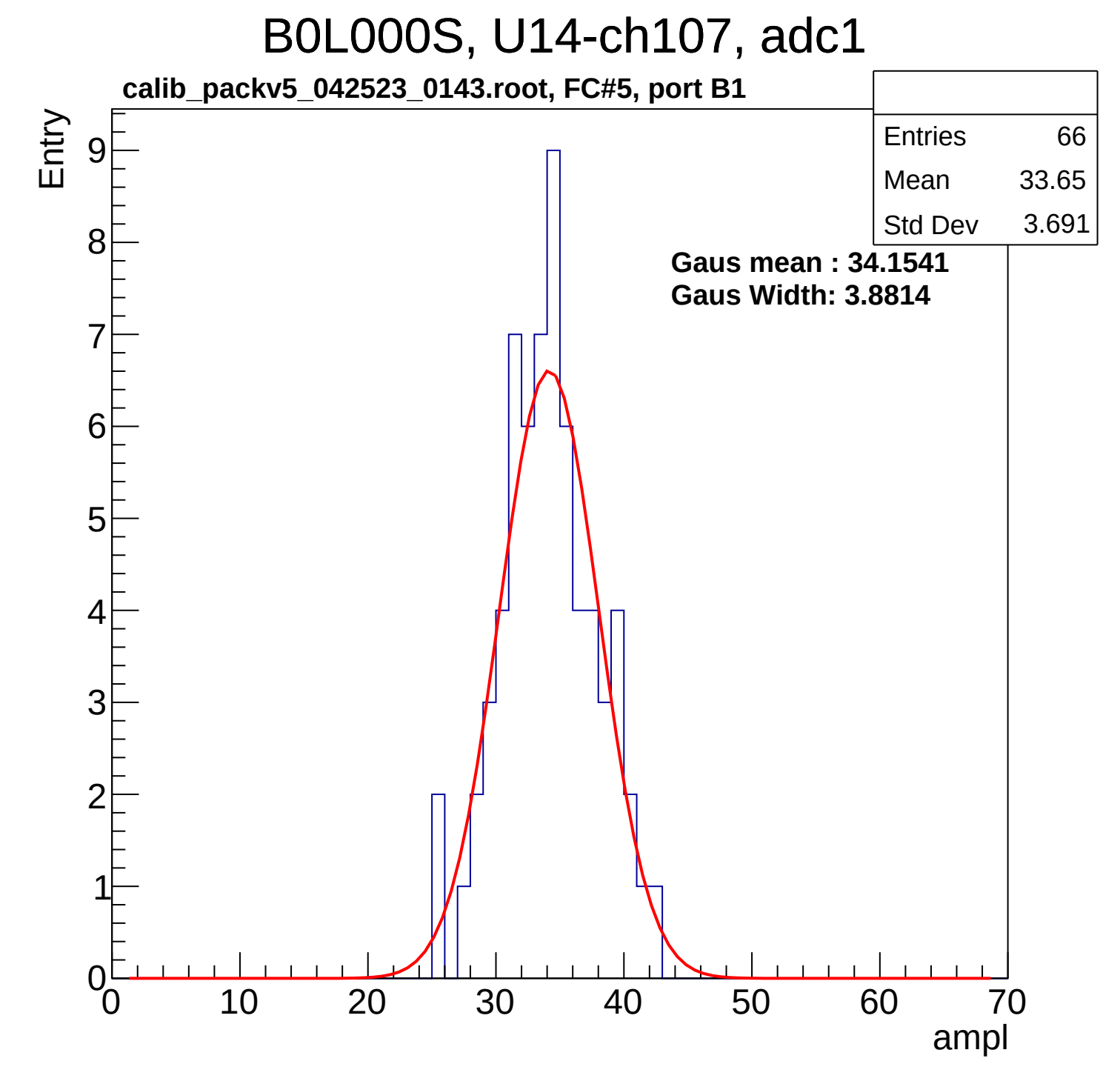
40
50
60
70
ampl


B0L000S, U14-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 27.54
Std Dev 4.729
Gaus mean : 28.4115
Gaus Width: 3.3572
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 33.65
Std Dev 3.691
Gaus mean : 34.1541
Gaus Width: 3.8814
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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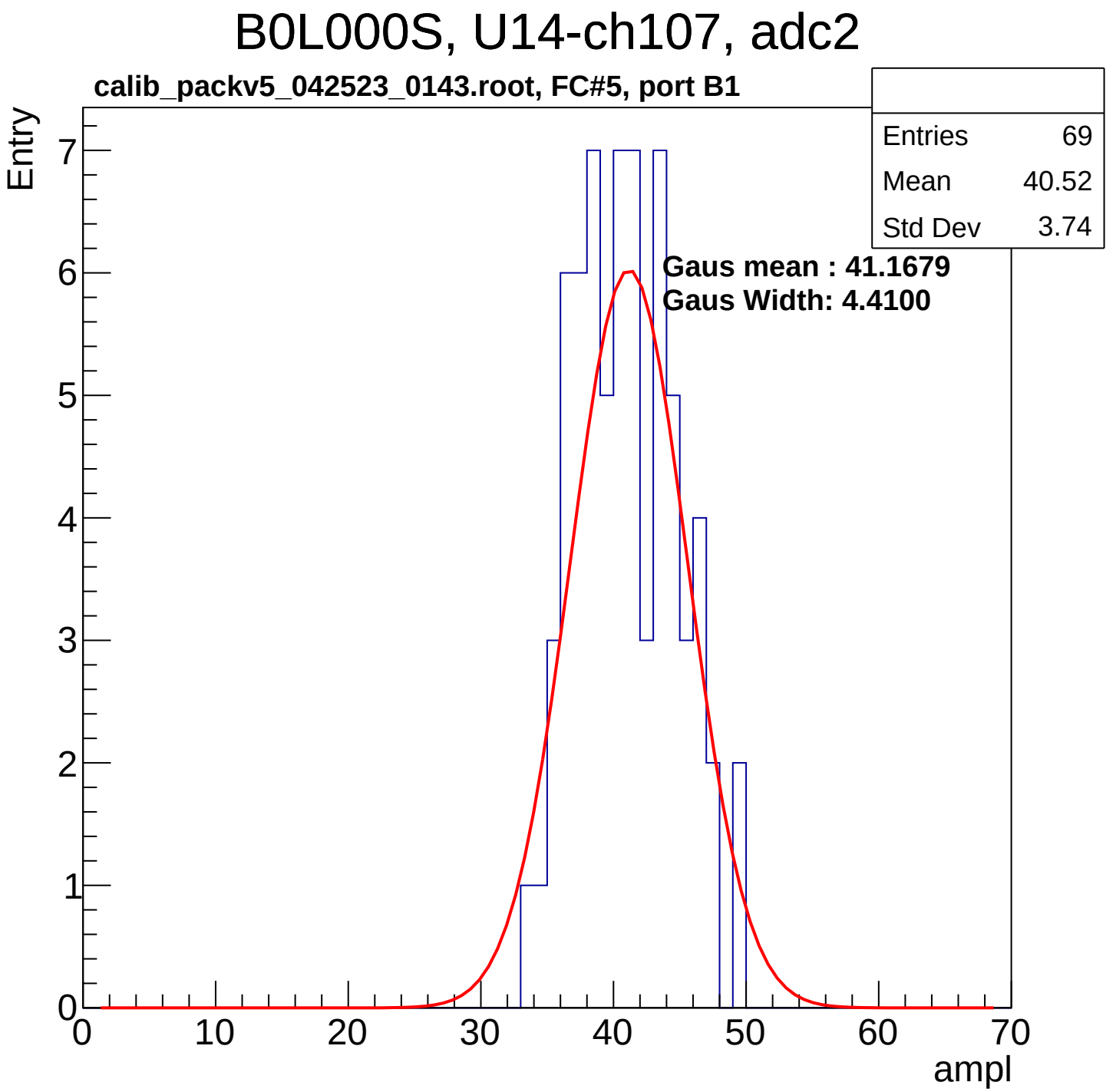
B0L000S, U14-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 40.52
Std Dev 3.74
Gaus mean : 41.1679
Gaus Width: 4.4100
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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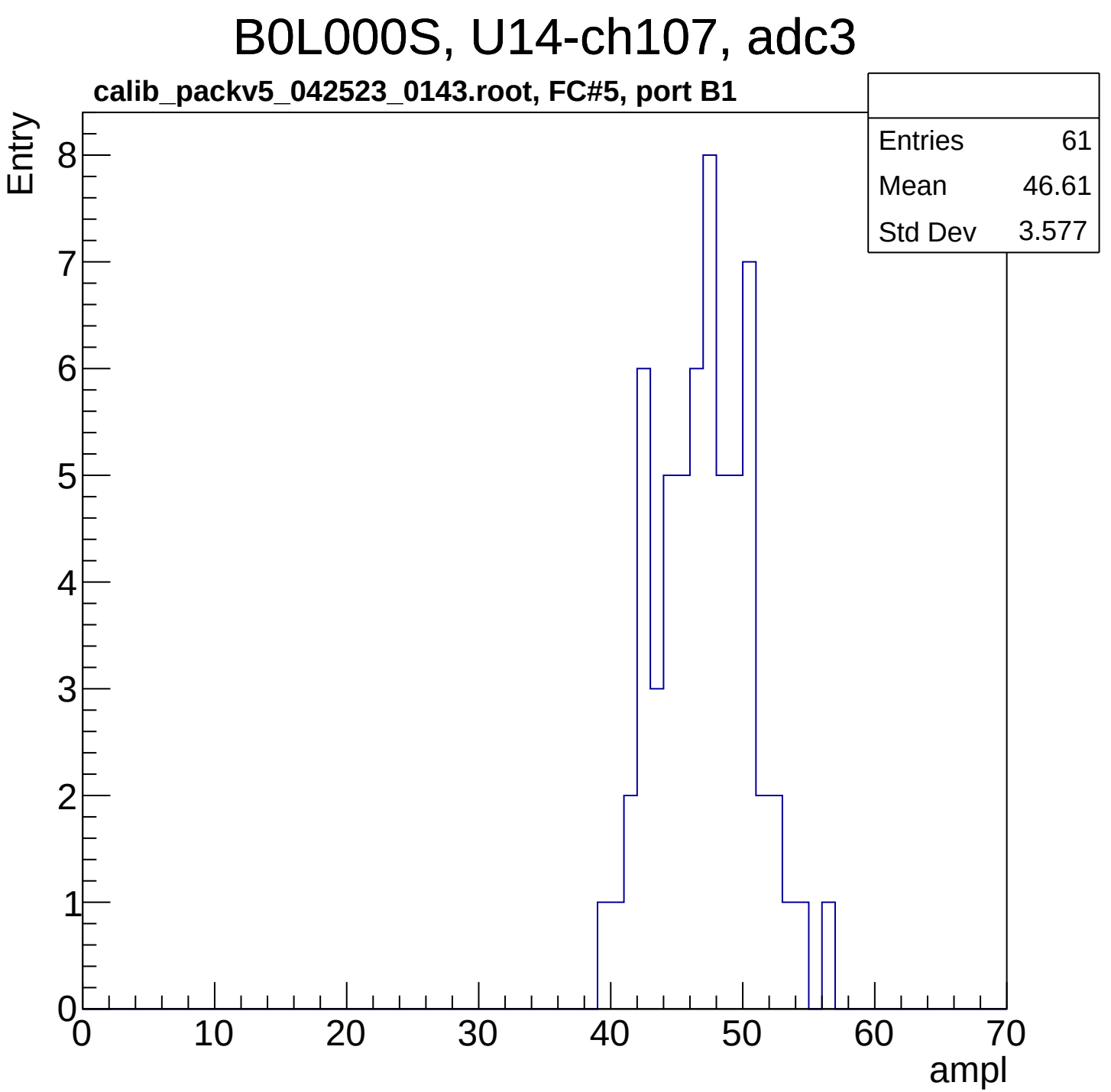
B0L000S, U14-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 46.61
Std Dev 3.577
Entry
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2
3
4
5
6
7
8
0
10
20
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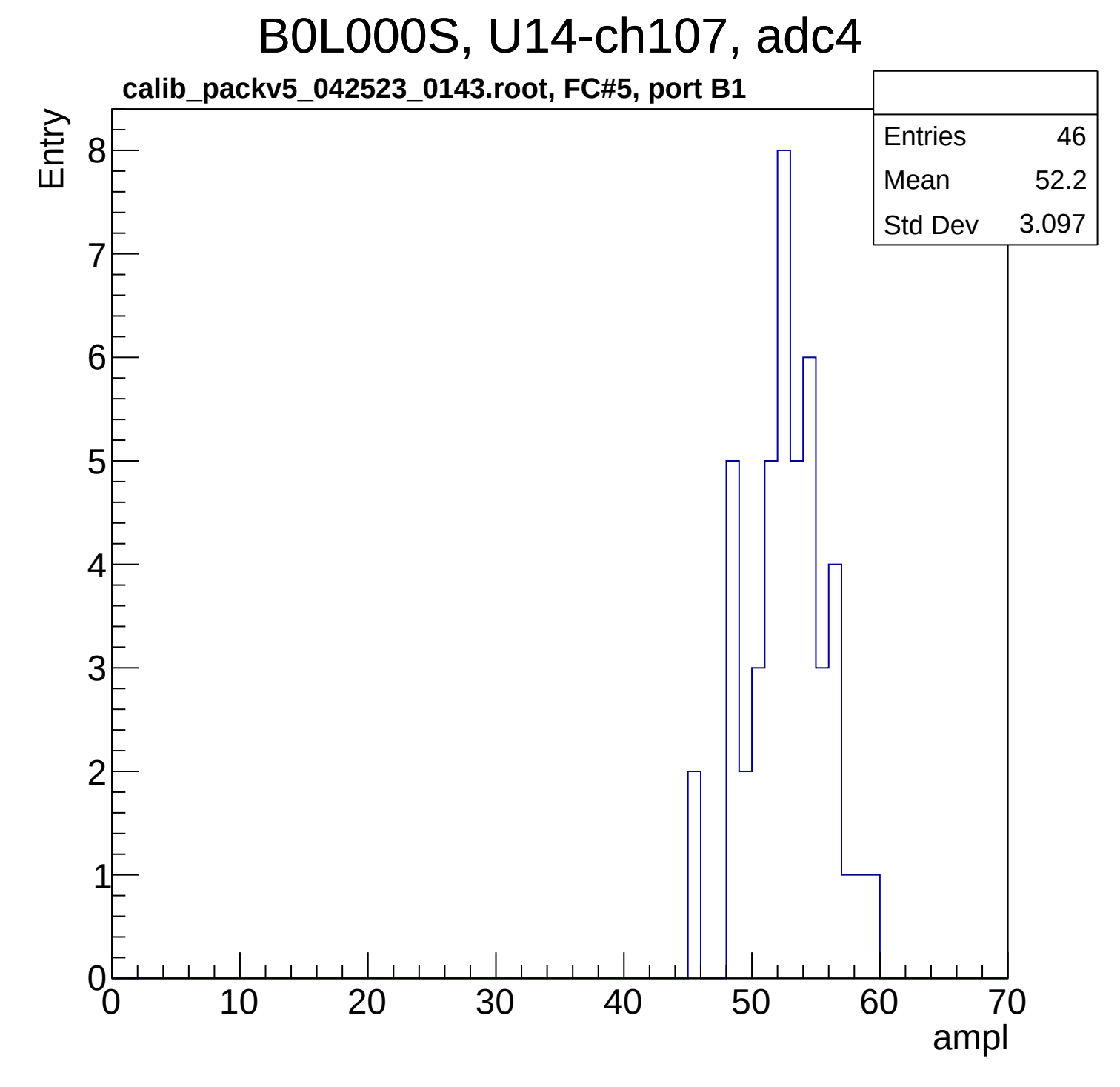
50
60
70
ampl


B0L000S, U14-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 52.2
Std Dev 3.097
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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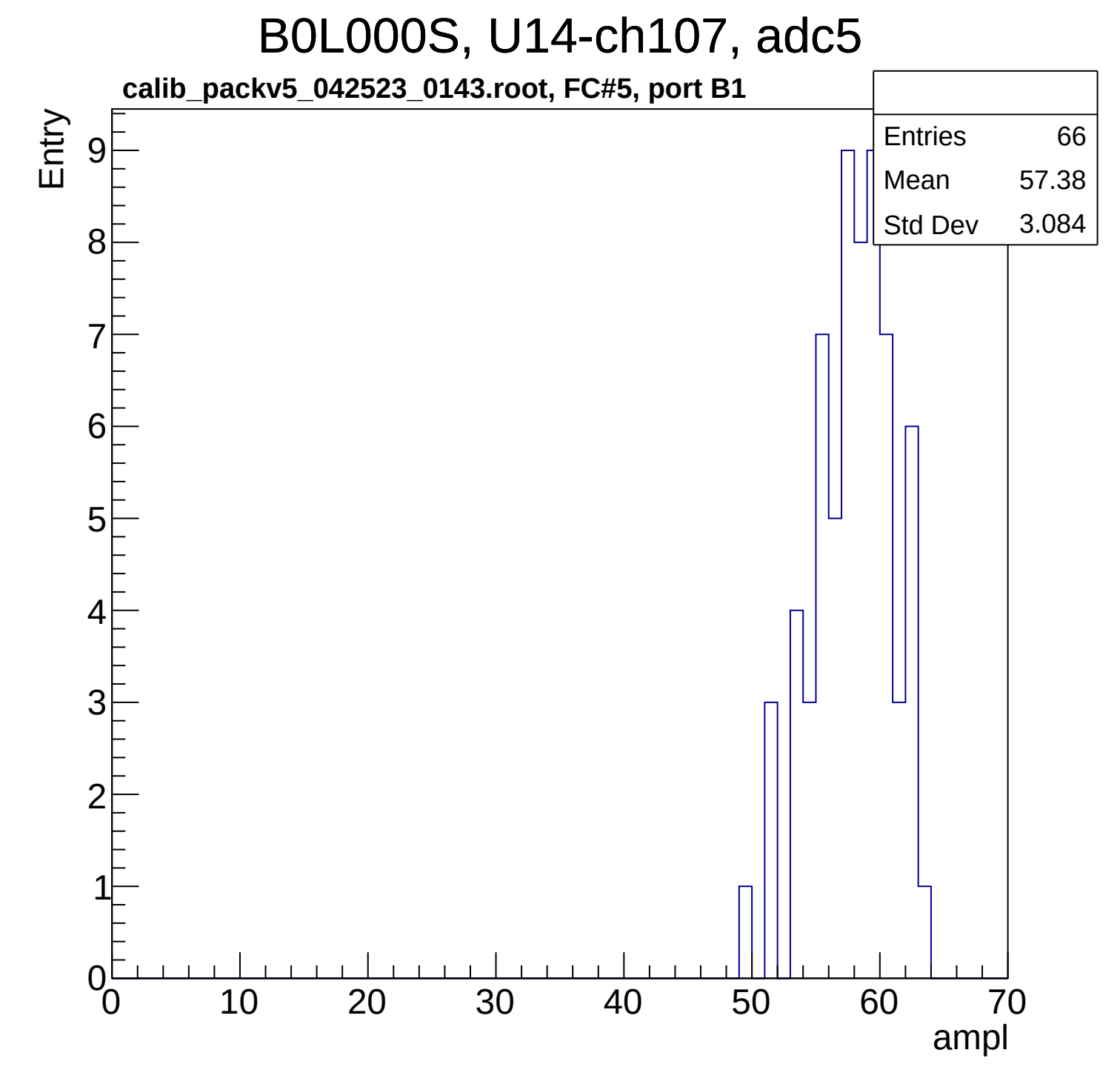
B0L000S, U14-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 57.38
Std Dev 3.084
Entry
ampl
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2
3
4
5
6
7
8
9
0
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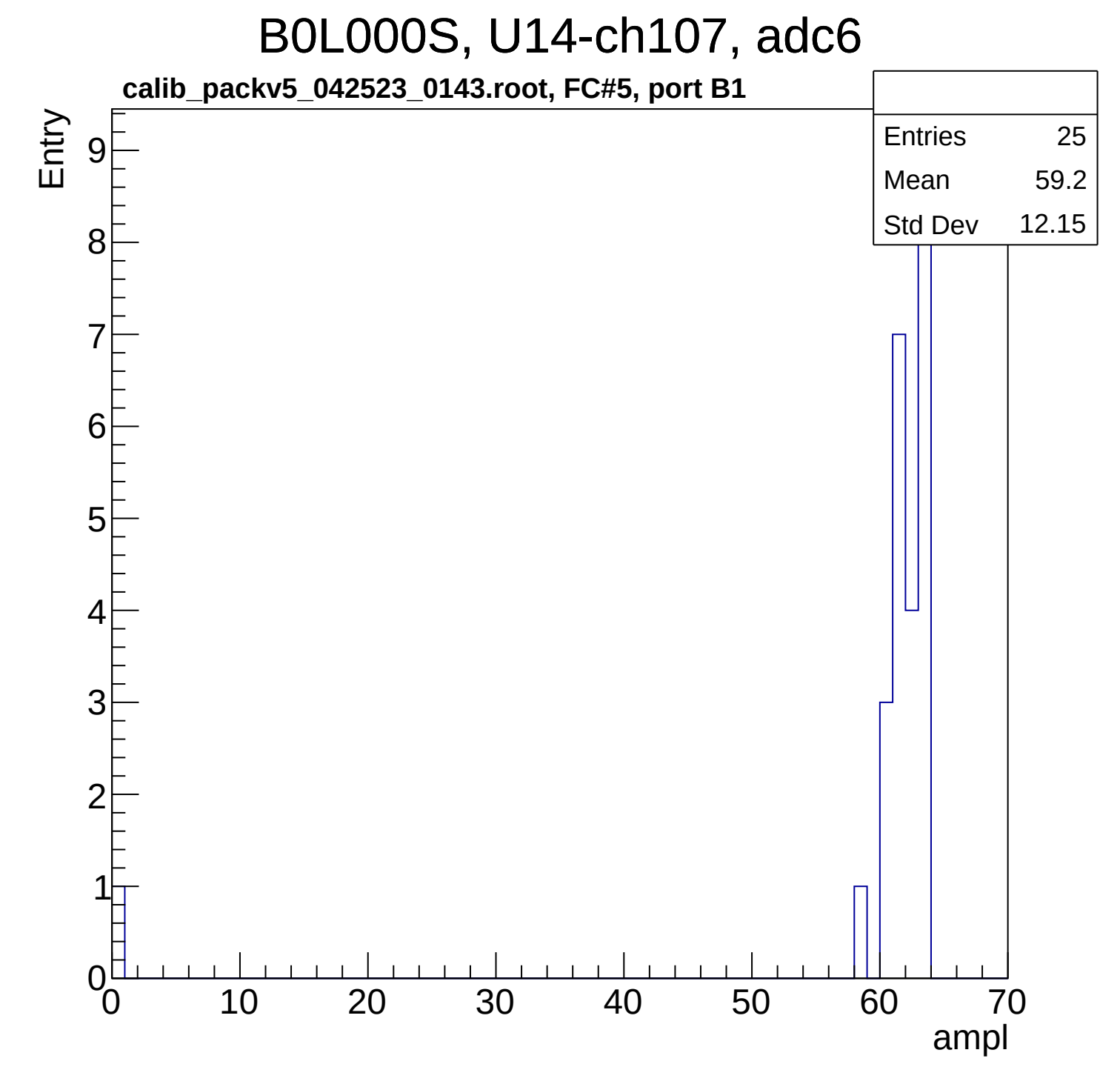
B0L000S, U14-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 59.2
Std Dev 12.15
Entry
0
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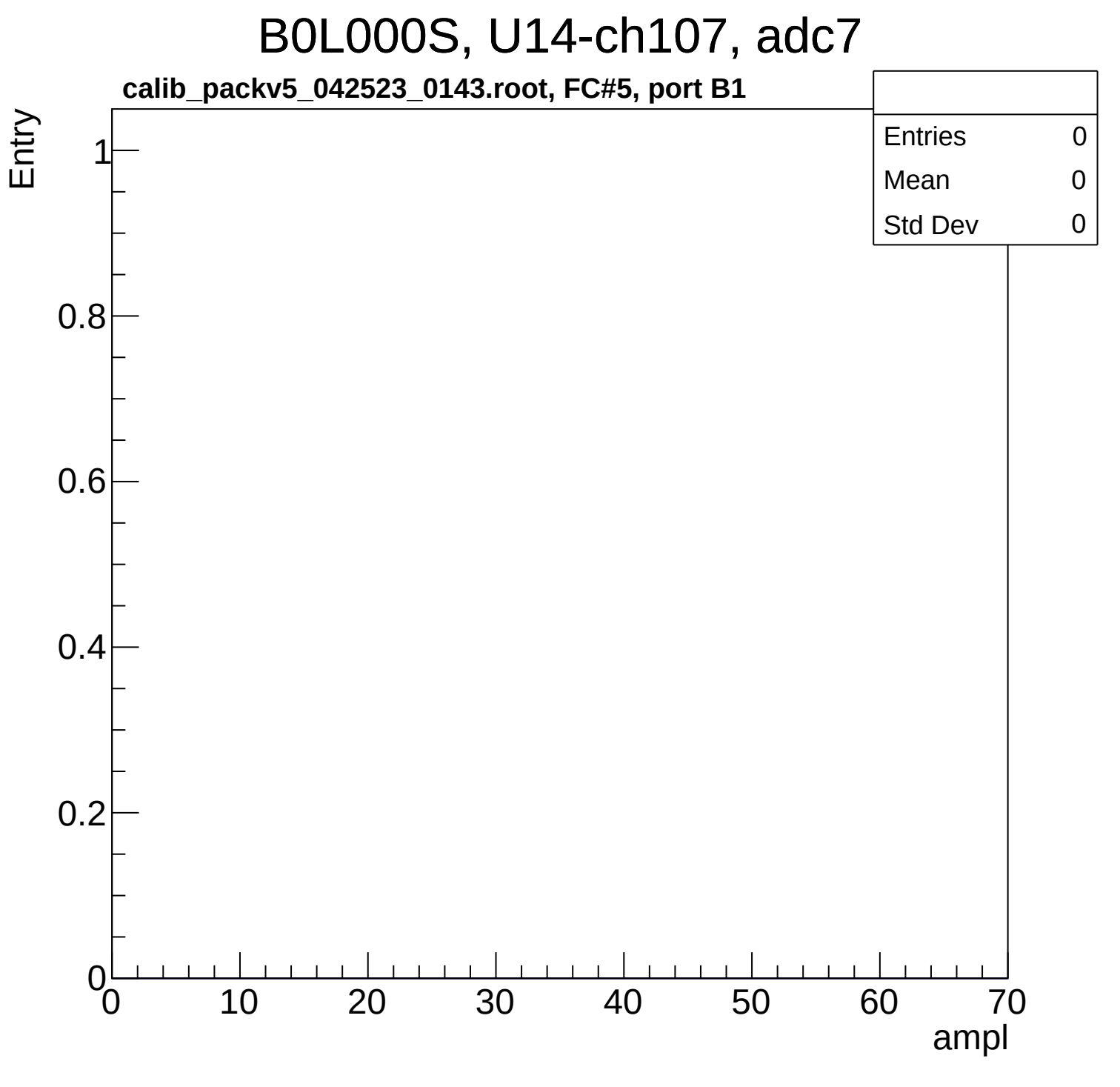
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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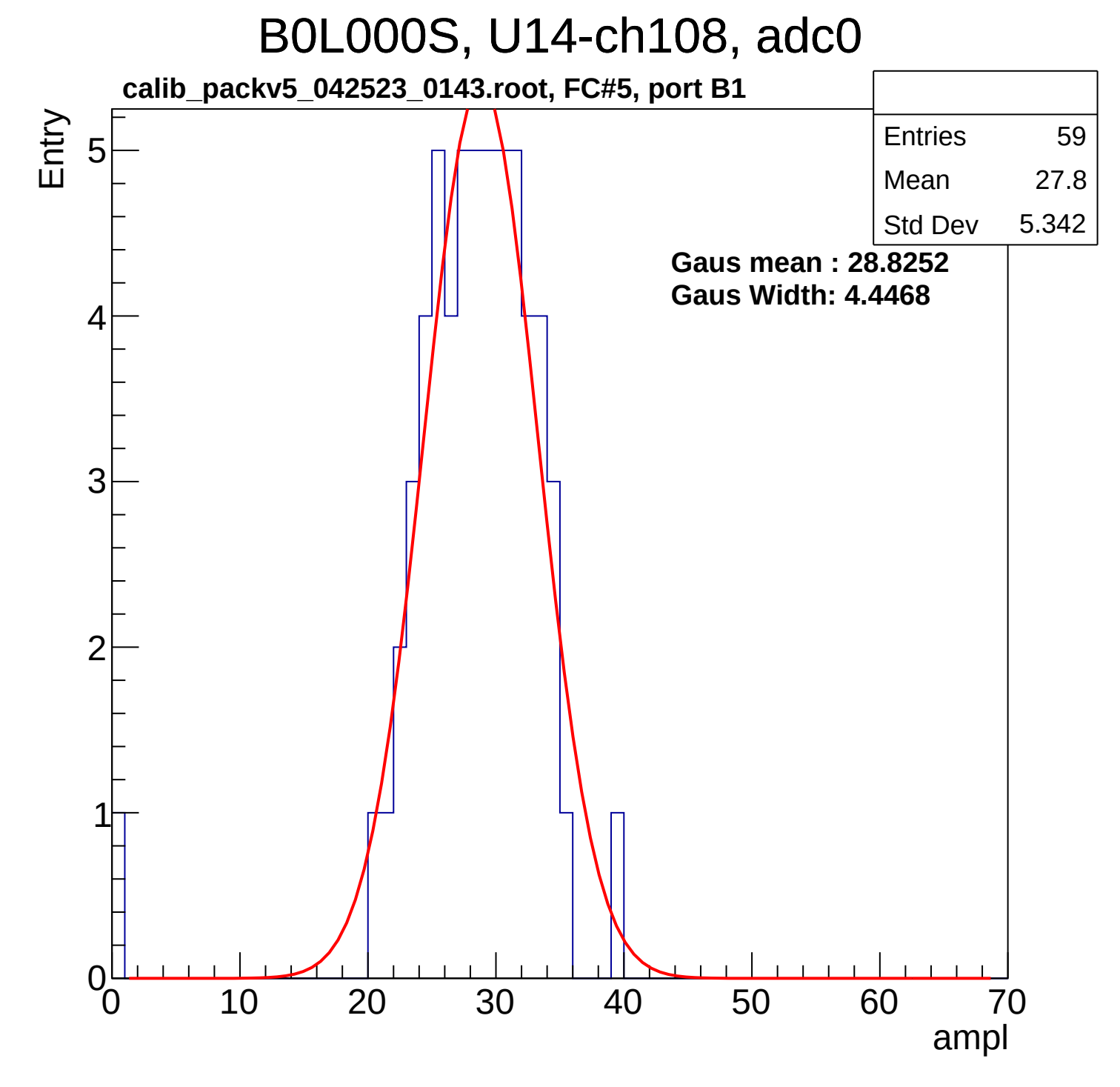
50
60
70
ampl


B0L000S, U14-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.8
Std Dev 5.342
Gaus mean : 28.8252
Gaus Width: 4.4468
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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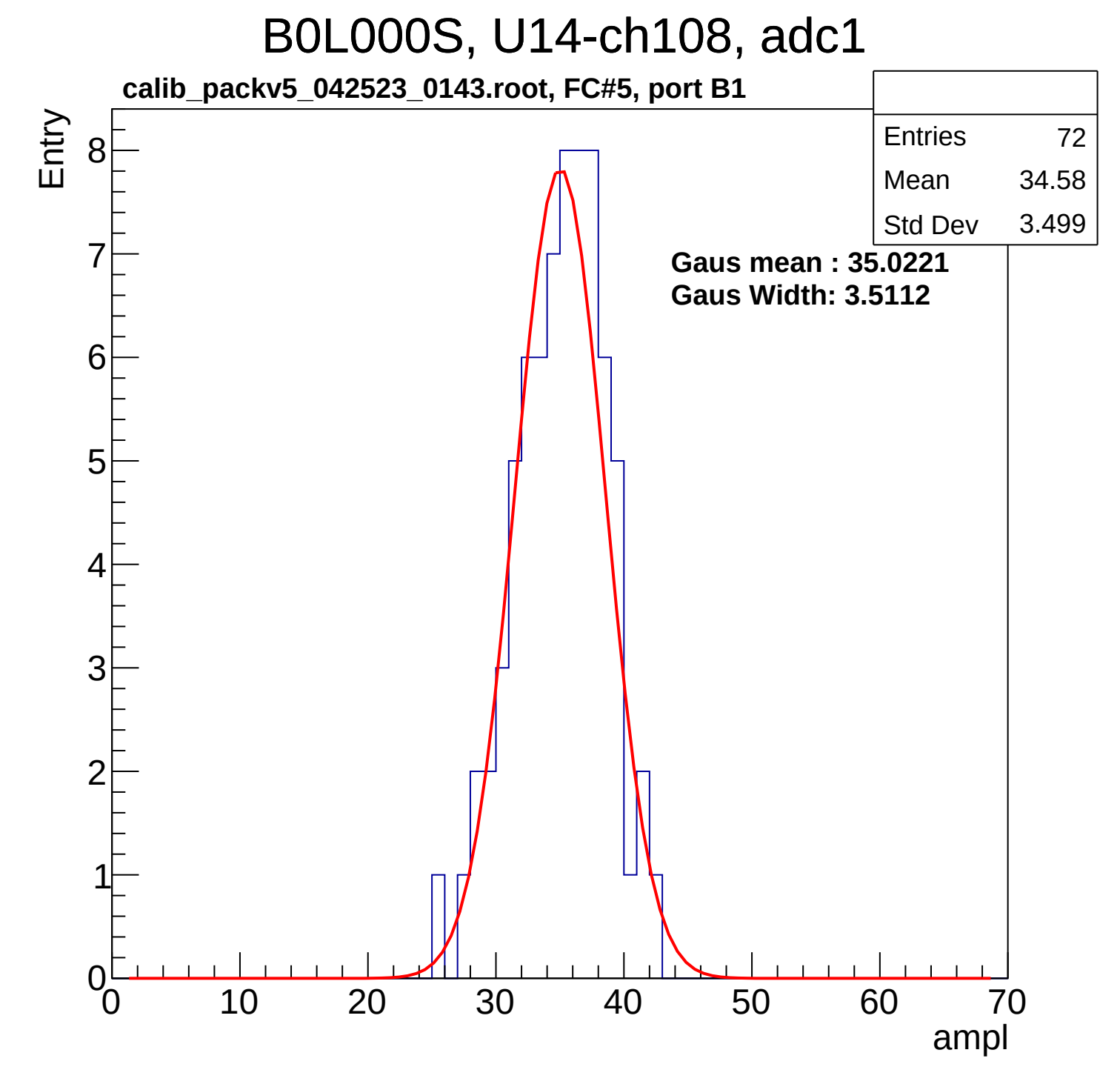
B0L000S, U14-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 34.58
Std Dev 3.499
Gaus mean : 35.0221
Gaus Width: 3.5112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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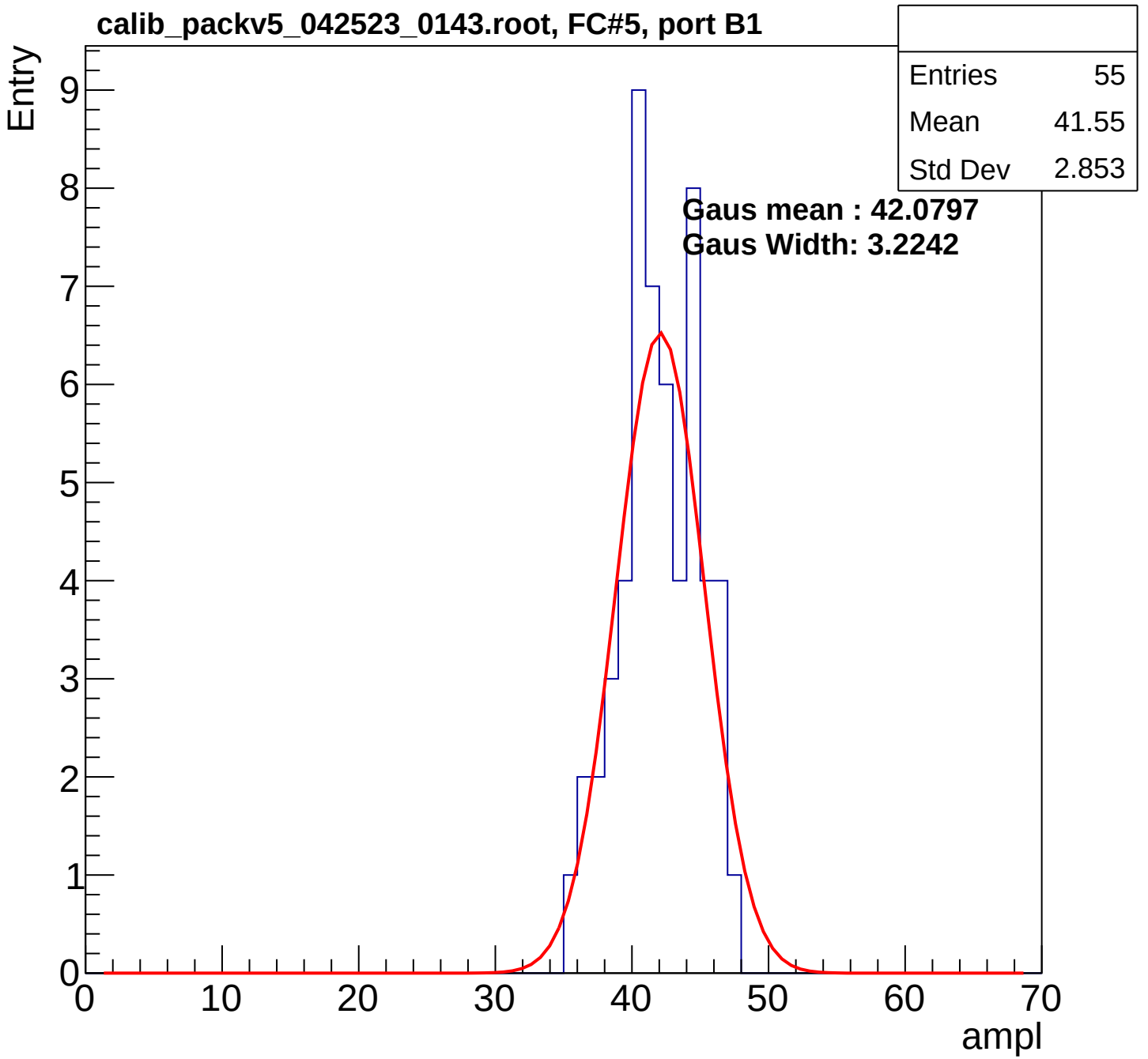
B0L000S, U14-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 41.55
Std Dev 2.853
Gaus mean : 42.0797
Gaus Width: 3.2242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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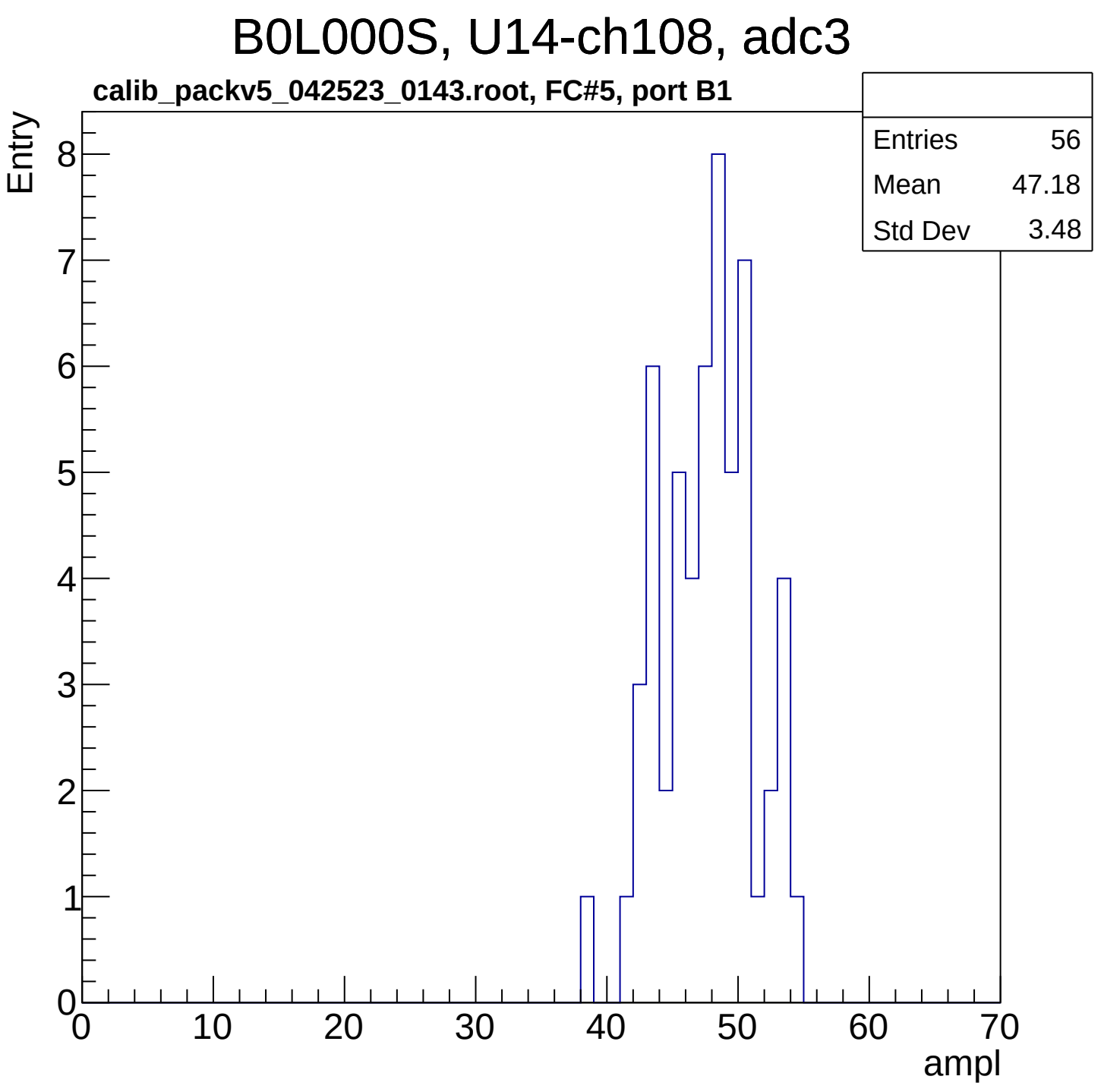
B0L000S, U14-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 47.18
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
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7
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0
10
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40
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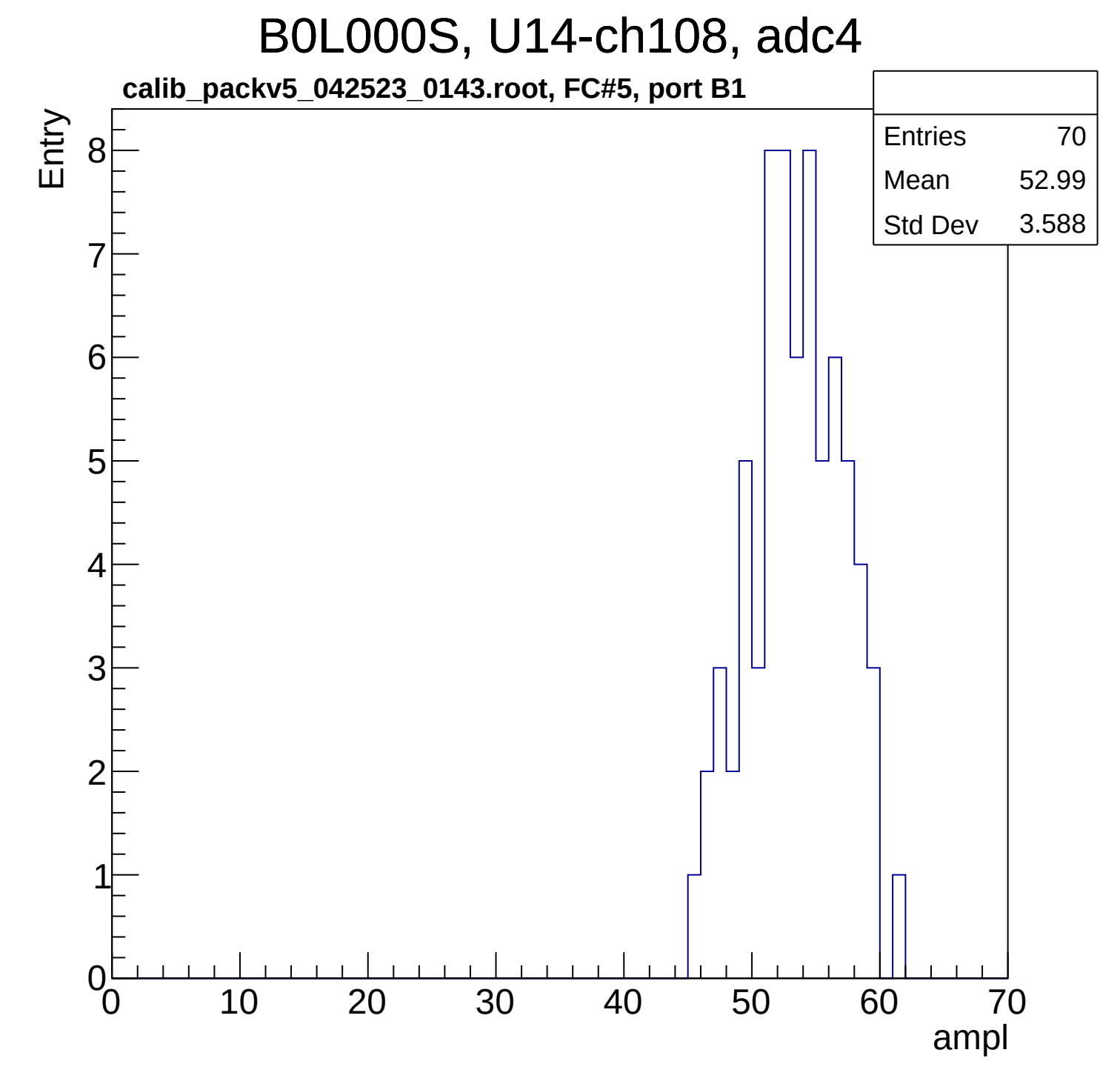
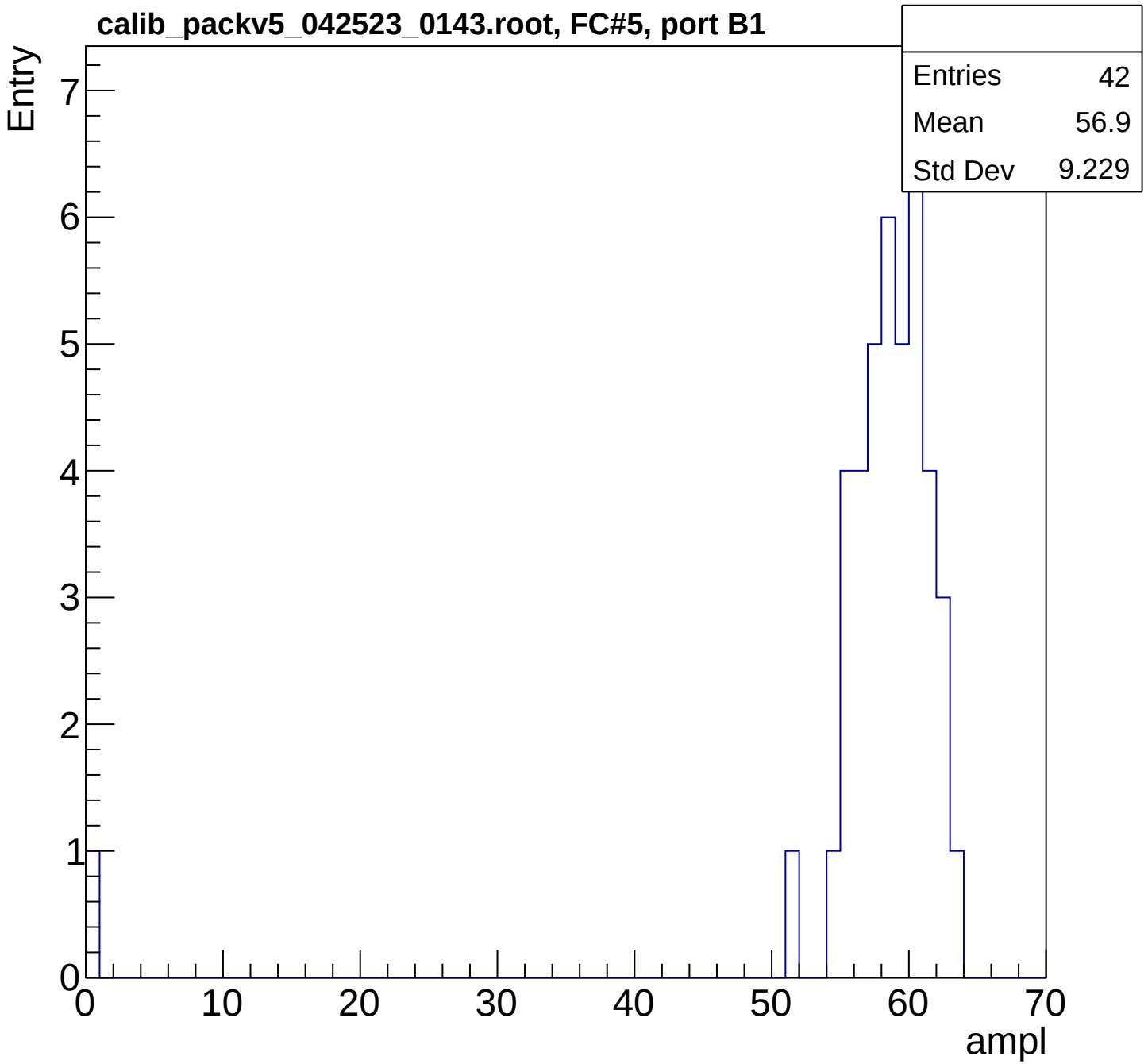
60
70


B0L000S, U14-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.99
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 56.9
Std Dev 9.229
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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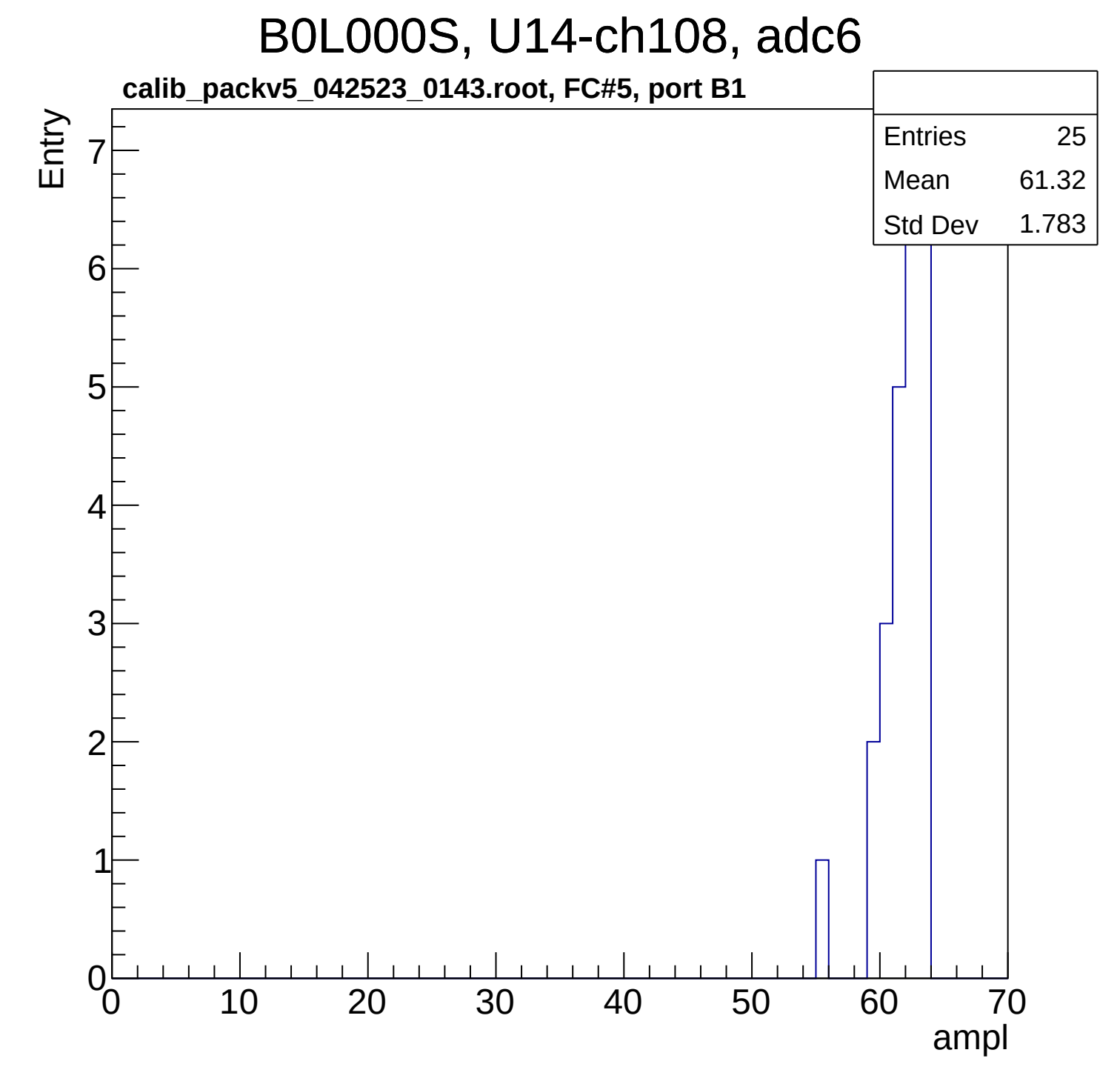
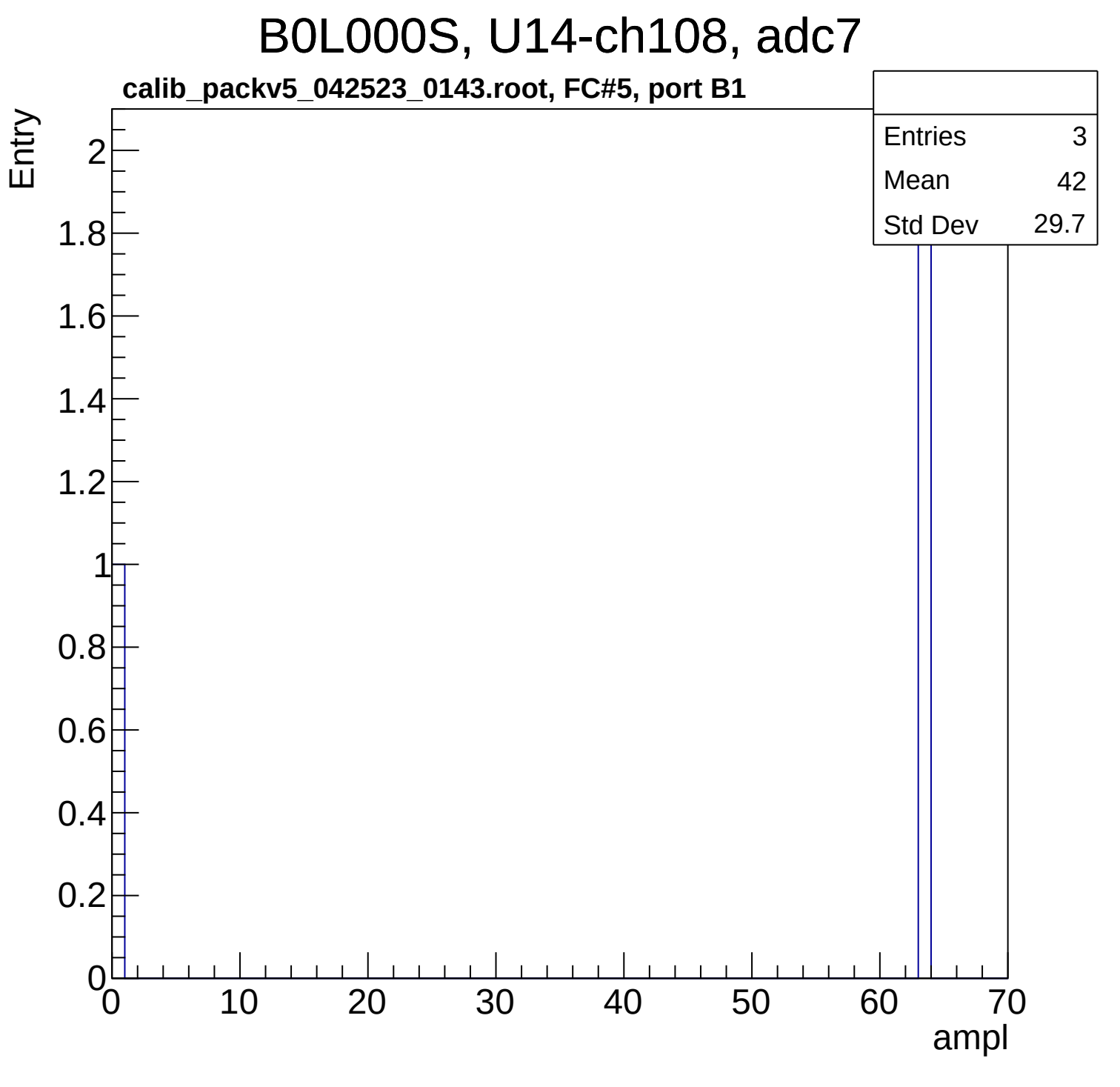

B0L000S, U14-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 61.32
Std Dev 1.783
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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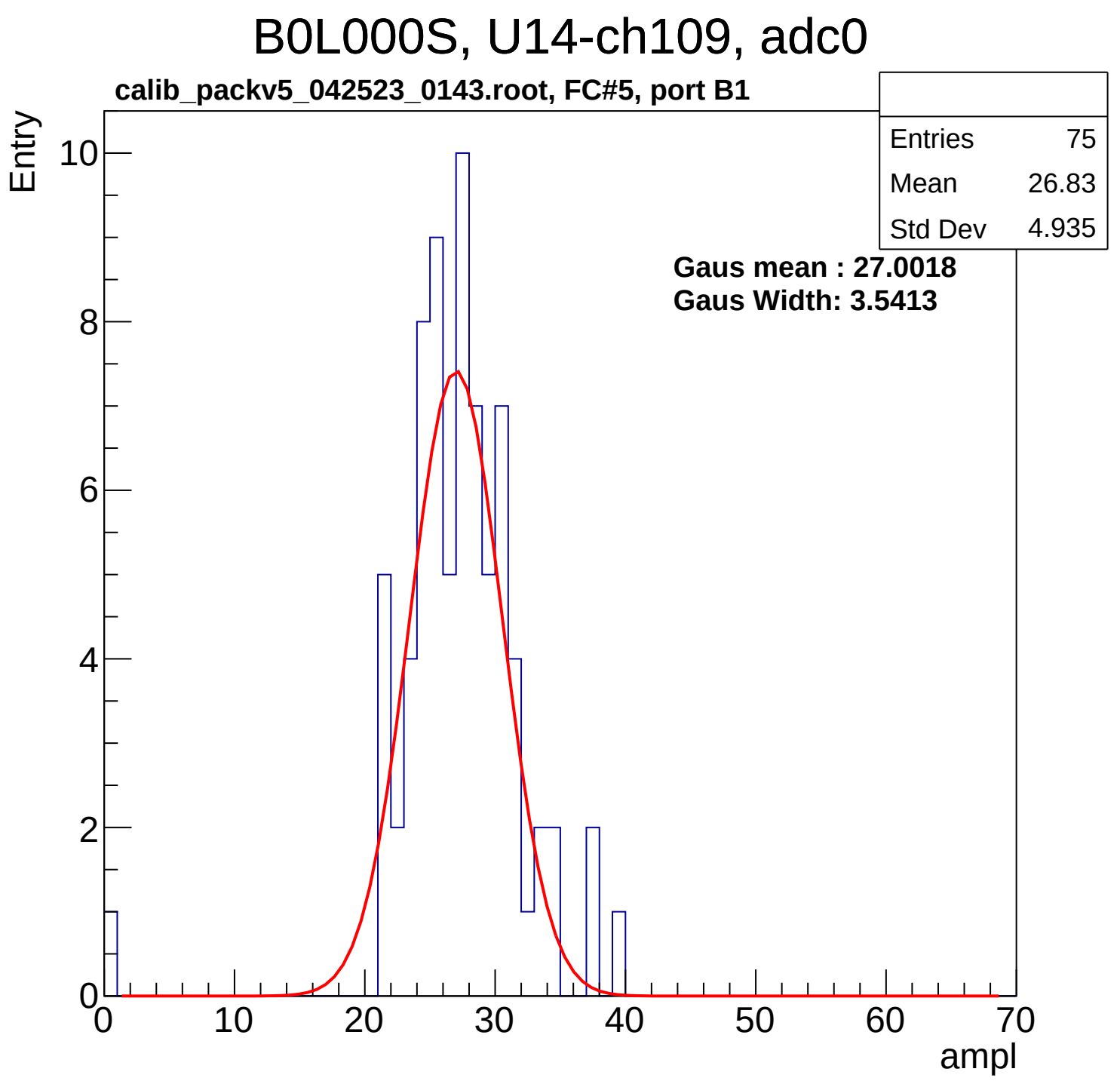
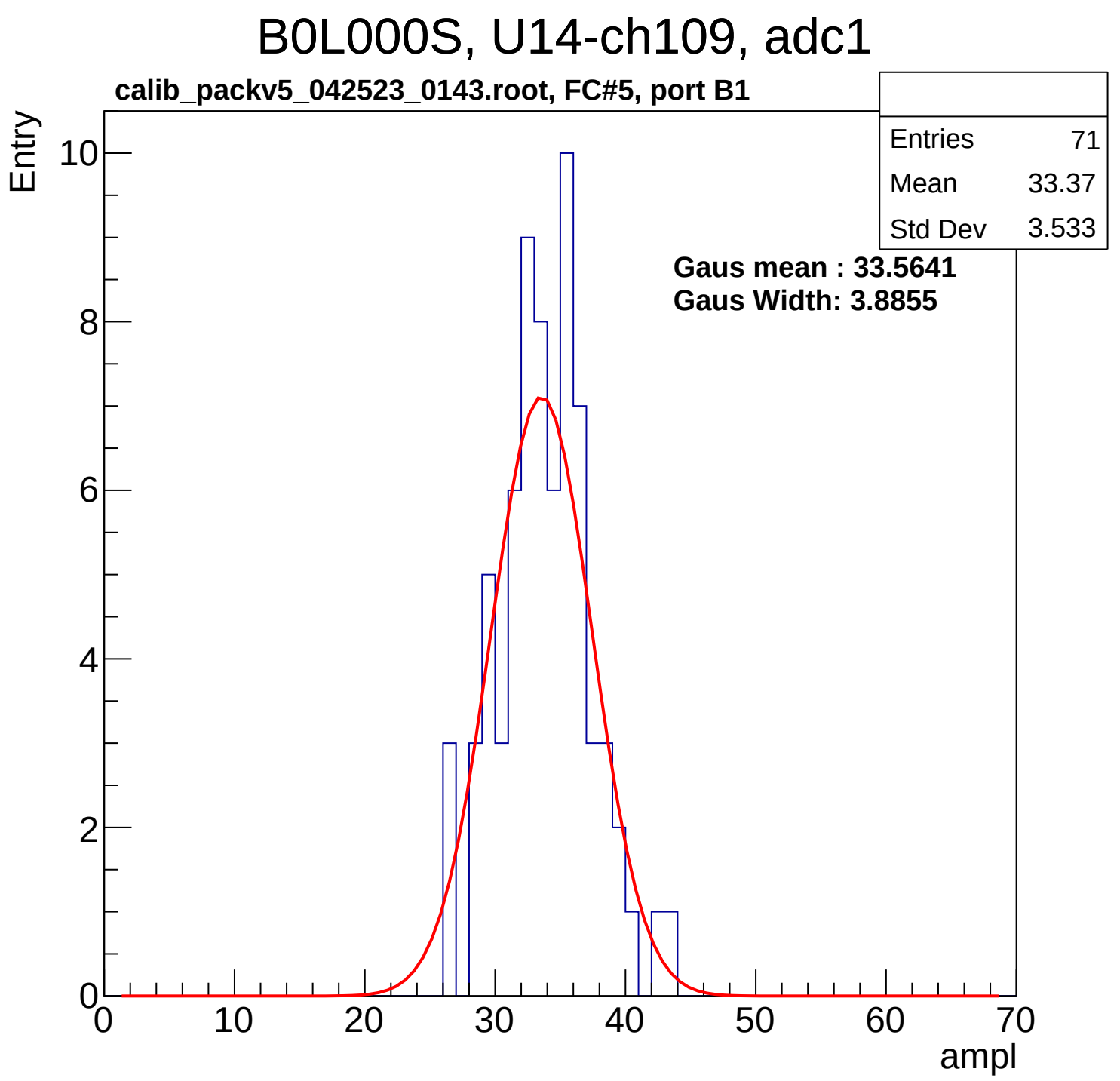
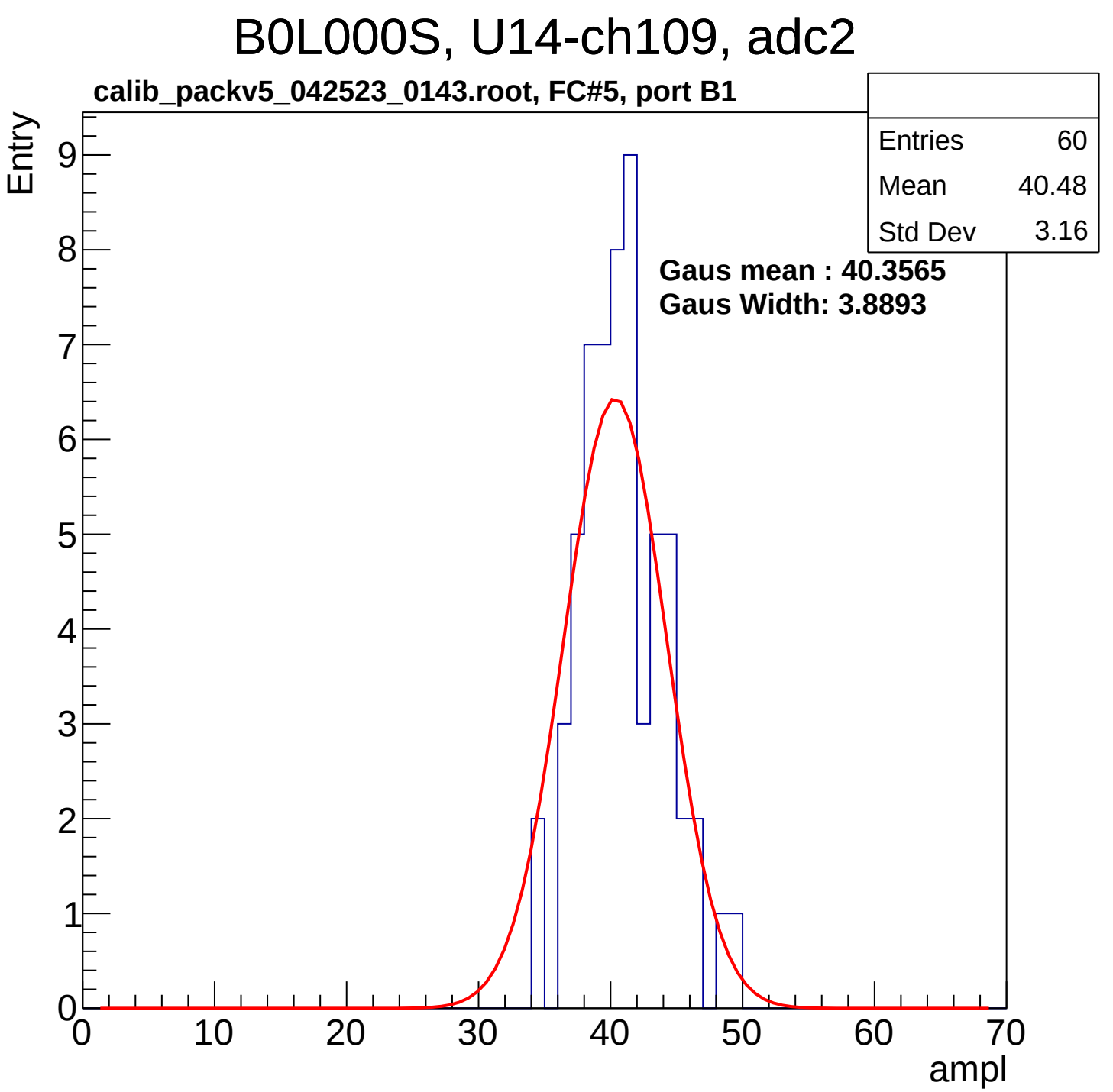
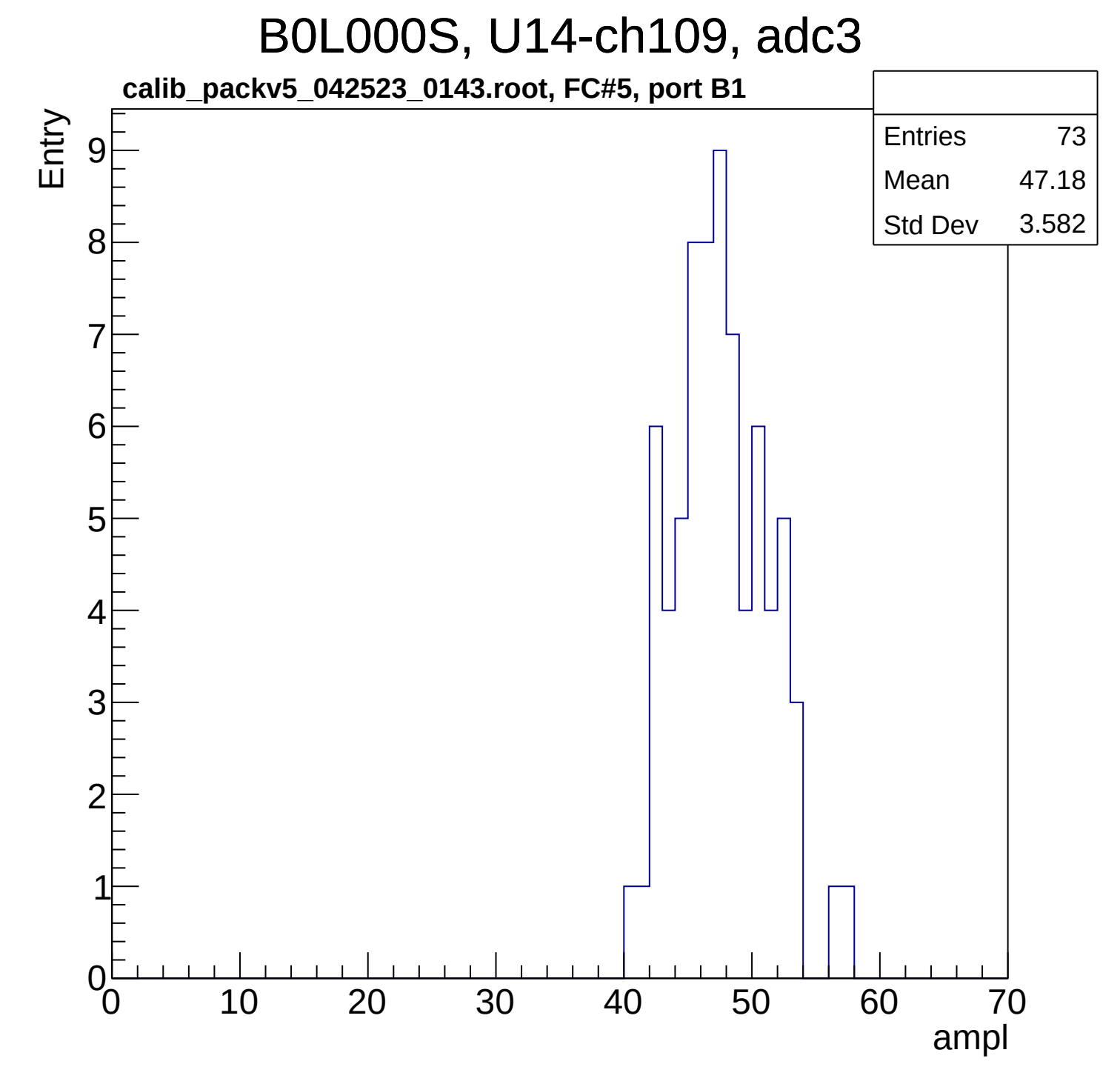
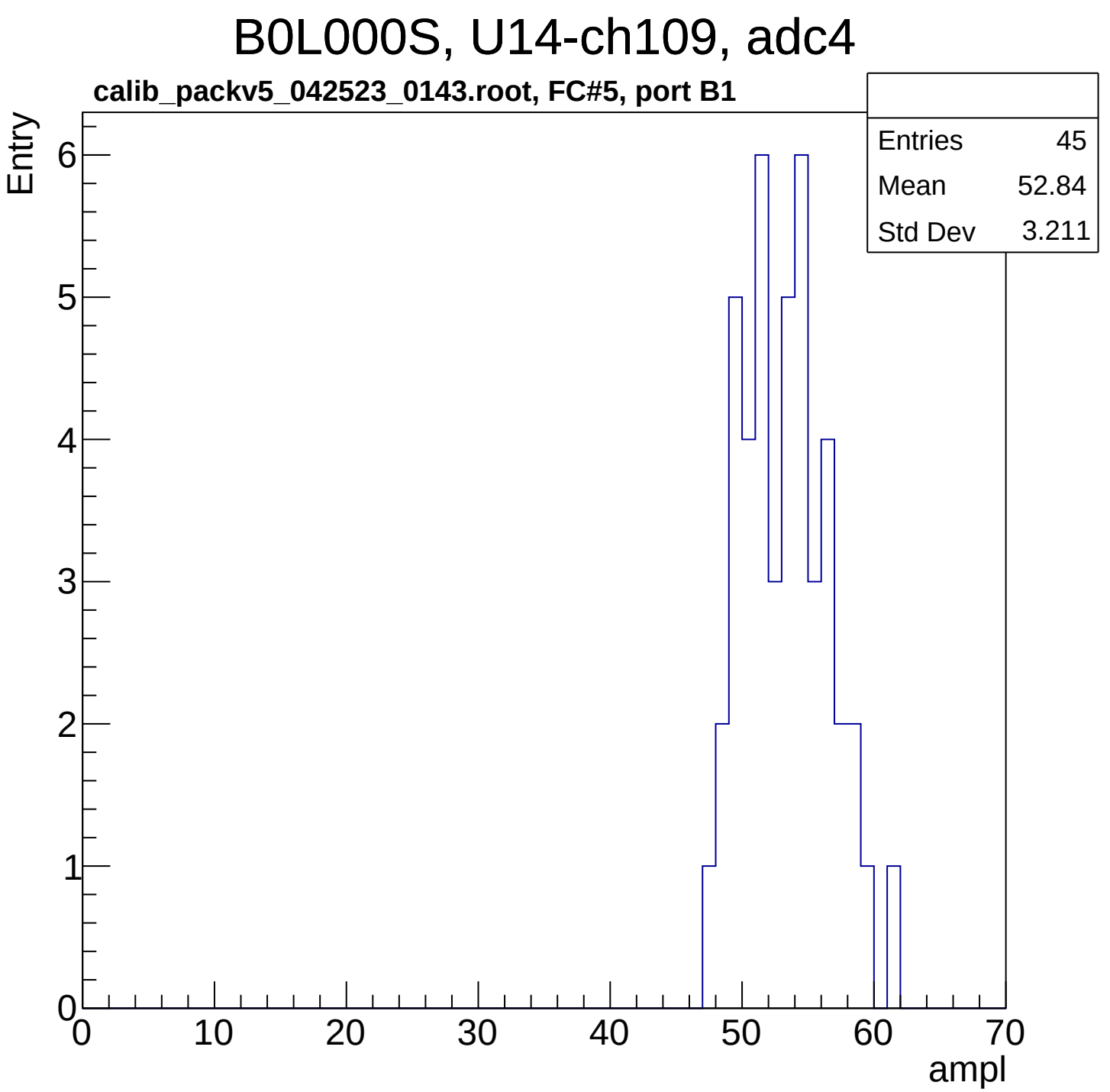
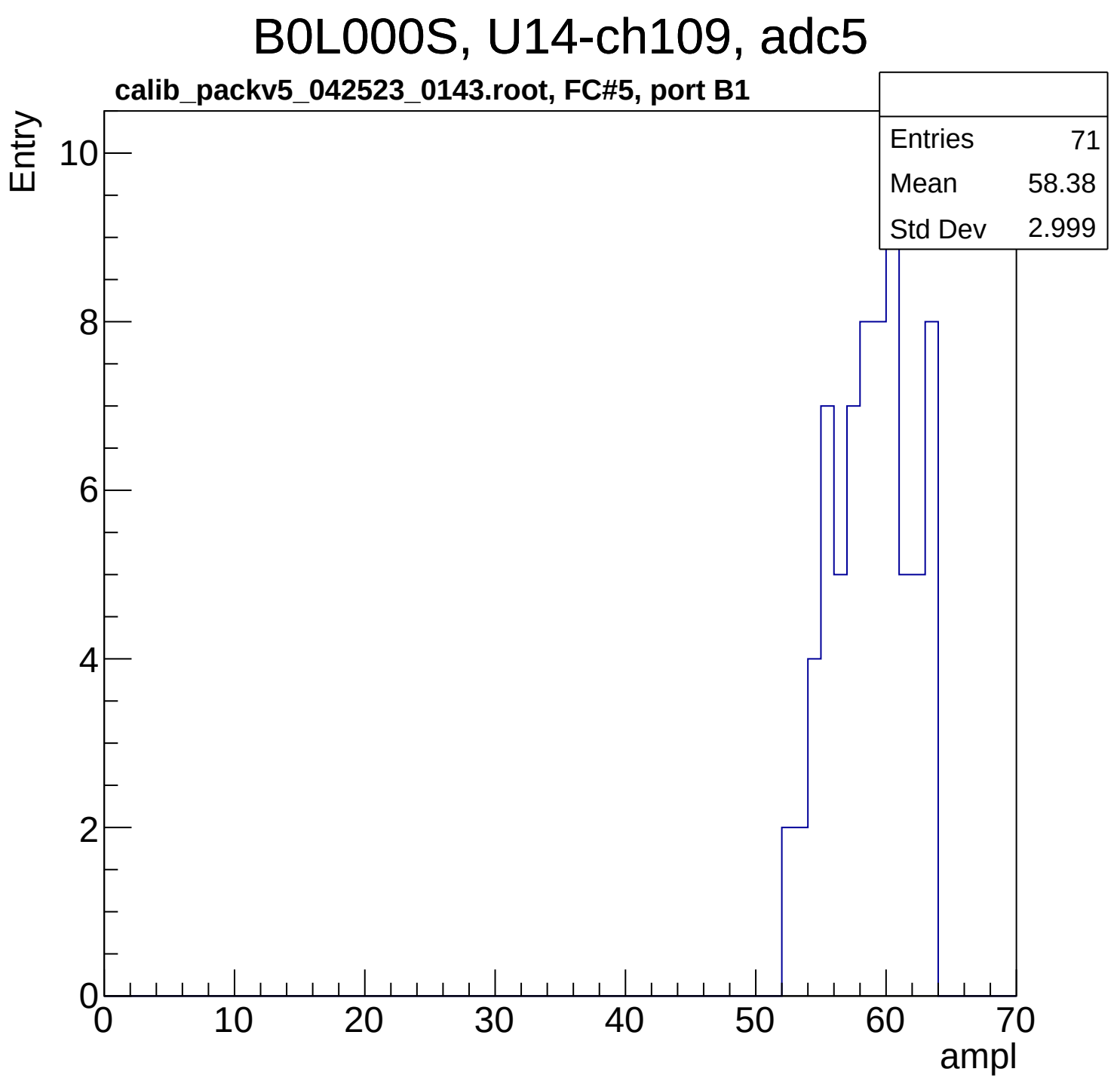

B0L000S, U14-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 26.83
Std Dev 4.935
Gaus mean : 27.0018
Gaus Width: 3.5413
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

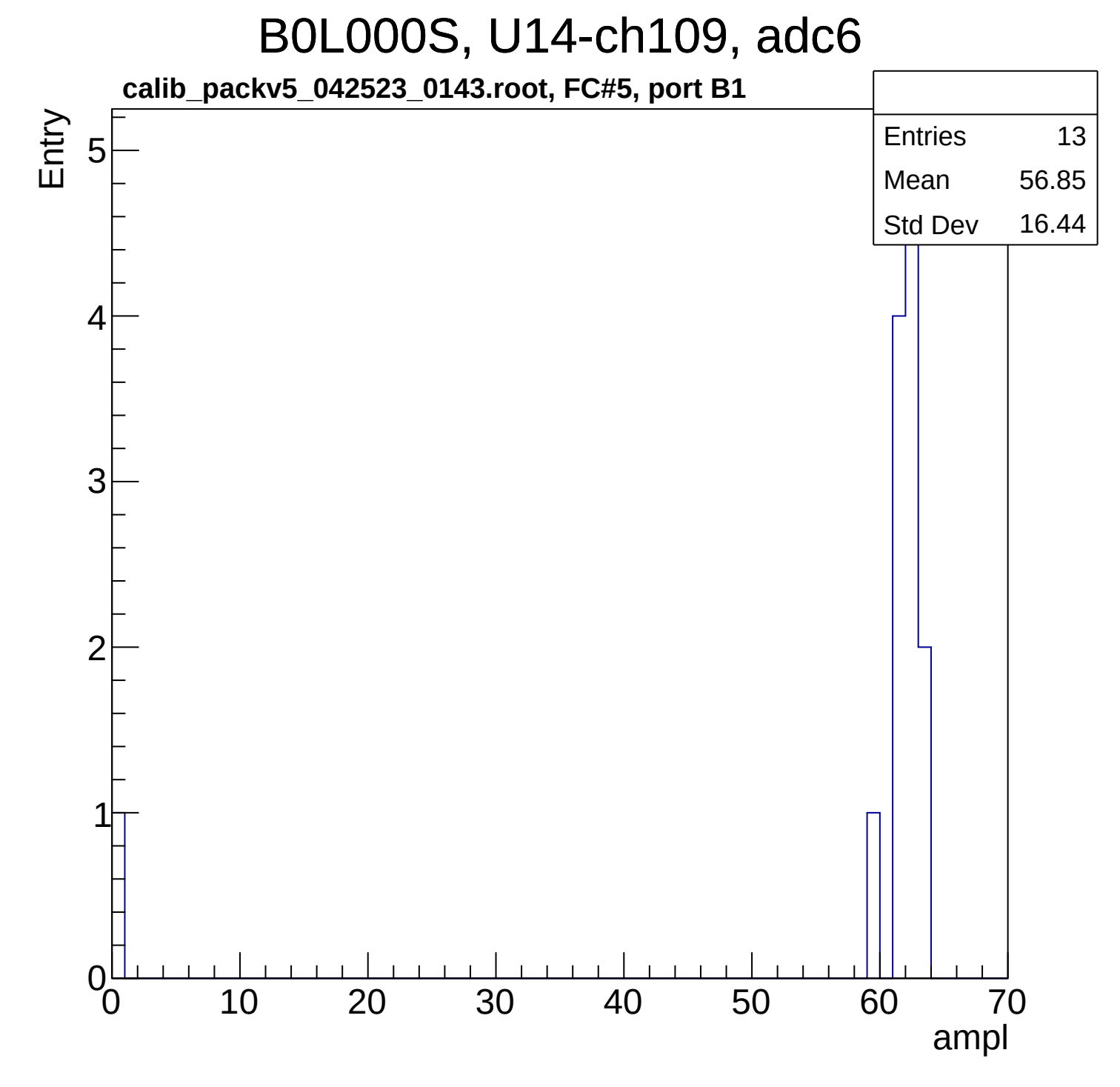

B0L000S, U14-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 33.37
Std Dev 3.533
Gaus mean : 33.5641
Gaus Width: 3.8855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

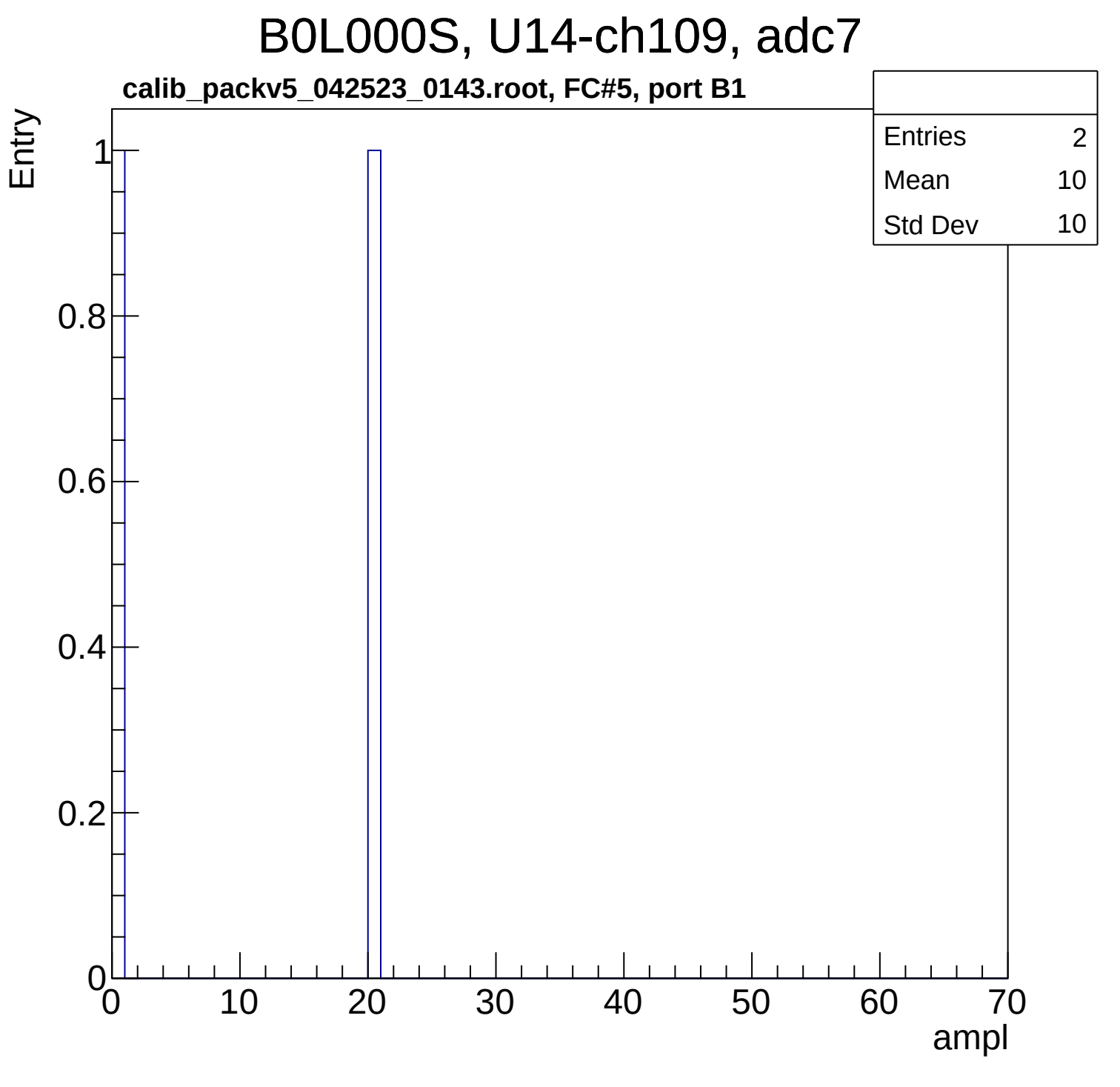

B0L000S, U14-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 40.48
Std Dev 3.16
Gaus mean : 40.3565
Gaus Width: 3.8893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

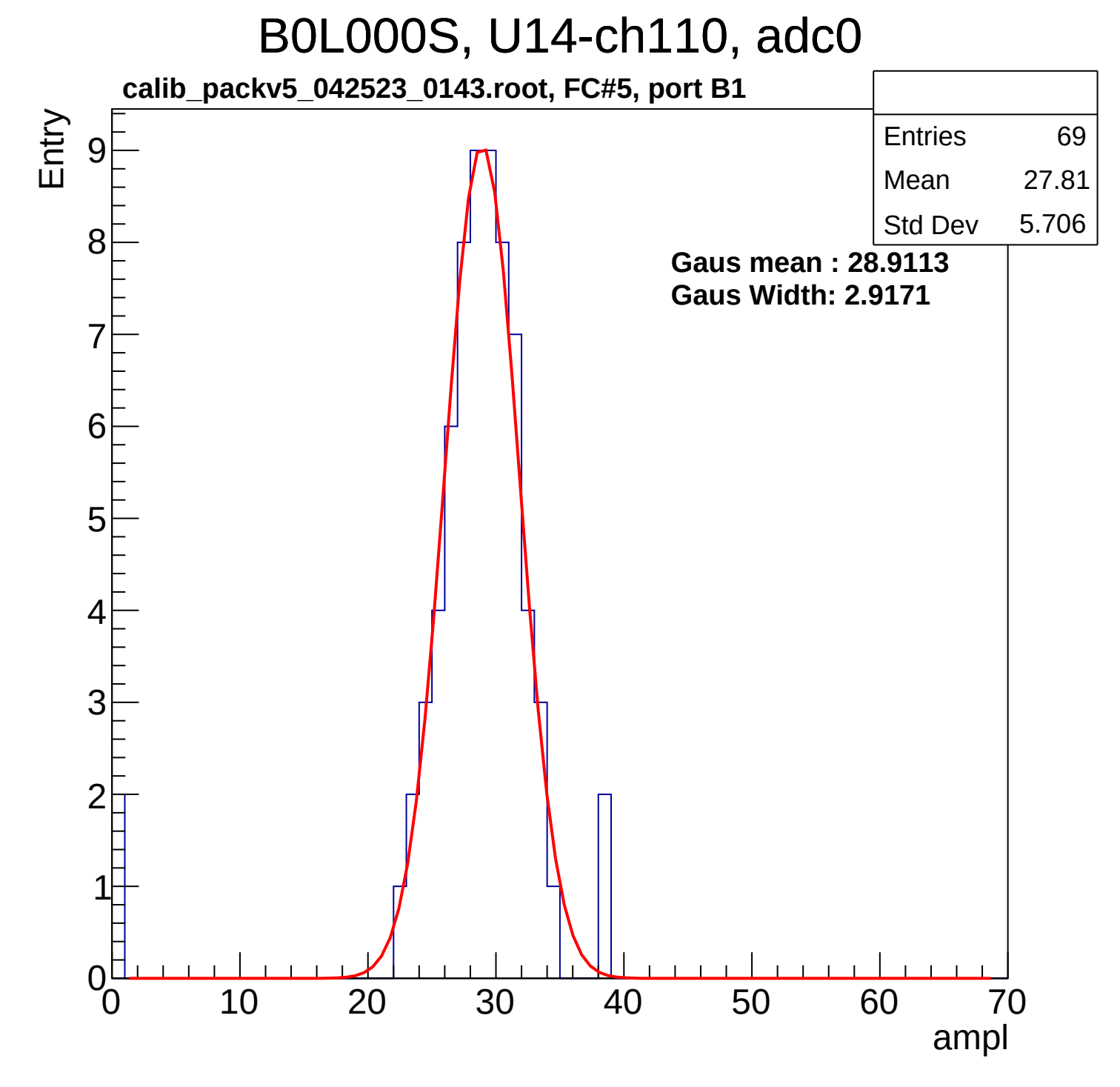

B0L000S, U14-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 47.18
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 52.84
Std Dev 3.211
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 58.38
Std Dev 2.999
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.85
Std Dev 16.44
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 27.81
Std Dev 5.706
Gaus mean : 28.9113
Gaus Width: 2.9171
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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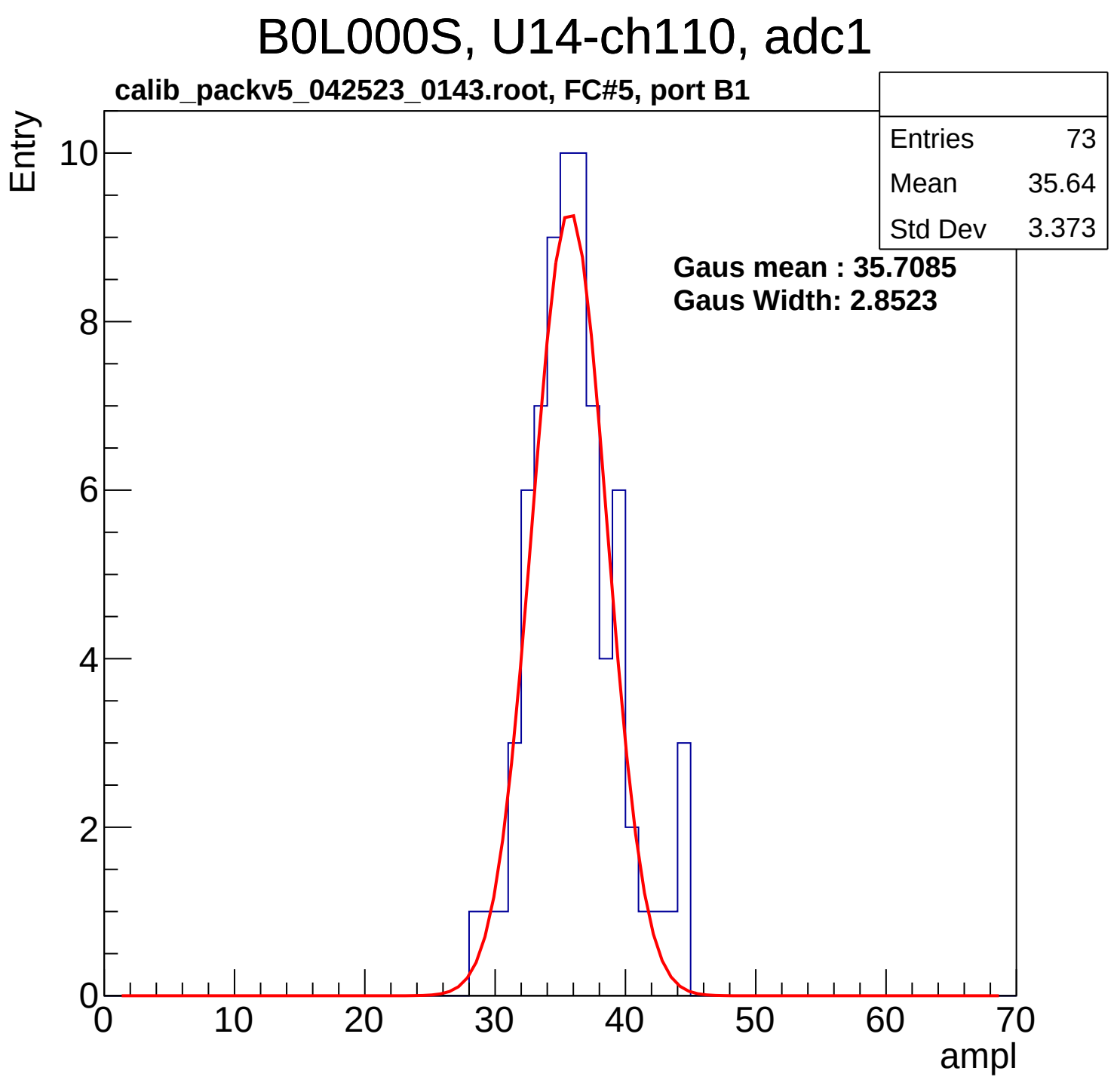
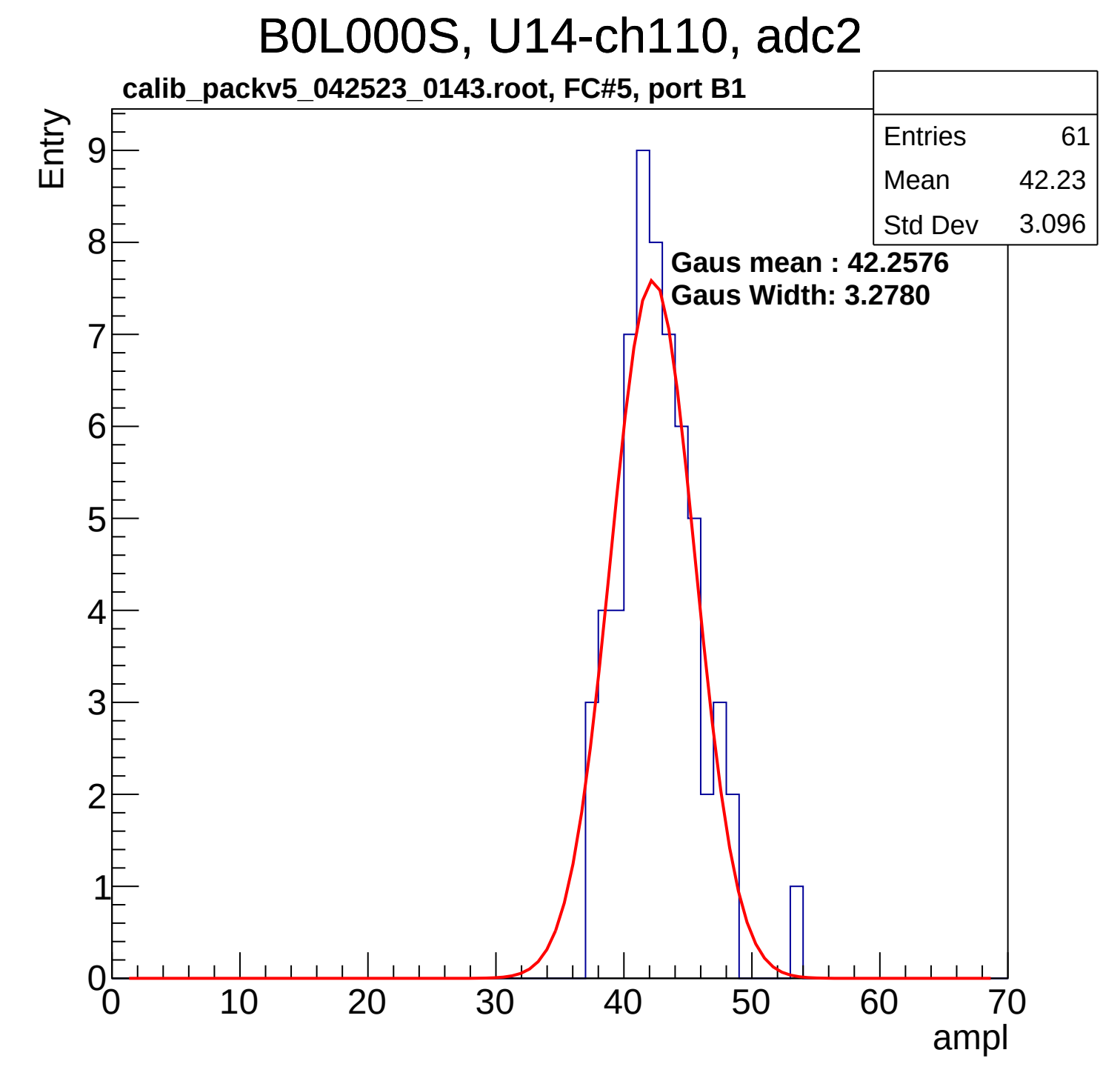

B0L000S, U14-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.64
Std Dev 3.373
Gaus mean : 35.7085
Gaus Width: 2.8523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 42.23
Std Dev 3.096
Gaus mean : 42.2576
Gaus Width: 3.2780
Entry
ampl
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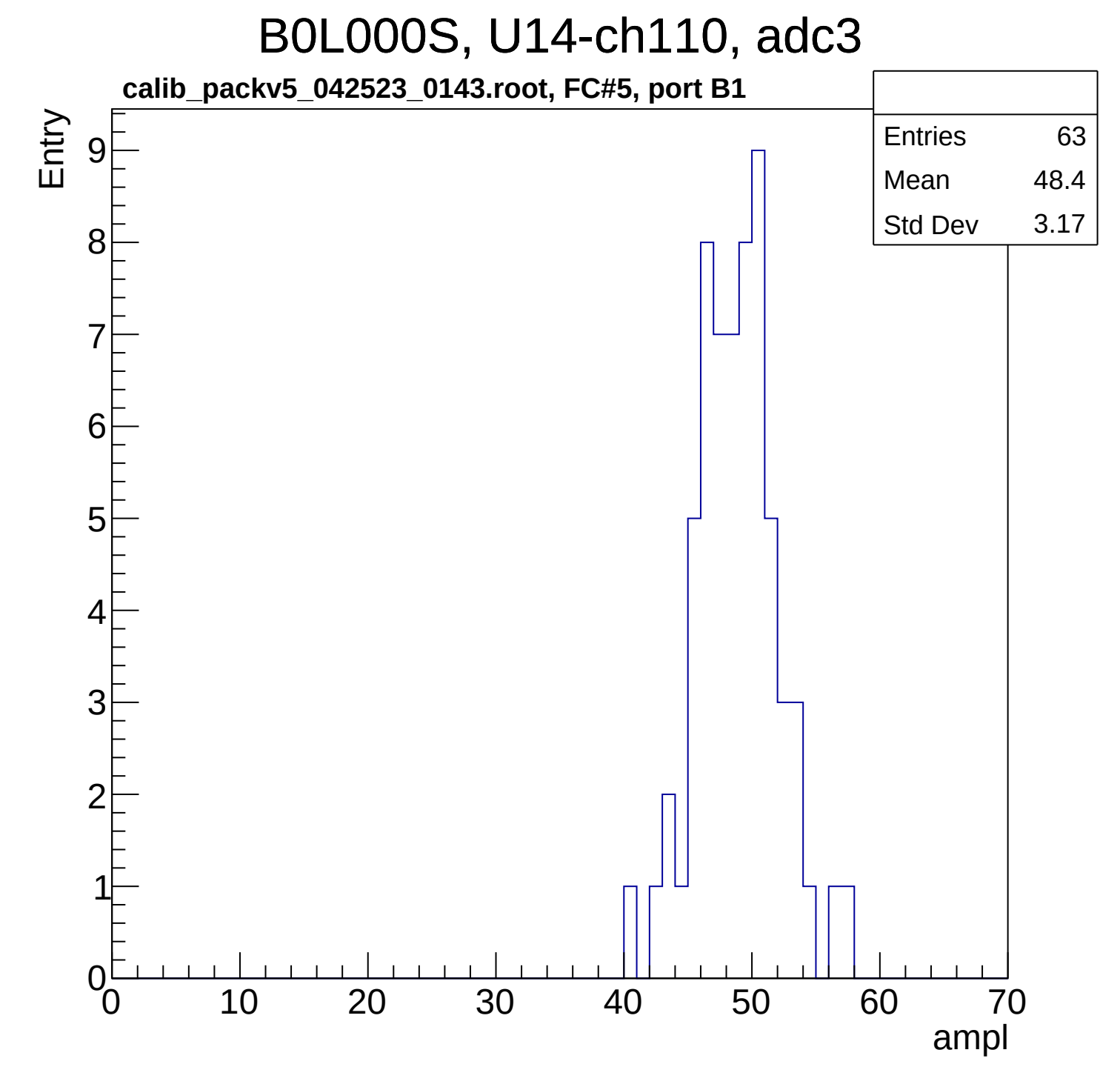
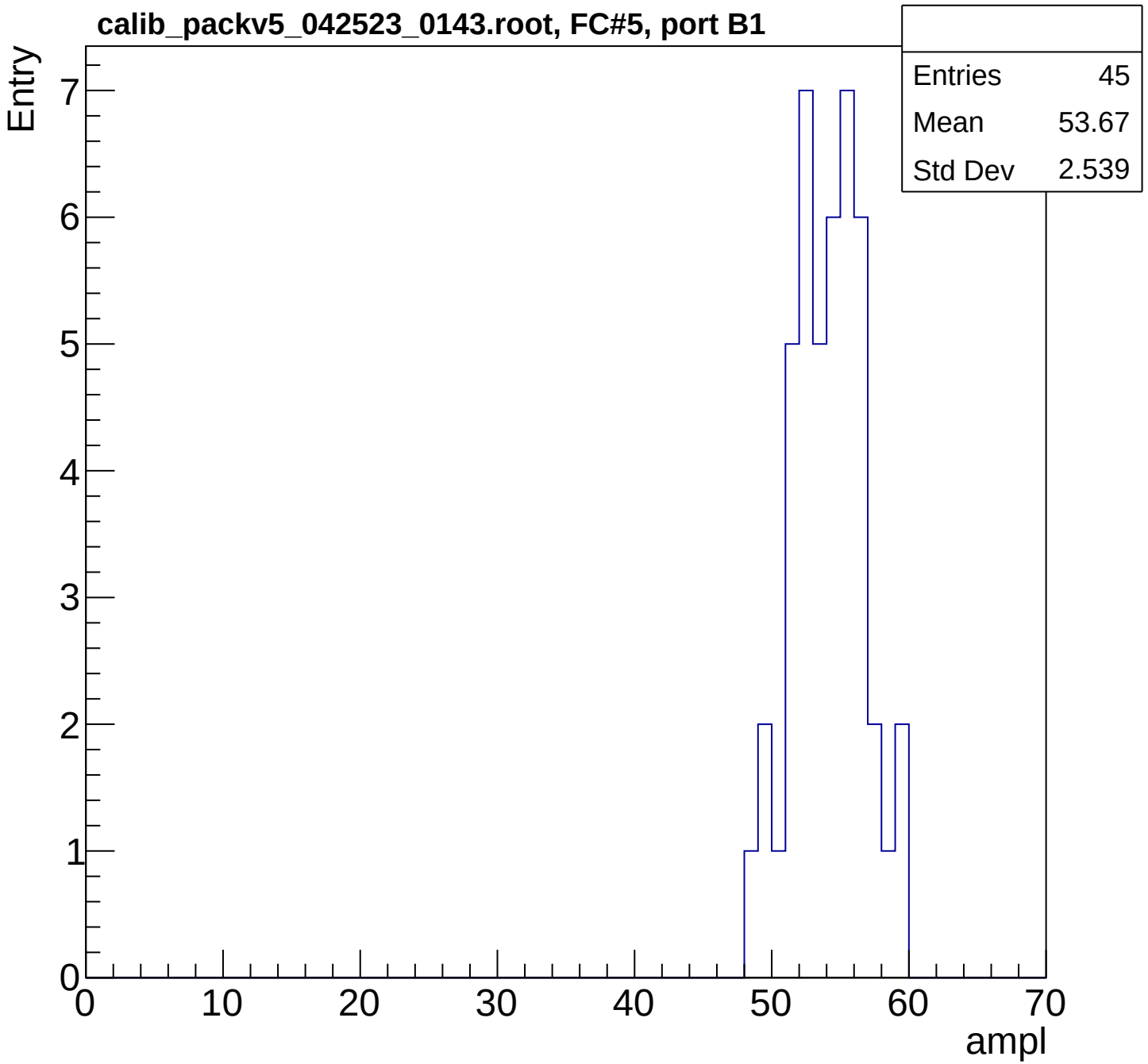
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 48.4
Std Dev 3.17
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 53.67
Std Dev 2.539
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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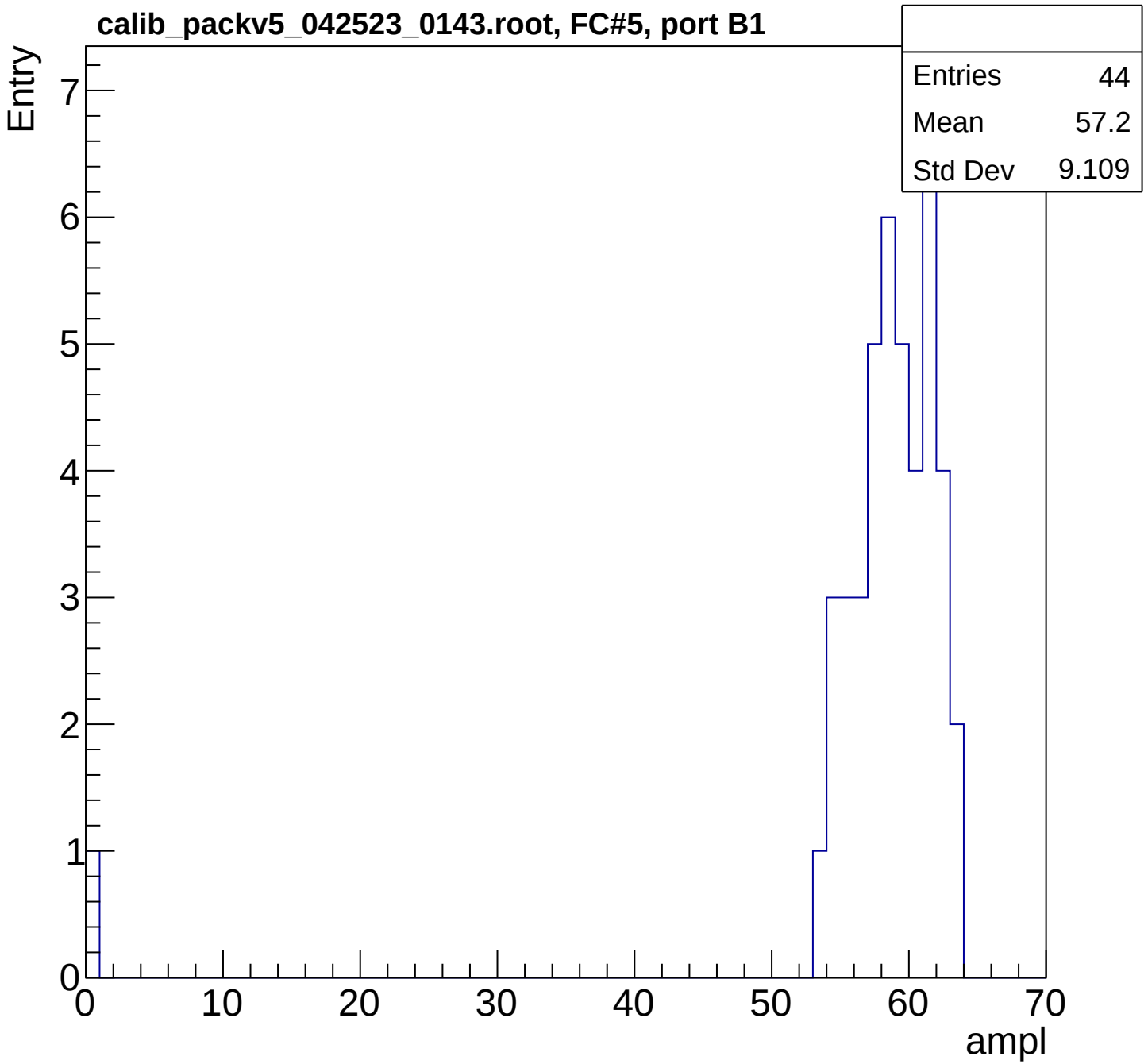
B0L000S, U14-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 57.2
Std Dev 9.109
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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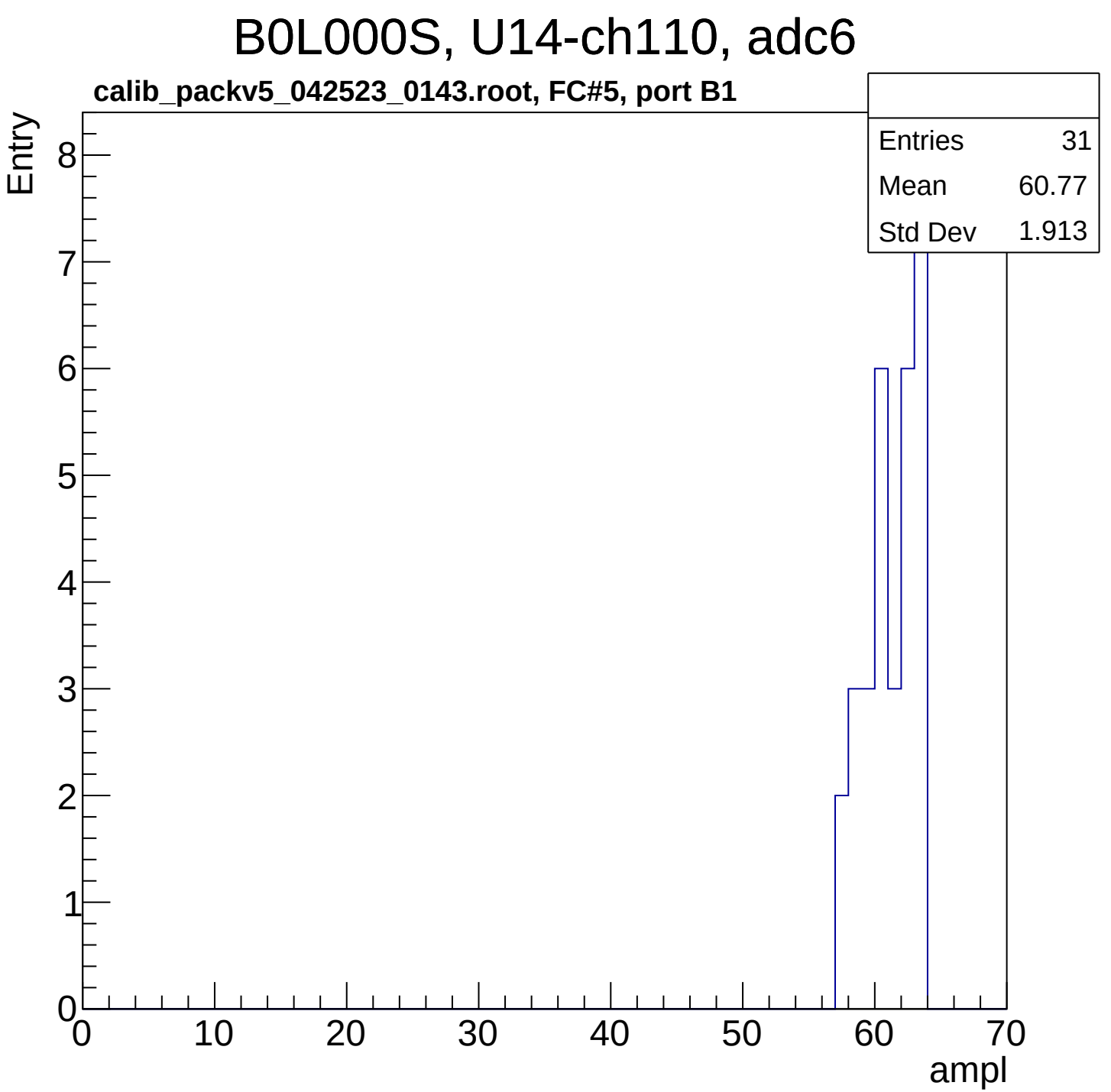
B0L000S, U14-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 60.77
Std Dev 1.913
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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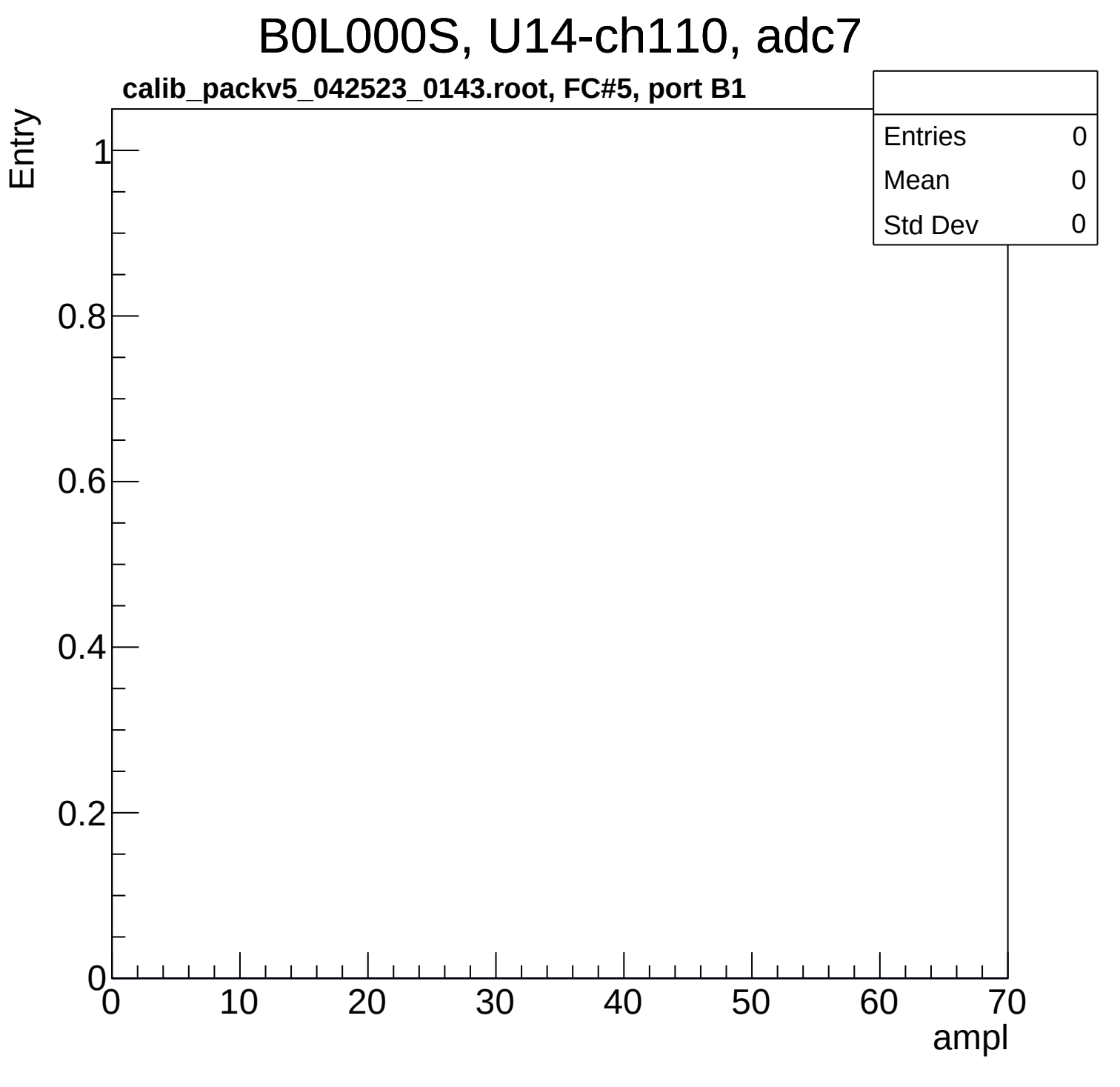
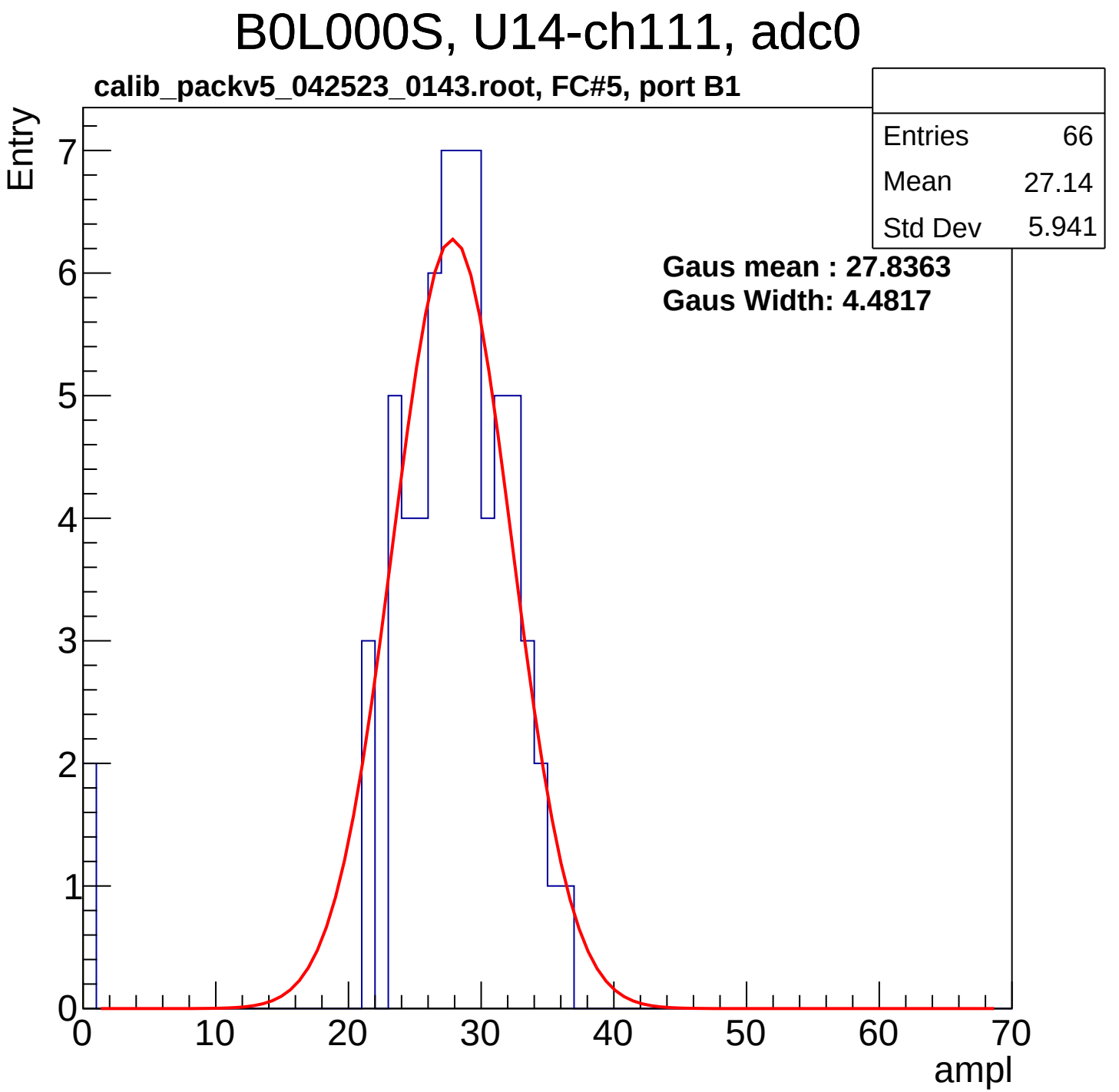
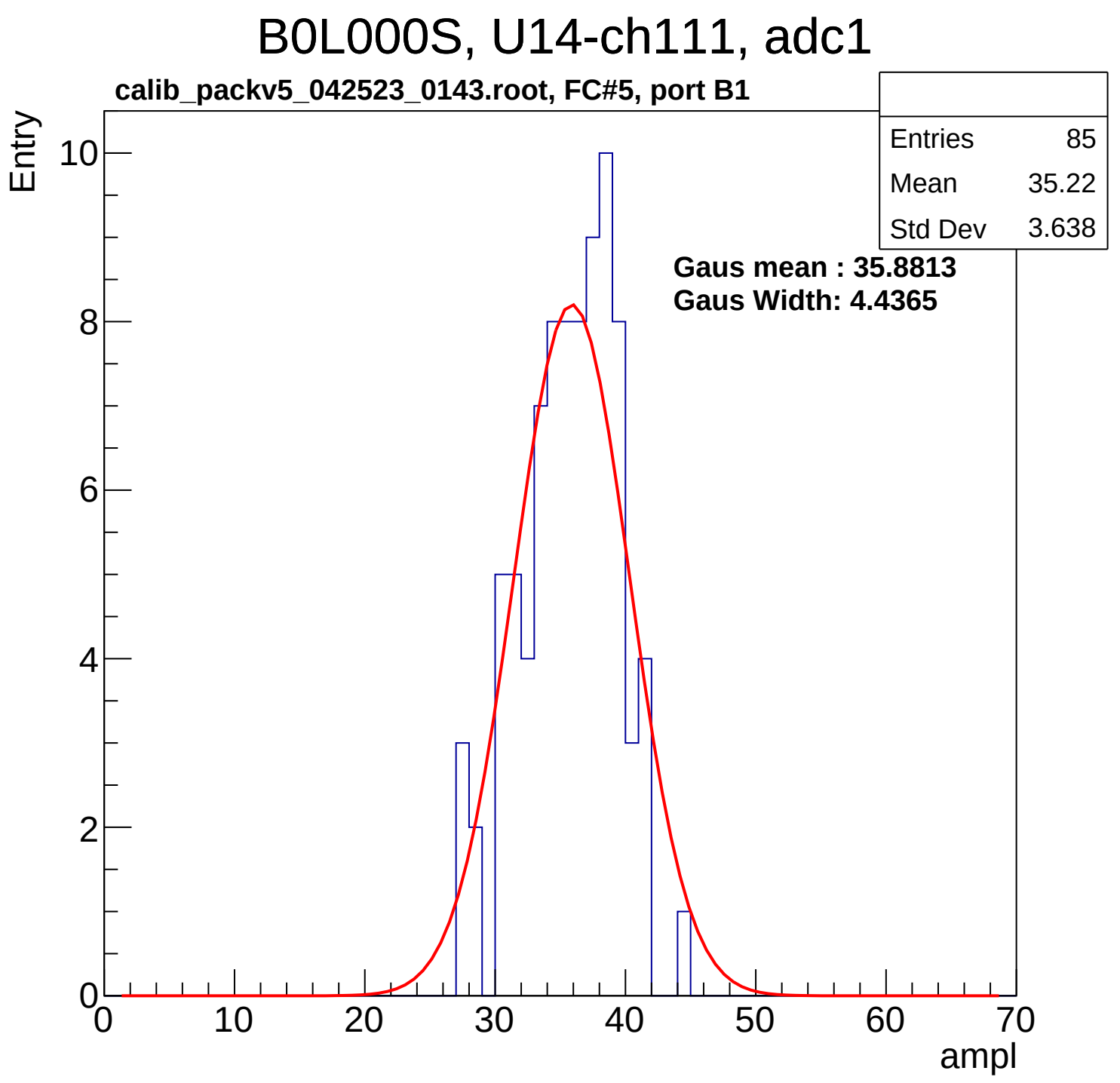
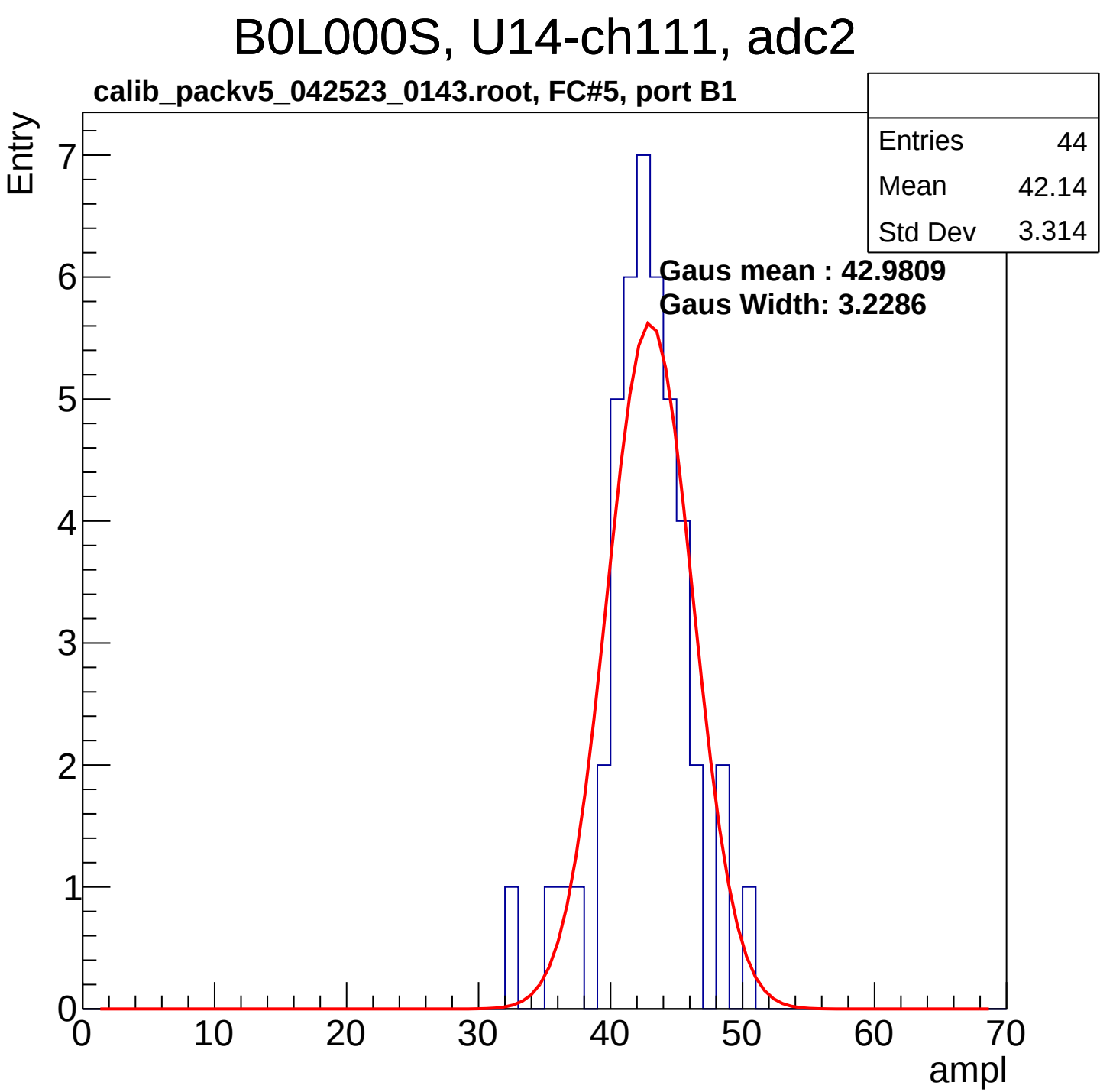

B0L000S, U14-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 27.14
Std Dev 5.941
Gaus mean : 27.8363
Gaus Width: 4.4817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 35.22
Std Dev 3.638
Gaus mean : 35.8813
Gaus Width: 4.4365
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 42.14
Std Dev 3.314
Gaus mean : 42.9809
Gaus Width: 3.2286
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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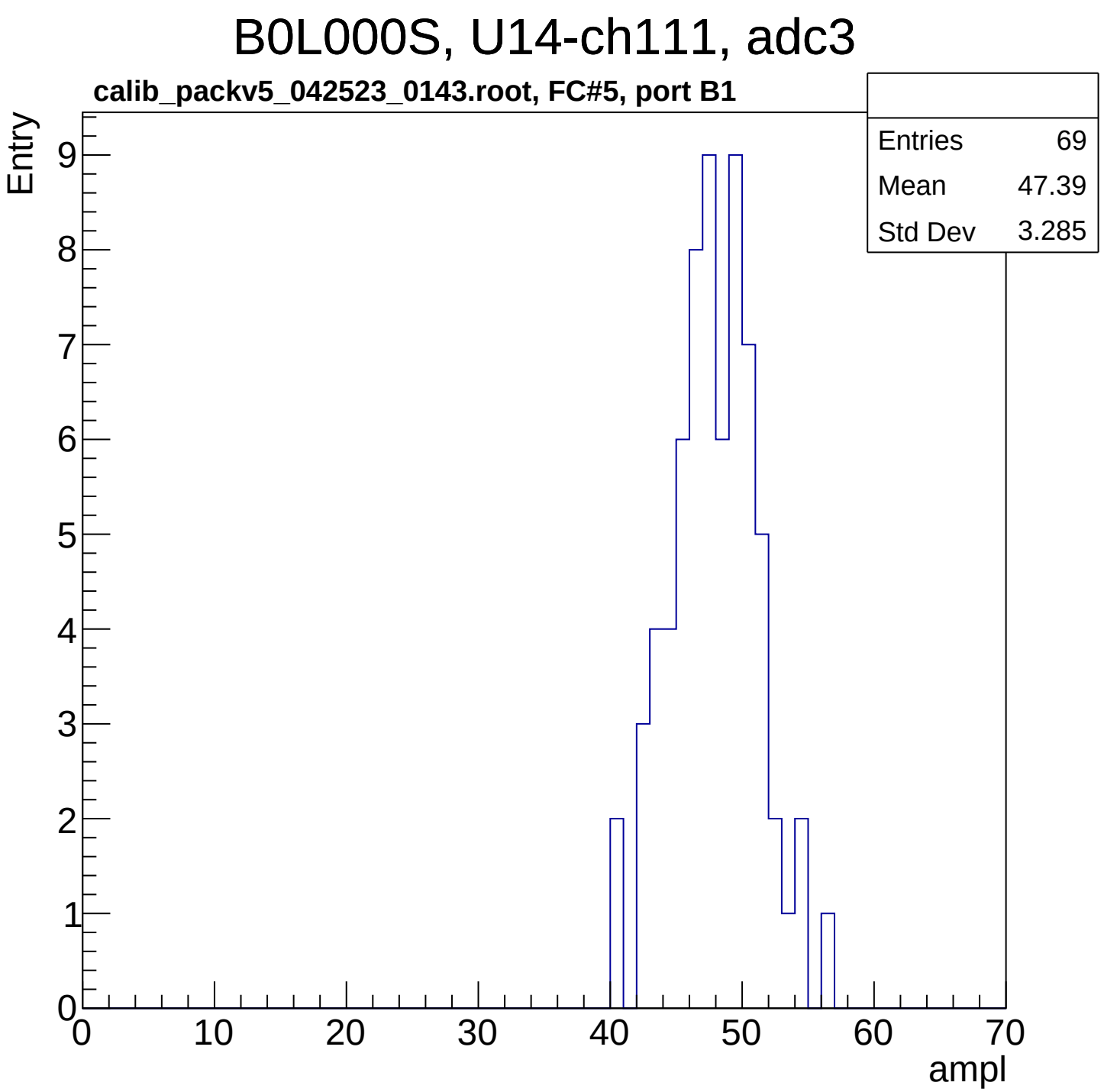
B0L000S, U14-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 47.39
Std Dev 3.285
Entry
ampl
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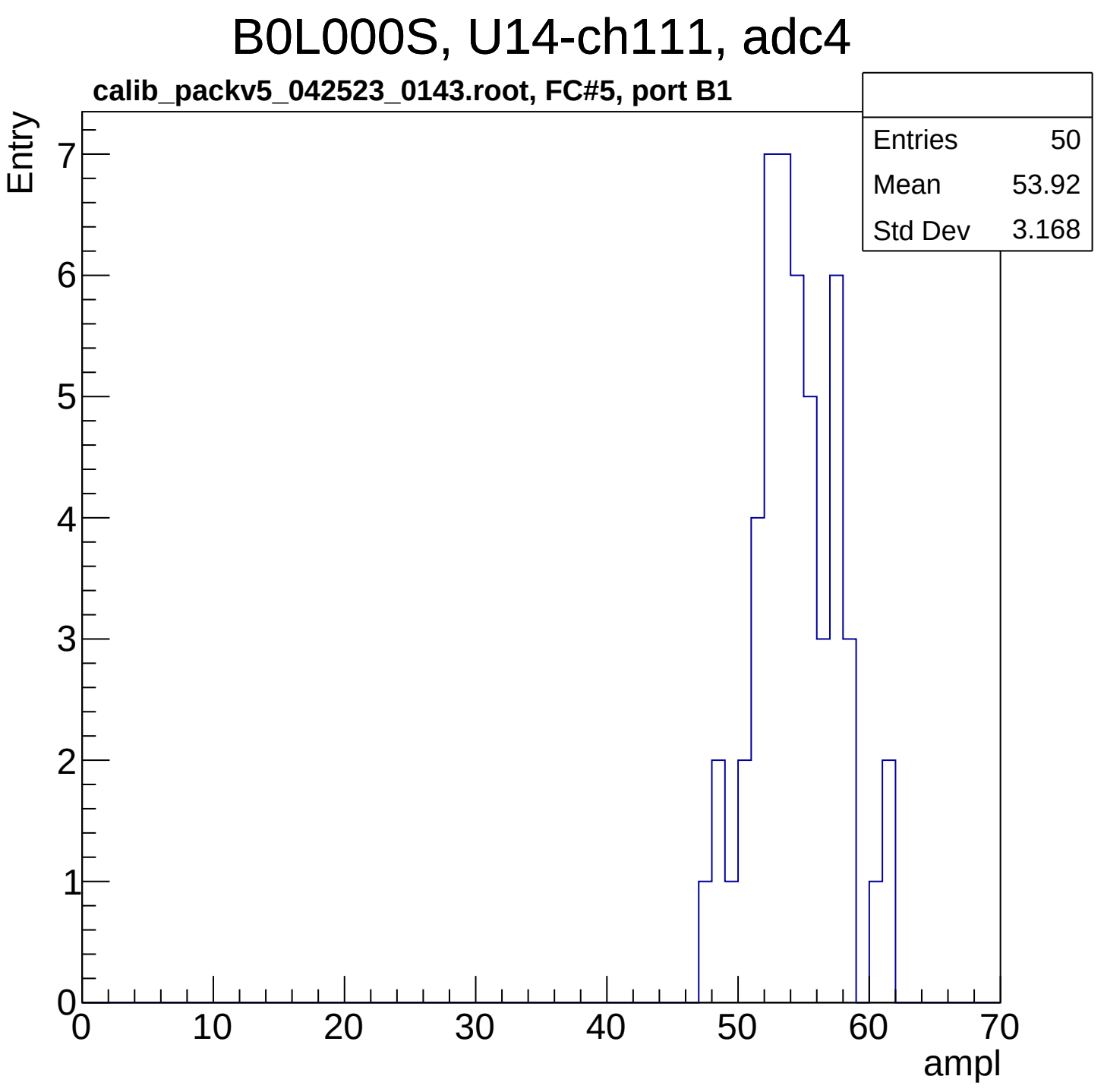
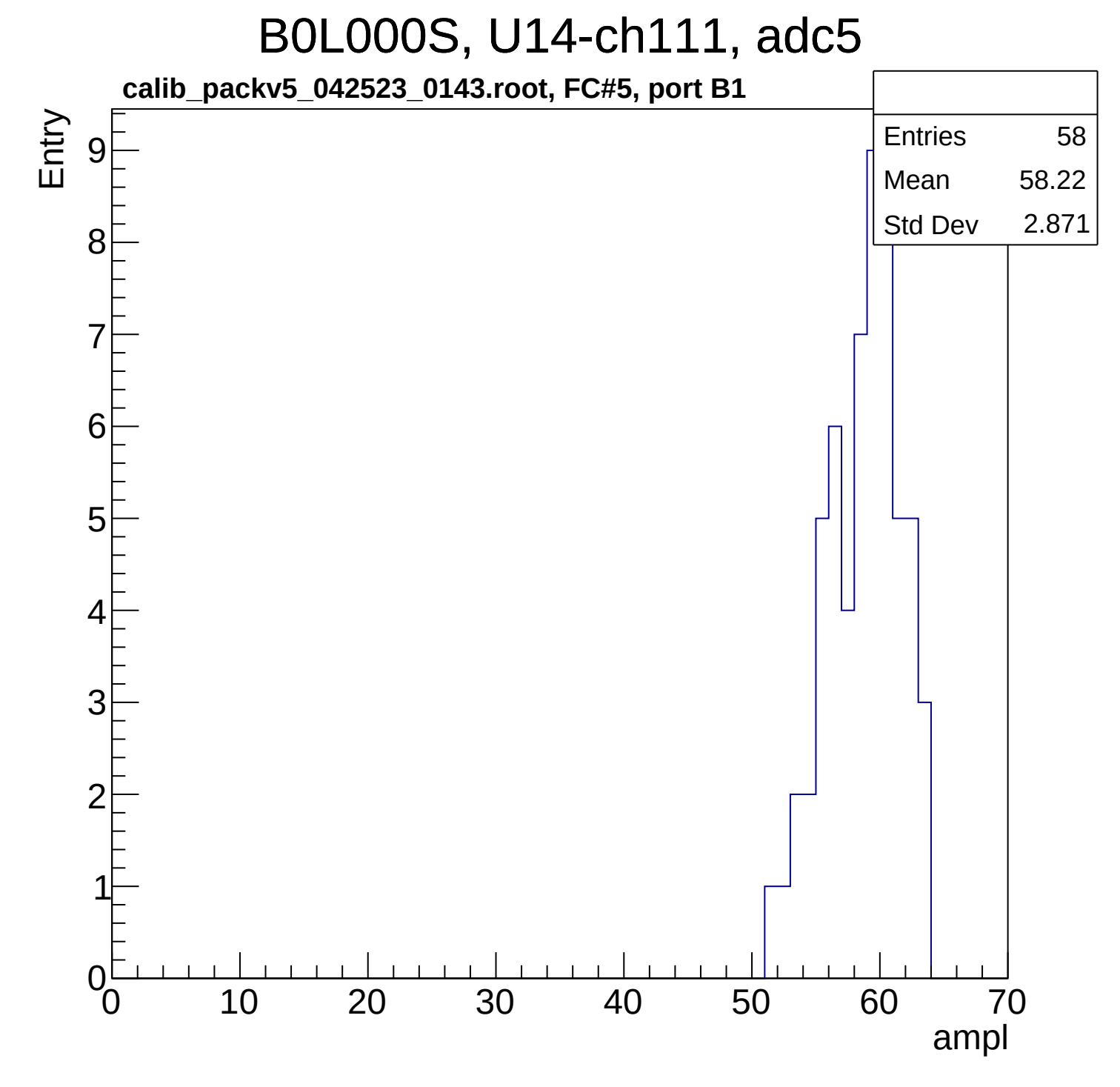
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 53.92
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 58.22
Std Dev 2.871
Entry
ampl
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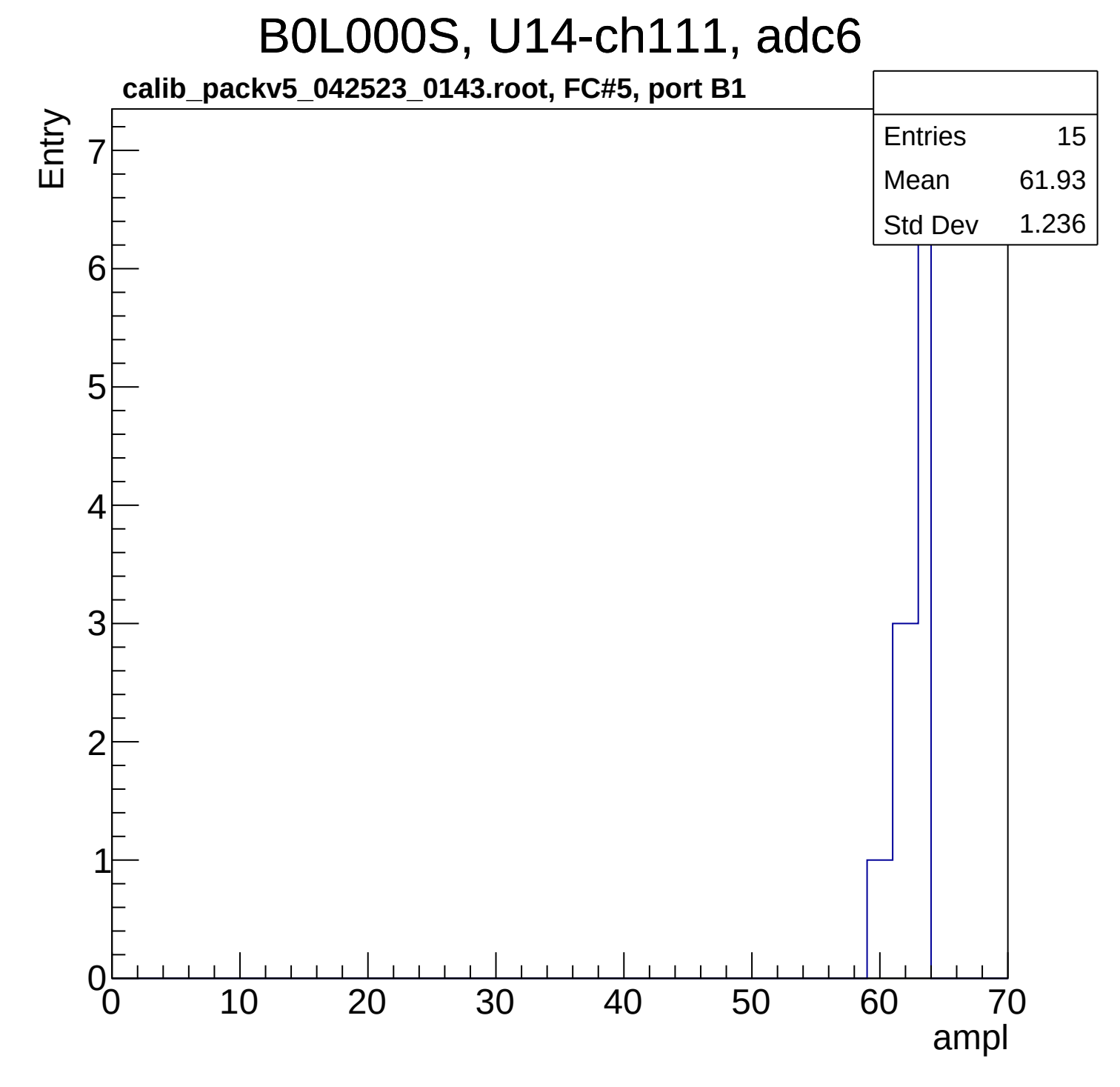
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.93
Std Dev 1.236
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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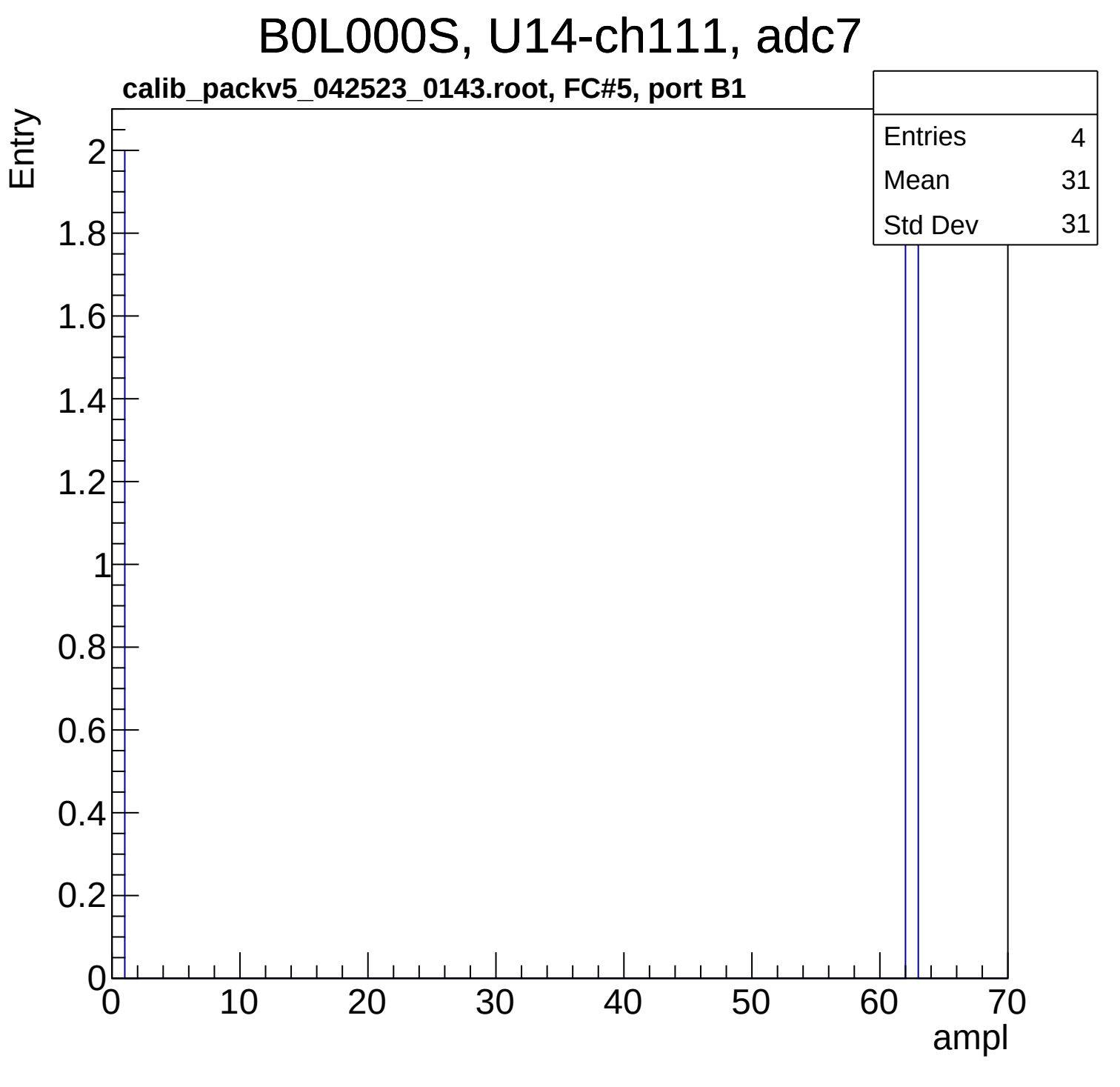
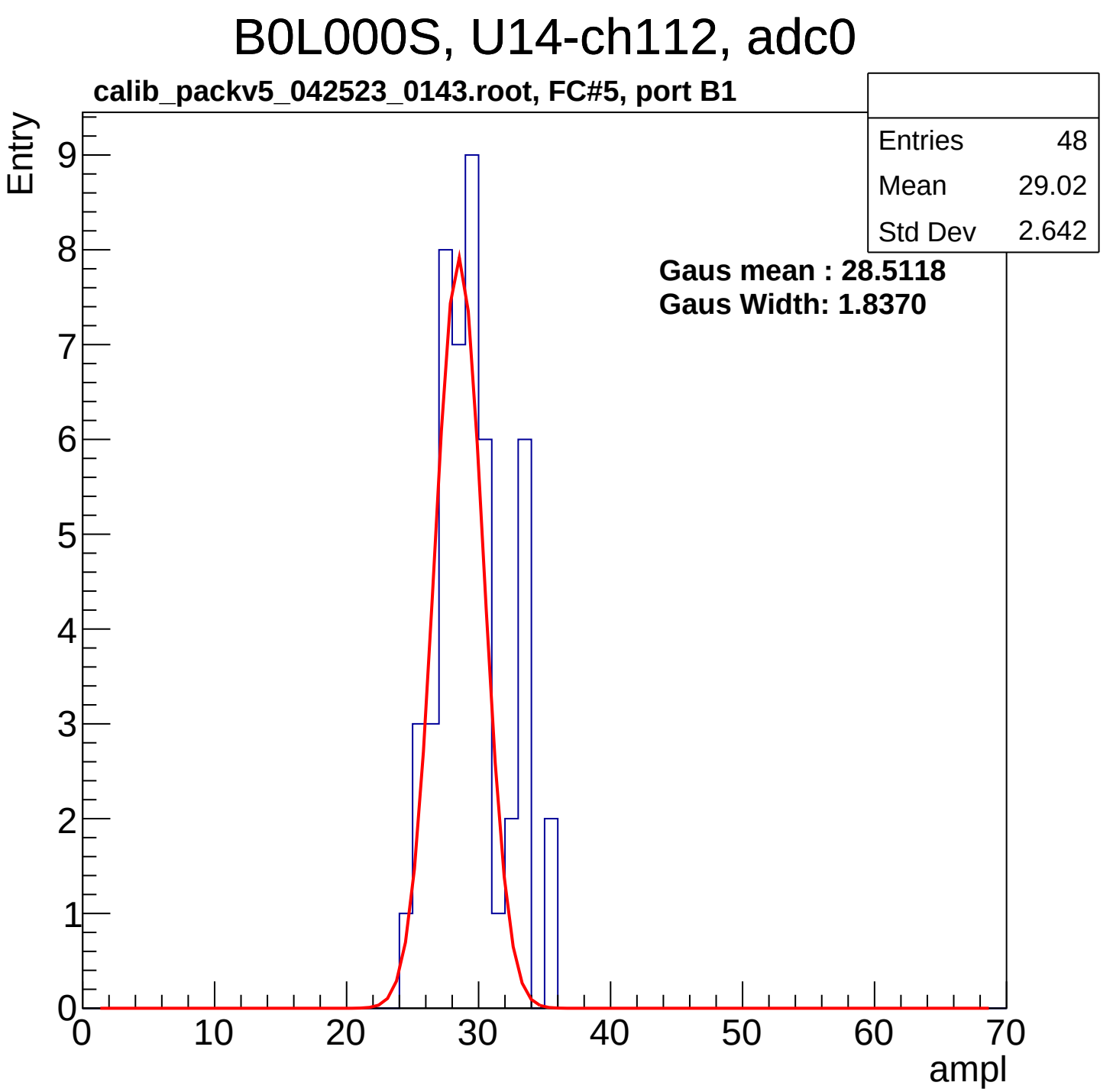
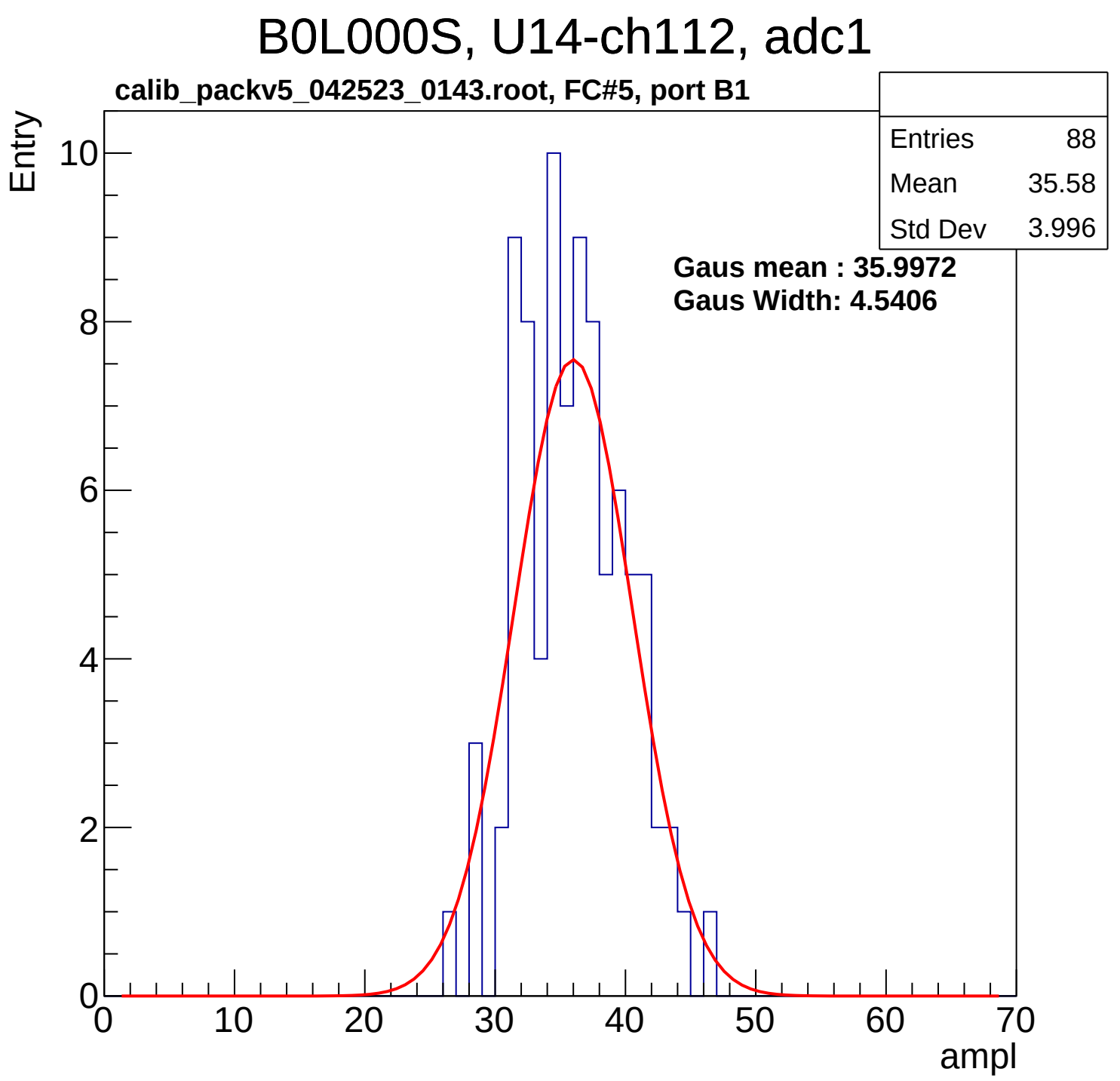

B0L000S, U14-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 31
Std Dev 31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 29.02
Std Dev 2.642
Gaus mean : 28.5118
Gaus Width: 1.8370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 35.58
Std Dev 3.996
Gaus mean : 35.9972
Gaus Width: 4.5406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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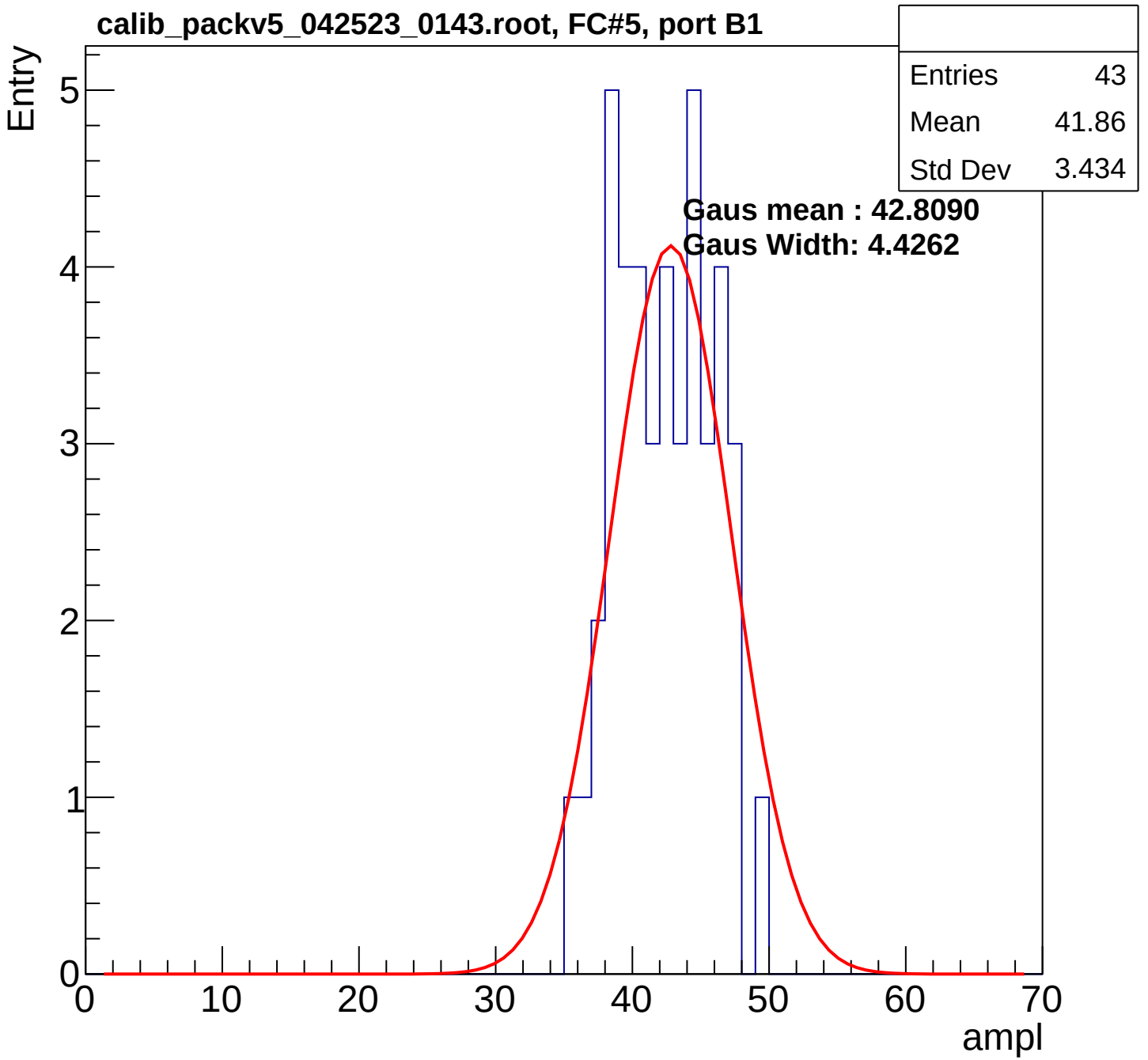
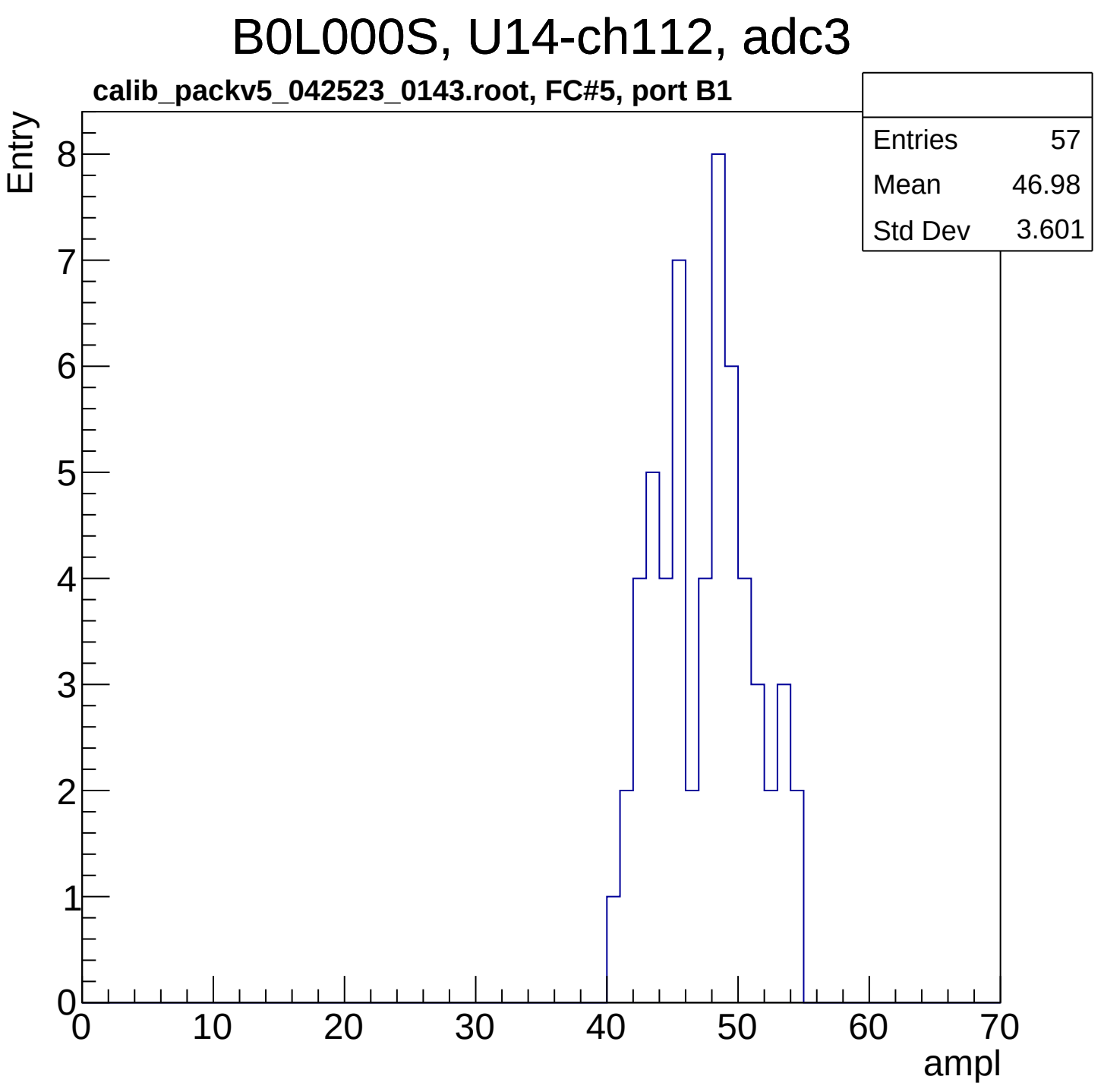
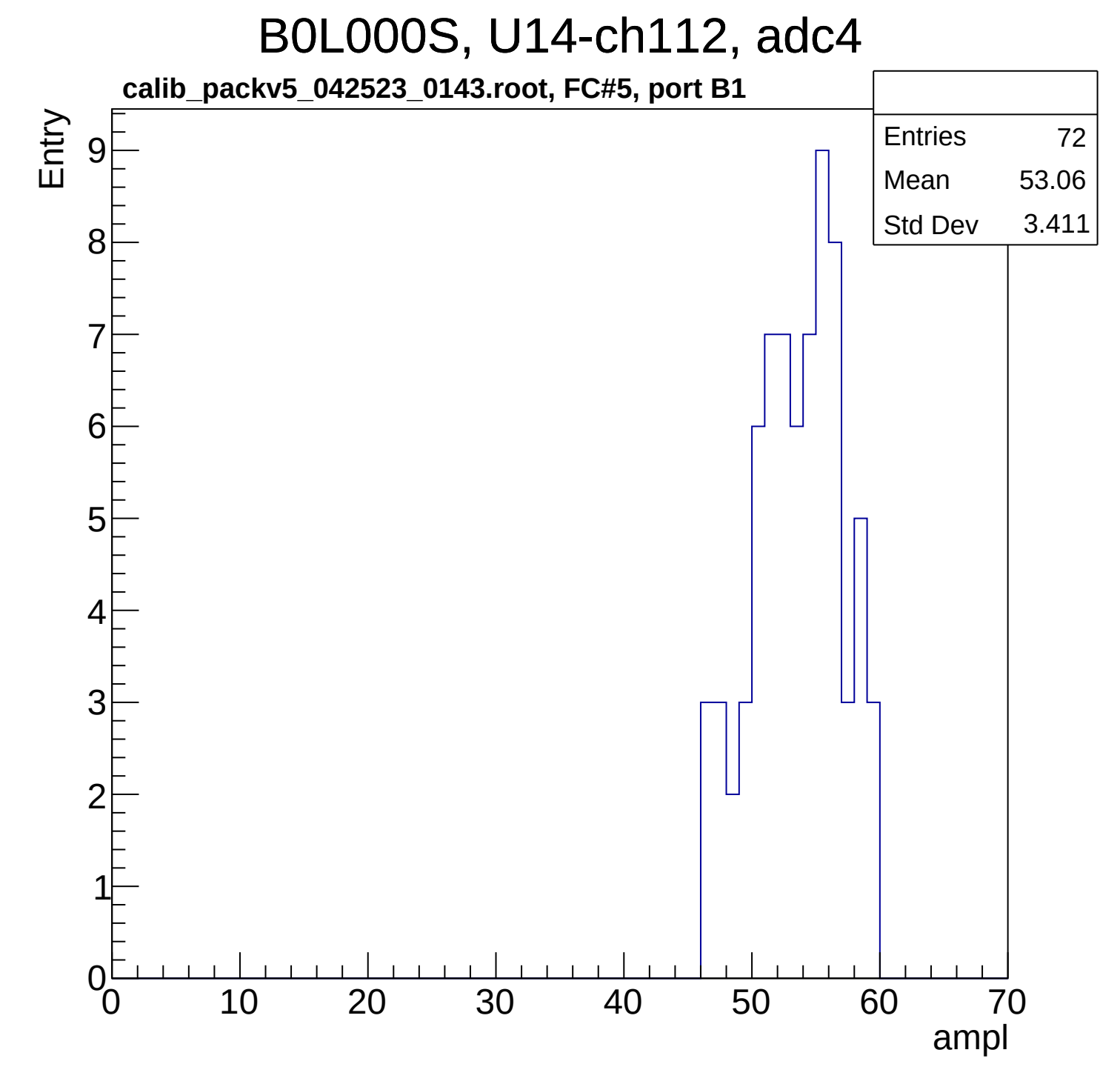

B0L000S, U14-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 41.86
Std Dev 3.434
Gaus mean : 42.8090
Gaus Width: 4.4262
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 46.98
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 53.06
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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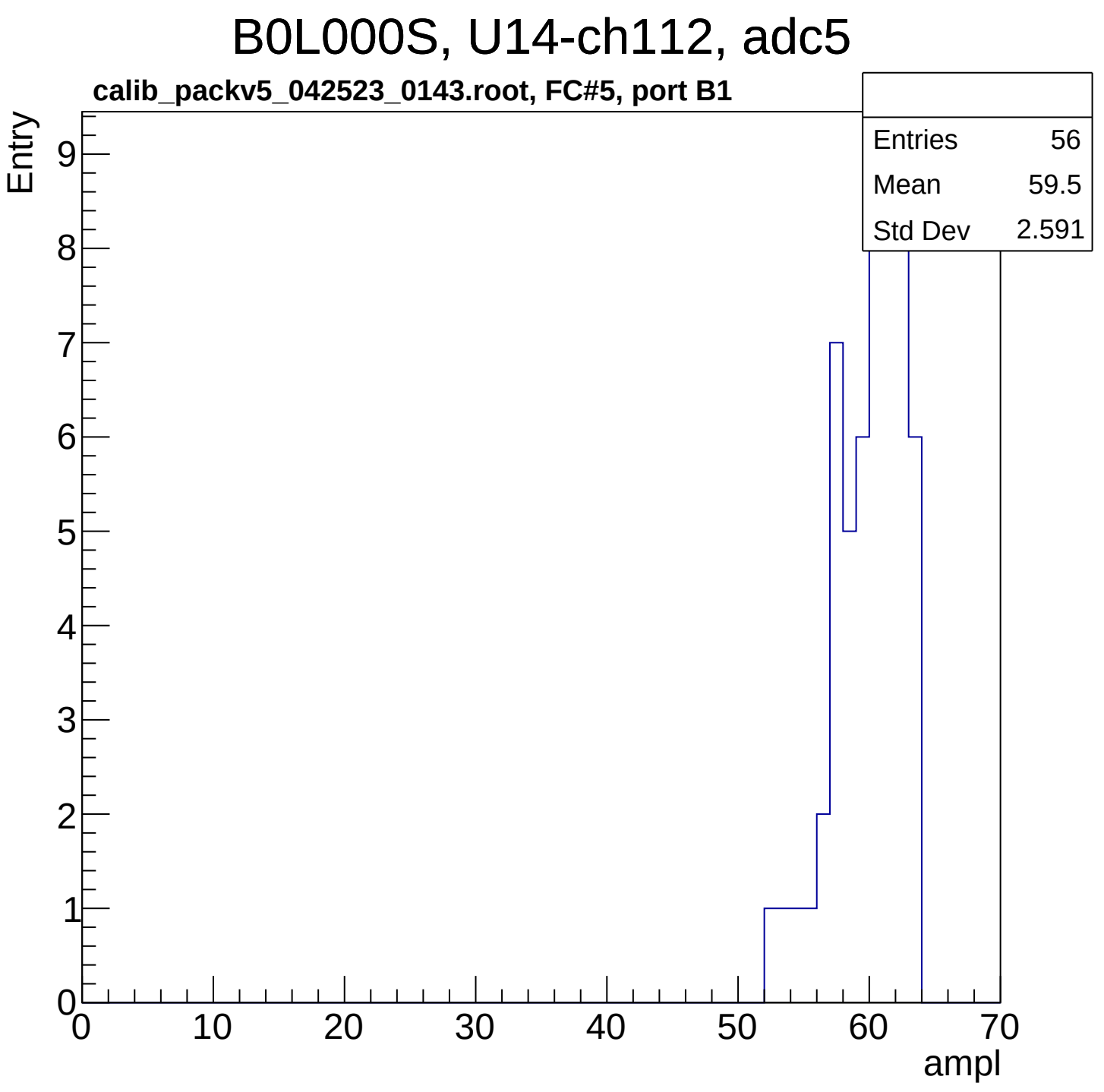
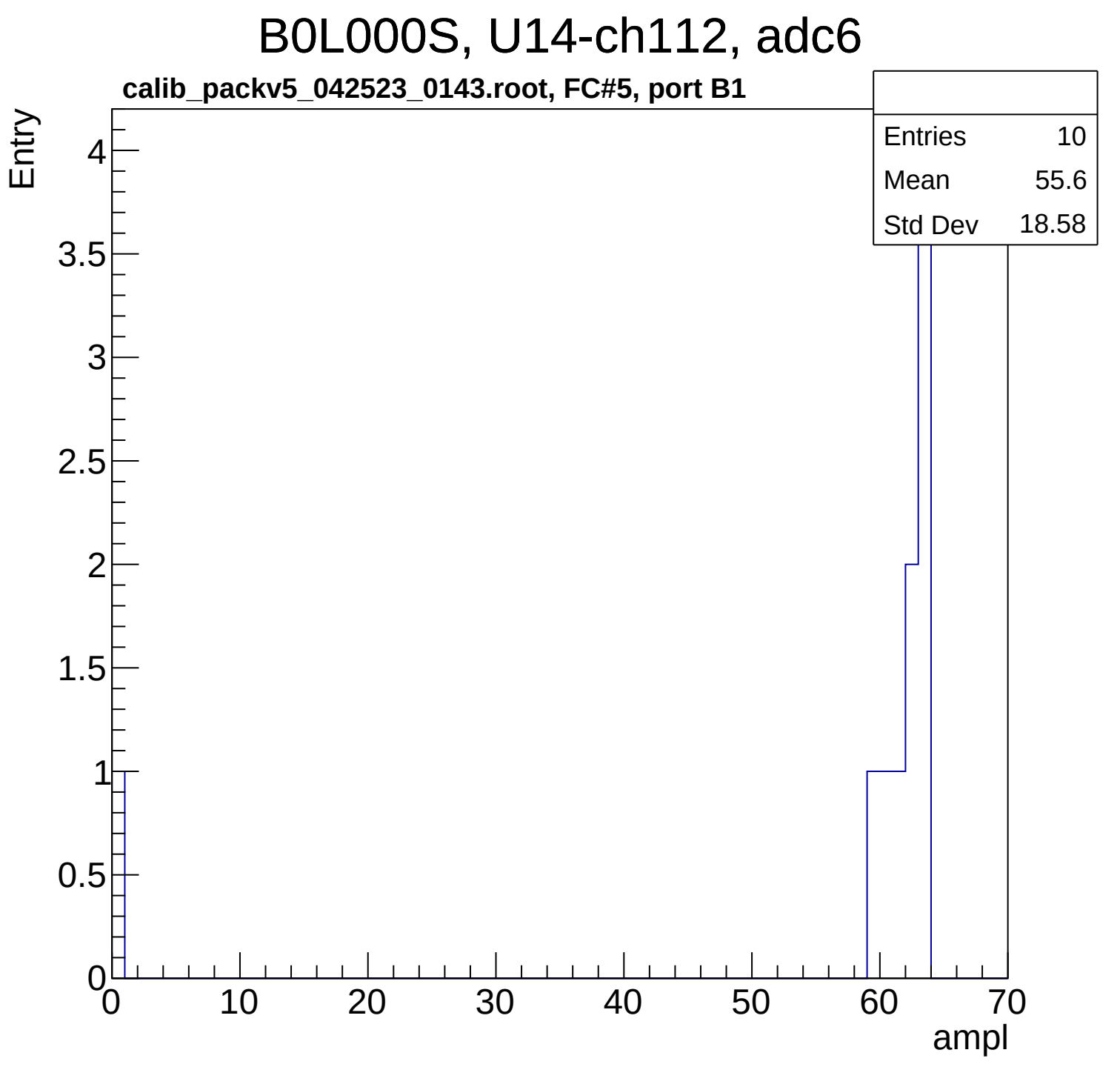
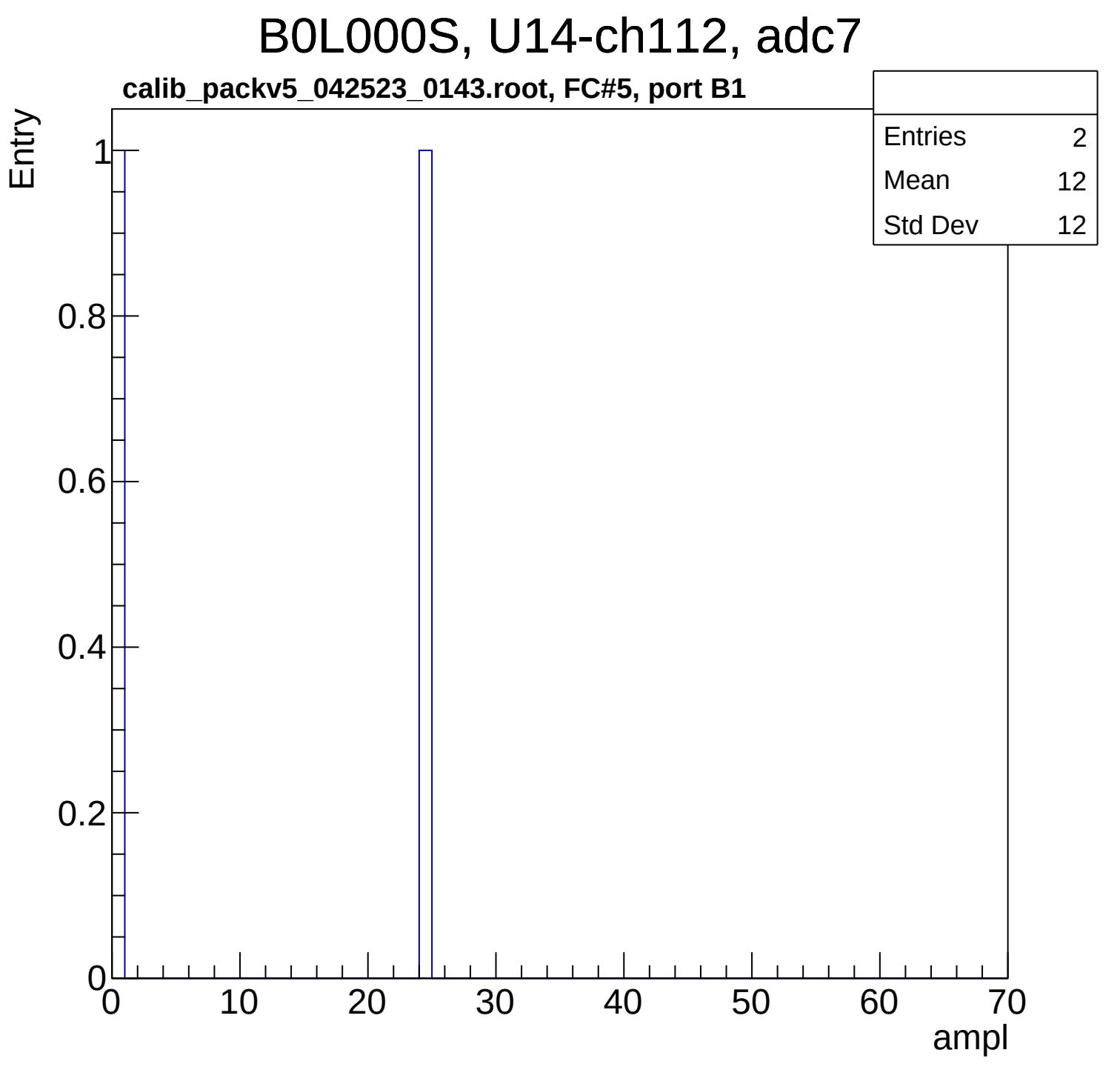
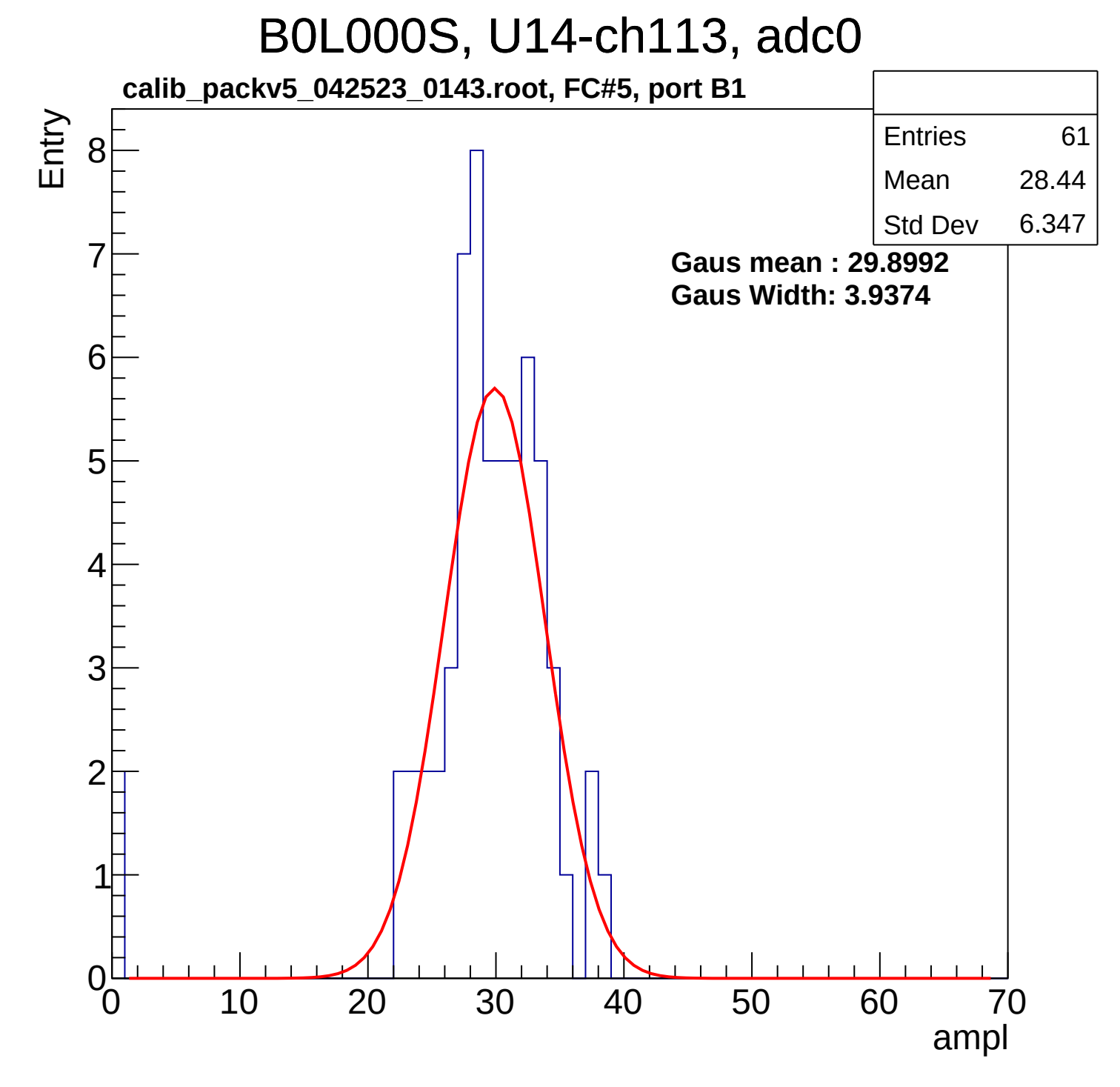
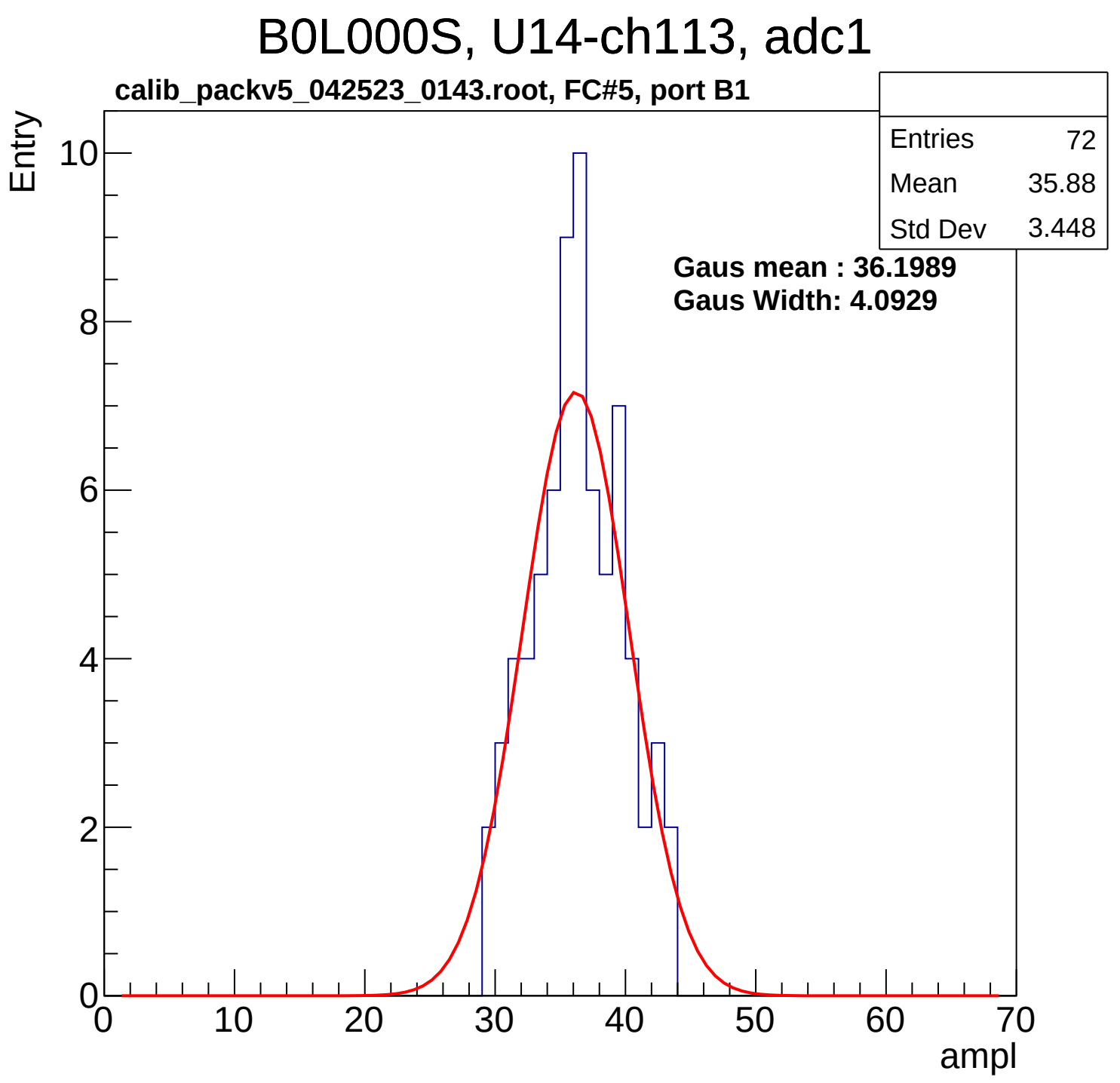
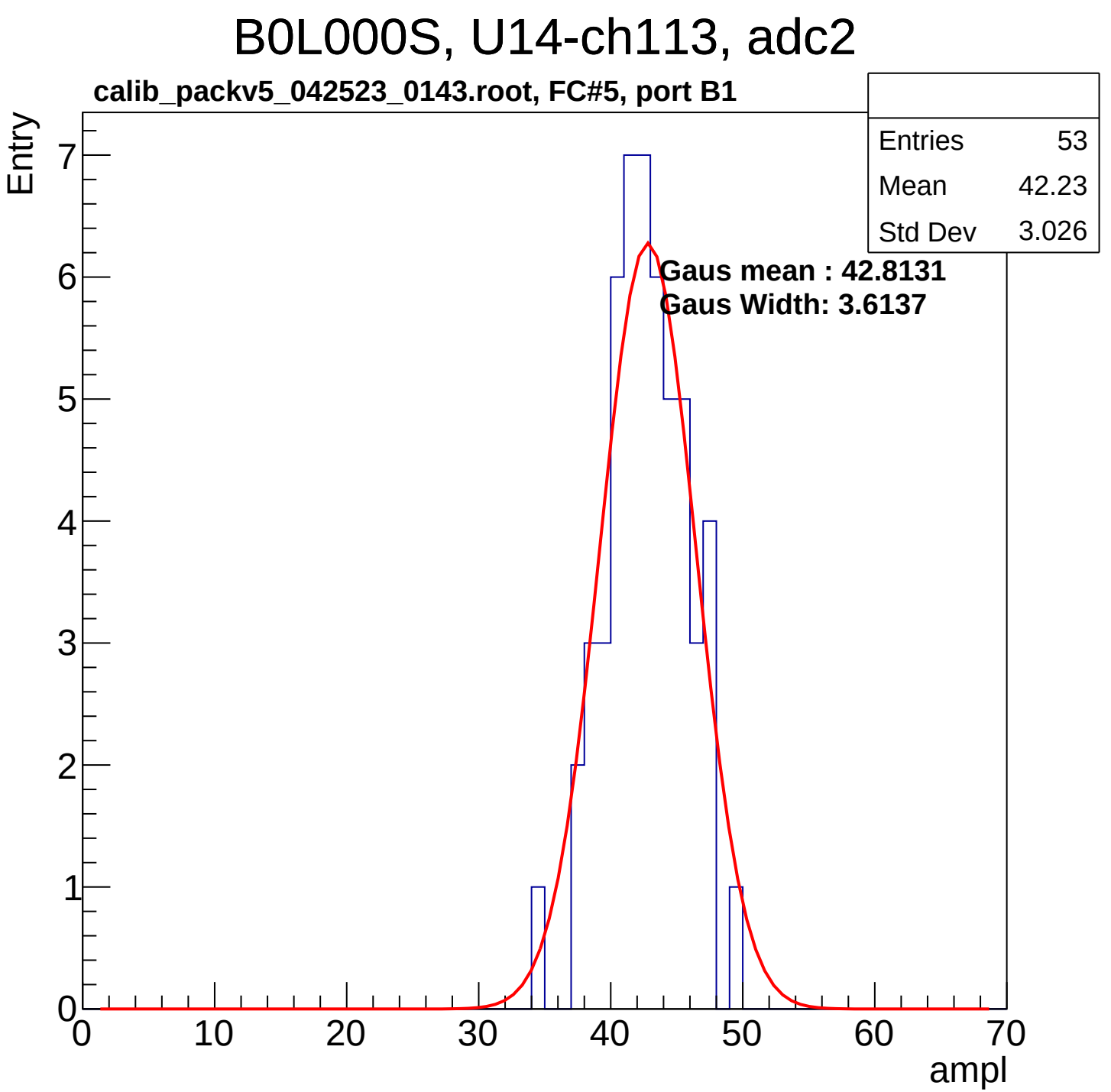

B0L000S, U14-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 59.5
Std Dev 2.591
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

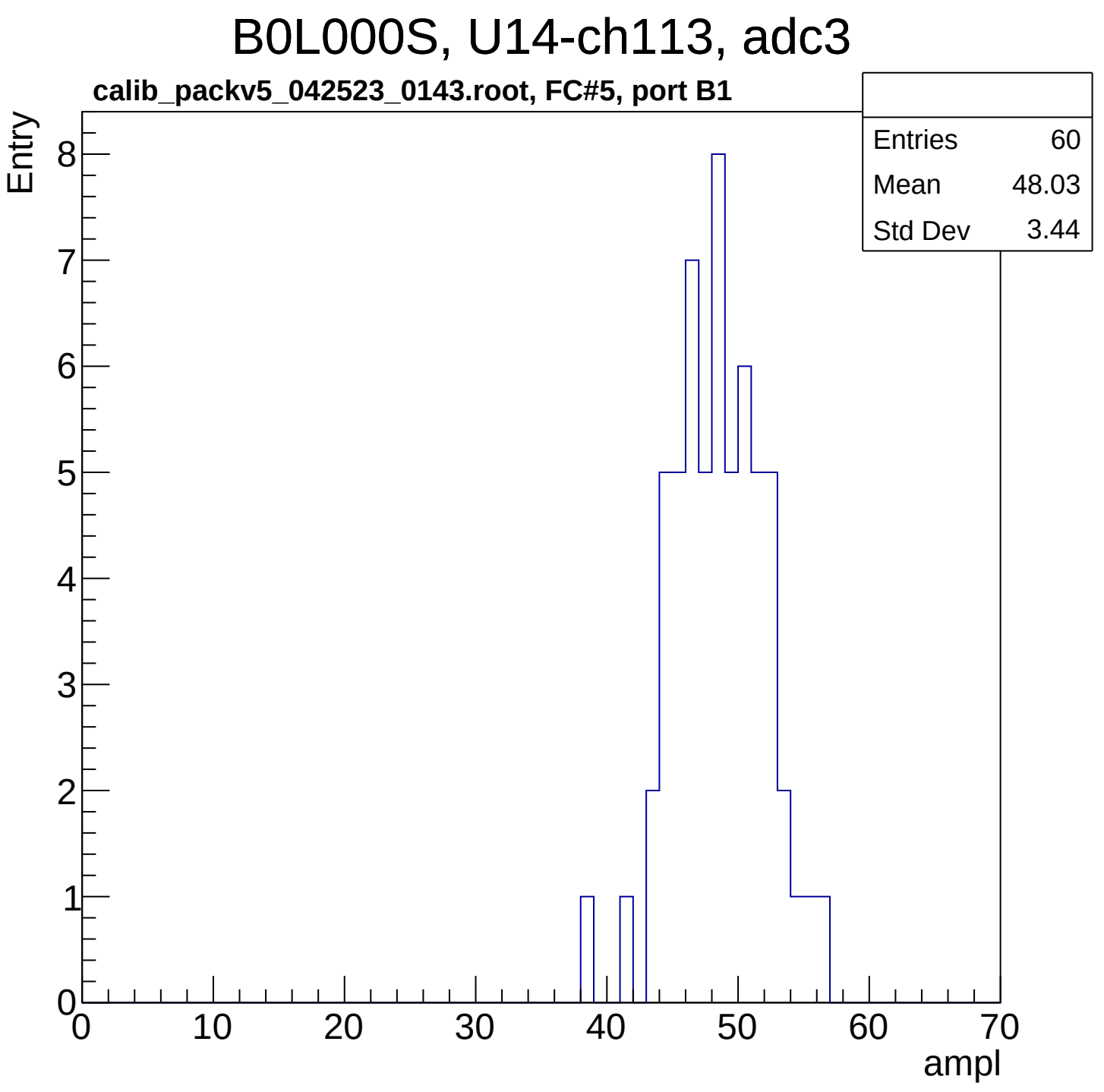

B0L000S, U14-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.6
Std Dev 18.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

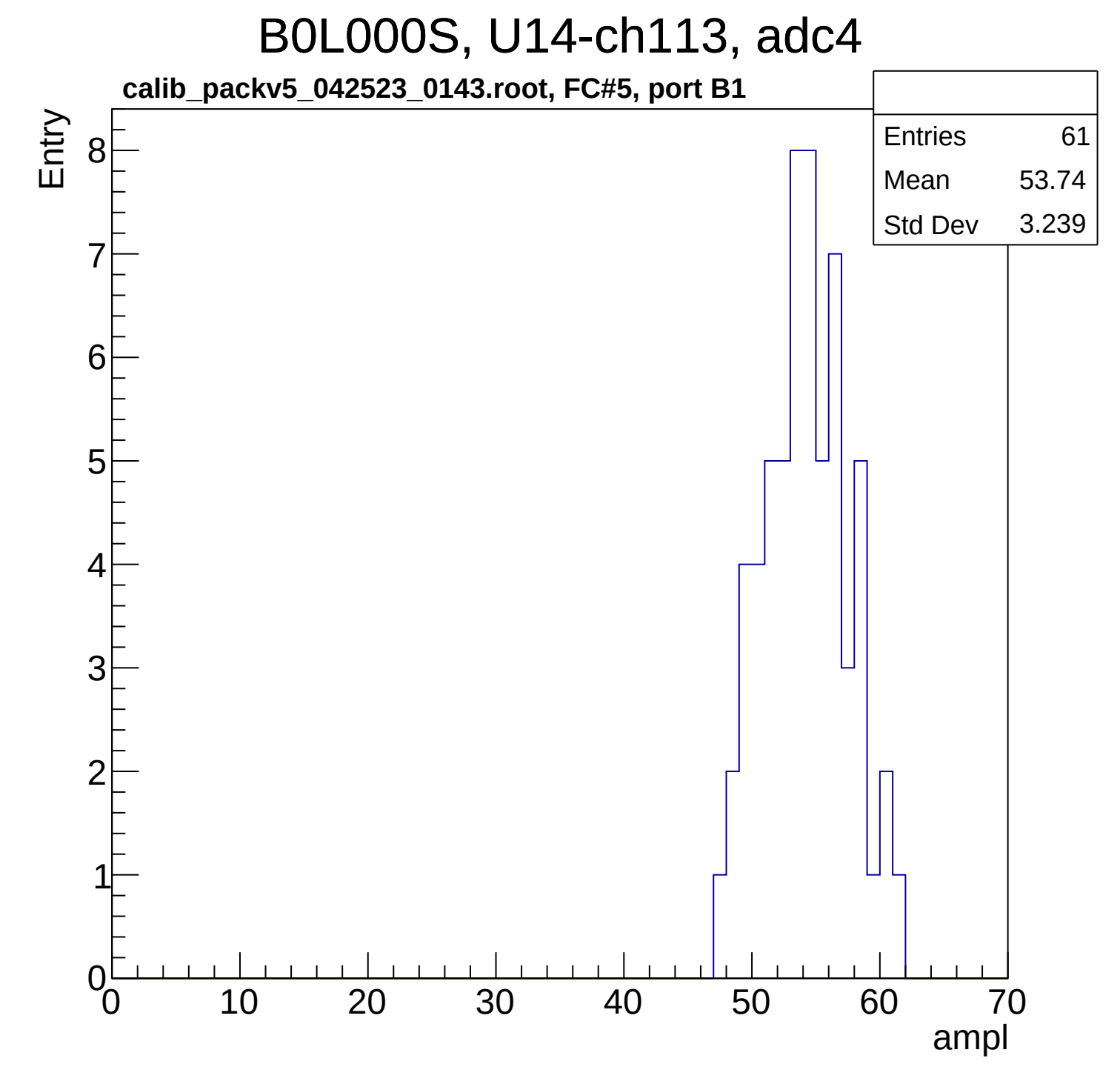

B0L000S, U14-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

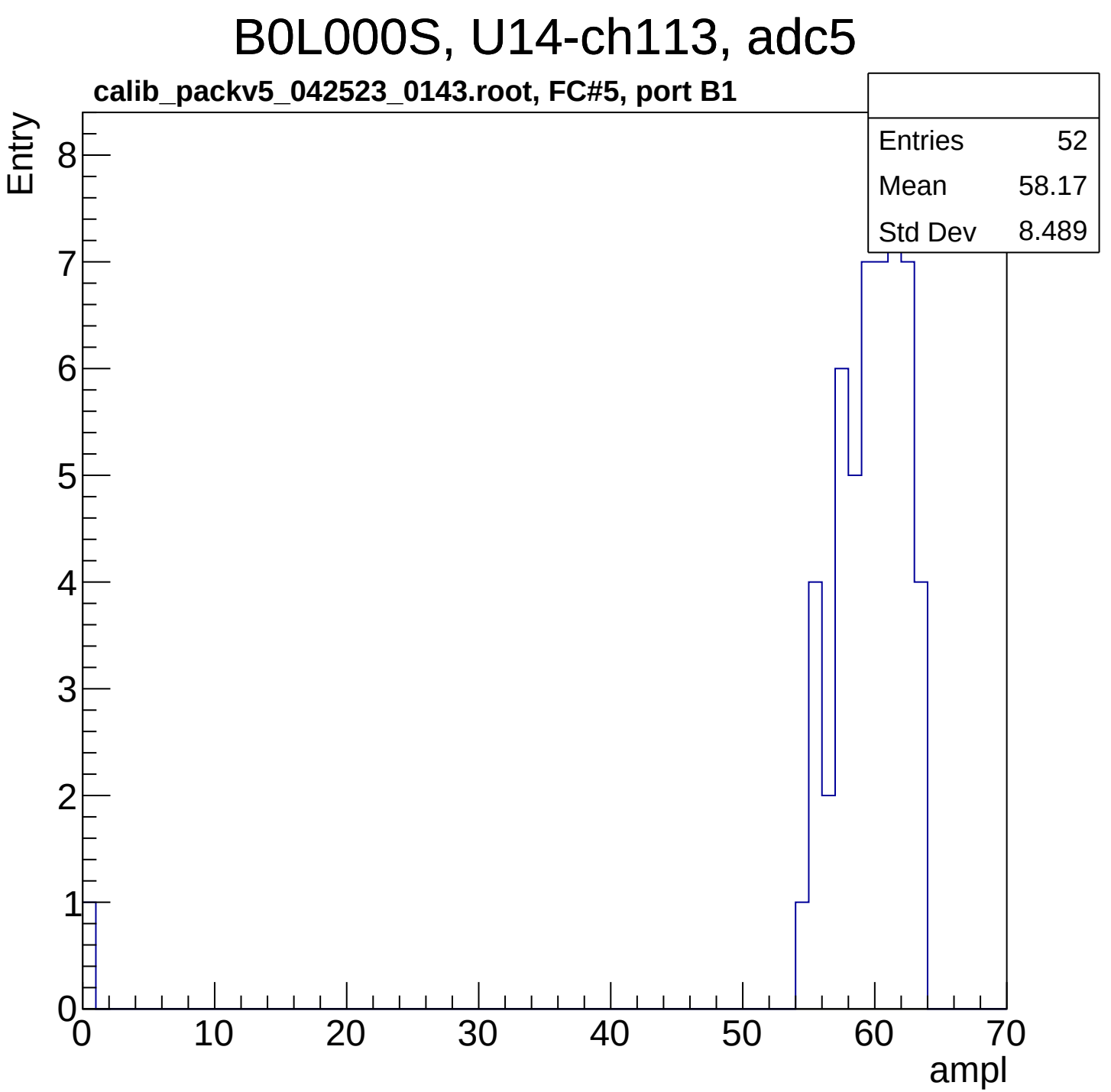

B0L000S, U14-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28.44
Std Dev 6.347
Gaus mean : 29.8992
Gaus Width: 3.9374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.88
Std Dev 3.448
Gaus mean : 36.1989
Gaus Width: 4.0929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 42.23
Std Dev 3.026
Gaus mean : 42.8131
Gaus Width: 3.6137
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.03
Std Dev 3.44
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 53.74
Std Dev 3.239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.17
Std Dev 8.489
Entry
ampl
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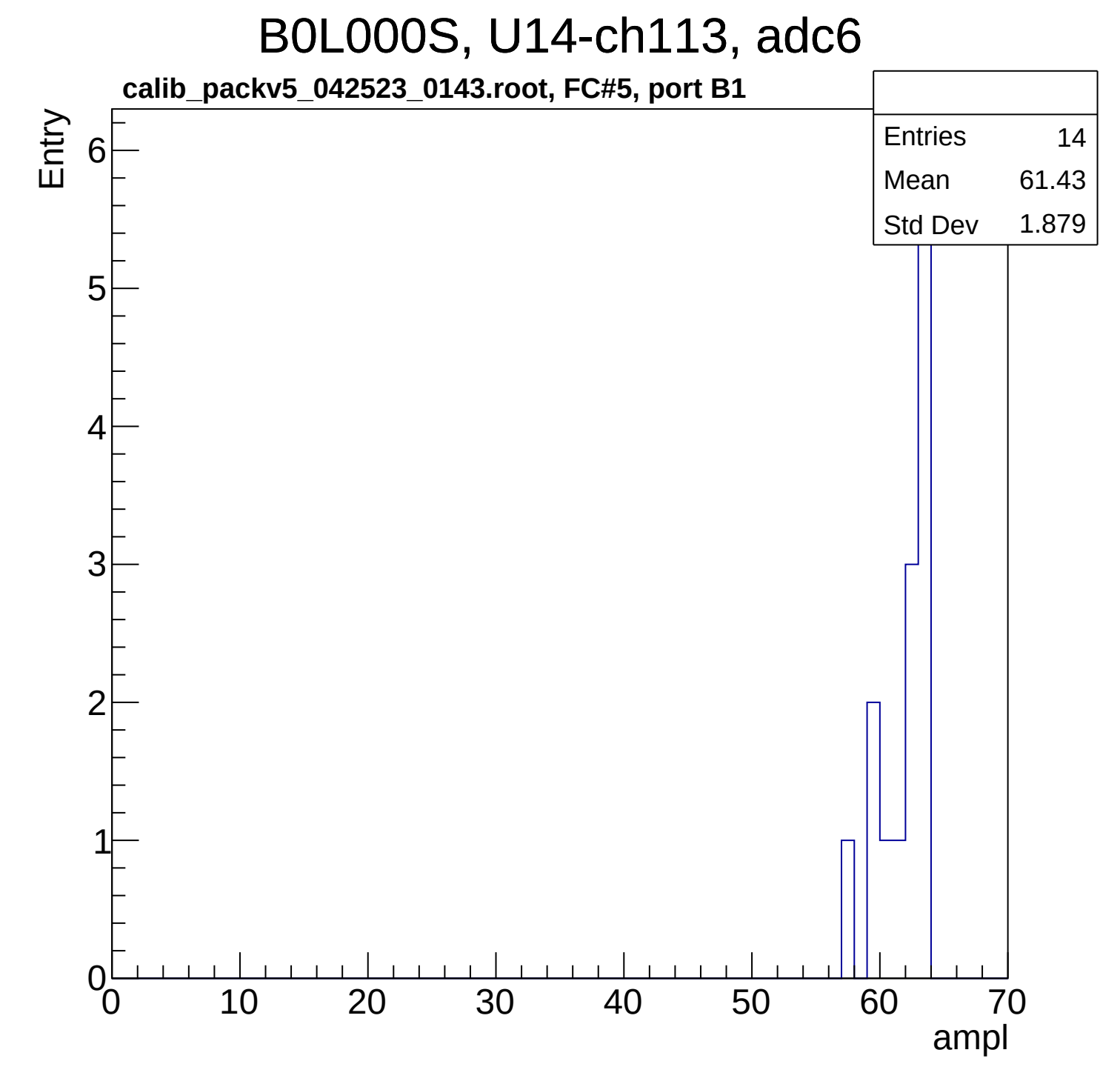
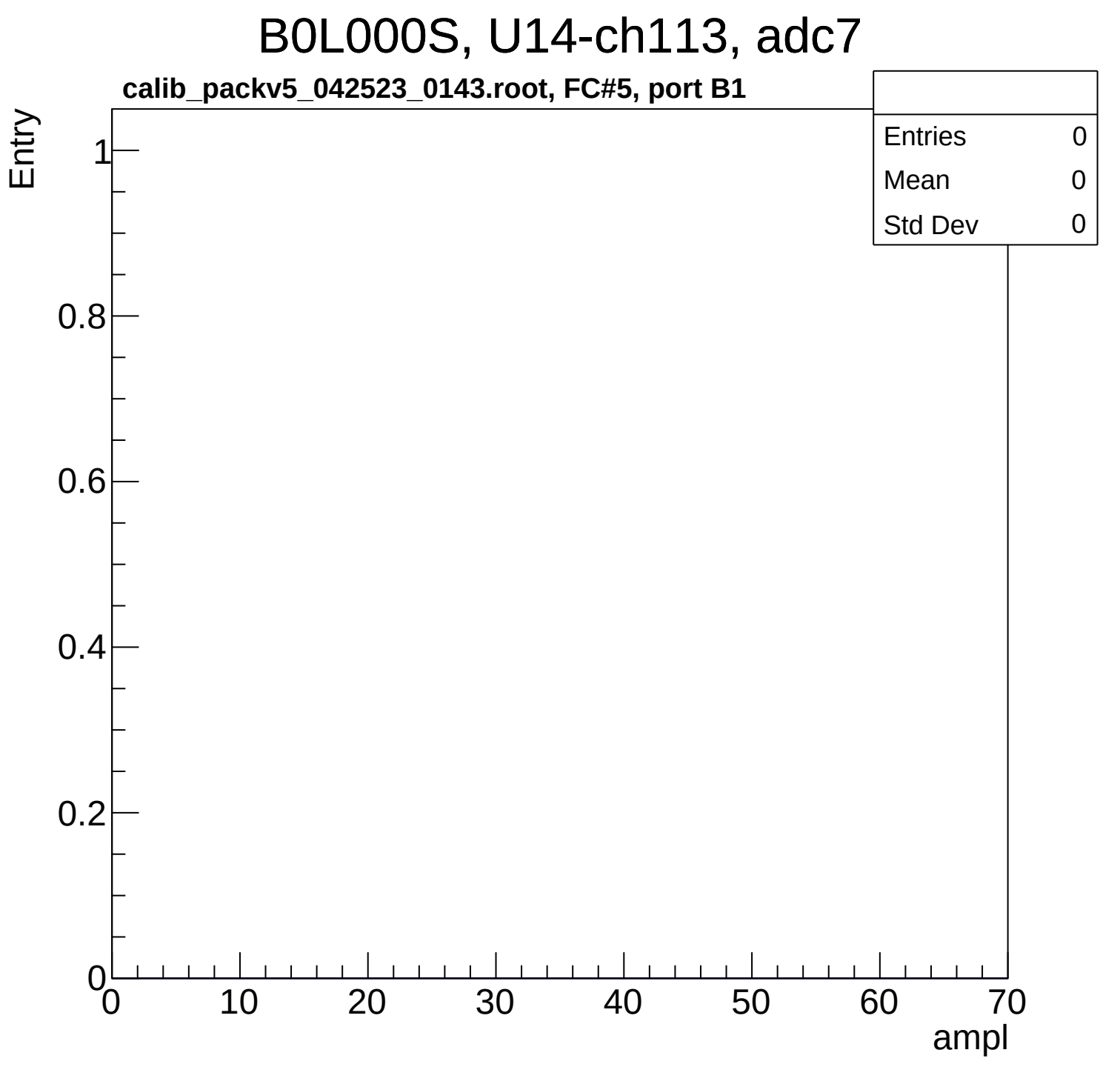
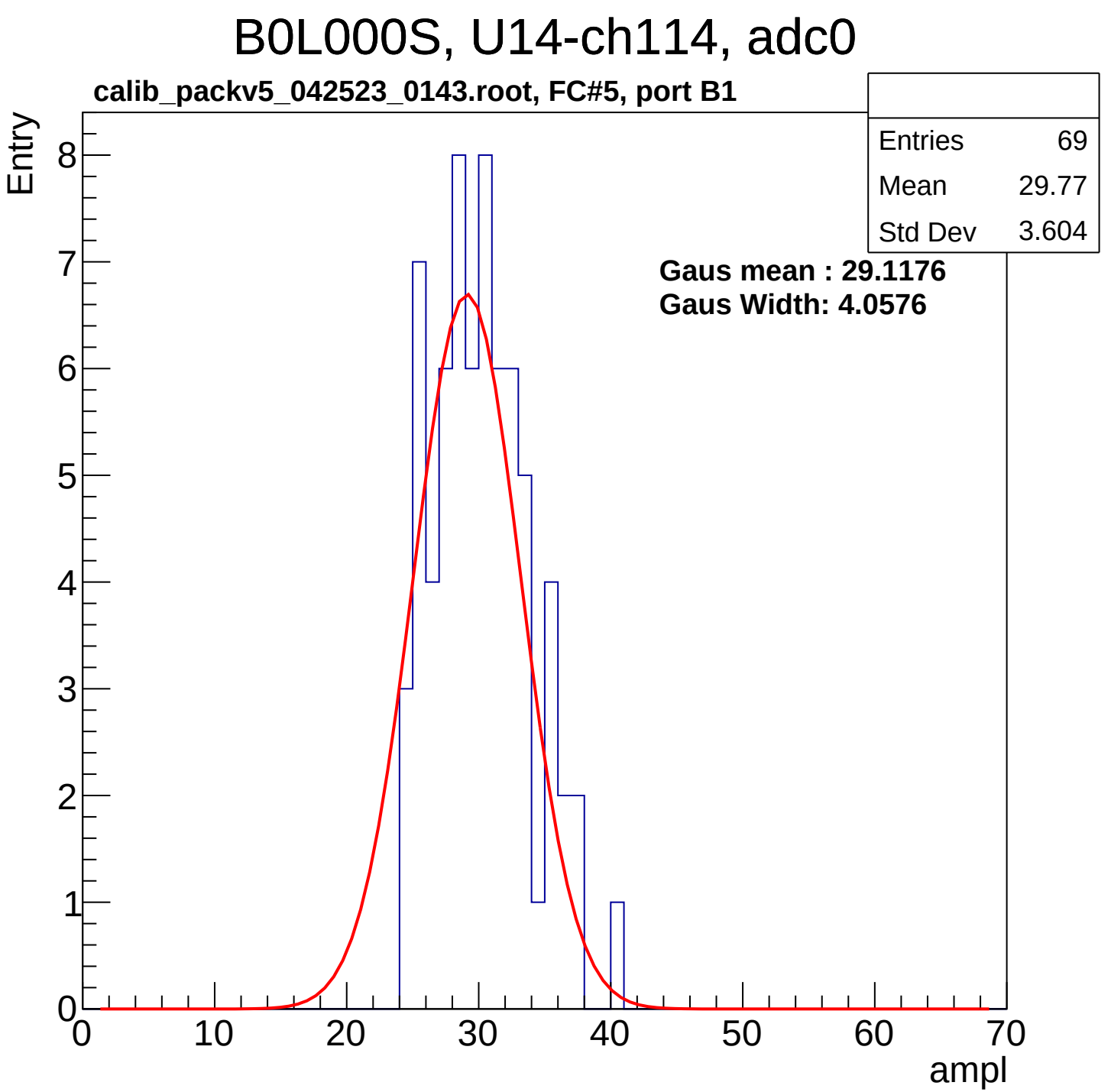
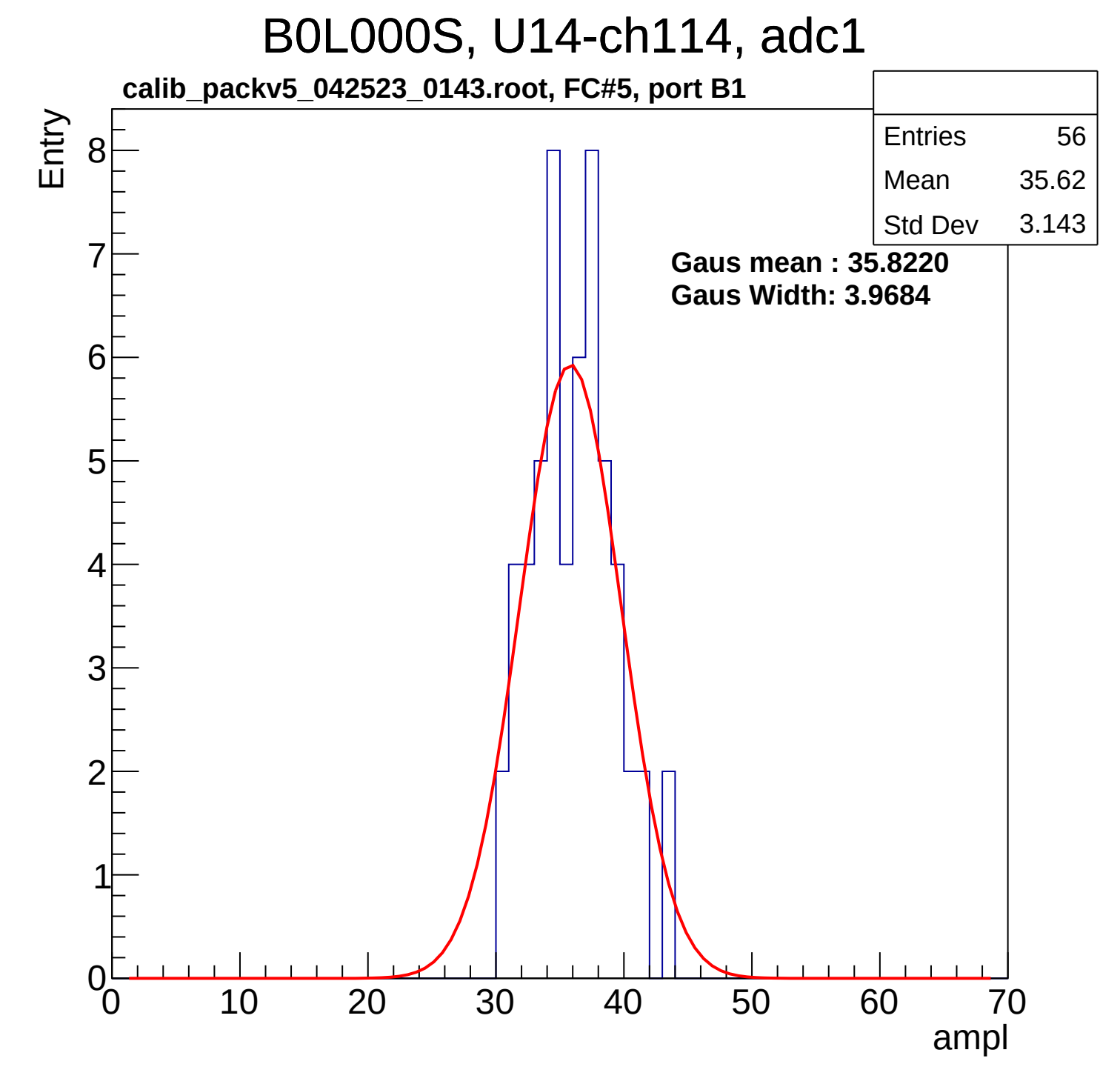
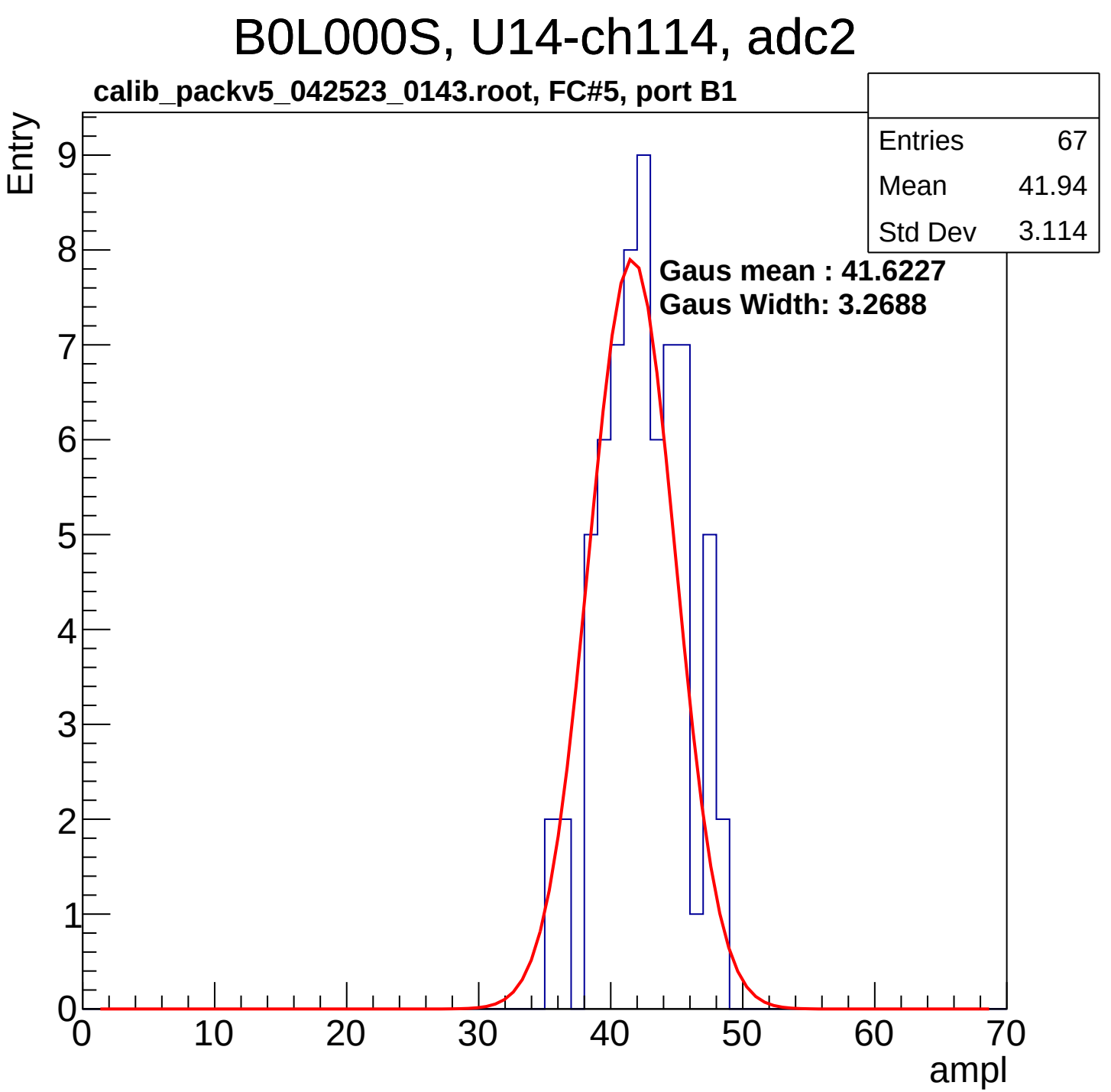
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

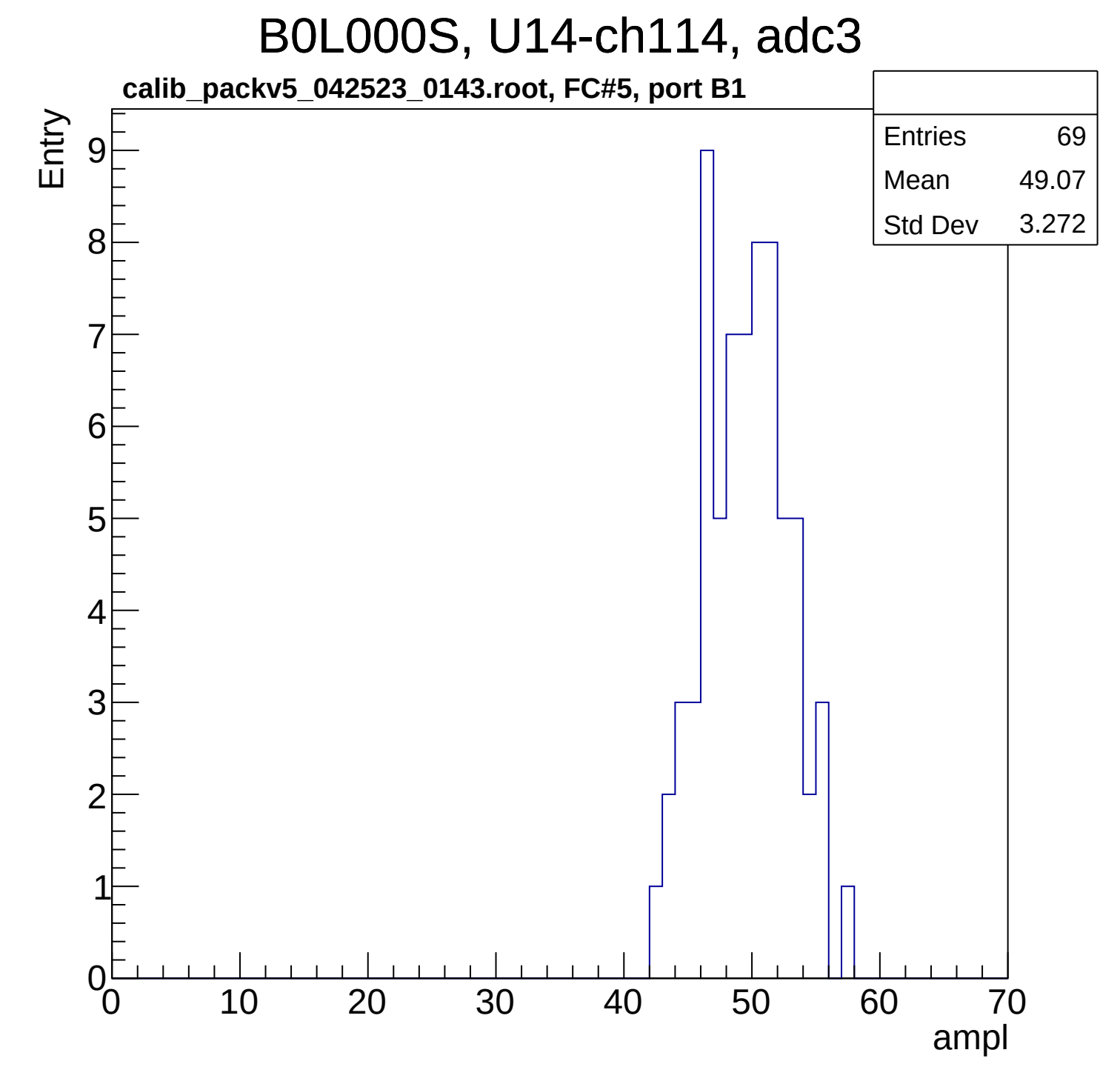

B0L000S, U14-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.43
Std Dev 1.879
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.77
Std Dev 3.604
Gaus mean : 29.1176
Gaus Width: 4.0576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.62
Std Dev 3.143
Gaus mean : 35.8220
Gaus Width: 3.9684
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.94
Std Dev 3.114
Gaus mean : 41.6227
Gaus Width: 3.2688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.07
Std Dev 3.272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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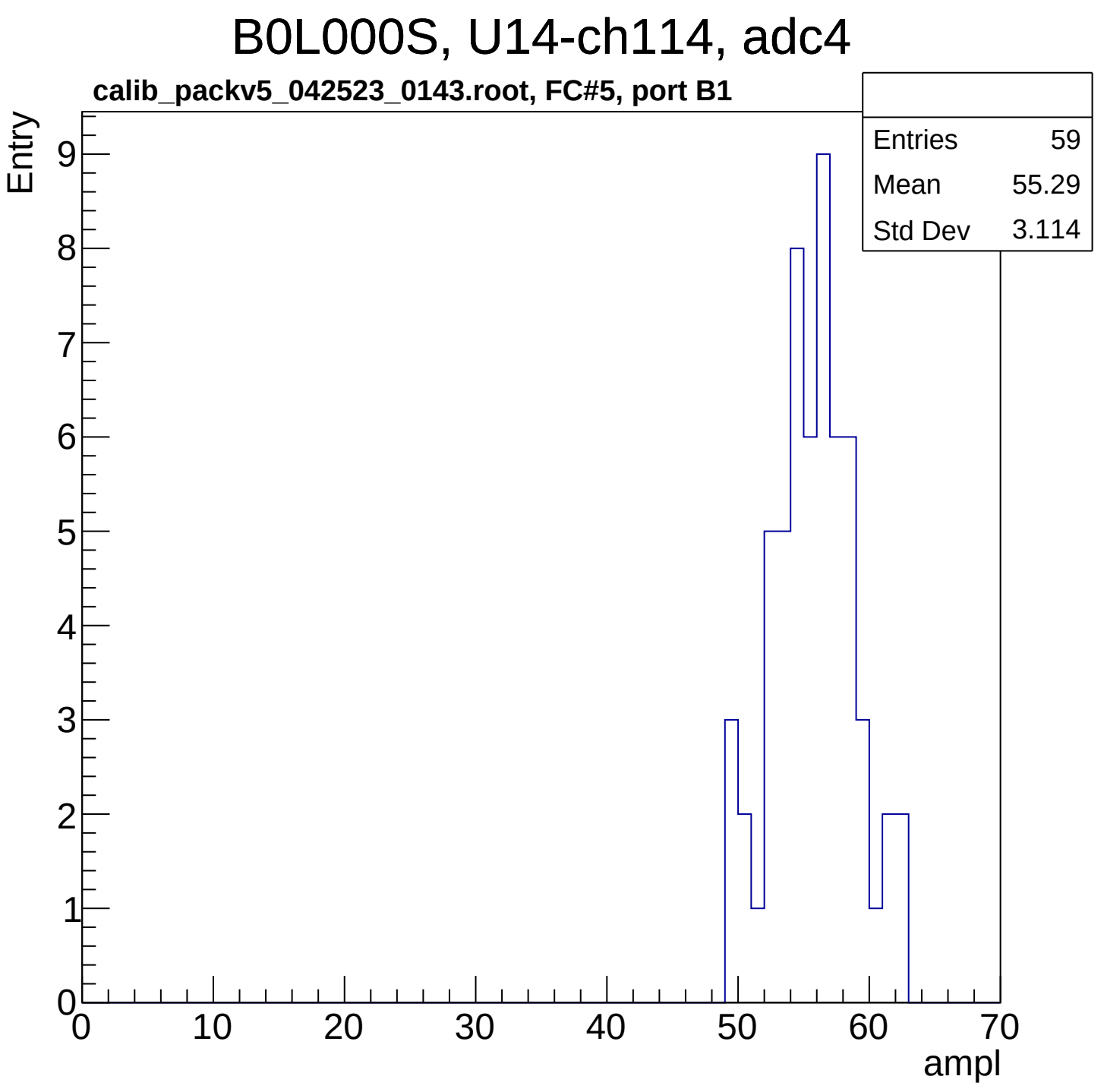
60
70


B0L000S, U14-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.29
Std Dev 3.114
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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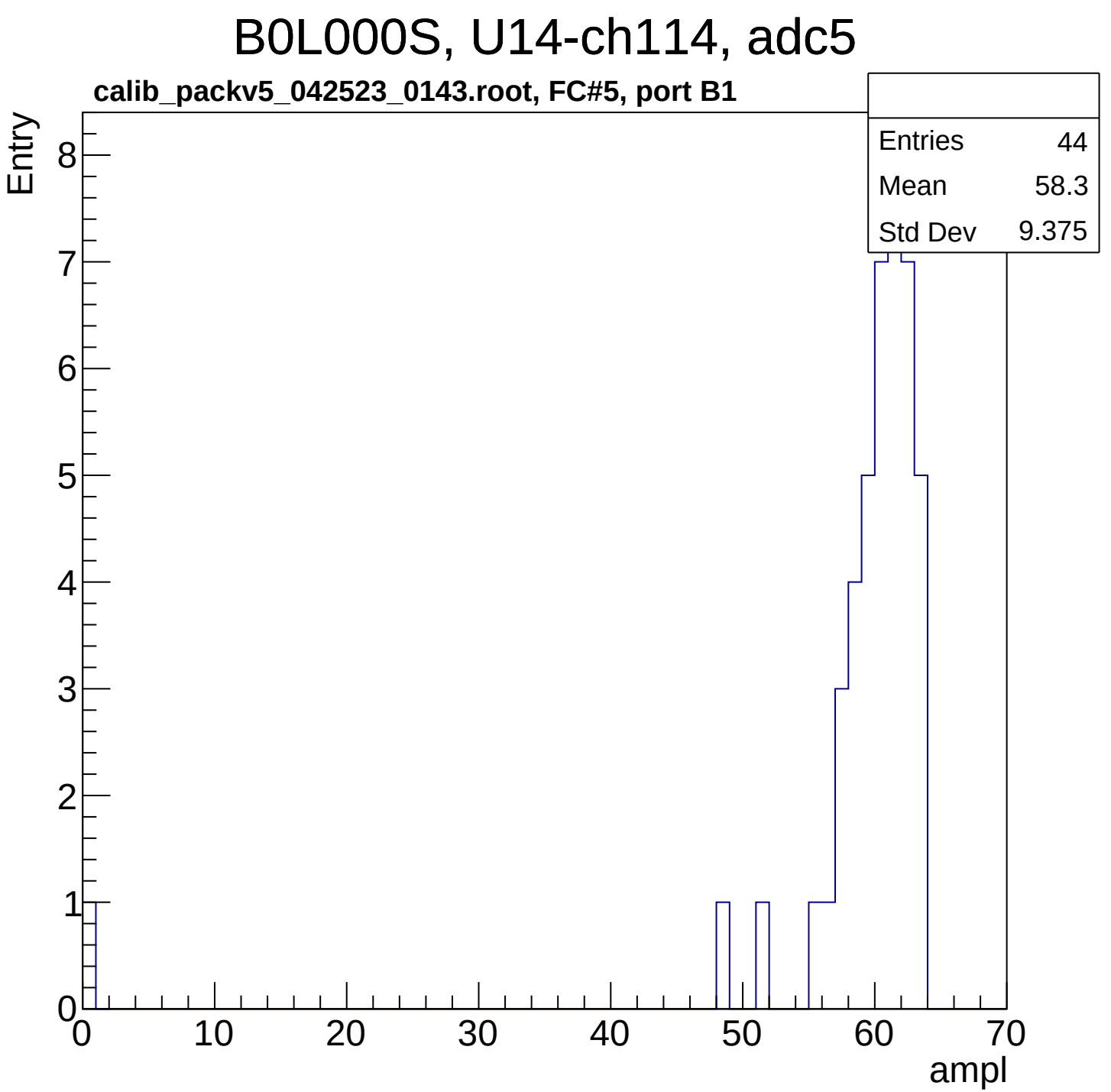
B0L000S, U14-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.3
Std Dev 9.375
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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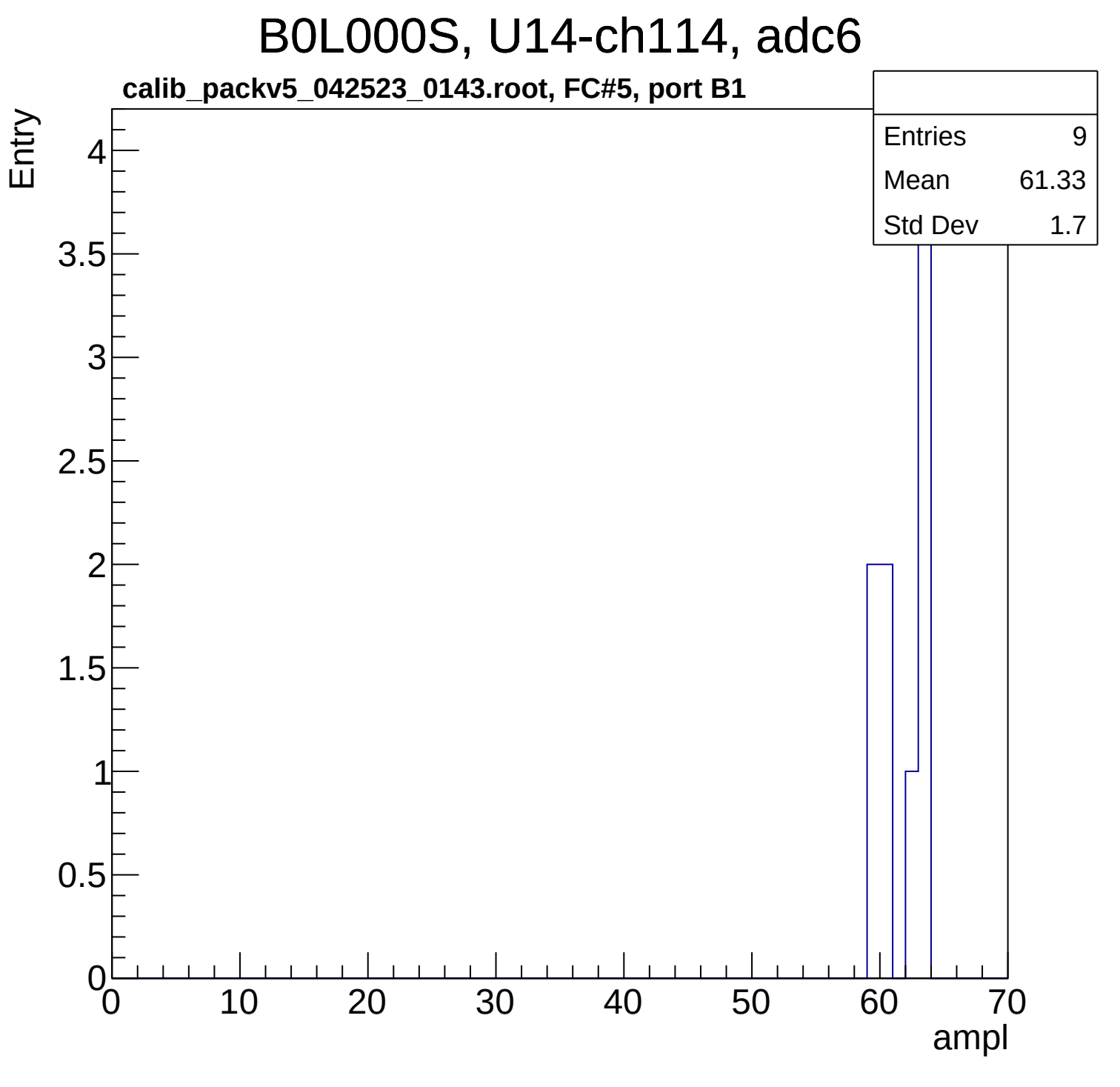
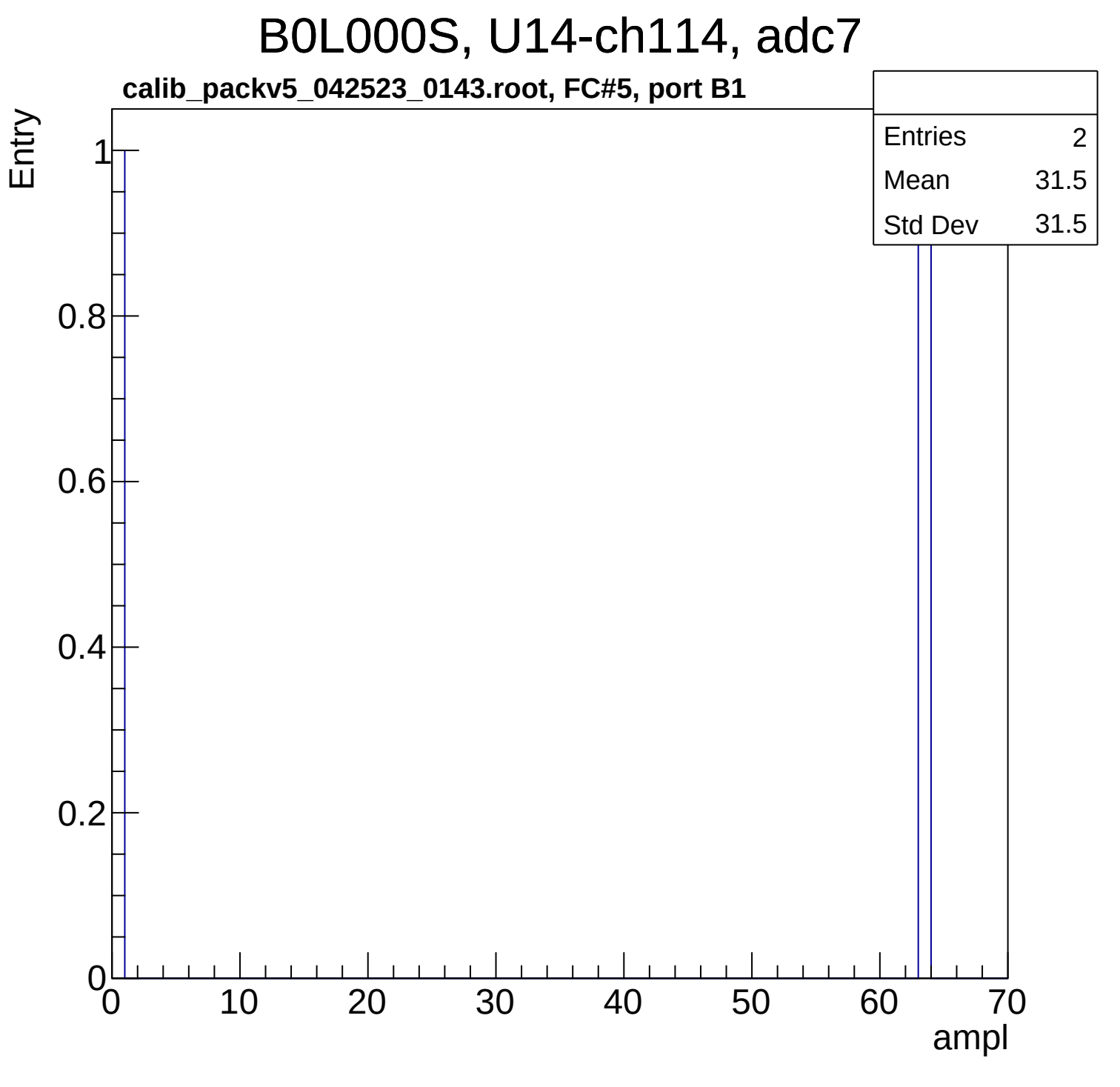
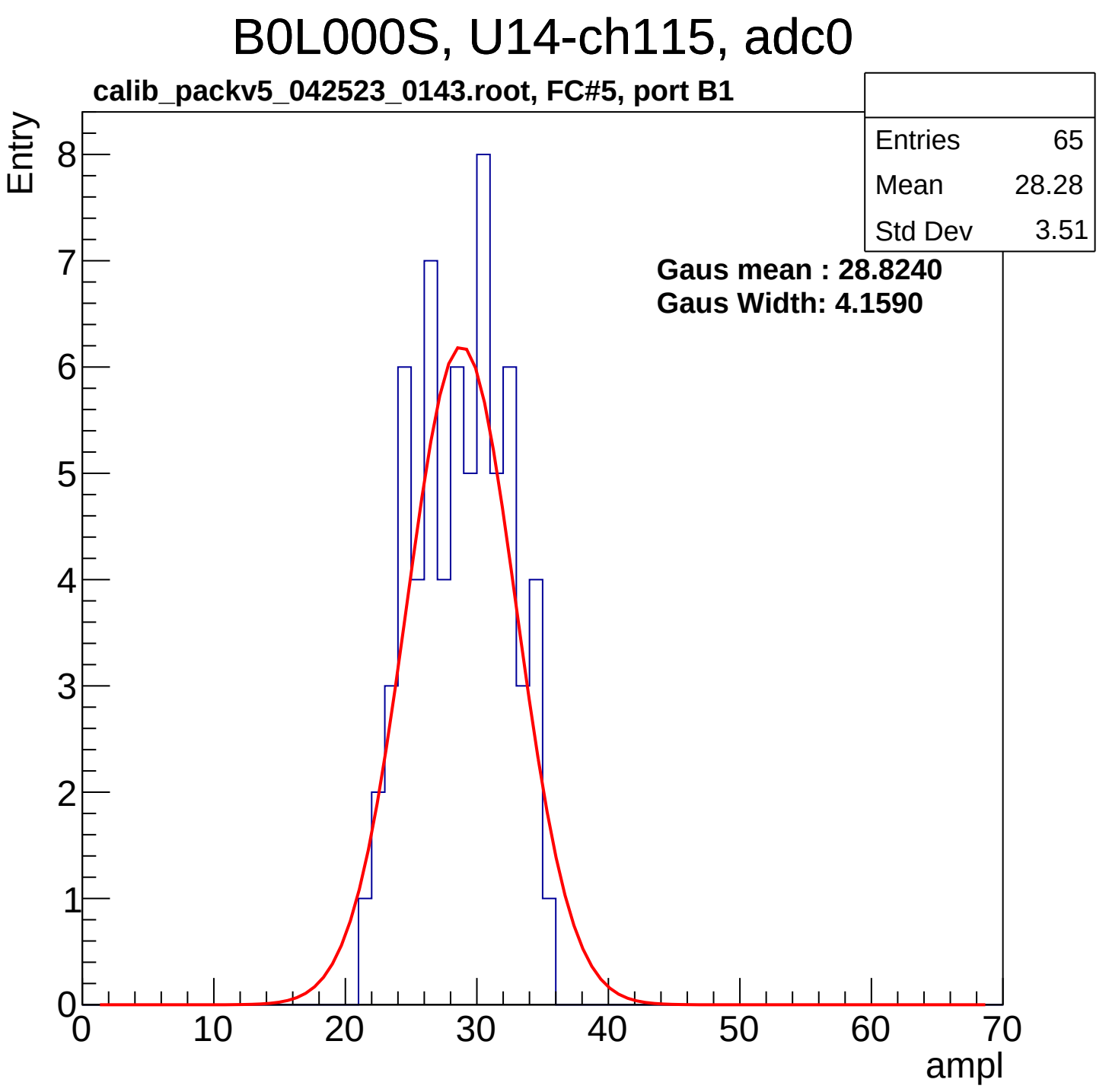
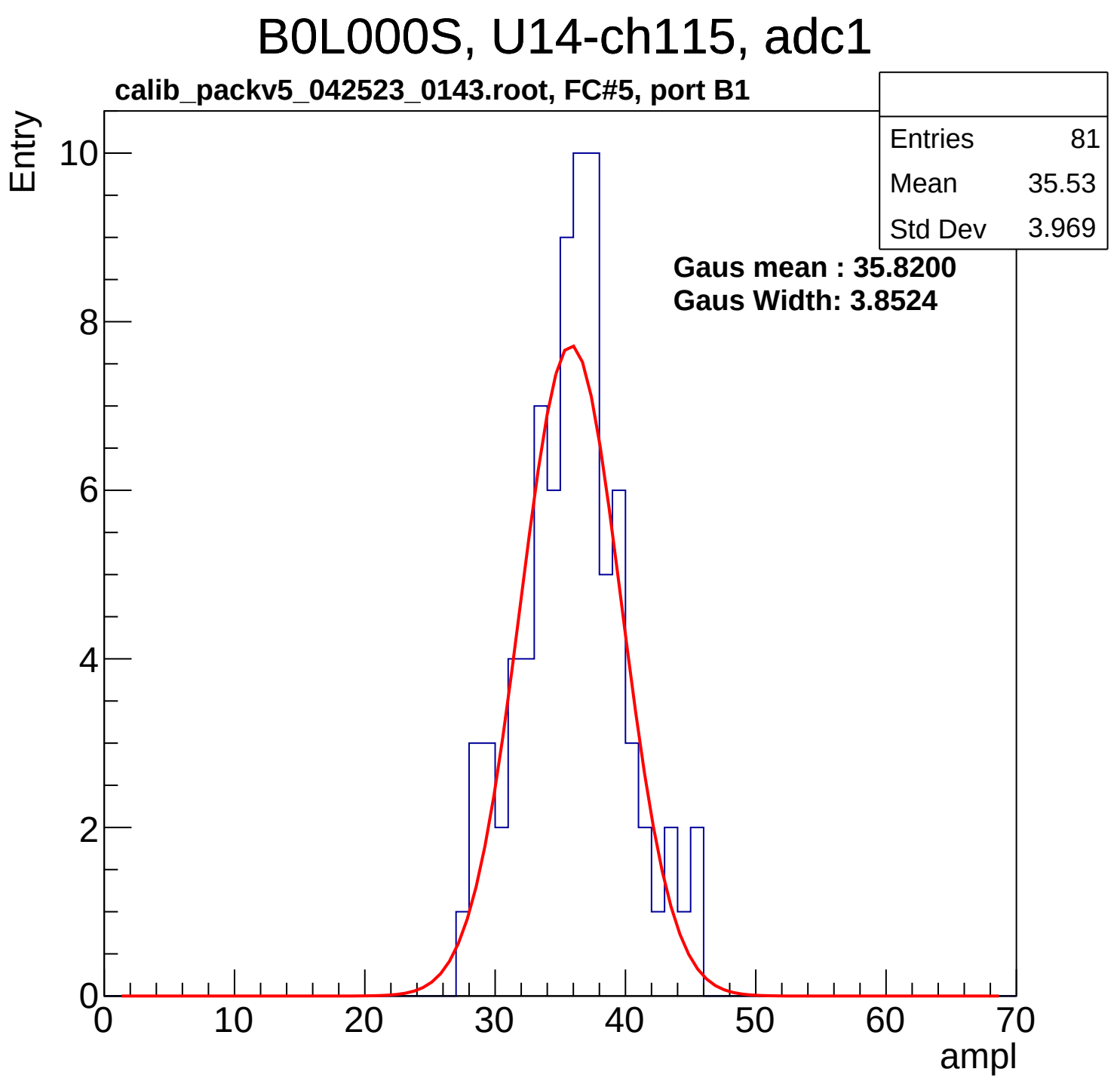
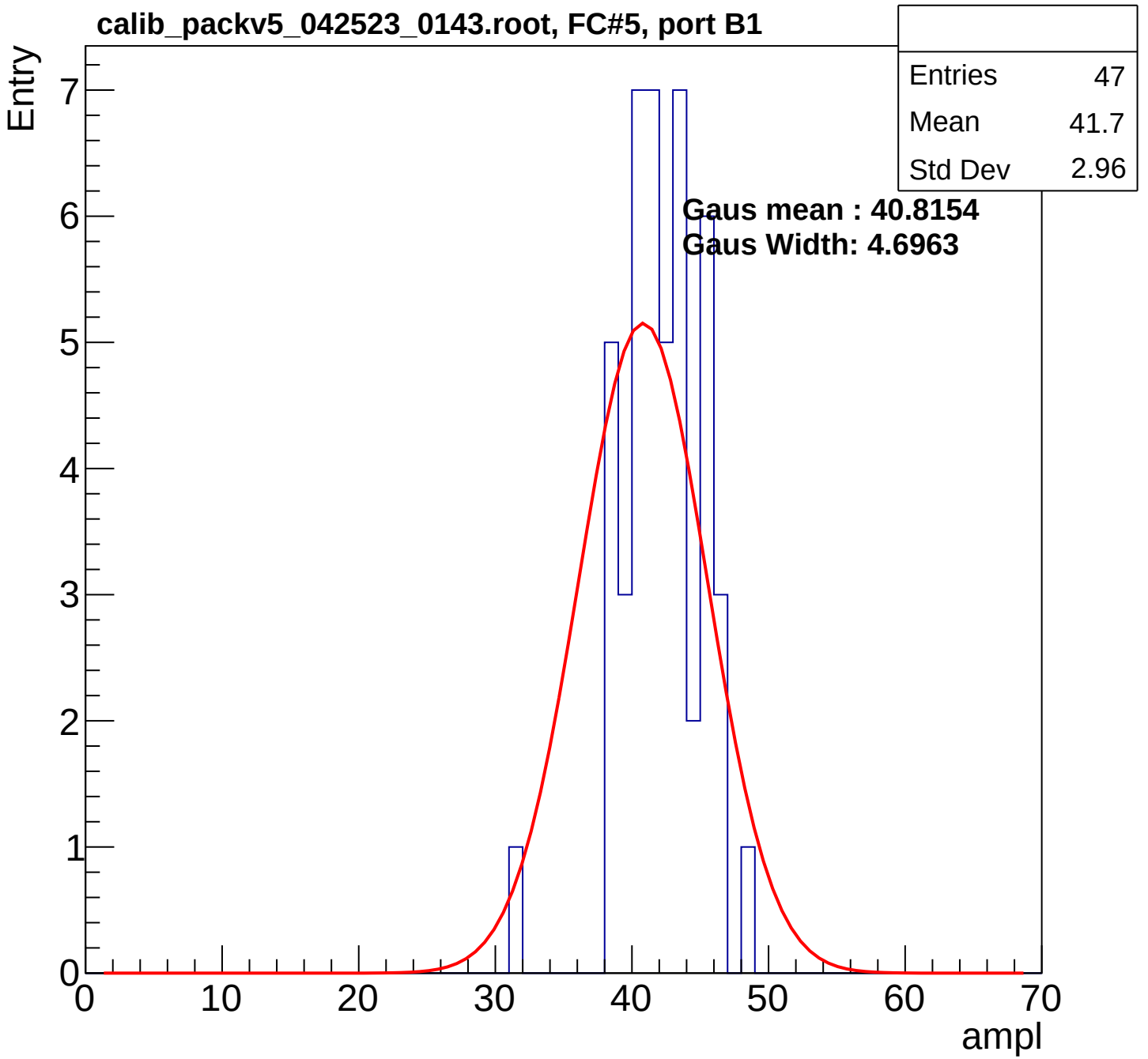
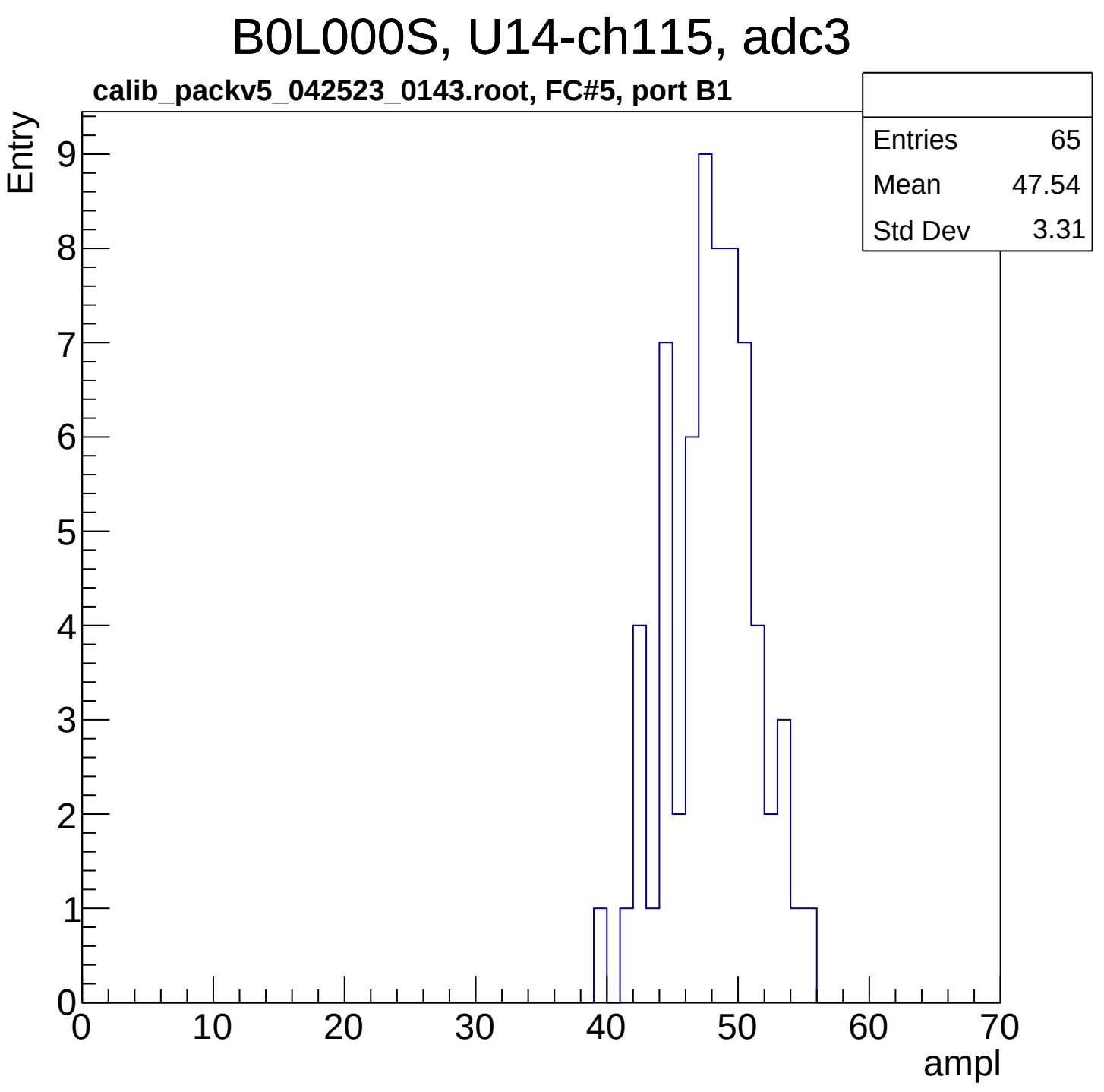

B0L000S, U14-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 28.28
Std Dev 3.51
Gaus mean : 28.8240
Gaus Width: 4.1590
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 35.53
Std Dev 3.969
Gaus mean : 35.8200
Gaus Width: 3.8524
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 41.7
Std Dev 2.96
Gaus mean : 40.8154
Gaus Width: 4.6963
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 47.54
Std Dev 3.31
Entry
ampl
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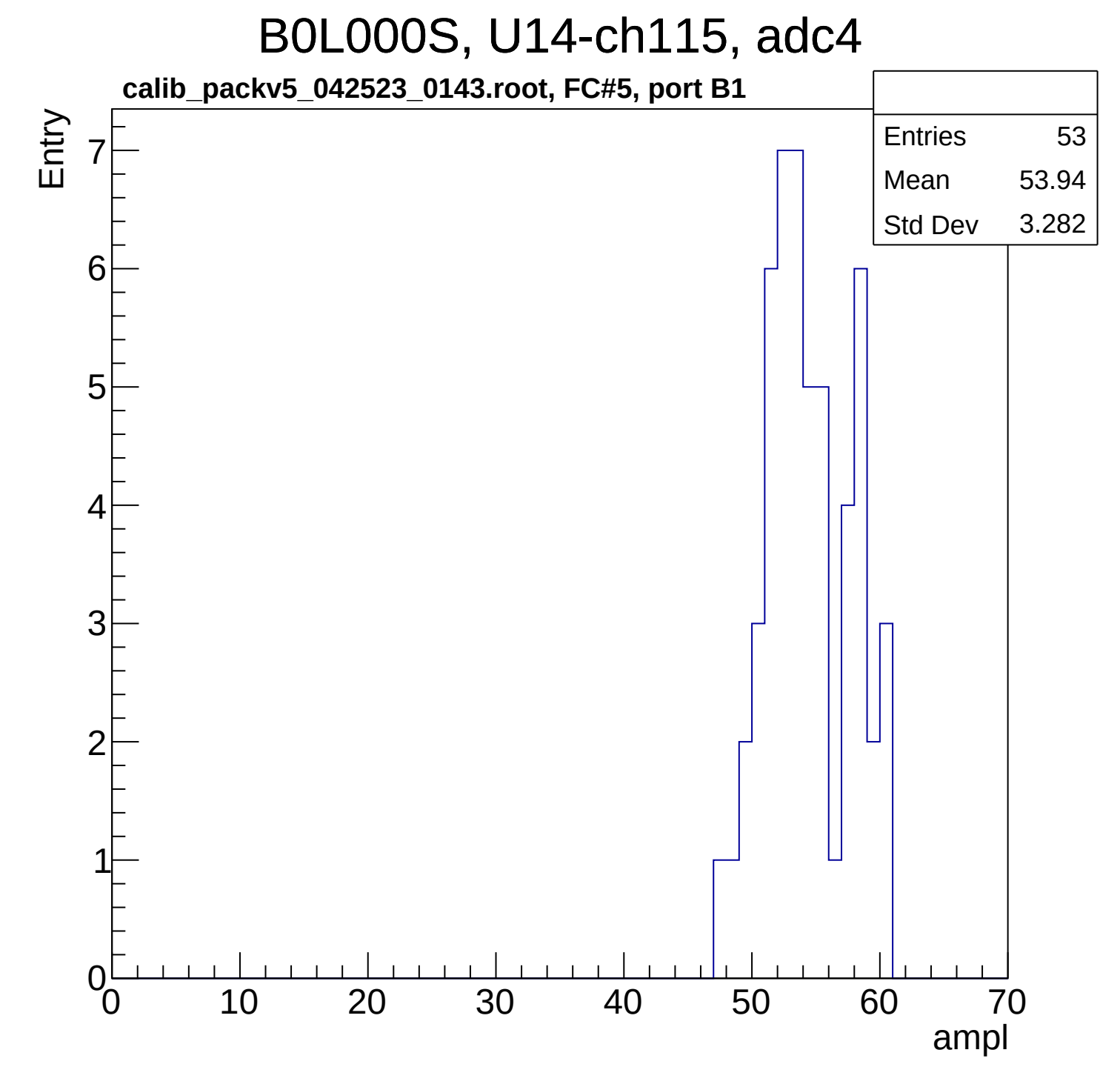
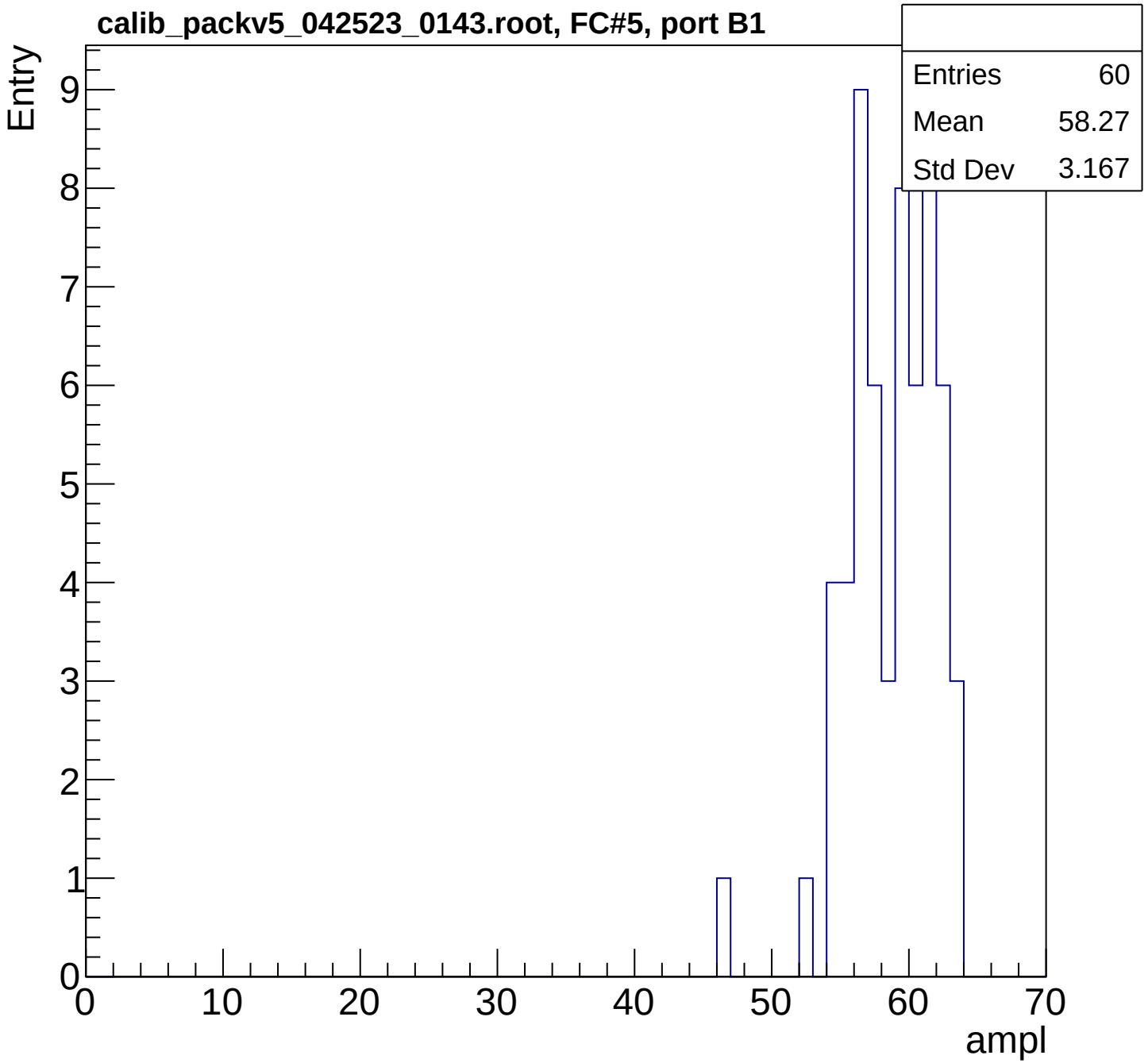
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.94
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.27
Std Dev 3.167
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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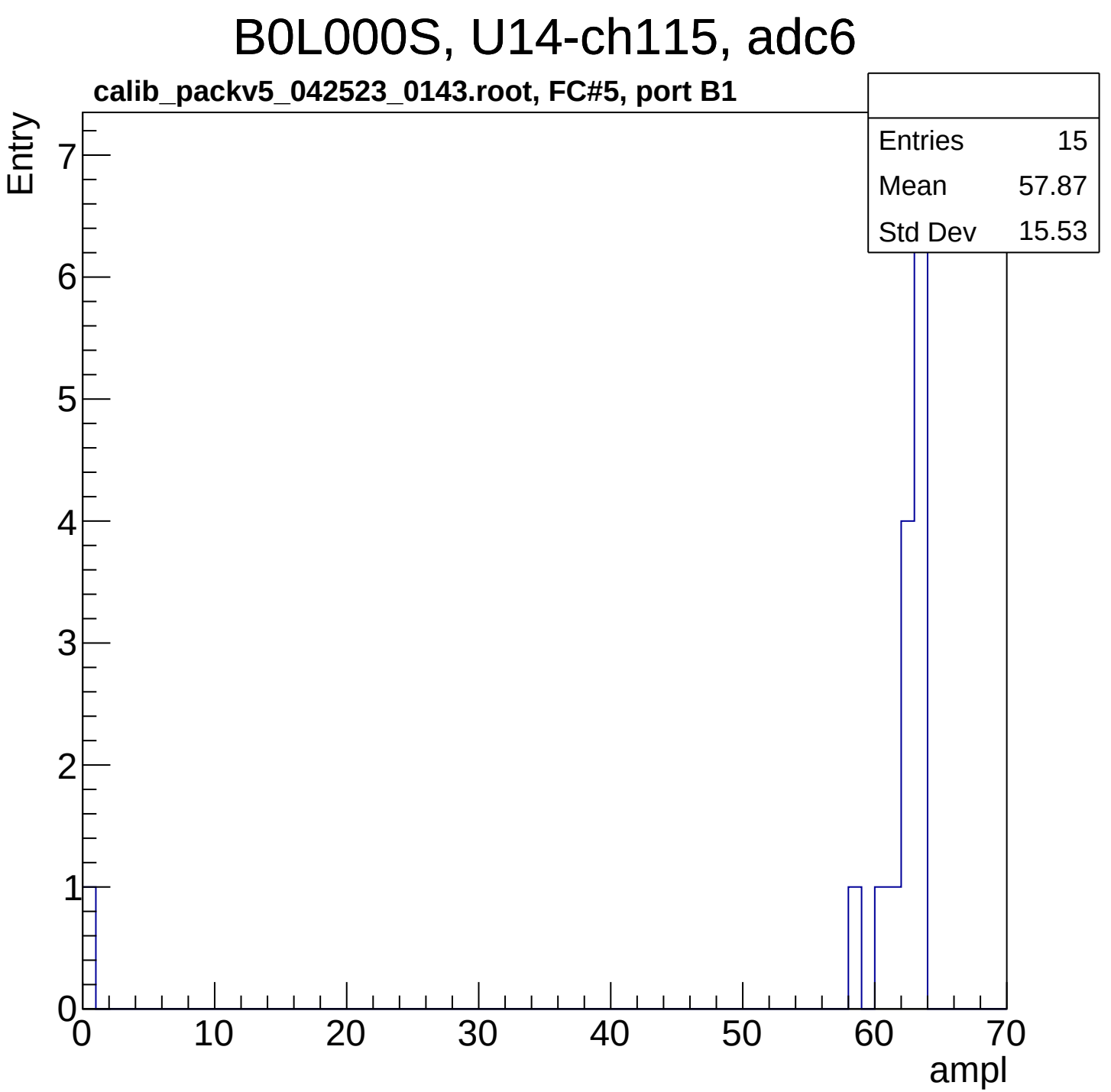
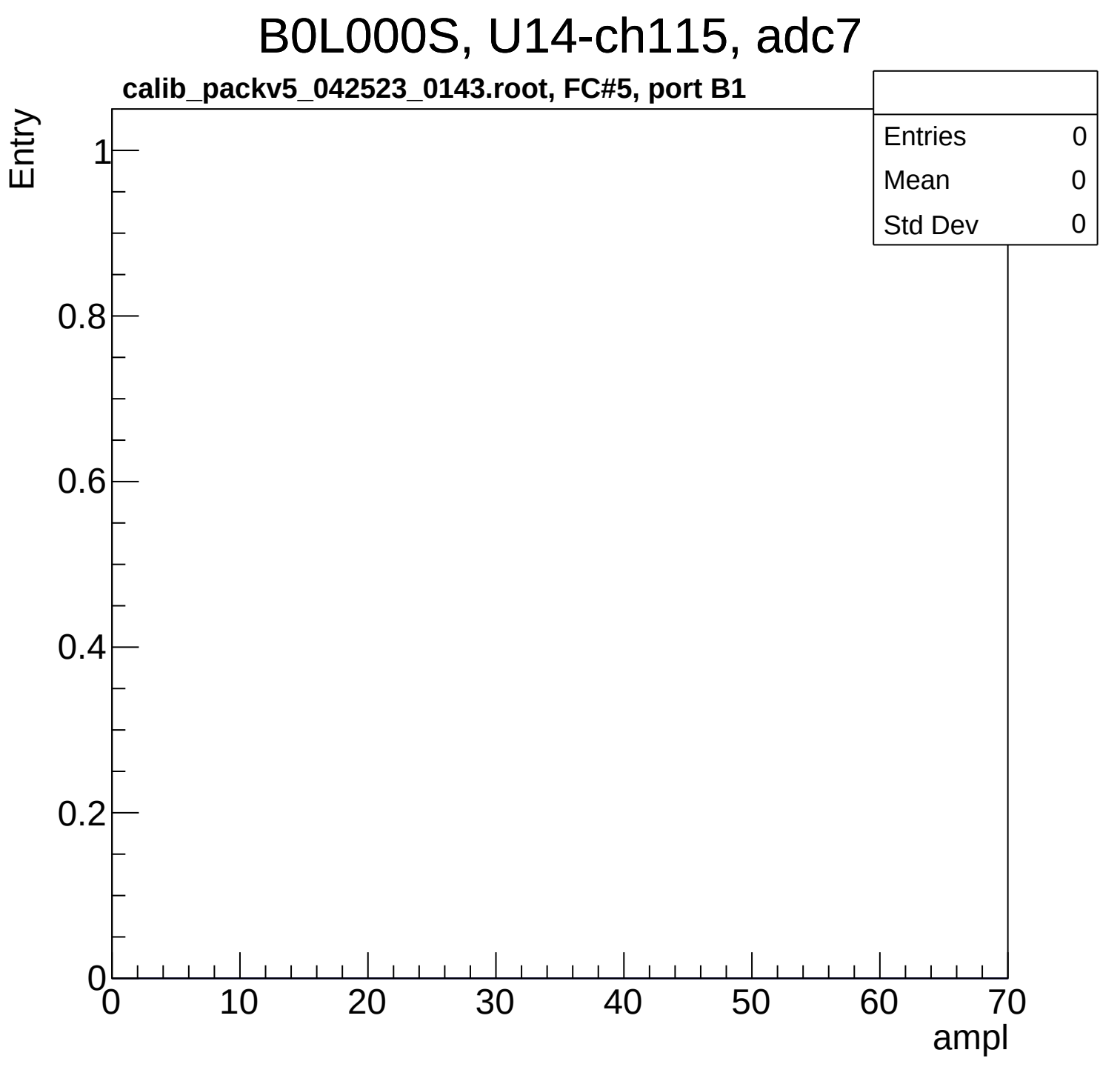
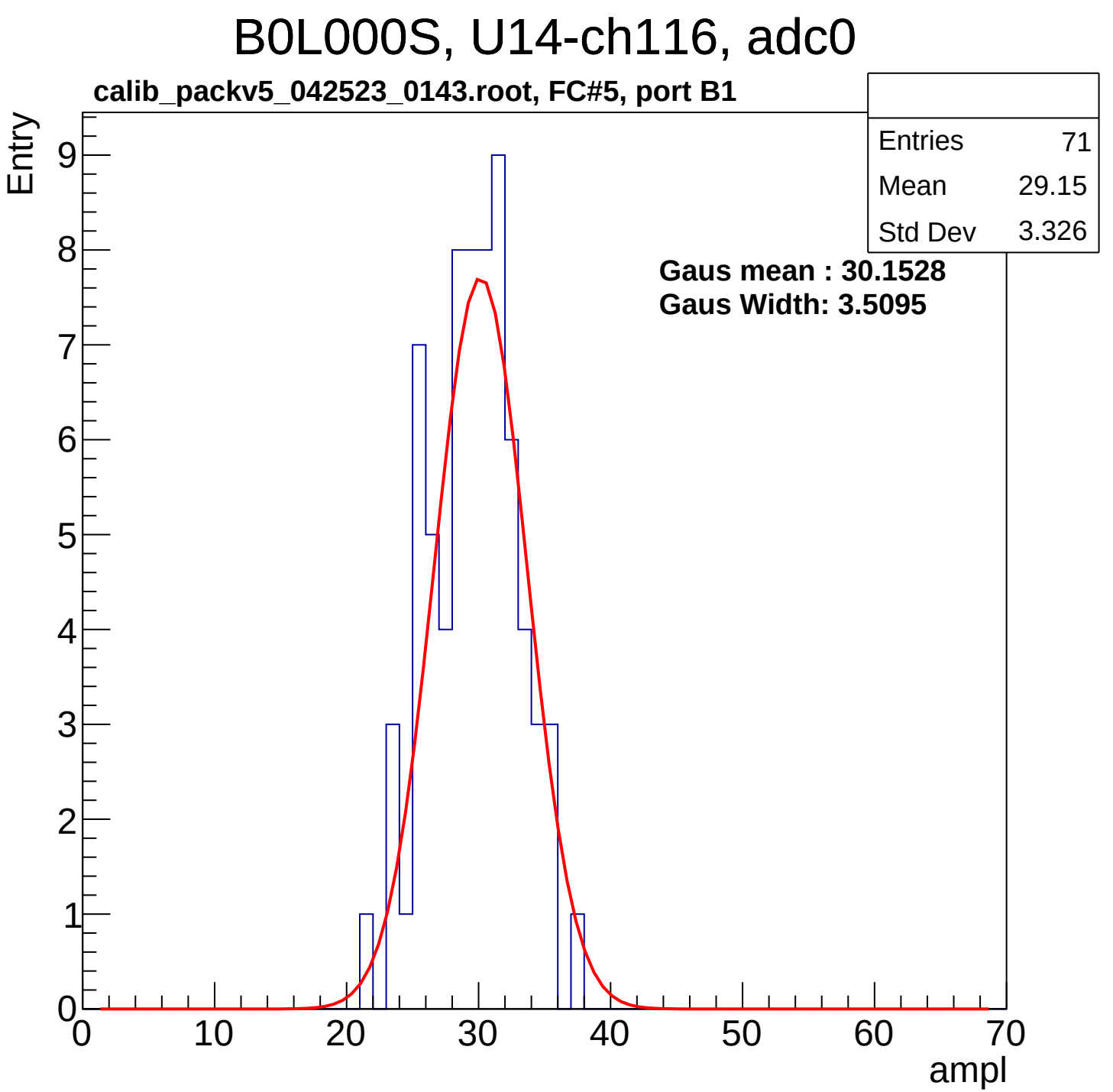
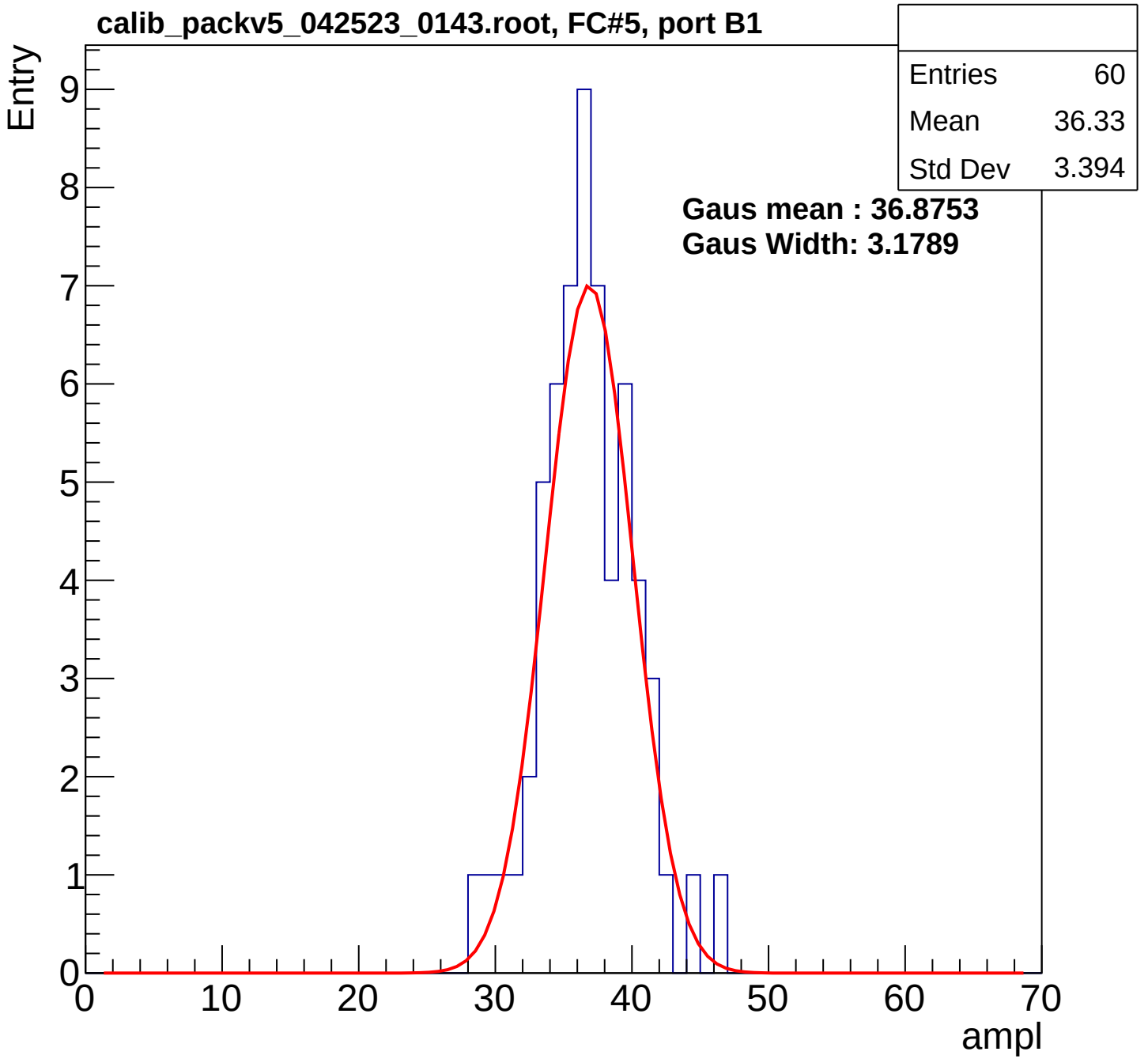

B0L000S, U14-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.87
Std Dev 15.53
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.15
Std Dev 3.326
Gaus mean : 30.1528
Gaus Width: 3.5095
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.33
Std Dev 3.394
Gaus mean : 36.8753
Gaus Width: 3.1789
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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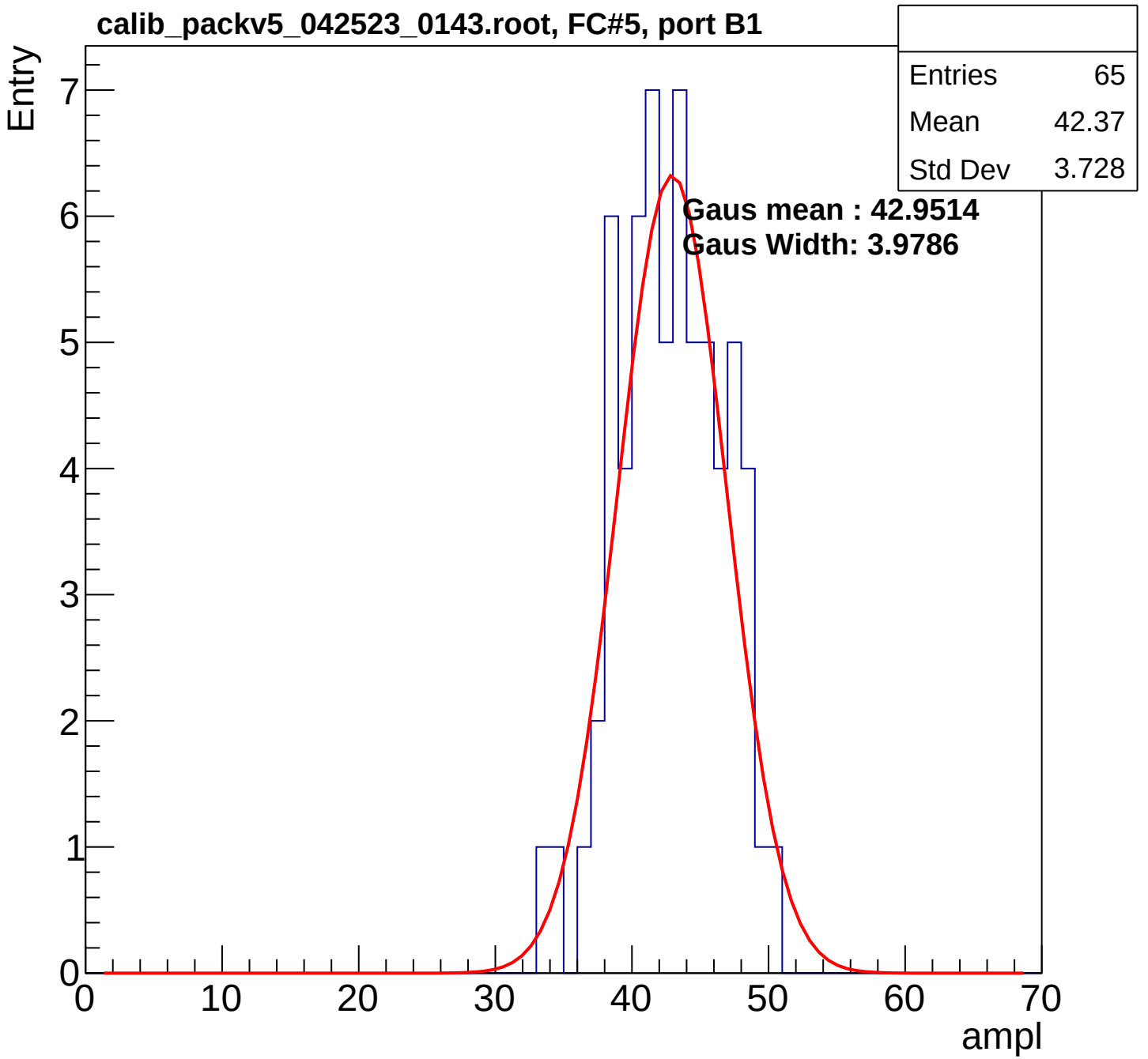
B0L000S, U14-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.37
Std Dev 3.728
Gaus mean : 42.9514
Gaus Width: 3.9786
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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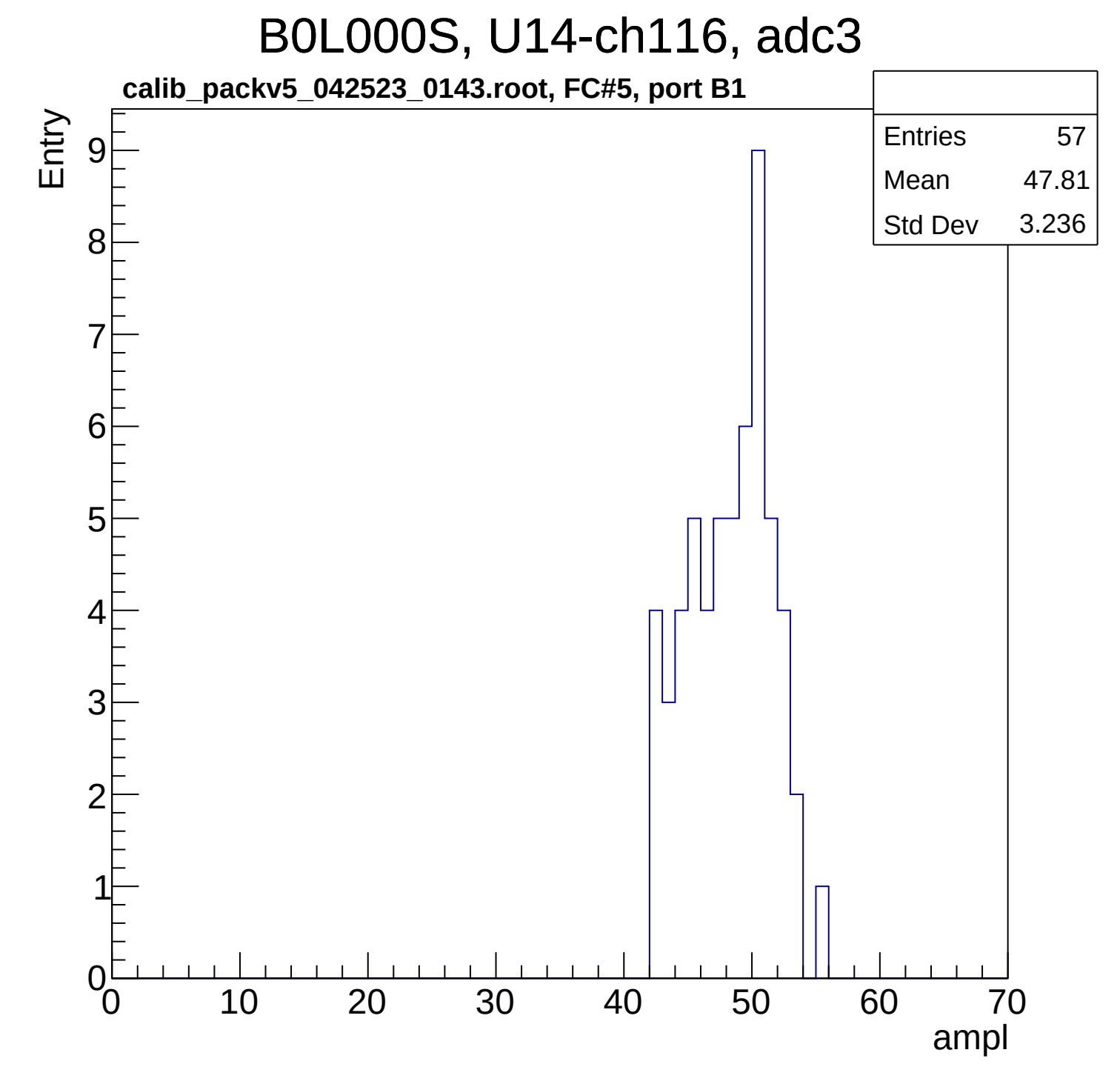
B0L000S, U14-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 47.81
Std Dev 3.236
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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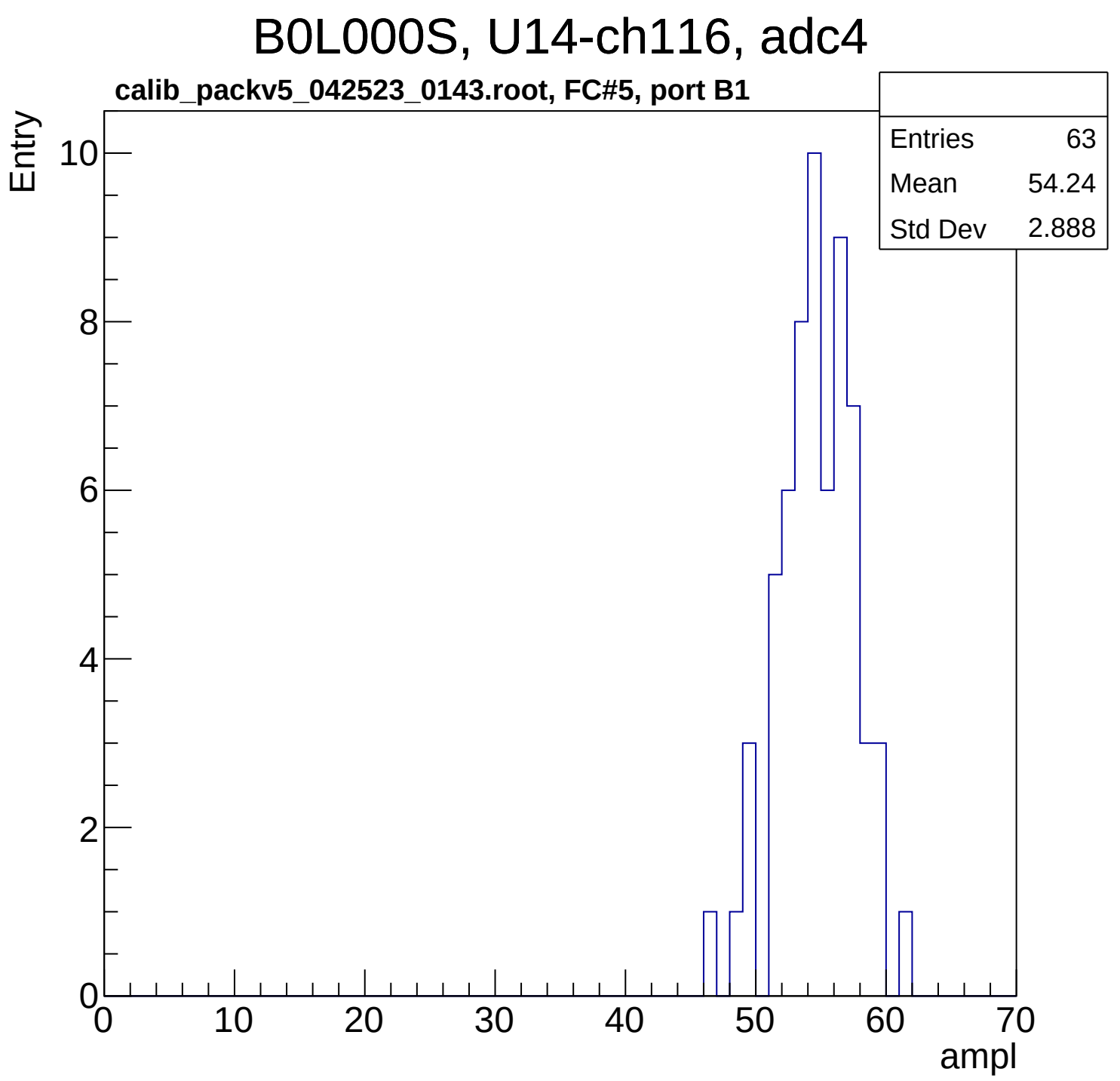
B0L000S, U14-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.24
Std Dev 2.888
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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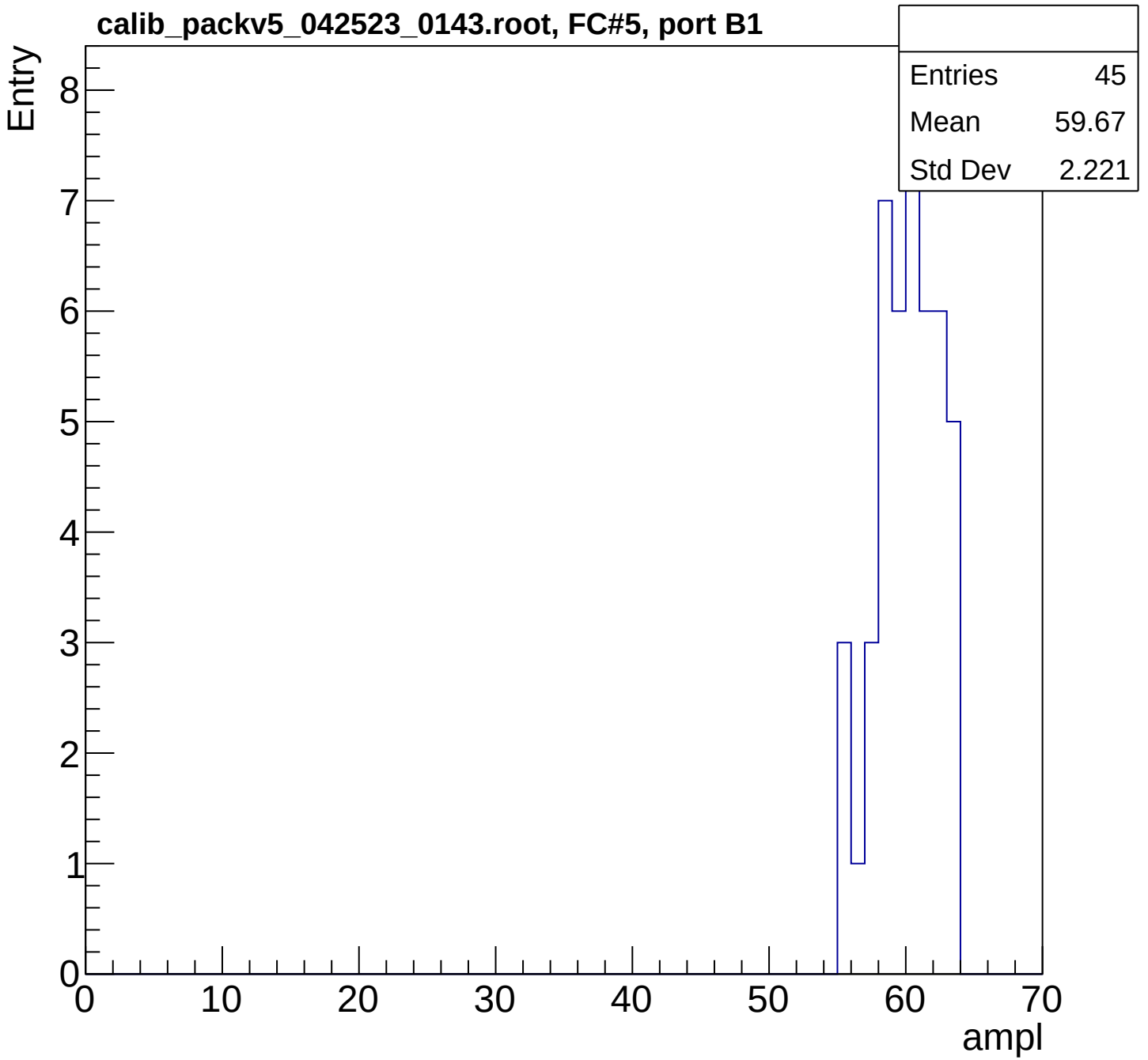
B0L000S, U14-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 59.67
Std Dev 2.221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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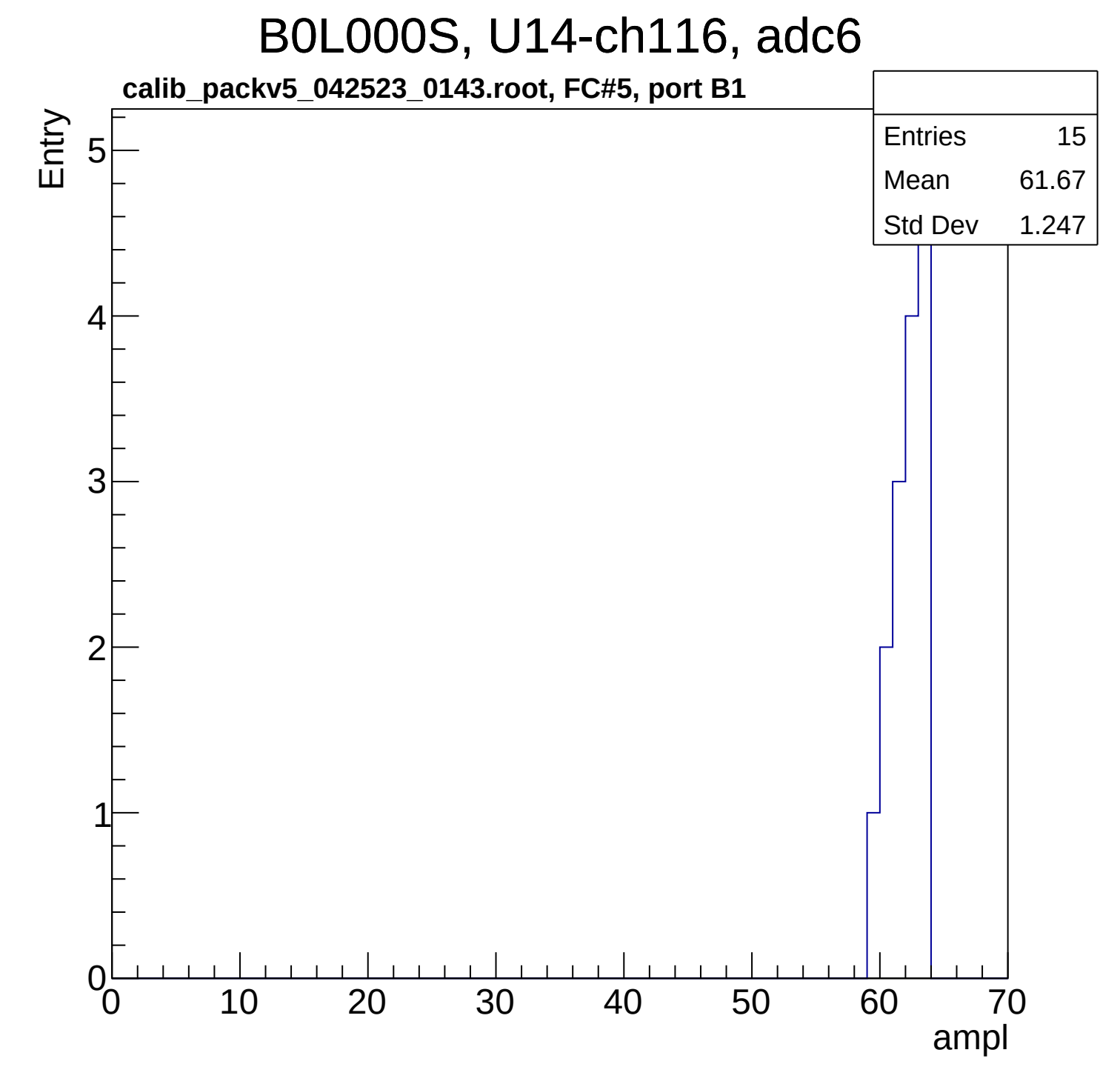
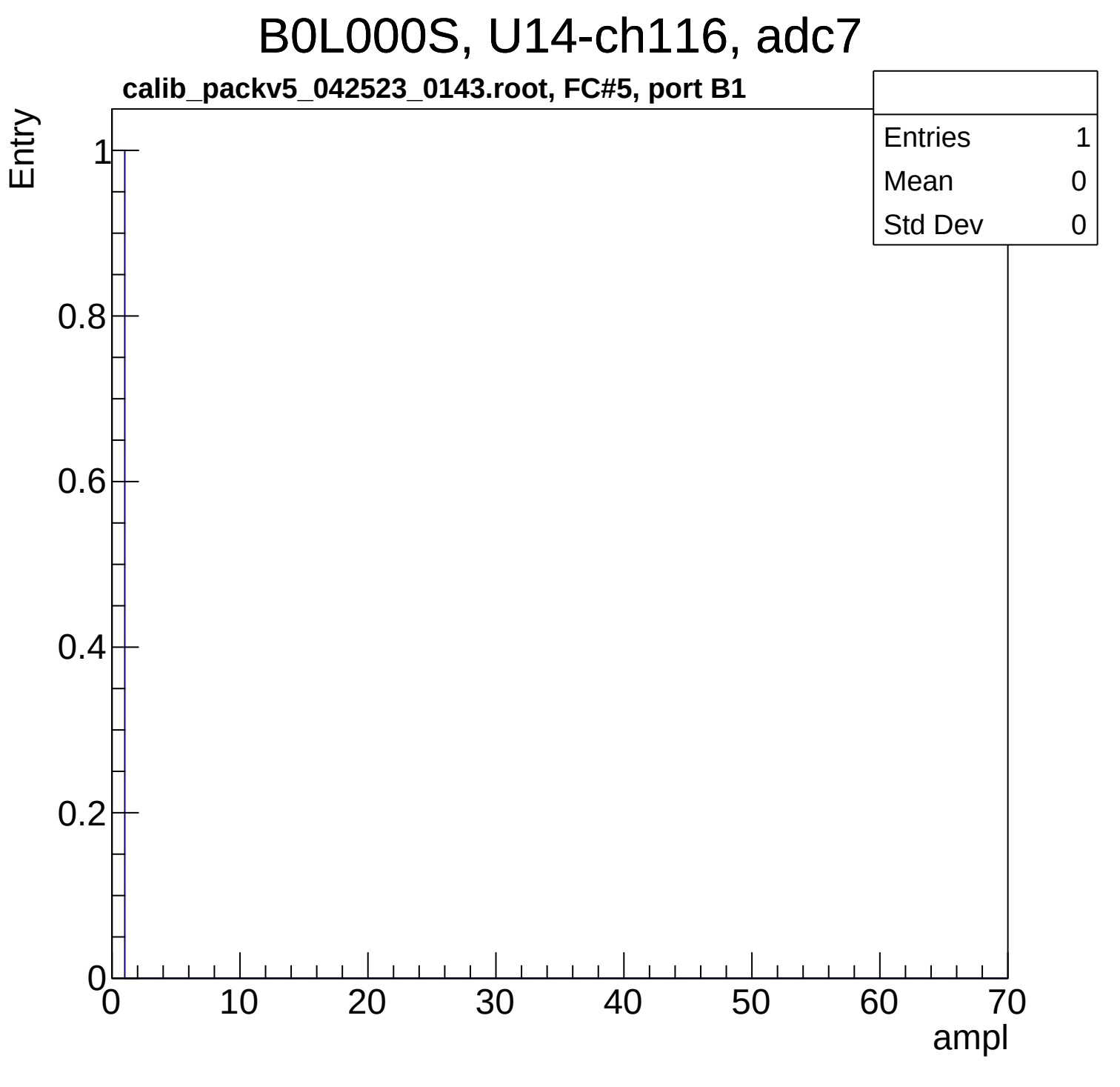

B0L000S, U14-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 61.67
Std Dev 1.247
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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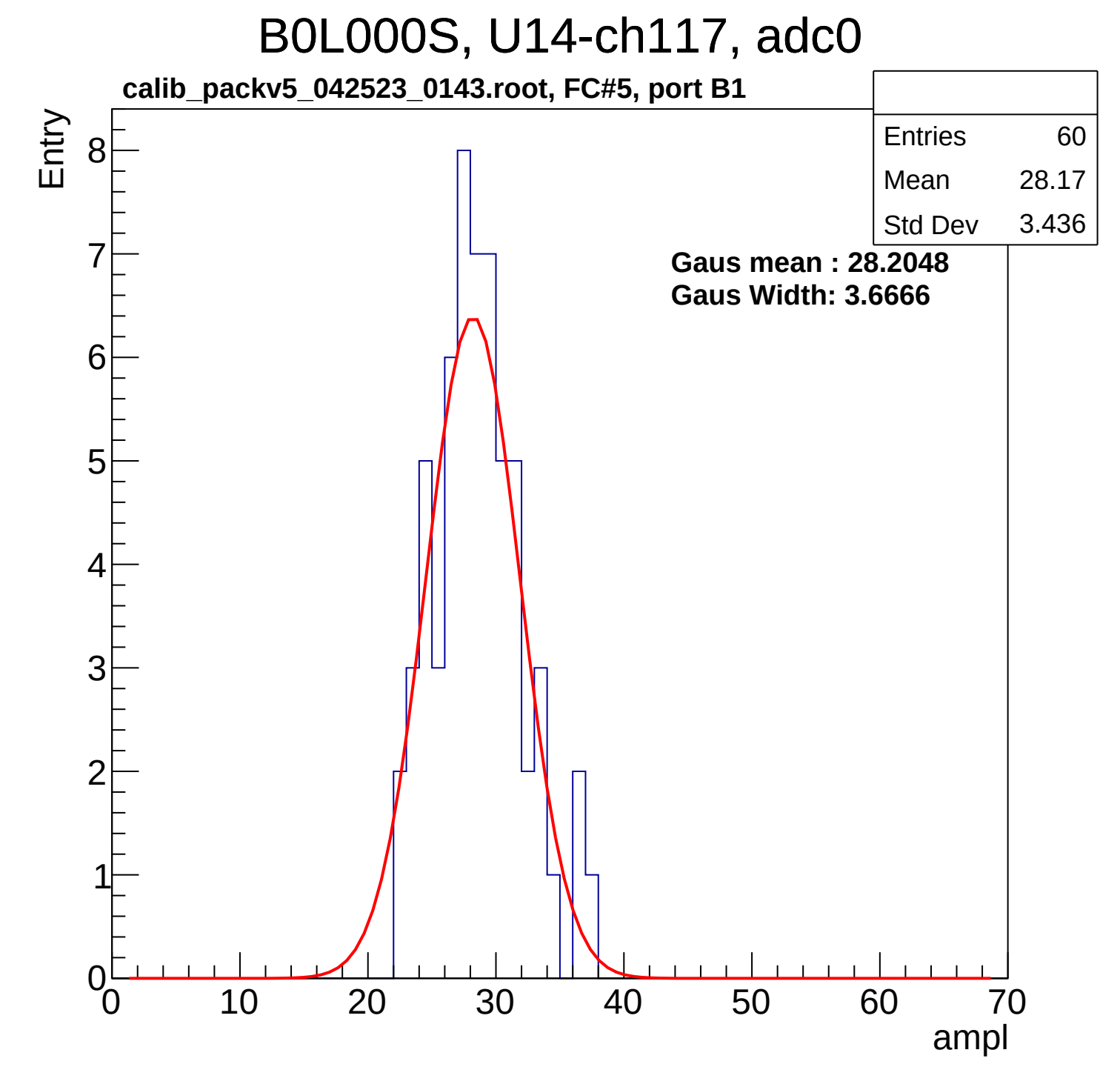
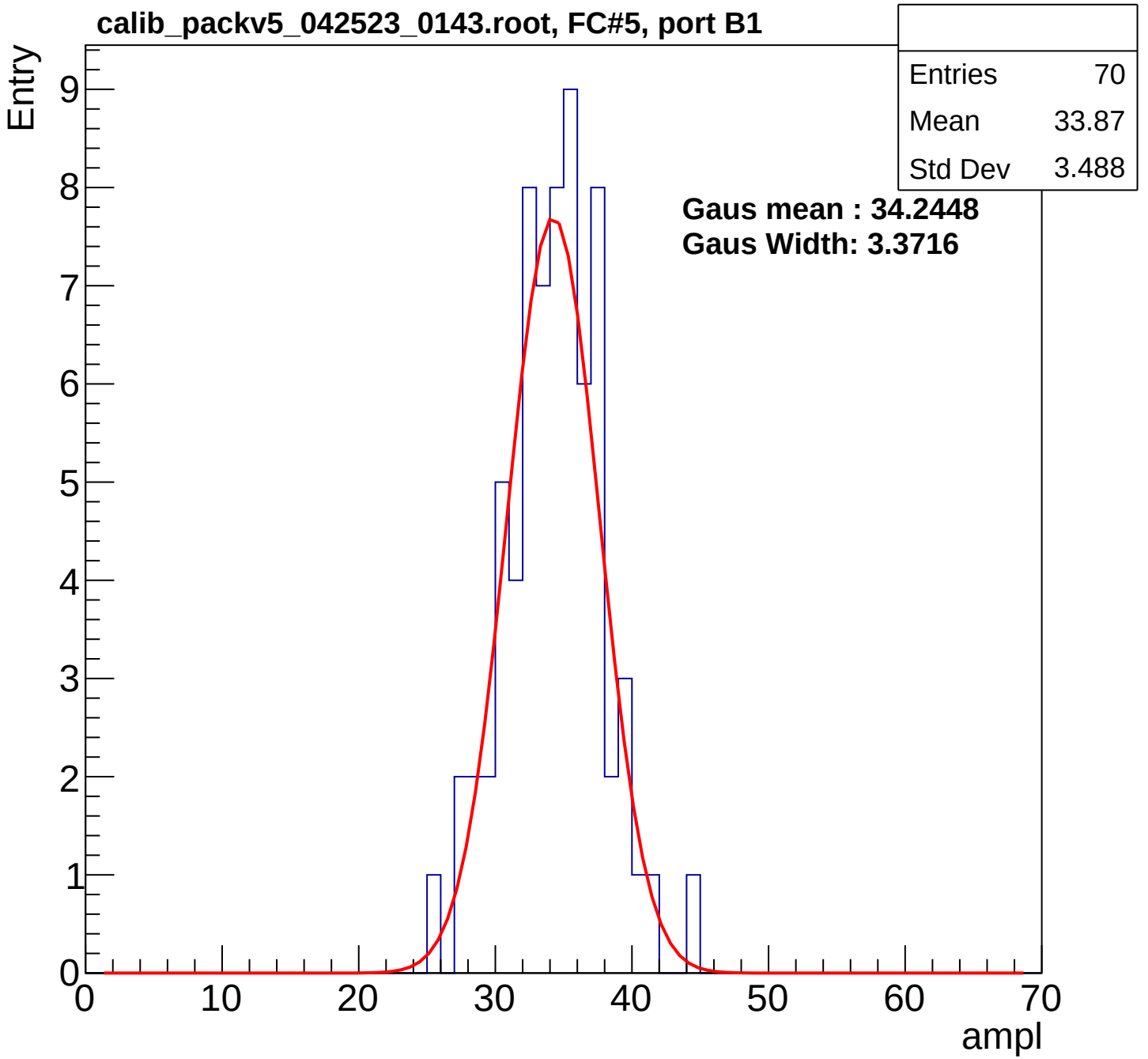
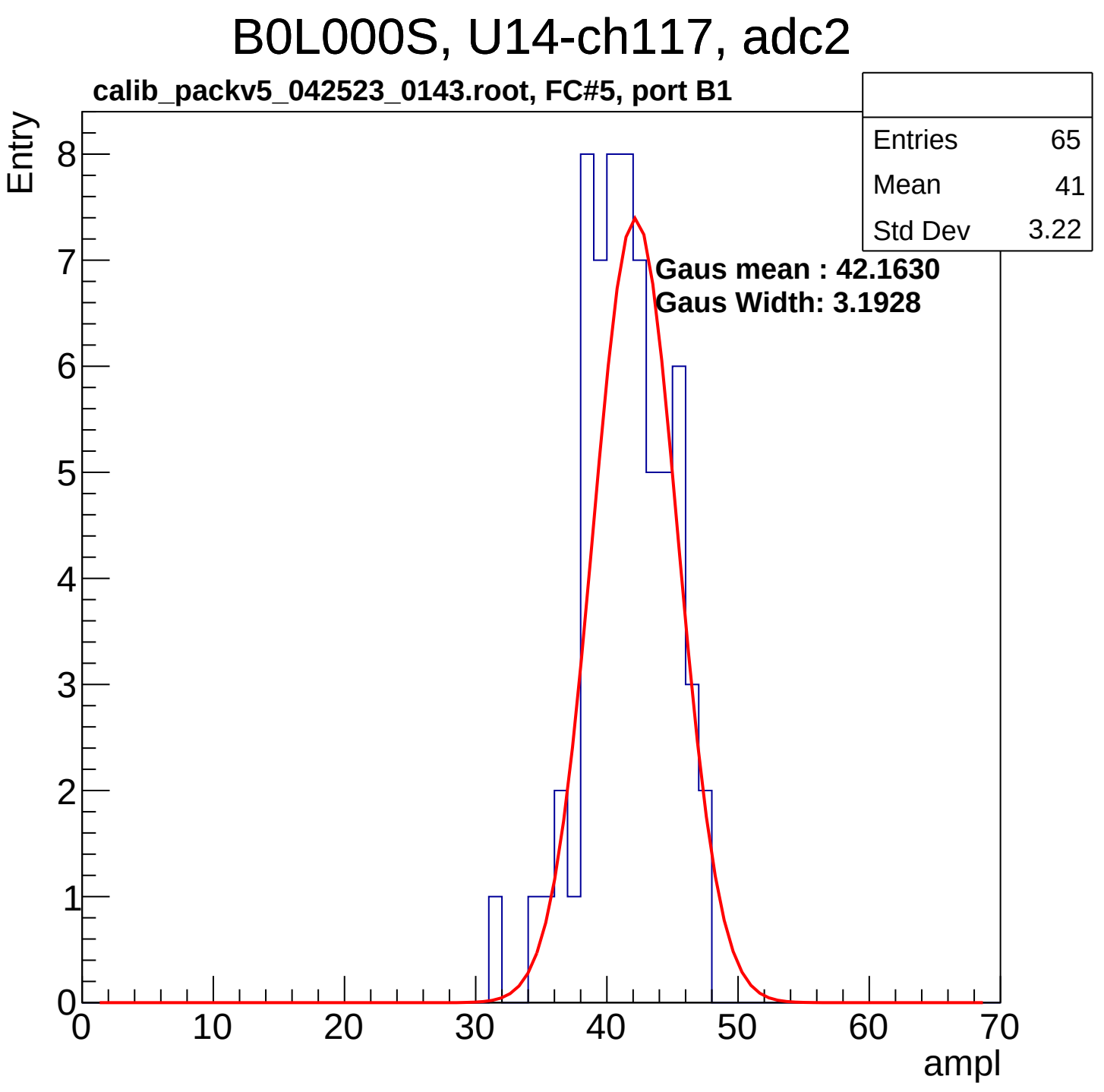
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.17
Std Dev 3.436
Gaus mean : 28.2048
Gaus Width: 3.6666
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 33.87
Std Dev 3.488
Gaus mean : 34.2448
Gaus Width: 3.3716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 41
Std Dev 3.22
Gaus mean : 42.1630
Gaus Width: 3.1928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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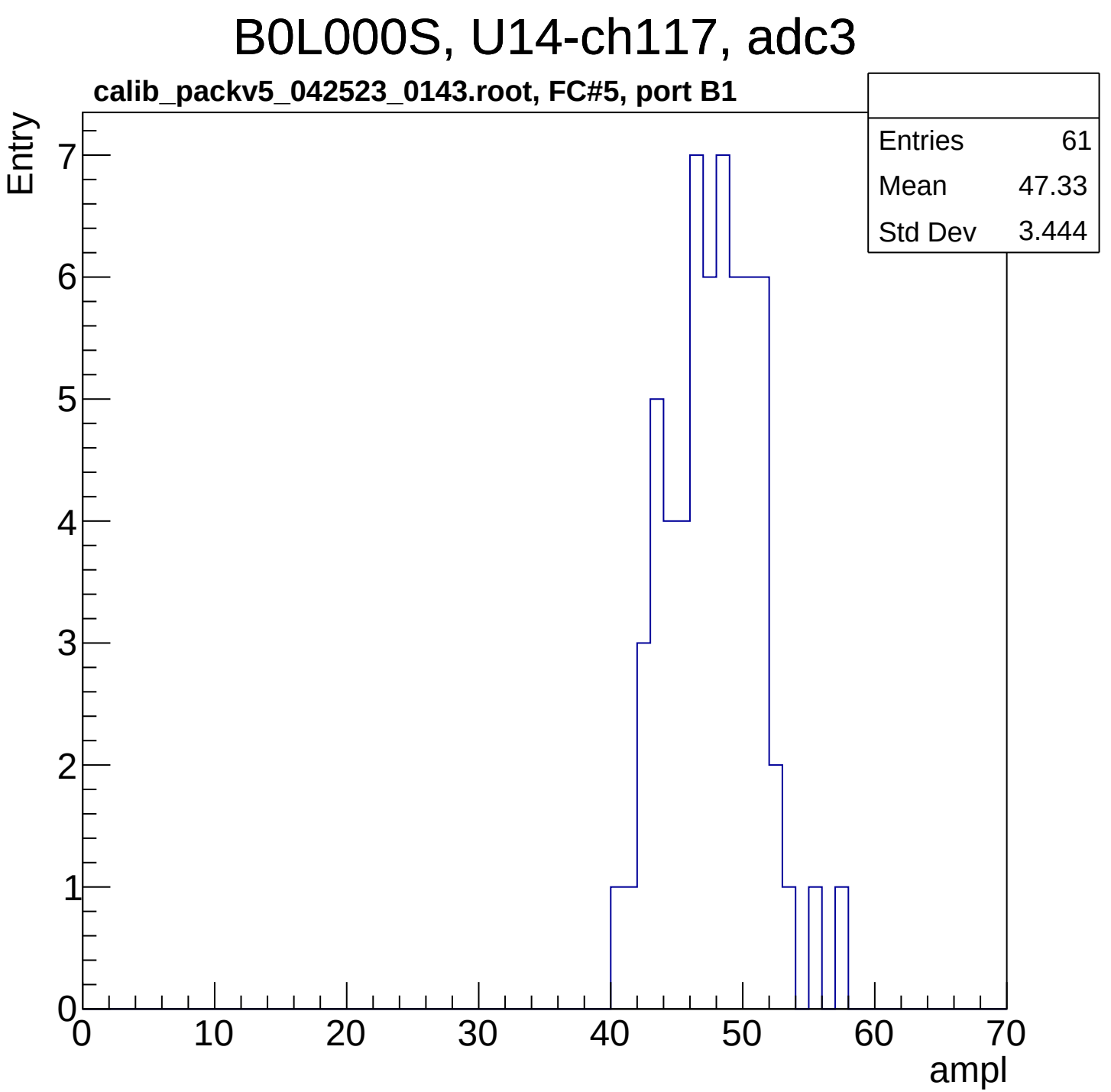
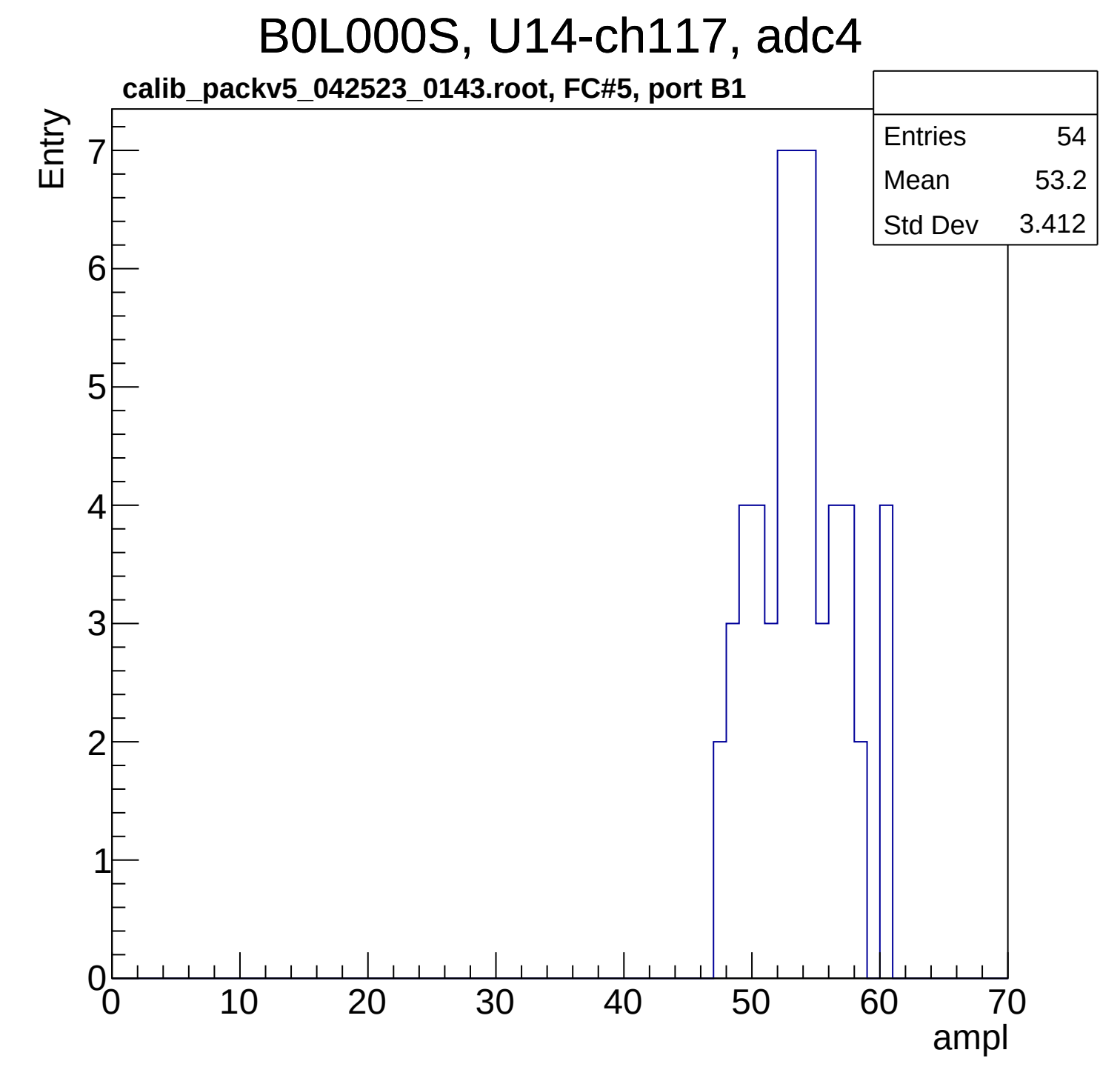
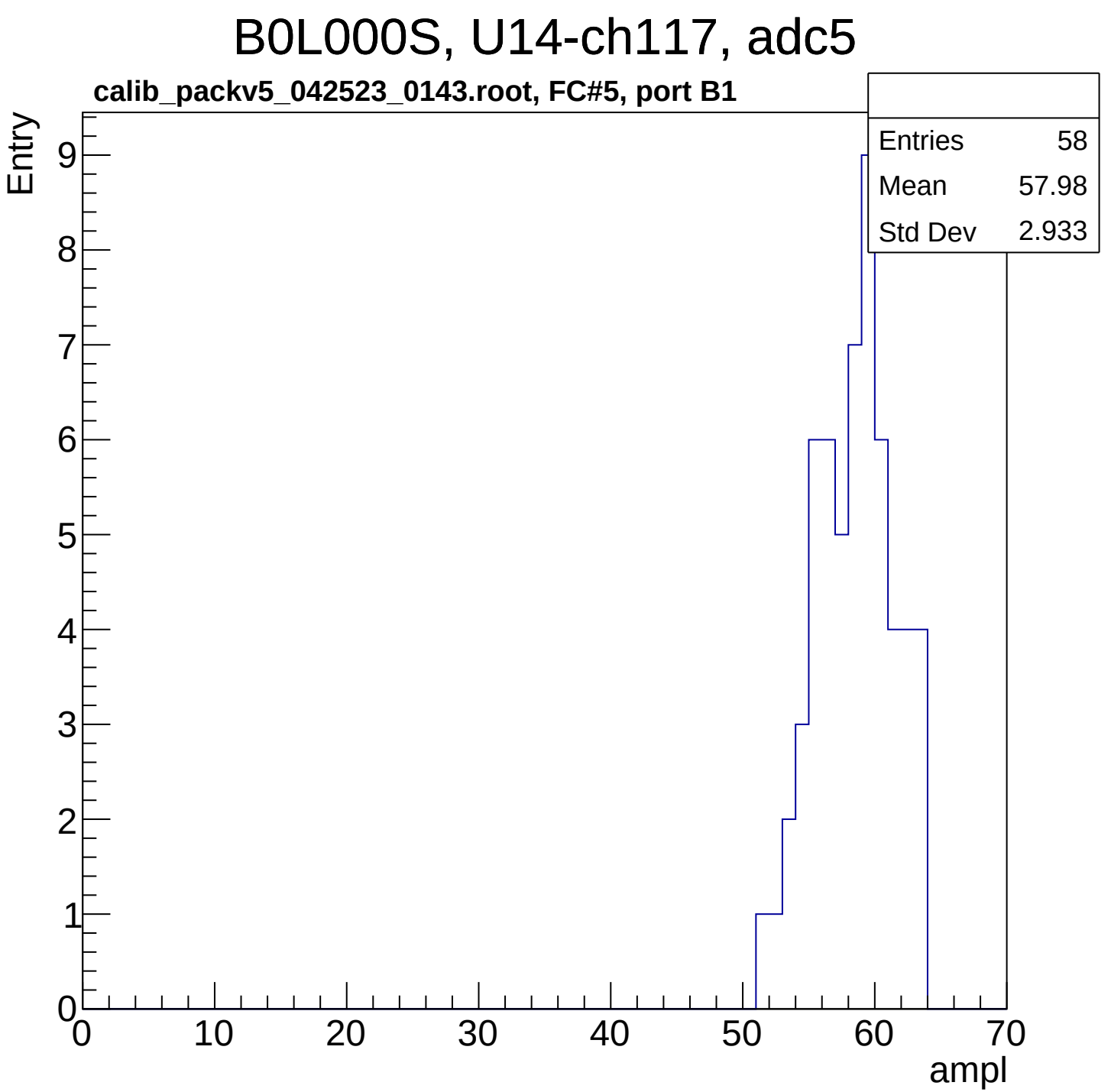
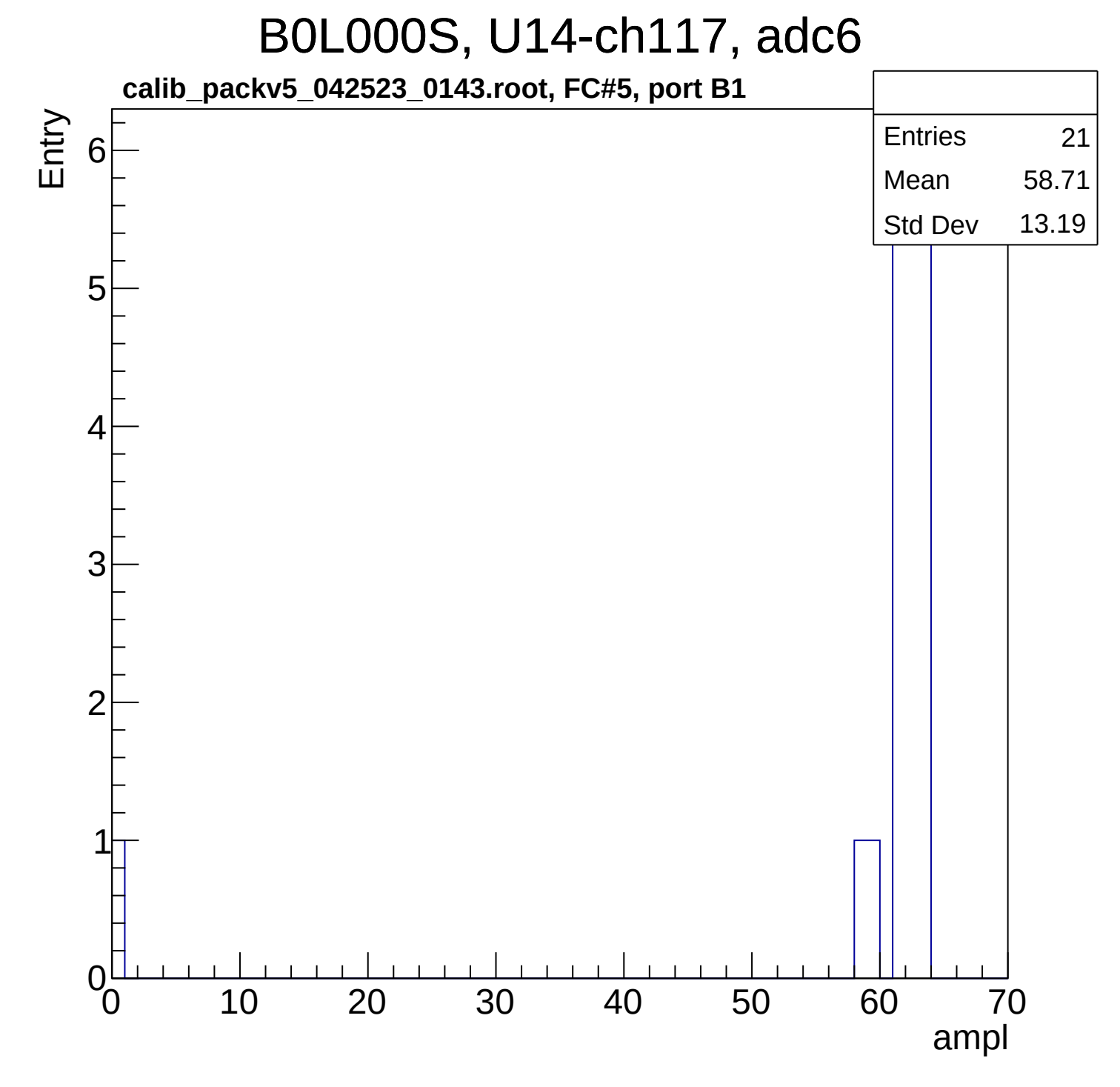
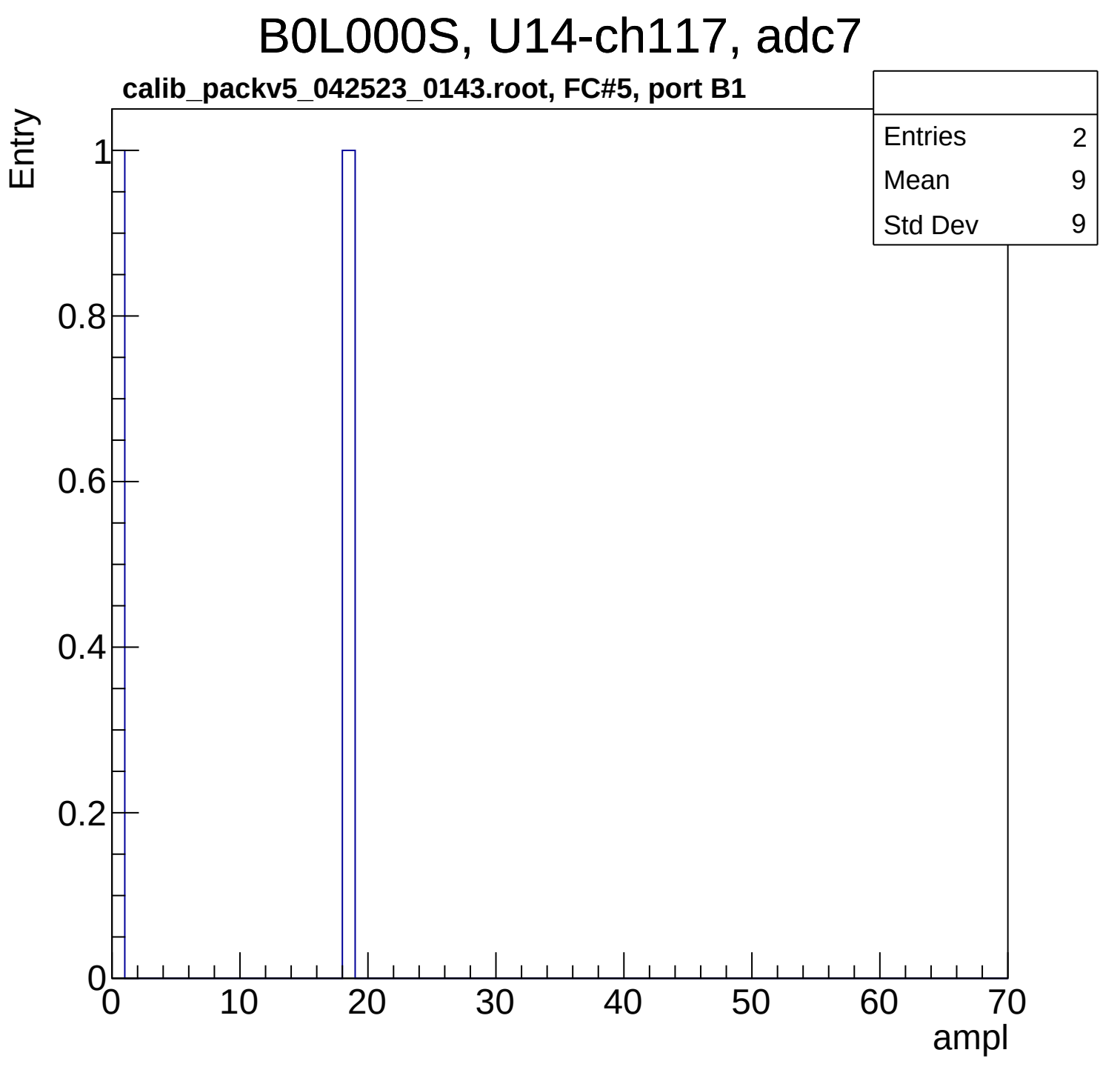
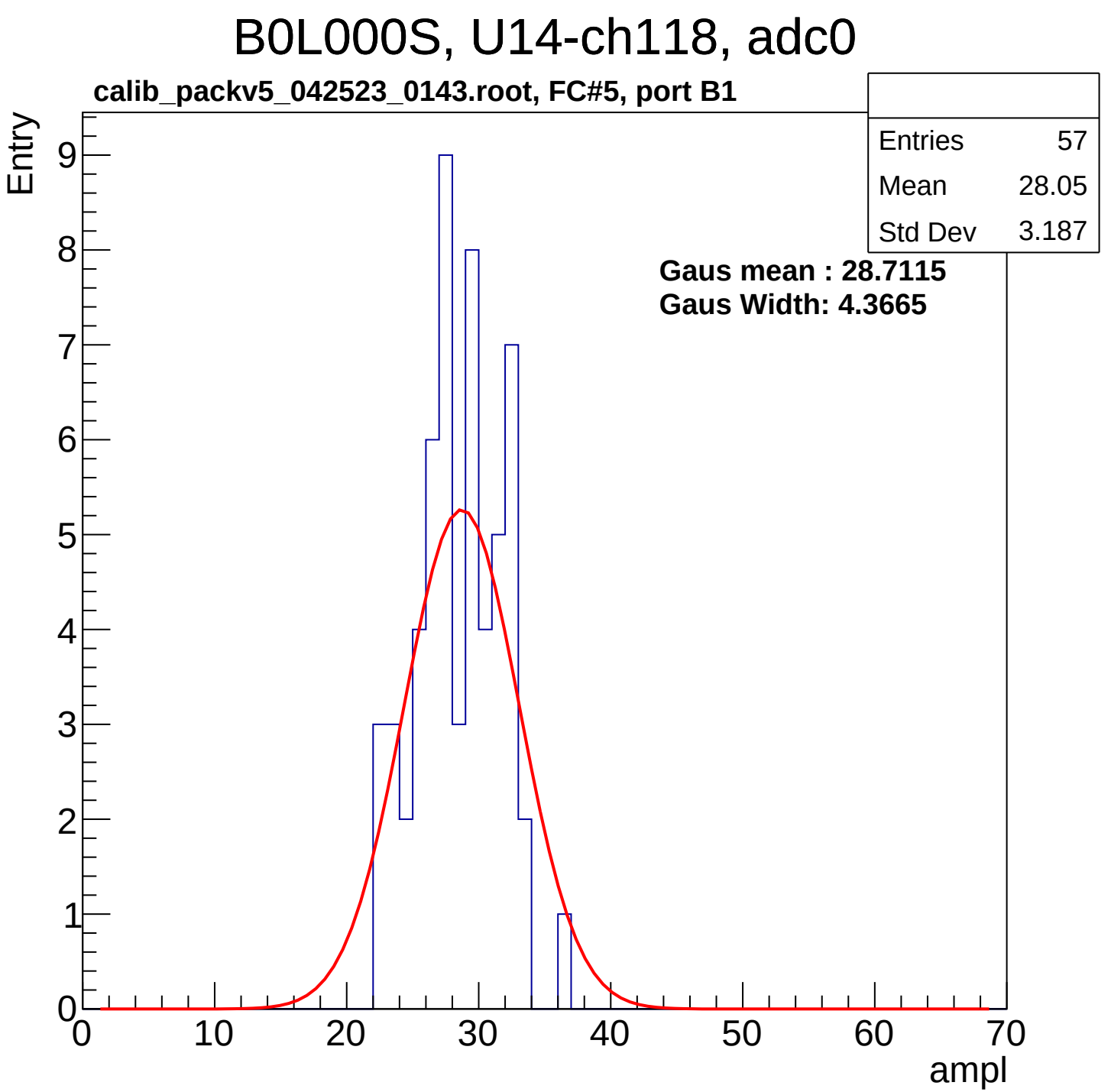

B0L000S, U14-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 47.33
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

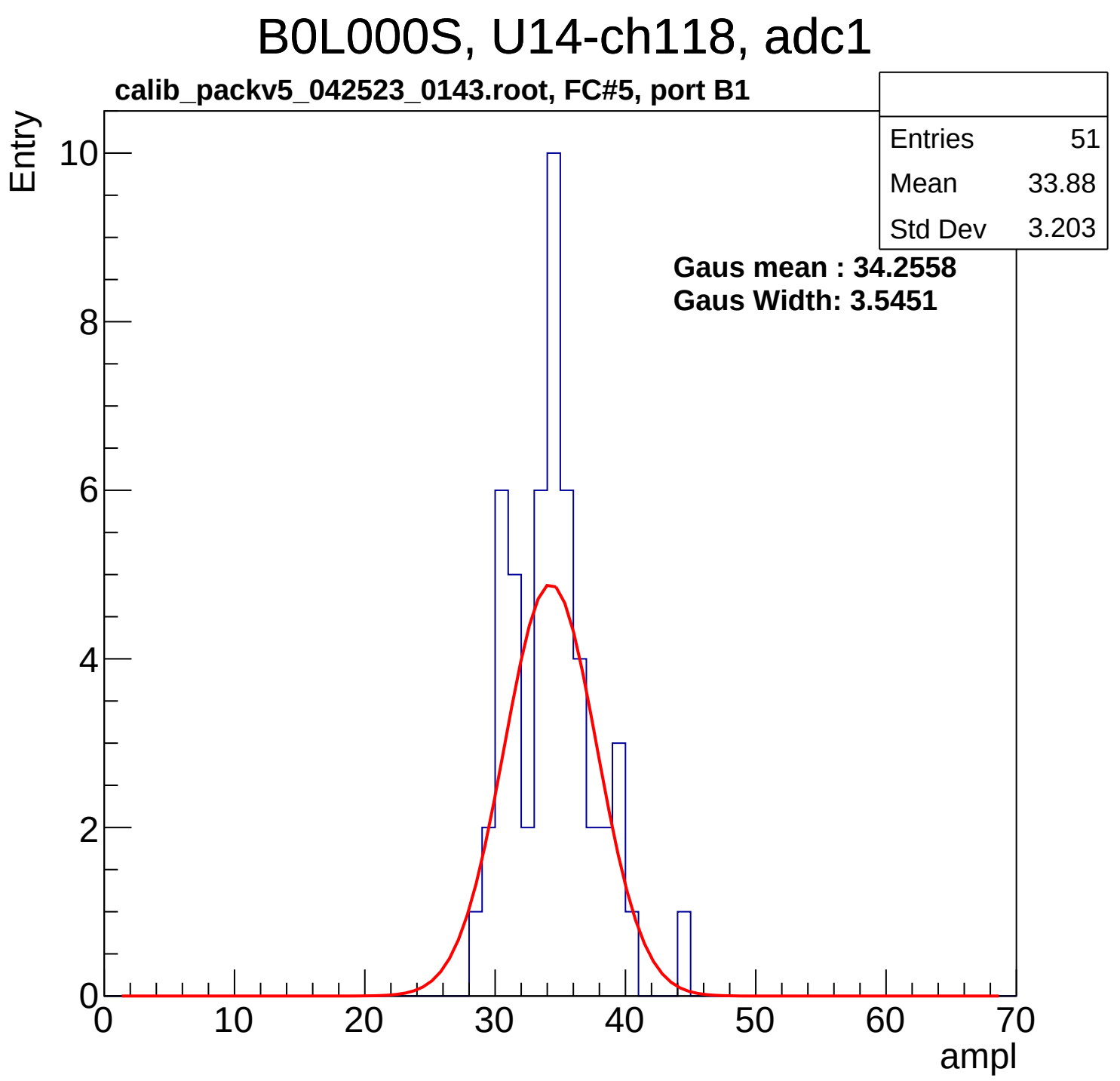

B0L000S, U14-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 53.2
Std Dev 3.412
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

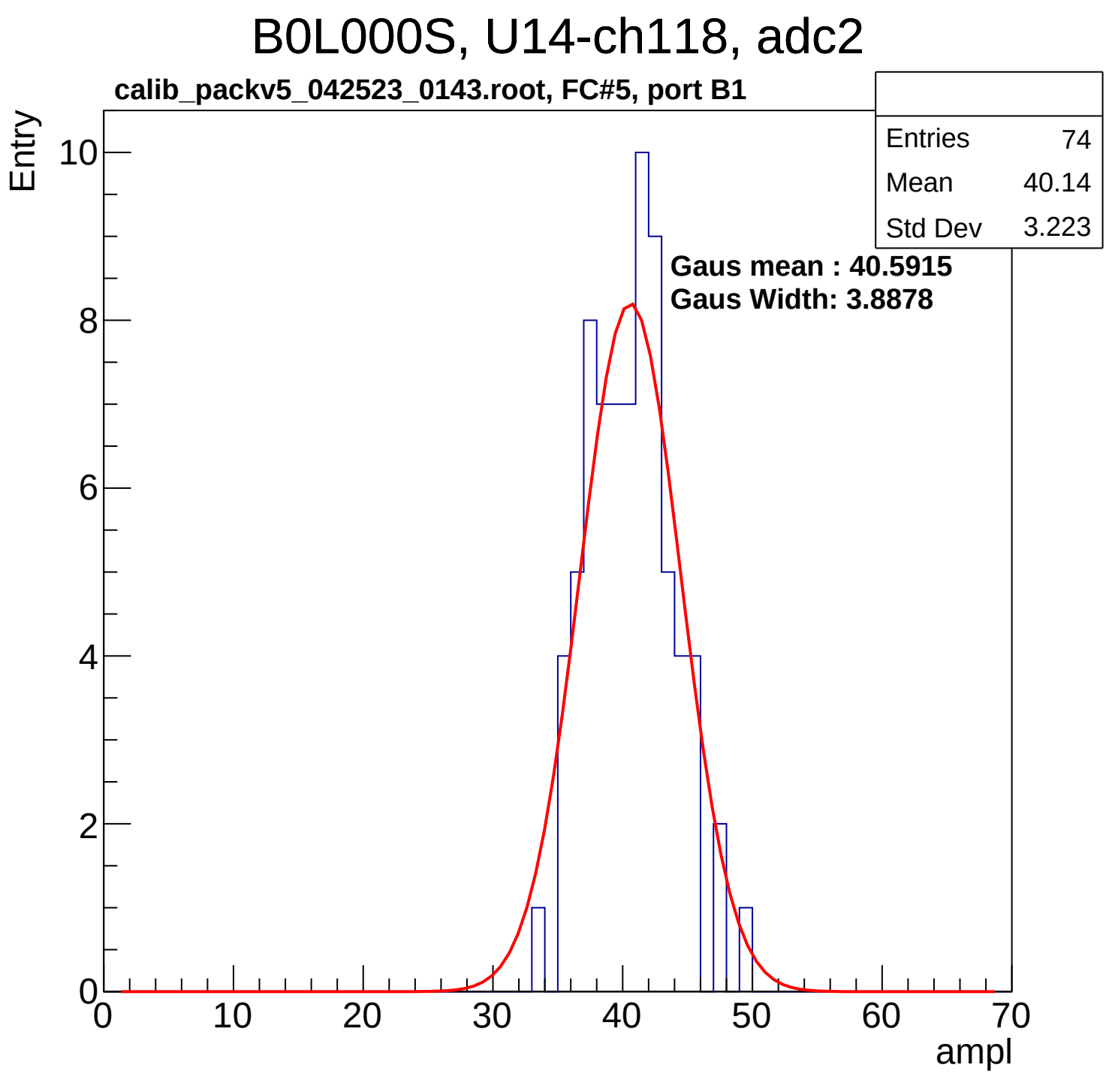

B0L000S, U14-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 57.98
Std Dev 2.933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 58.71
Std Dev 13.19
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9
Std Dev 9
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 28.05
Std Dev 3.187
Gaus mean : 28.7115
Gaus Width: 4.3665
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 33.88
Std Dev 3.203
Gaus mean : 34.2558
Gaus Width: 3.5451
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 40.14
Std Dev 3.223
Gaus mean : 40.5915
Gaus Width: 3.8878
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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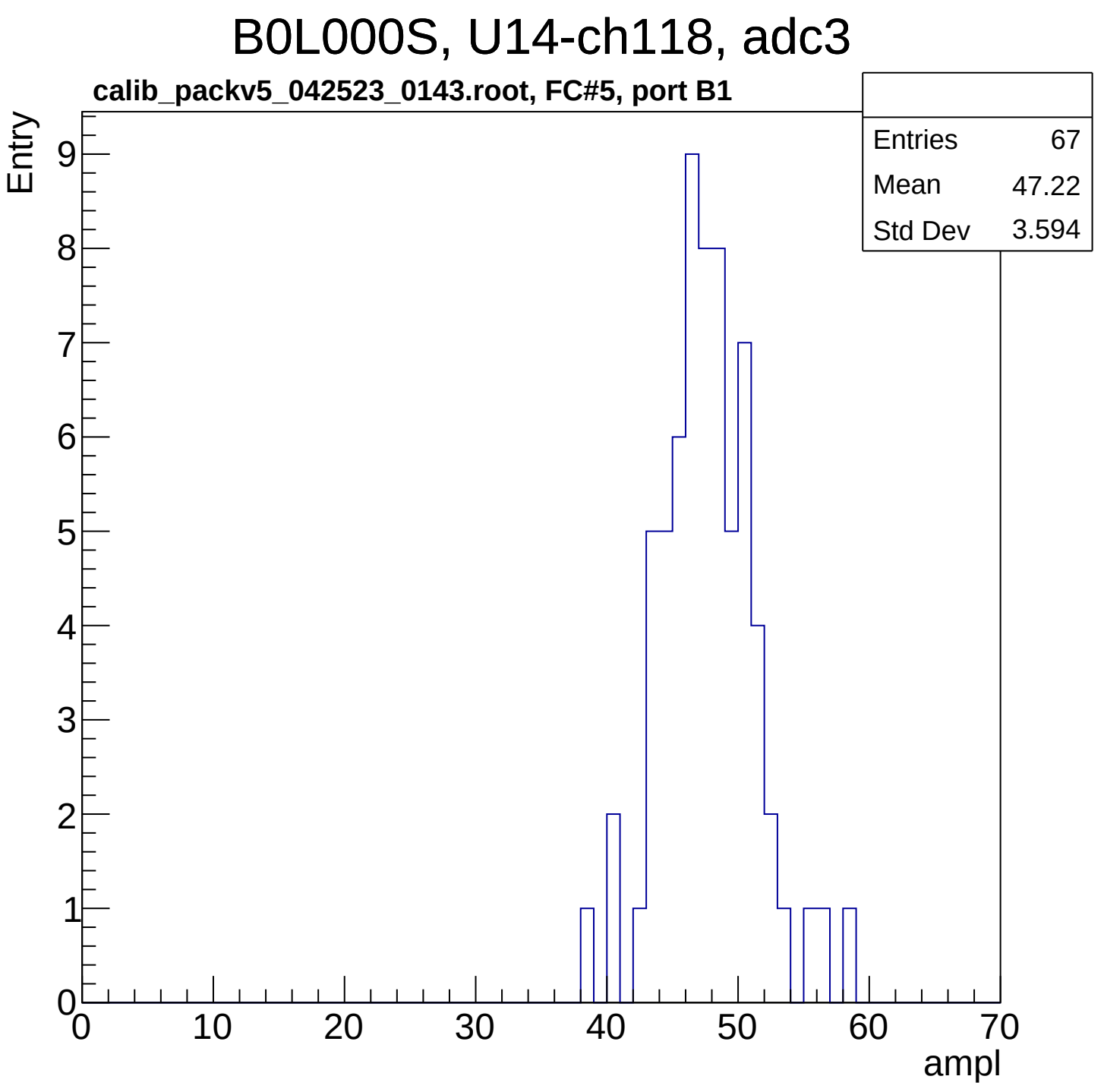
B0L000S, U14-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 47.22
Std Dev 3.594
Entry
ampl
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4
5
6
7
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0
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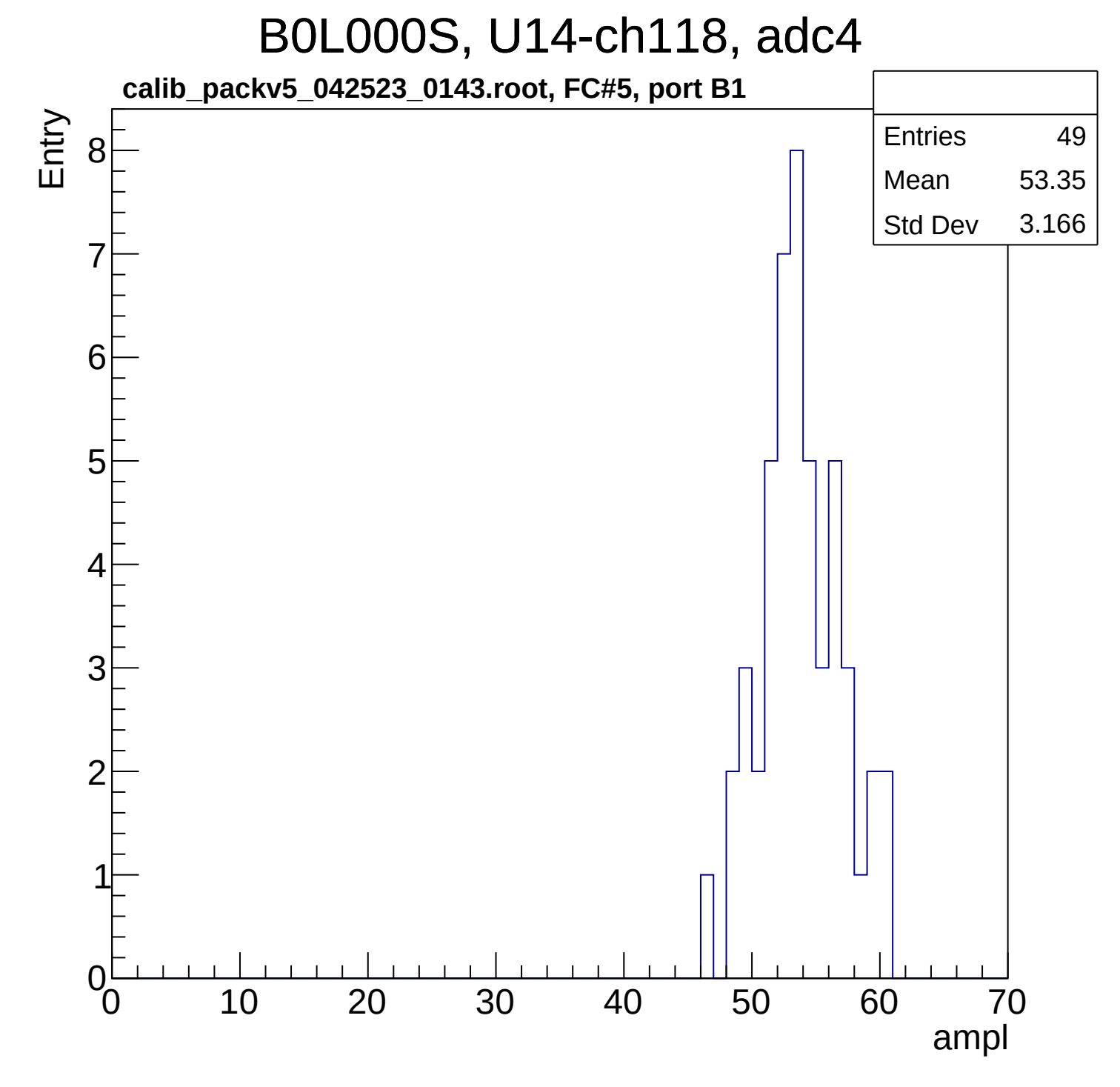
B0L000S, U14-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 53.35
Std Dev 3.166
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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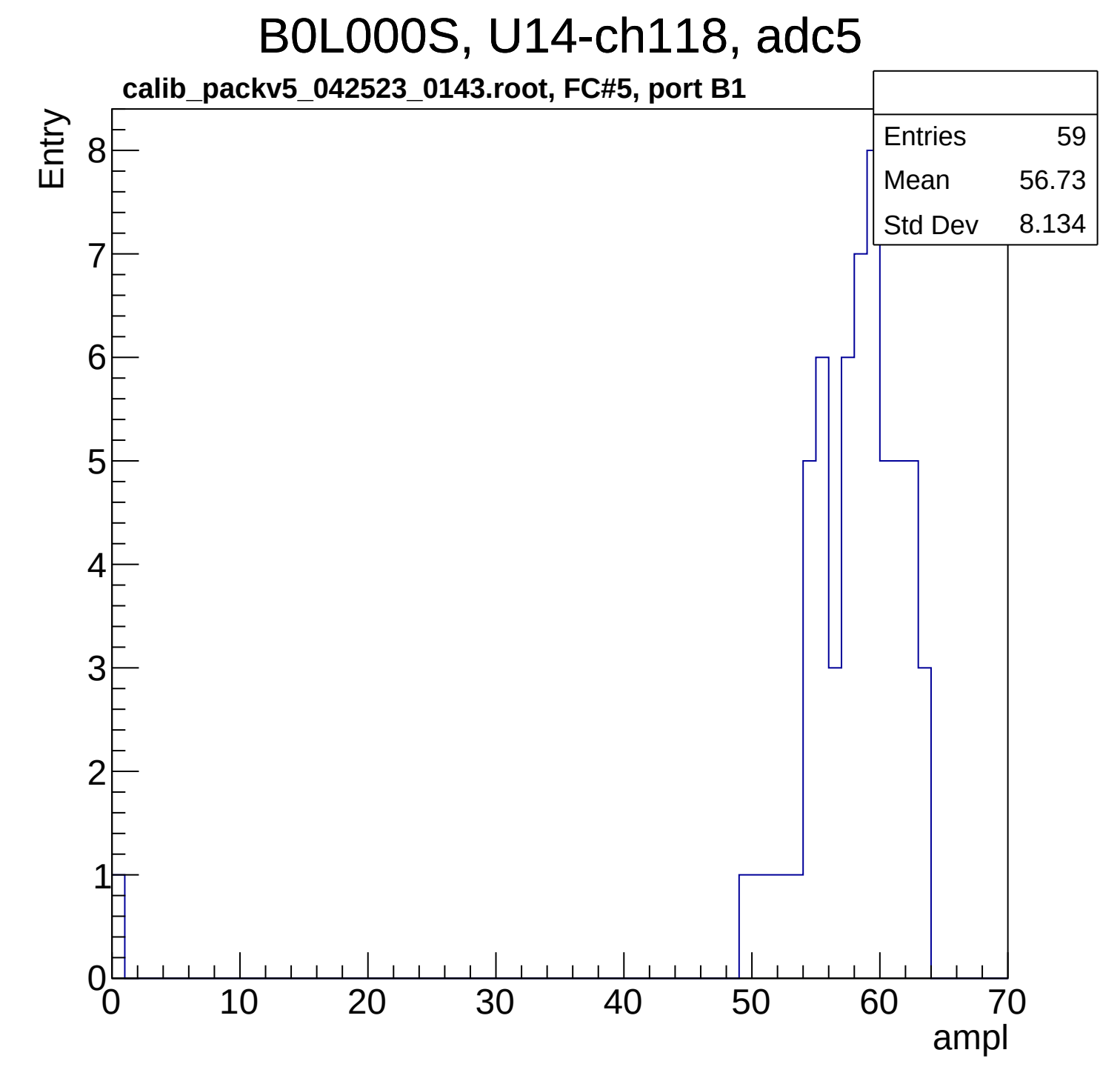
B0L000S, U14-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.73
Std Dev 8.134
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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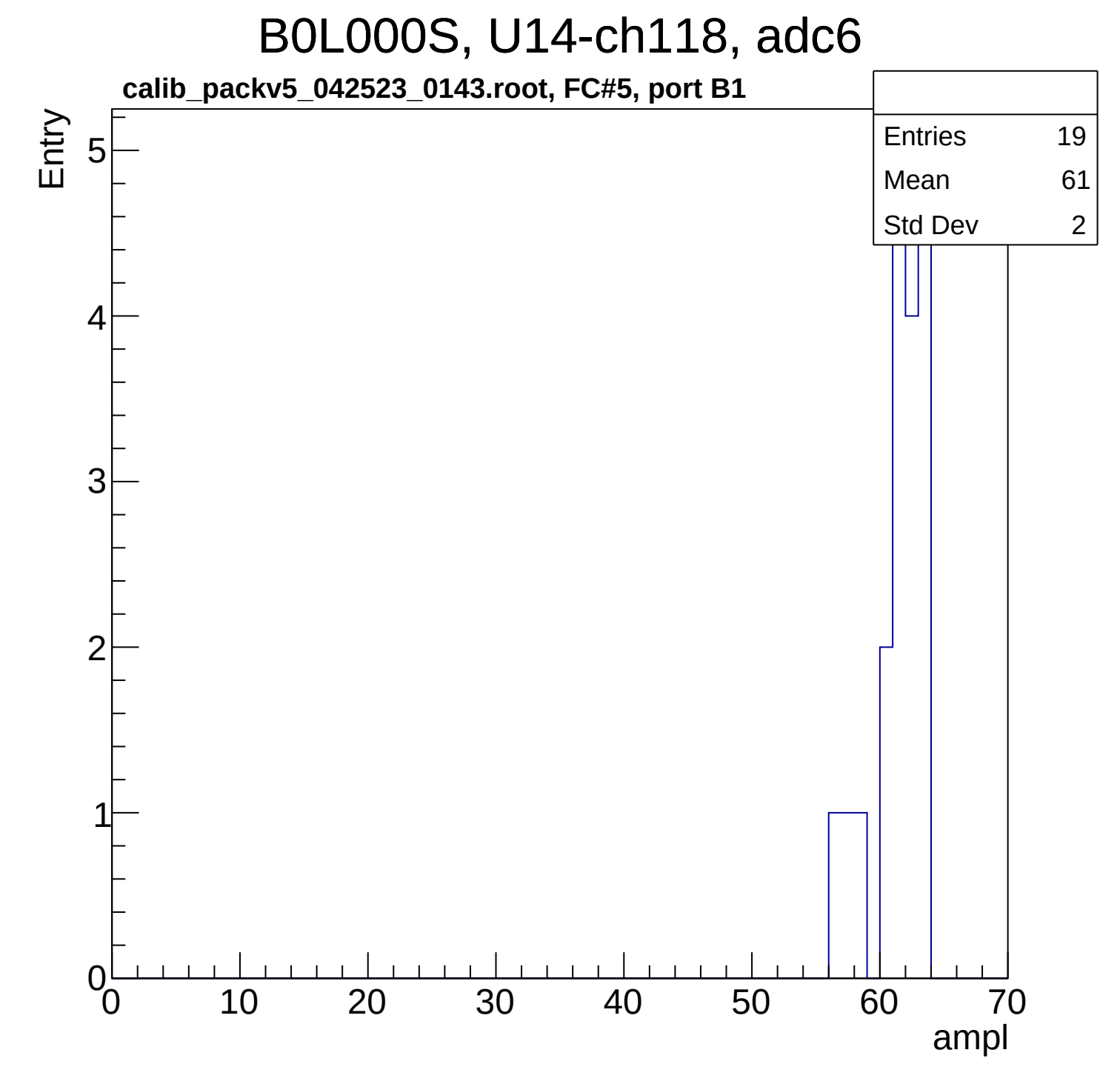
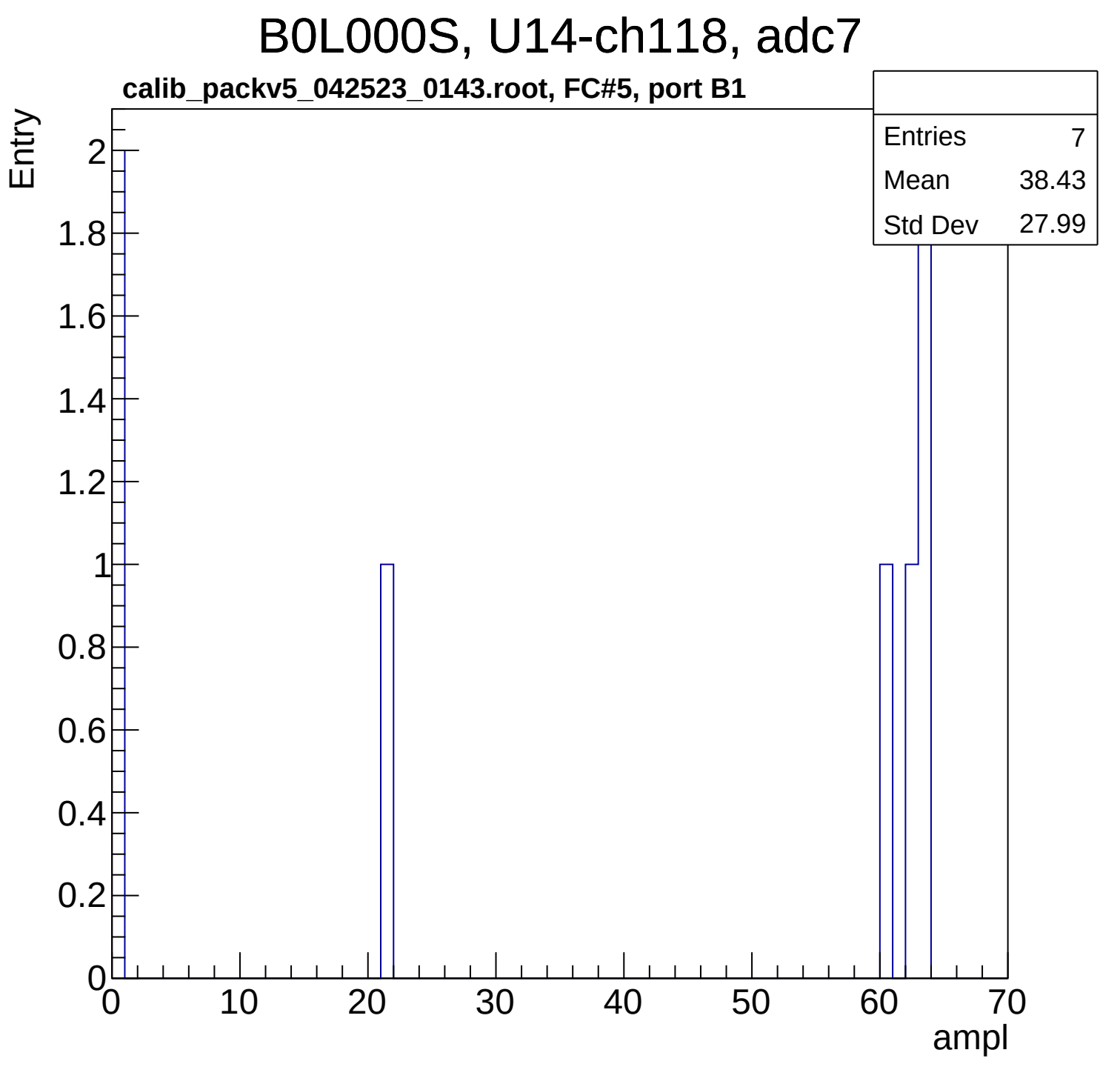
60
70


B0L000S, U14-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61
Std Dev 2
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 38.43
Std Dev 27.99
Entry
ampl
0
0.2
0.4
0.6
0.8
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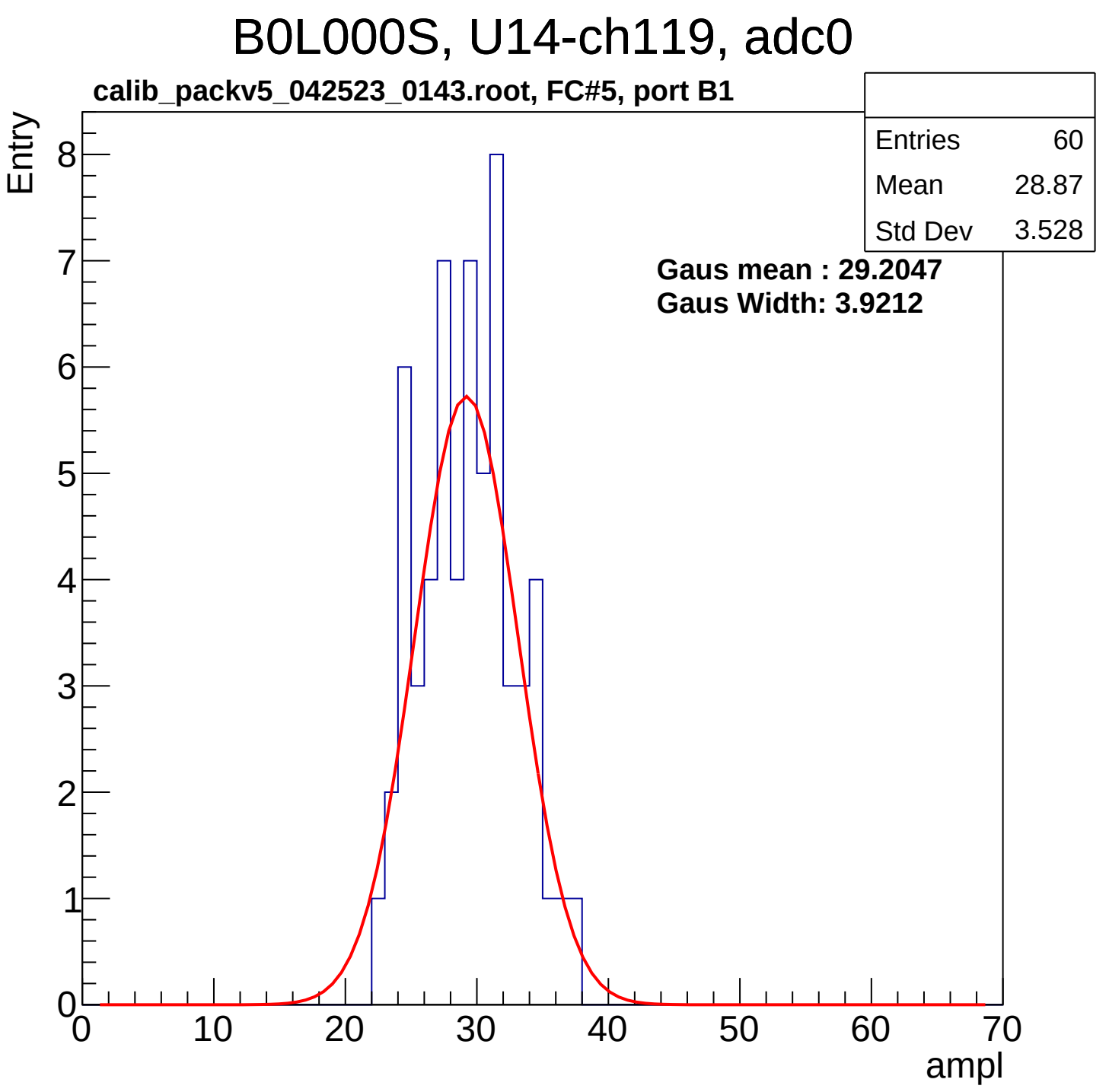
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.87
Std Dev 3.528
Gaus mean : 29.2047
Gaus Width: 3.9212
Entry
ampl
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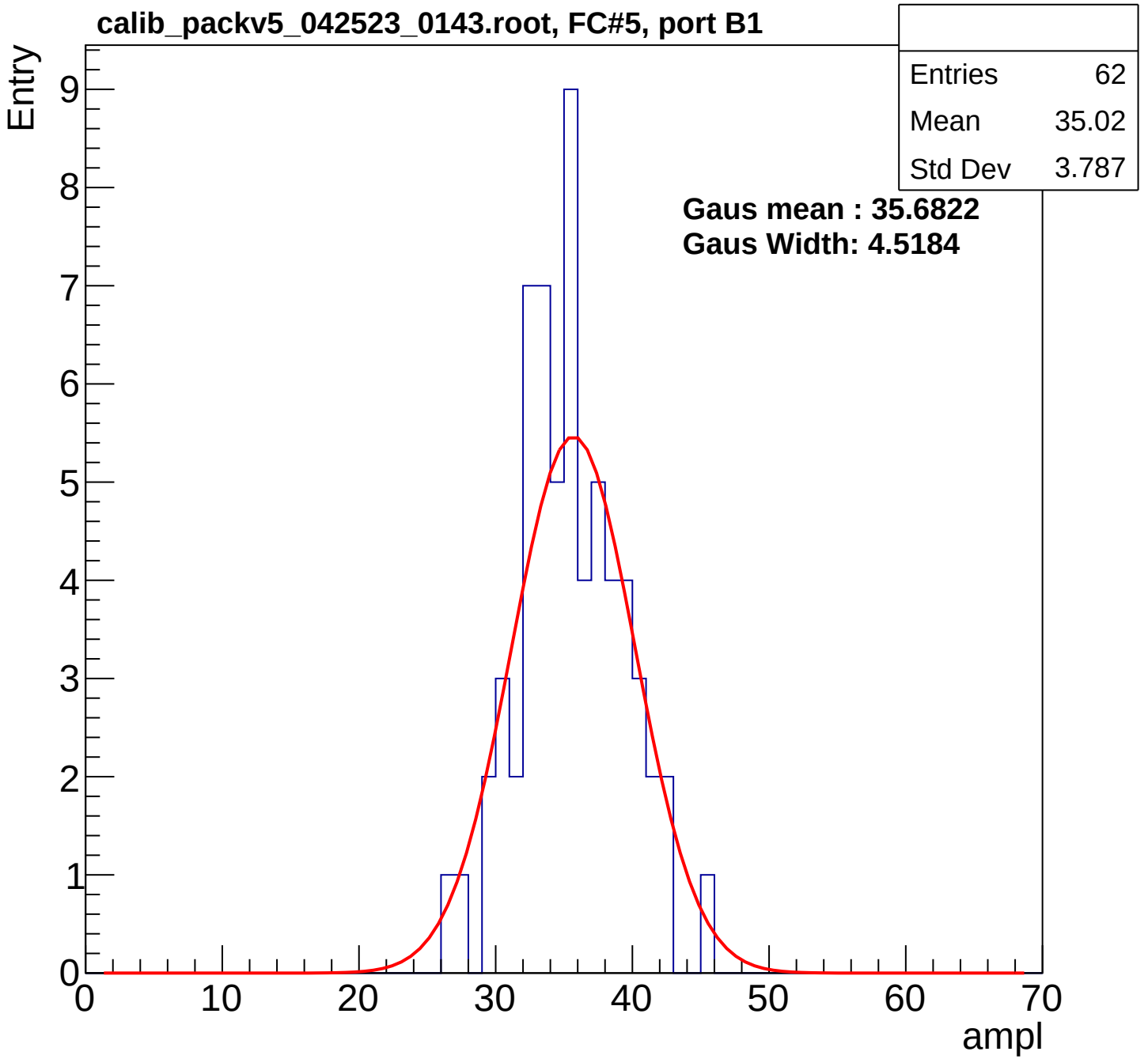
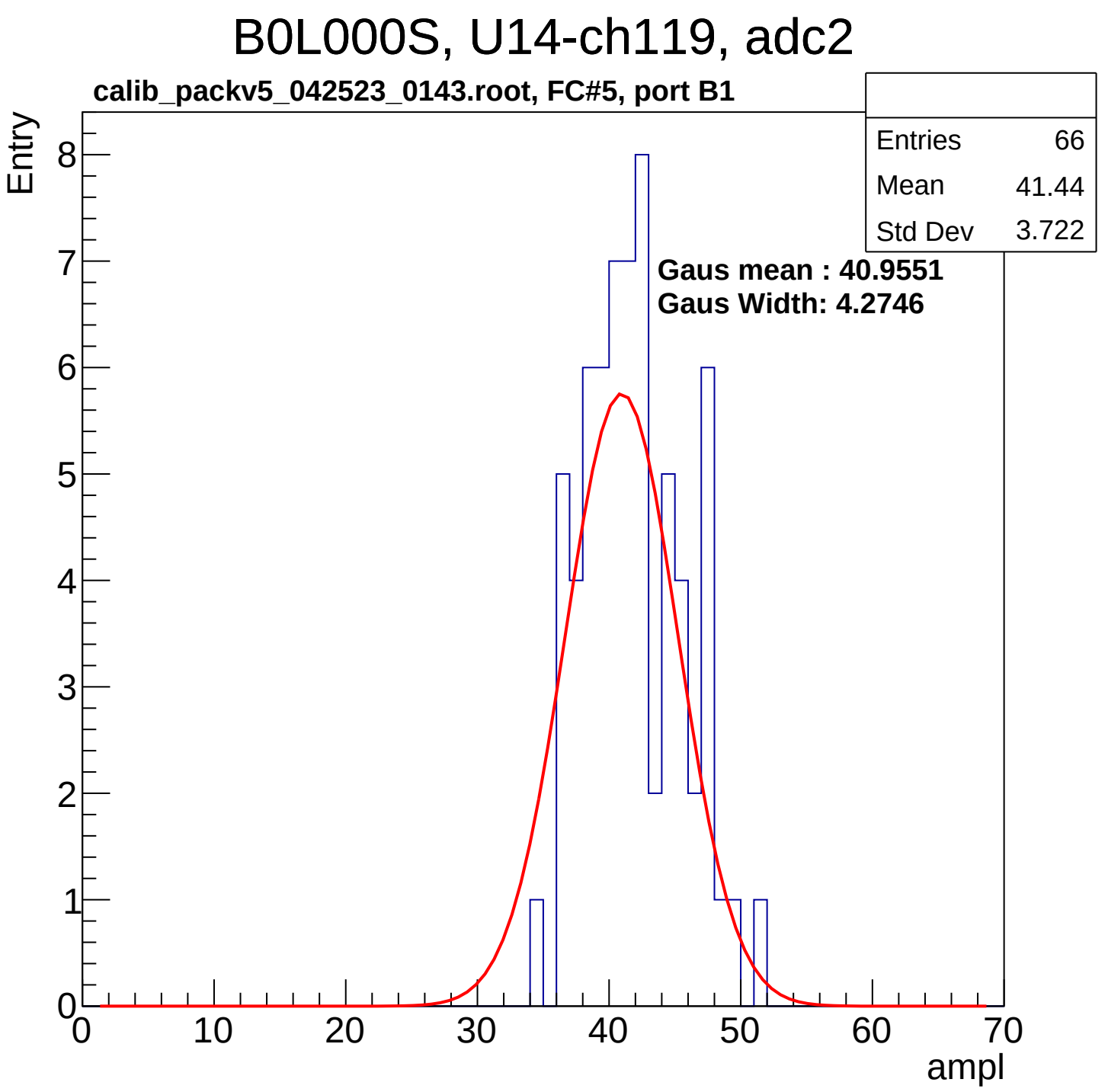
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.02
Std Dev 3.787
Gaus mean : 35.6822
Gaus Width: 4.5184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U14-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 41.44
Std Dev 3.722
Gaus mean : 40.9551
Gaus Width: 4.2746
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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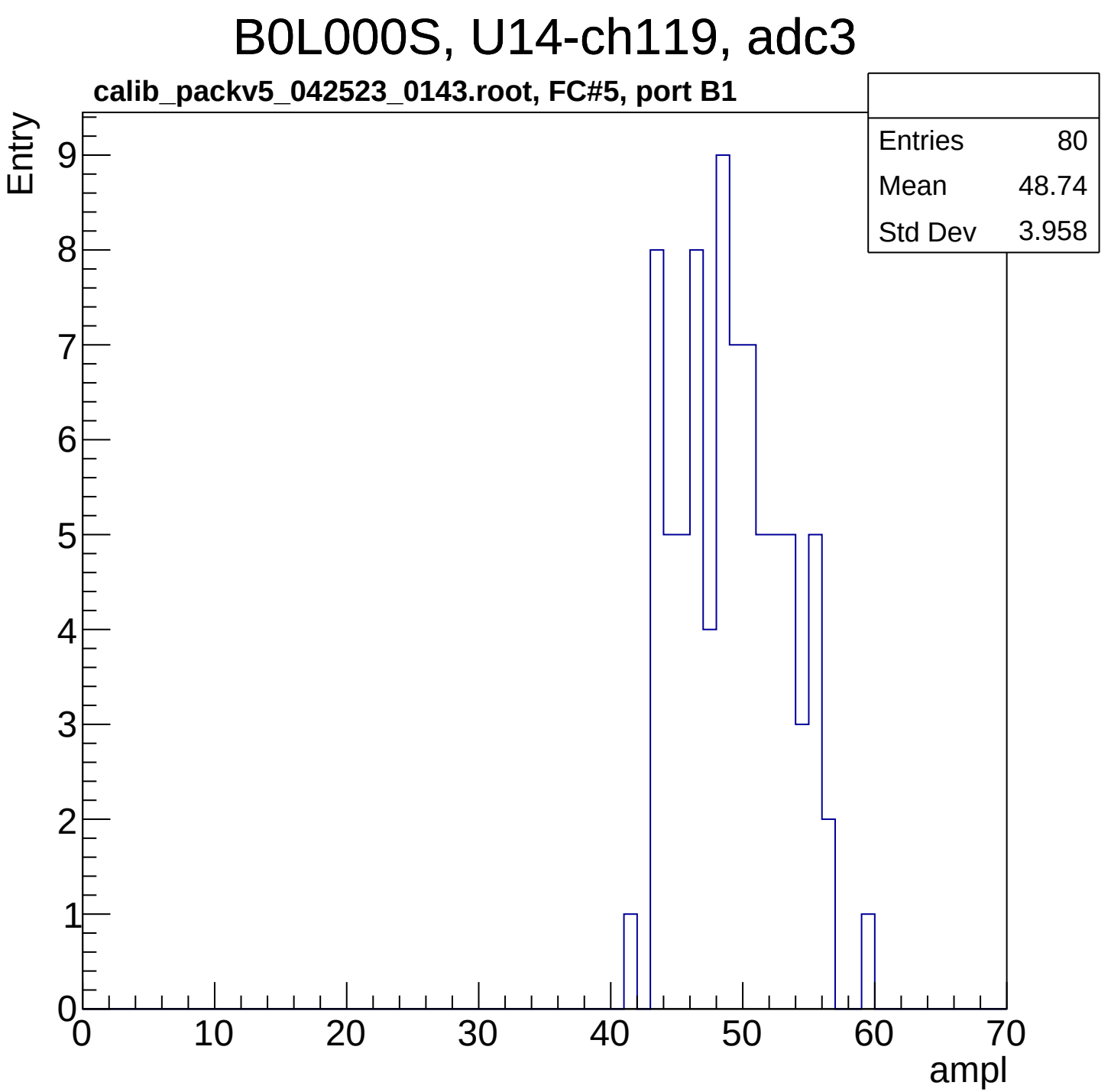
B0L000S, U14-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 48.74
Std Dev 3.958
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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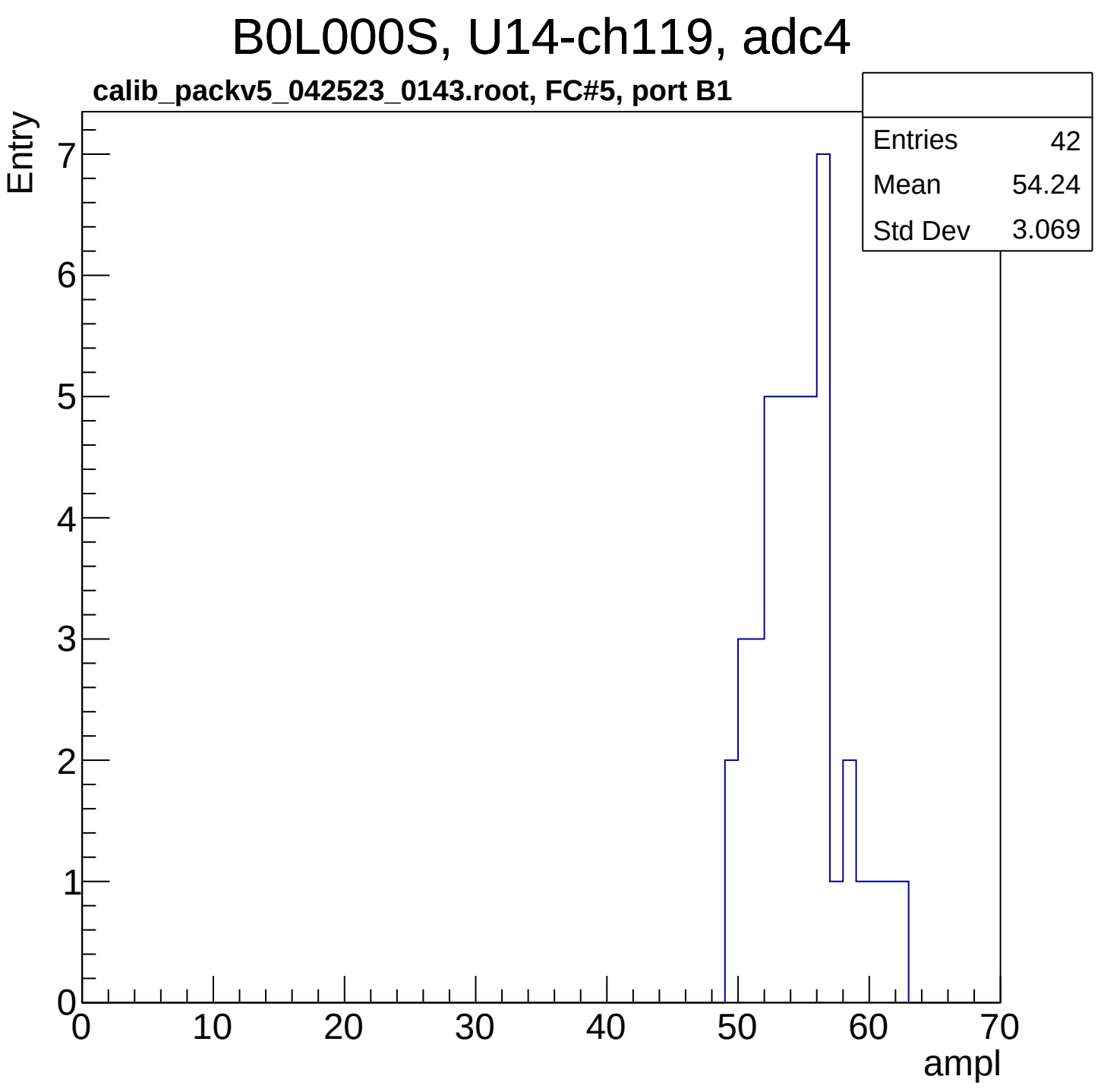
B0L000S, U14-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 54.24
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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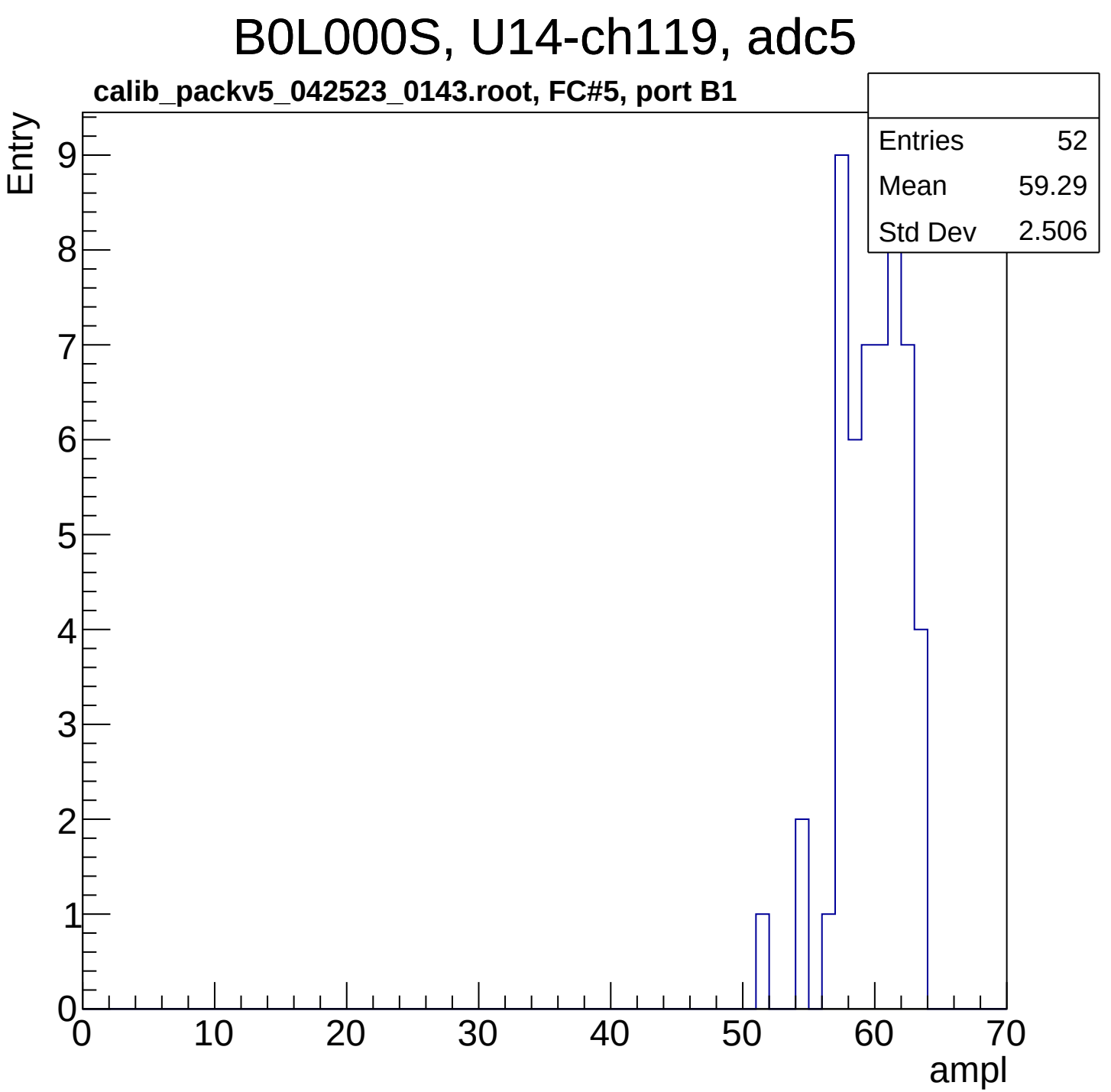
B0L000S, U14-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.29
Std Dev 2.506
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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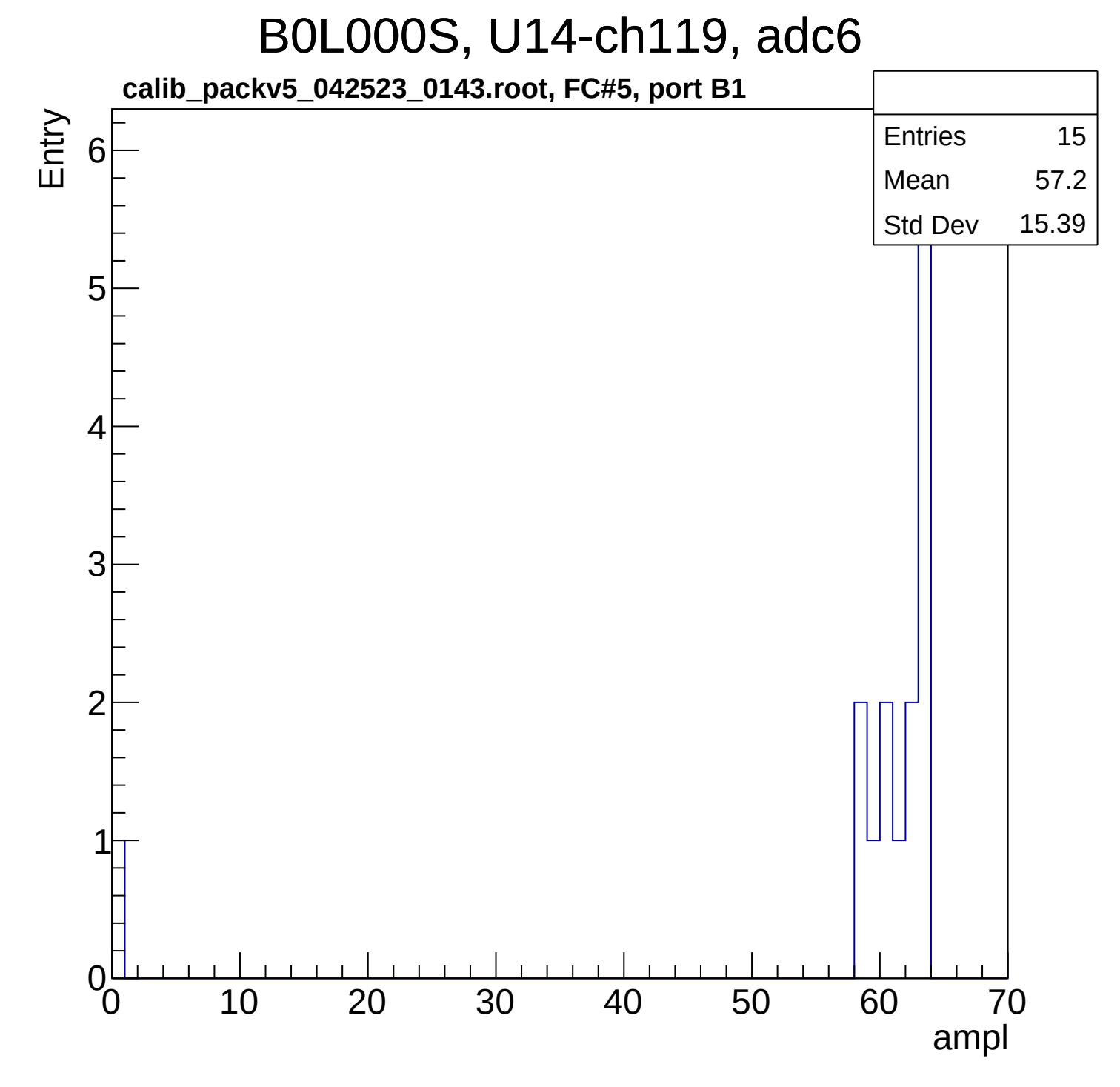
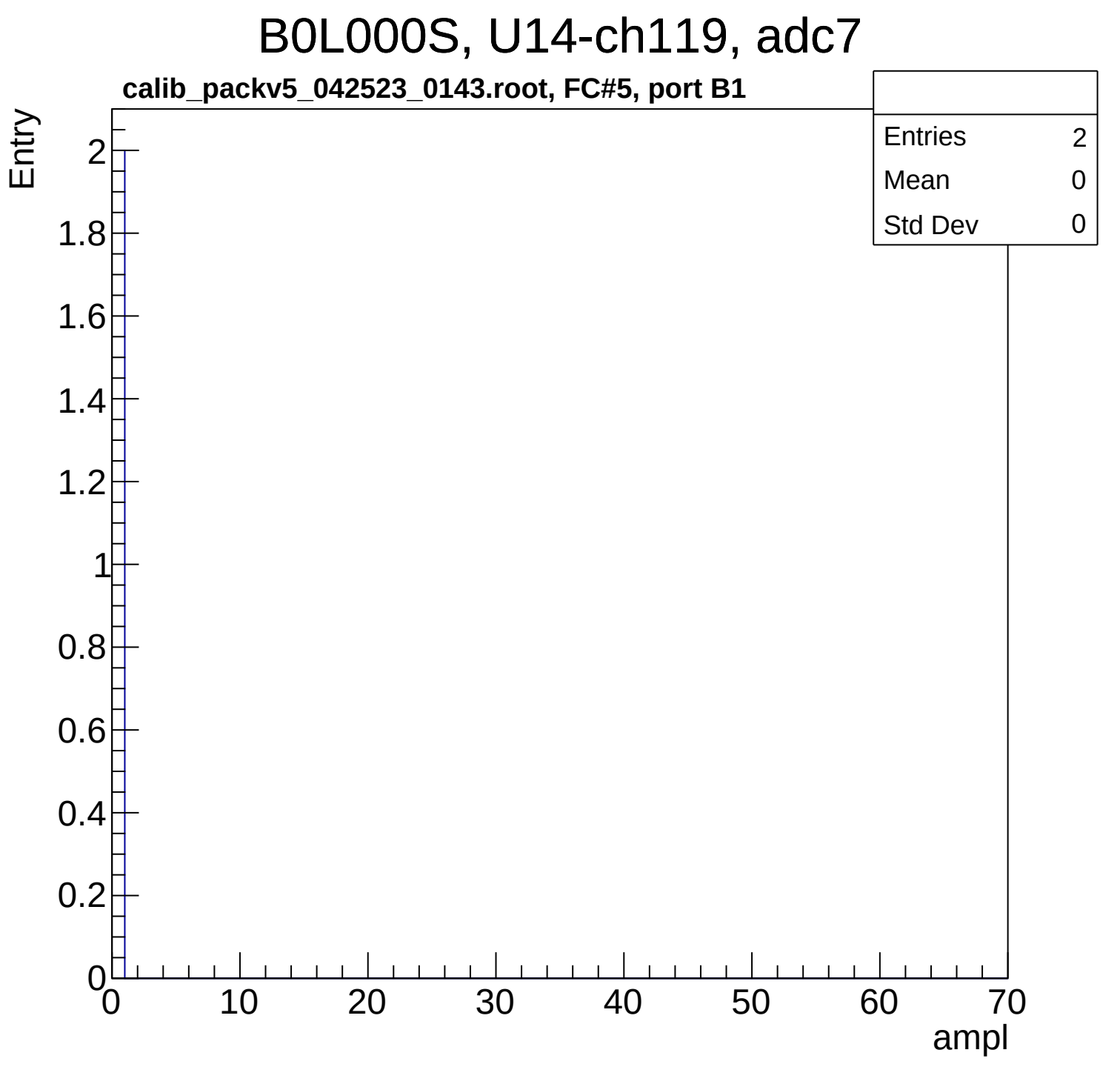

B0L000S, U14-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 15
Mean 57.2
Std Dev 15.39
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
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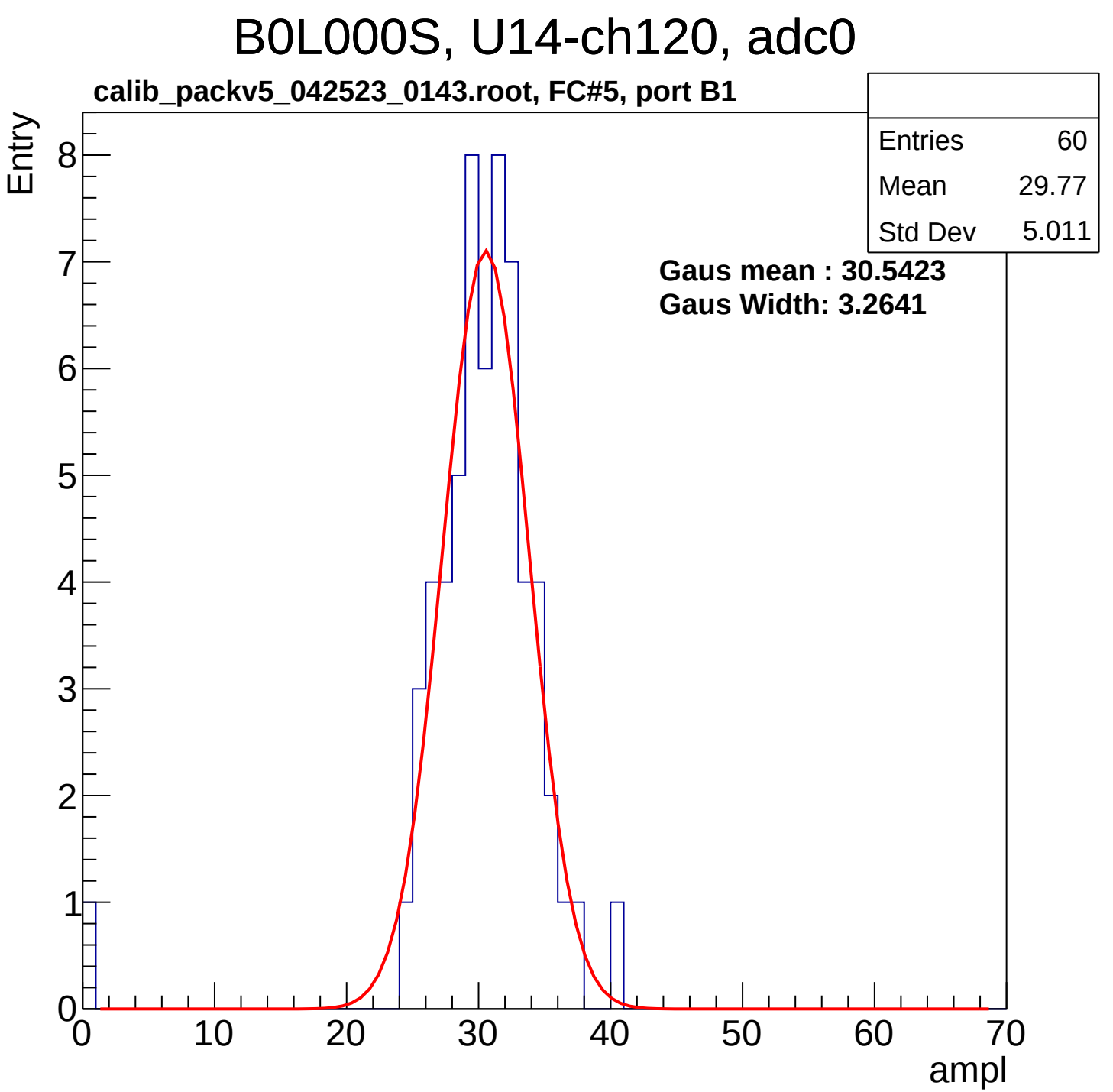
40
50
60
70
ampl


B0L000S, U14-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.77
Std Dev 5.011
Gaus mean : 30.5423
Gaus Width: 3.2641
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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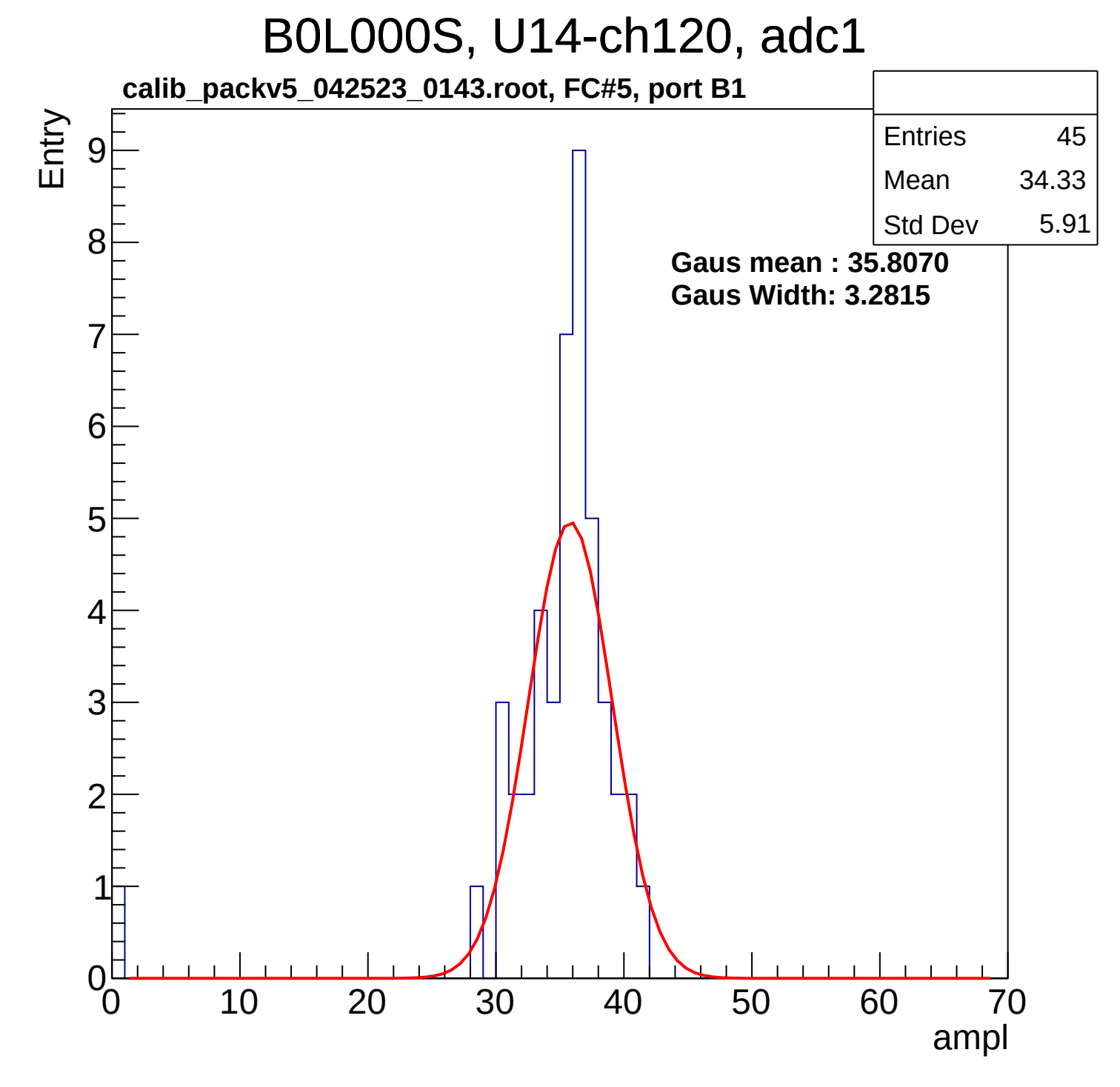
B0L000S, U14-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 34.33
Std Dev 5.91
Gaus mean : 35.8070
Gaus Width: 3.2815
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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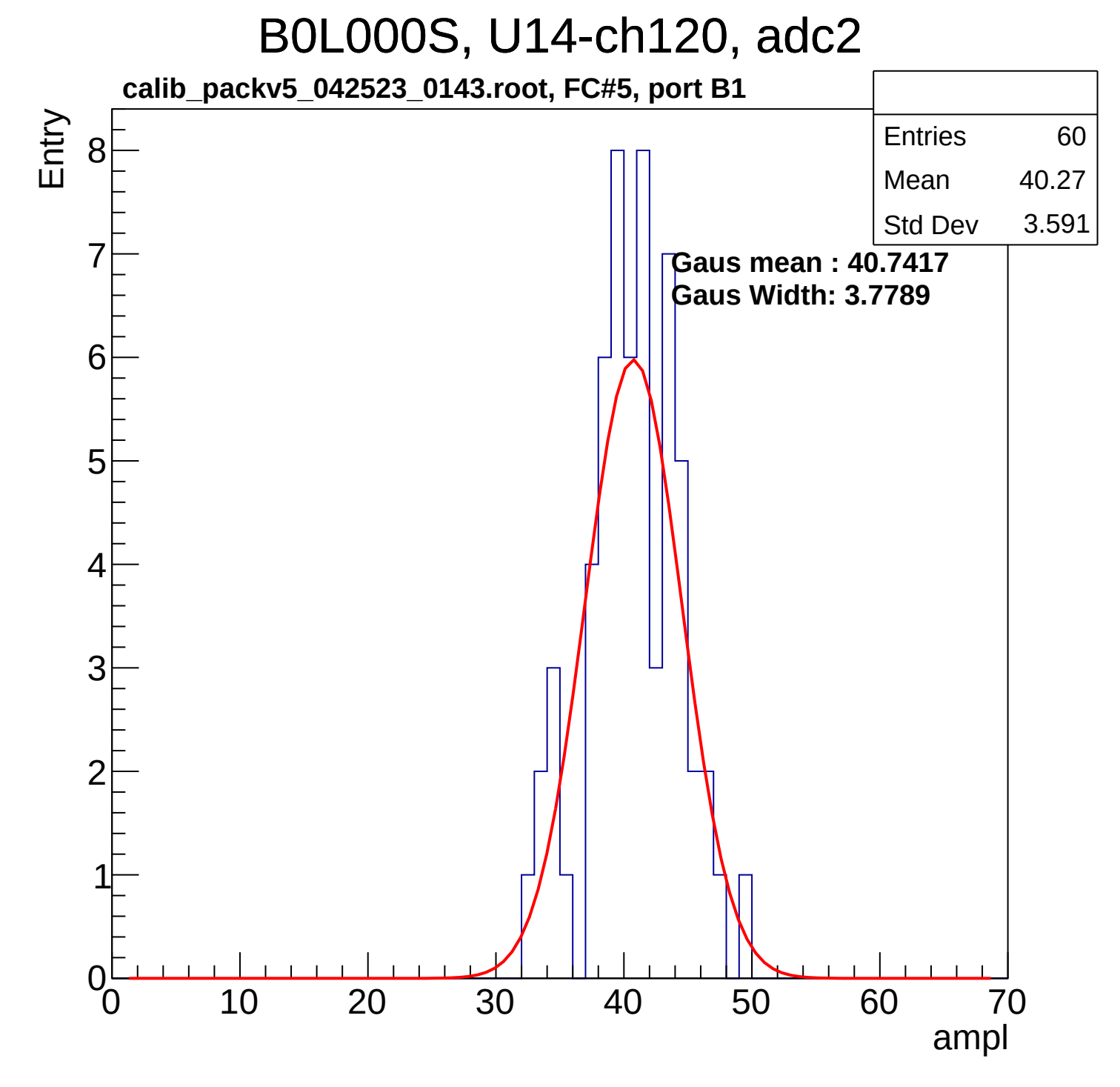
B0L000S, U14-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 40.27
Std Dev 3.591
Gaus mean : 40.7417
Gaus Width: 3.7789
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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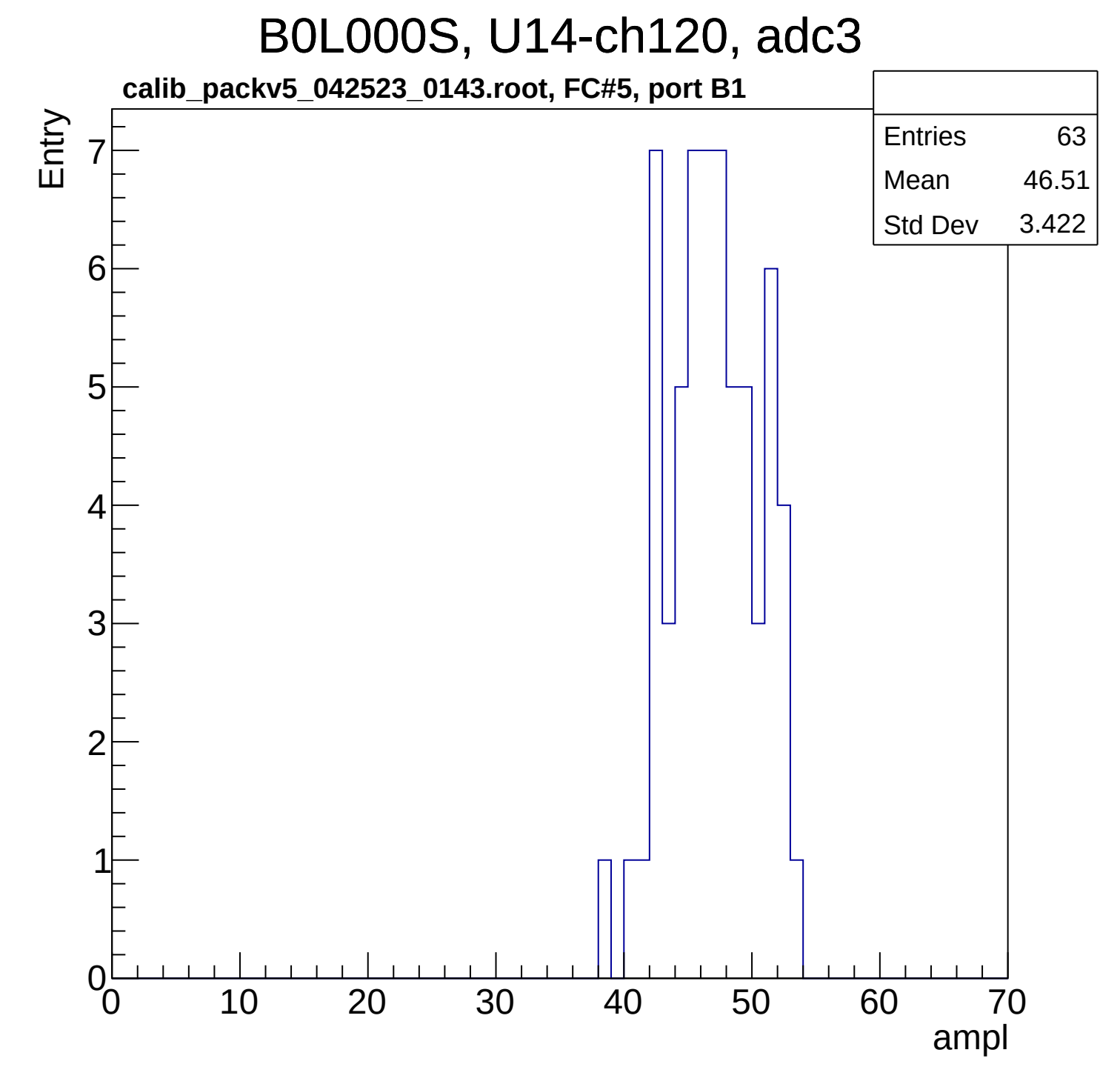
B0L000S, U14-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 46.51
Std Dev 3.422
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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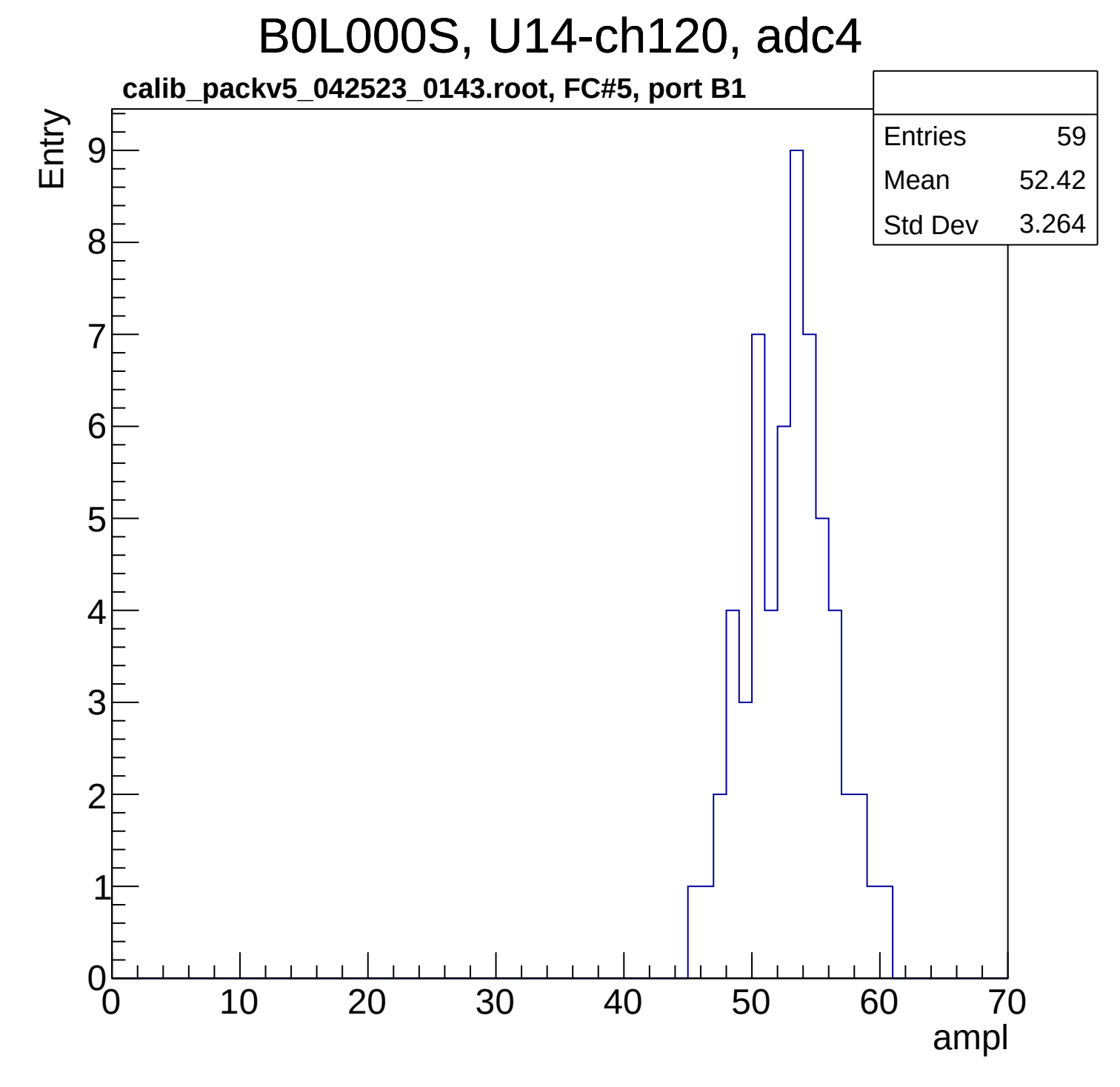
B0L000S, U14-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 52.42
Std Dev 3.264
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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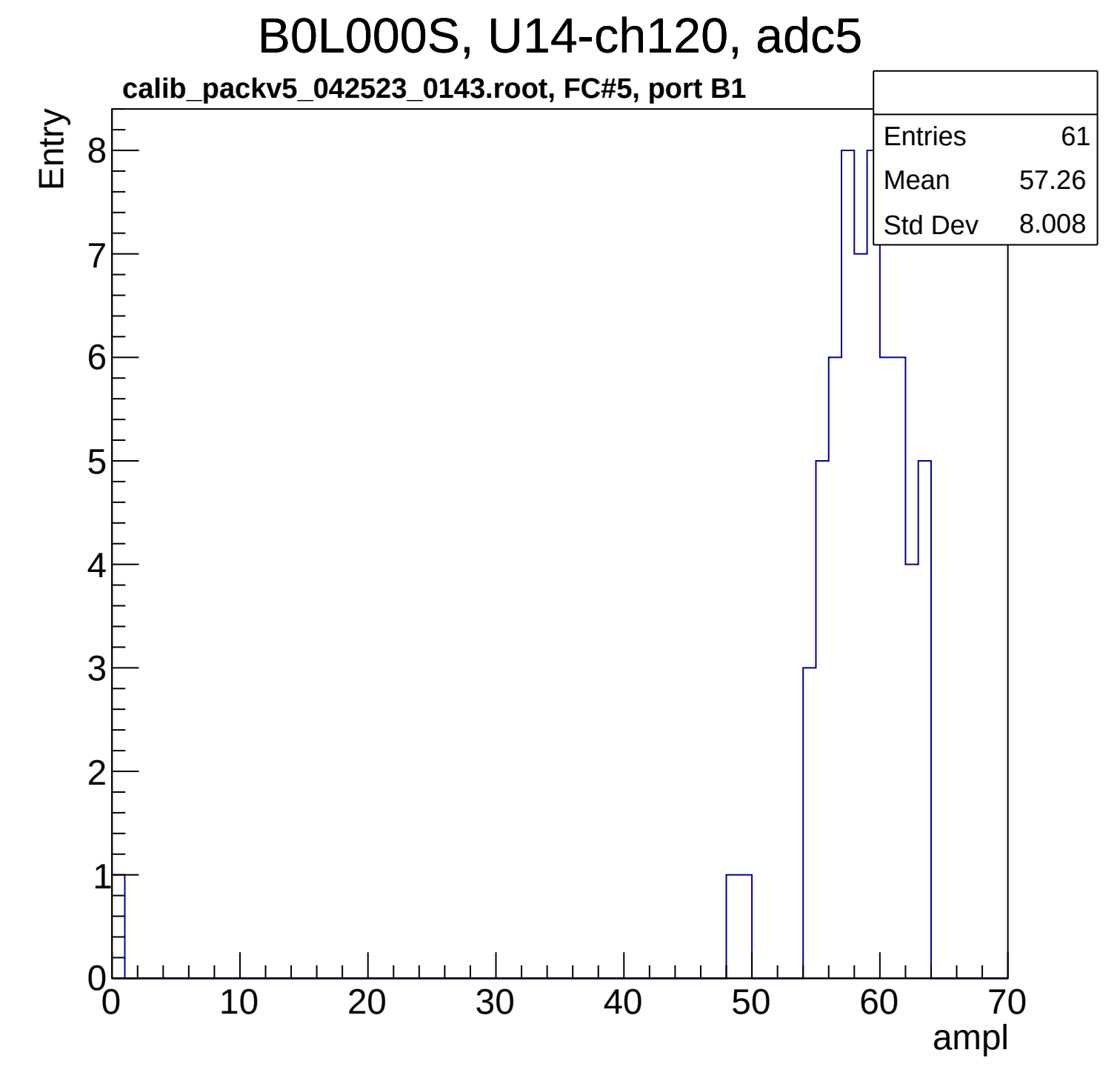
B0L000S, U14-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.26
Std Dev 8.008
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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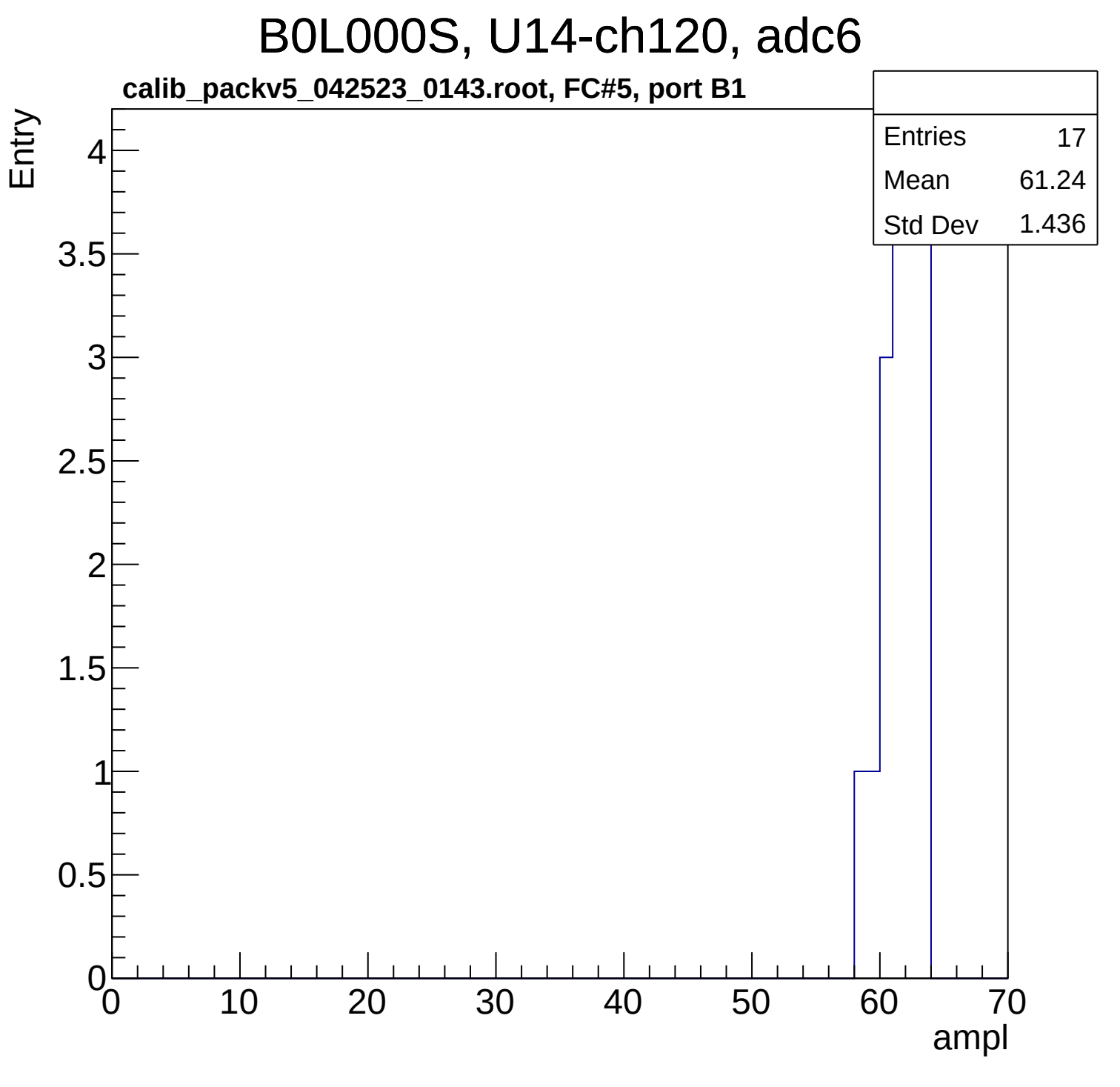
B0L000S, U14-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.24
Std Dev 1.436
Entry
ampl
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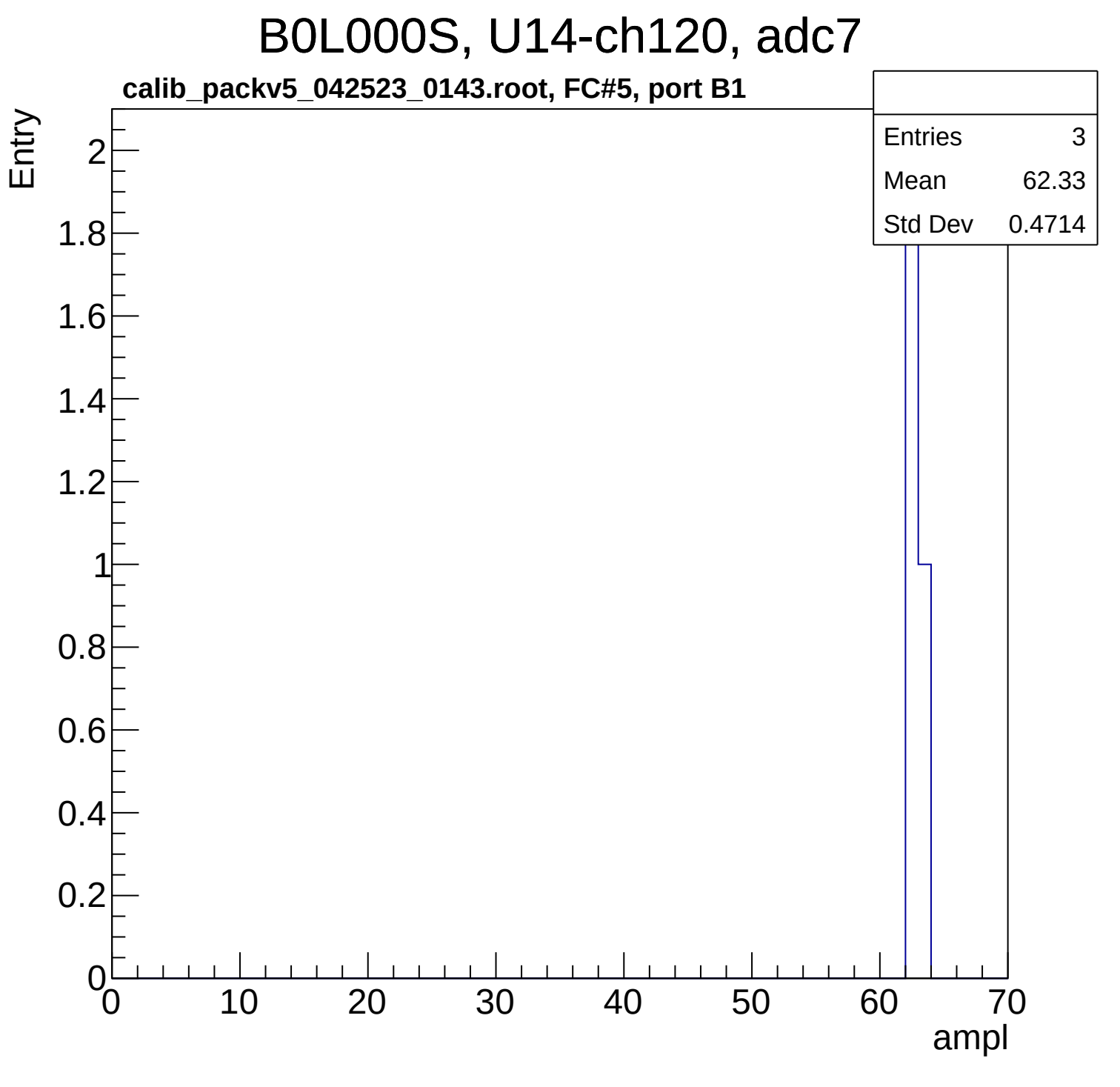
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U14-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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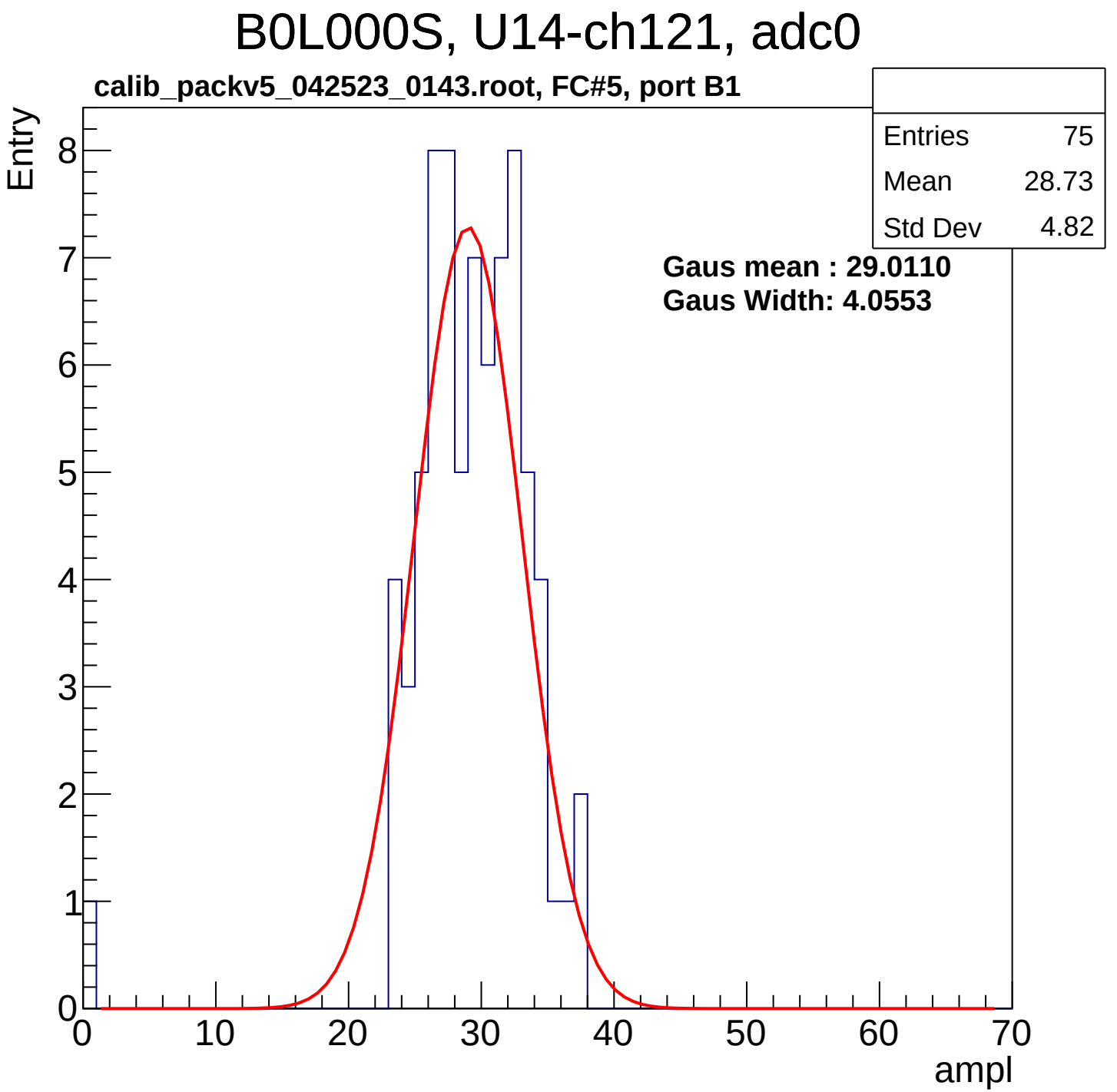
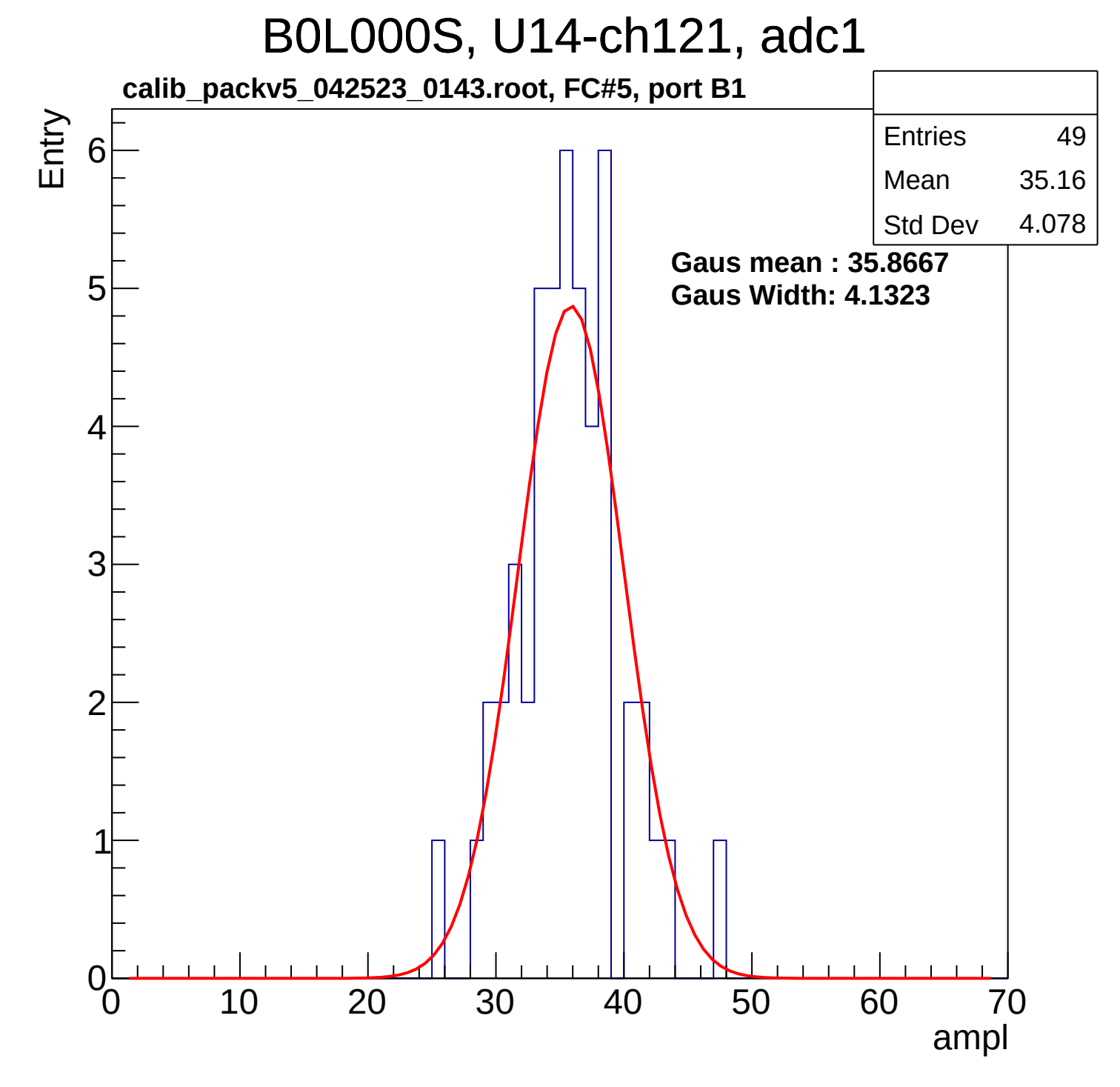

B0L000S, U14-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 28.73
Std Dev 4.82
Gaus mean : 29.0110
Gaus Width: 4.0553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U14-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 35.16
Std Dev 4.078
Gaus mean : 35.8667
Gaus Width: 4.1323
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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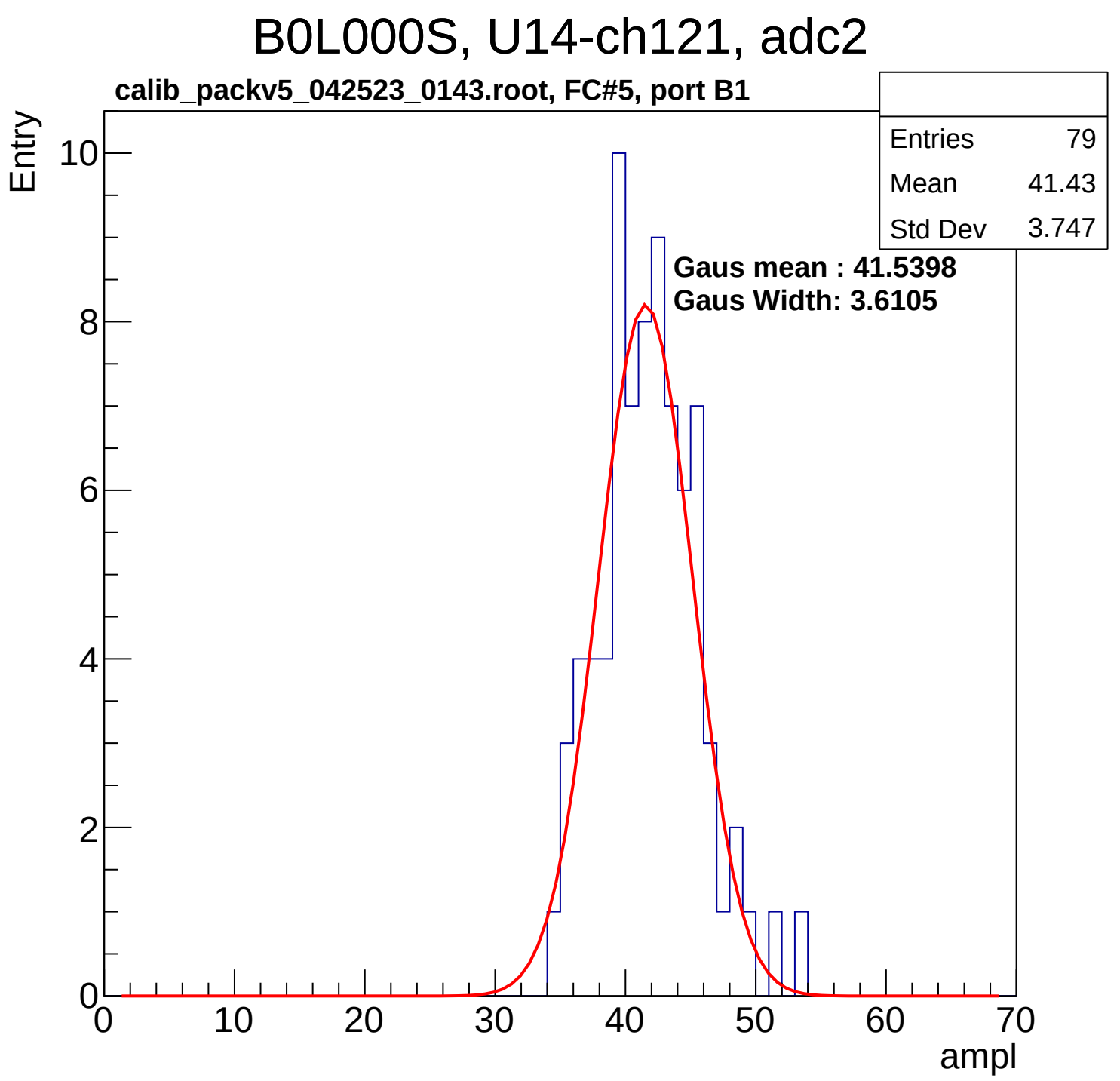
B0L000S, U14-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 41.43
Std Dev 3.747
Gaus mean : 41.5398
Gaus Width: 3.6105
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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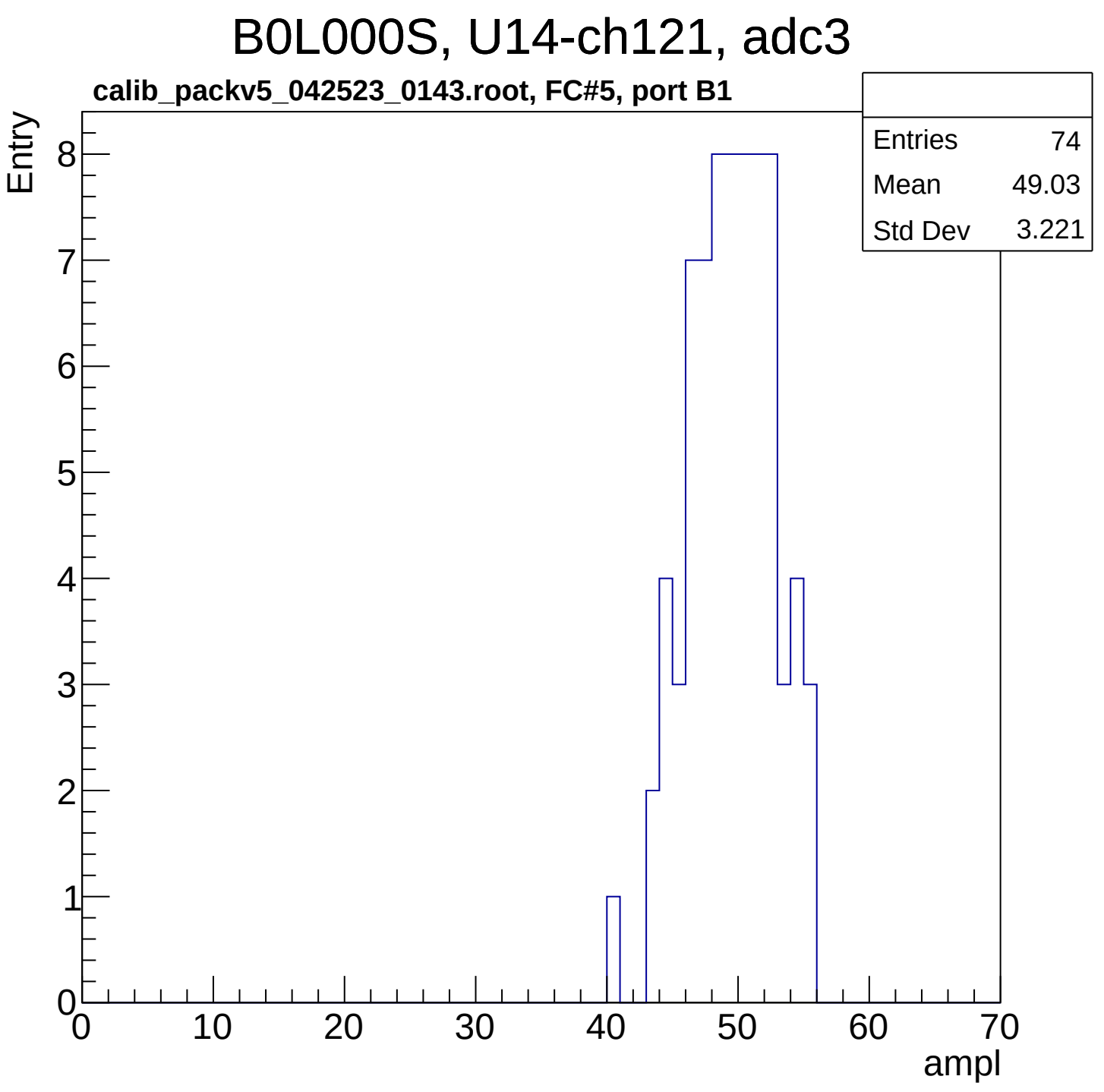
B0L000S, U14-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.03
Std Dev 3.221
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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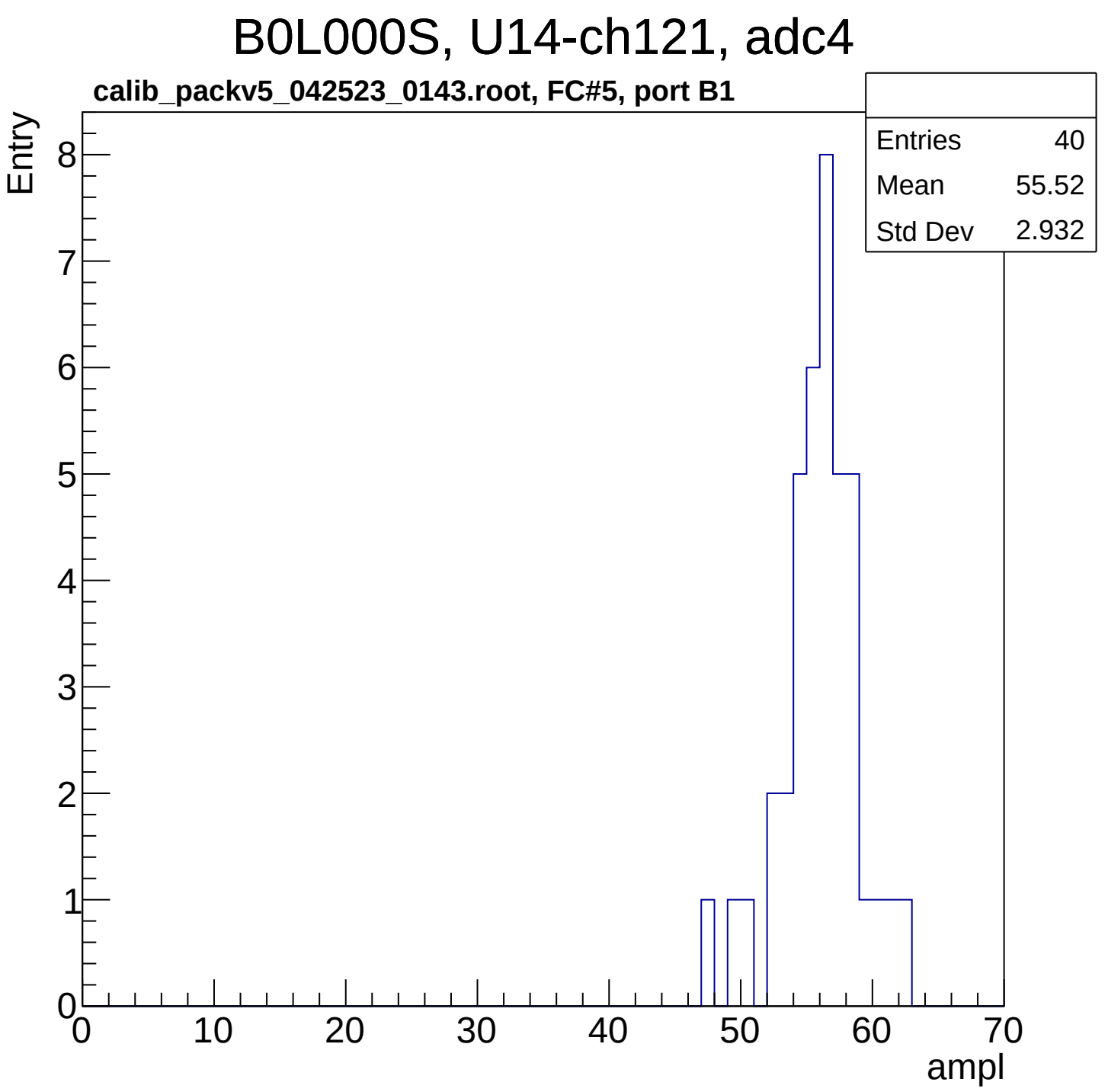
B0L000S, U14-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 55.52
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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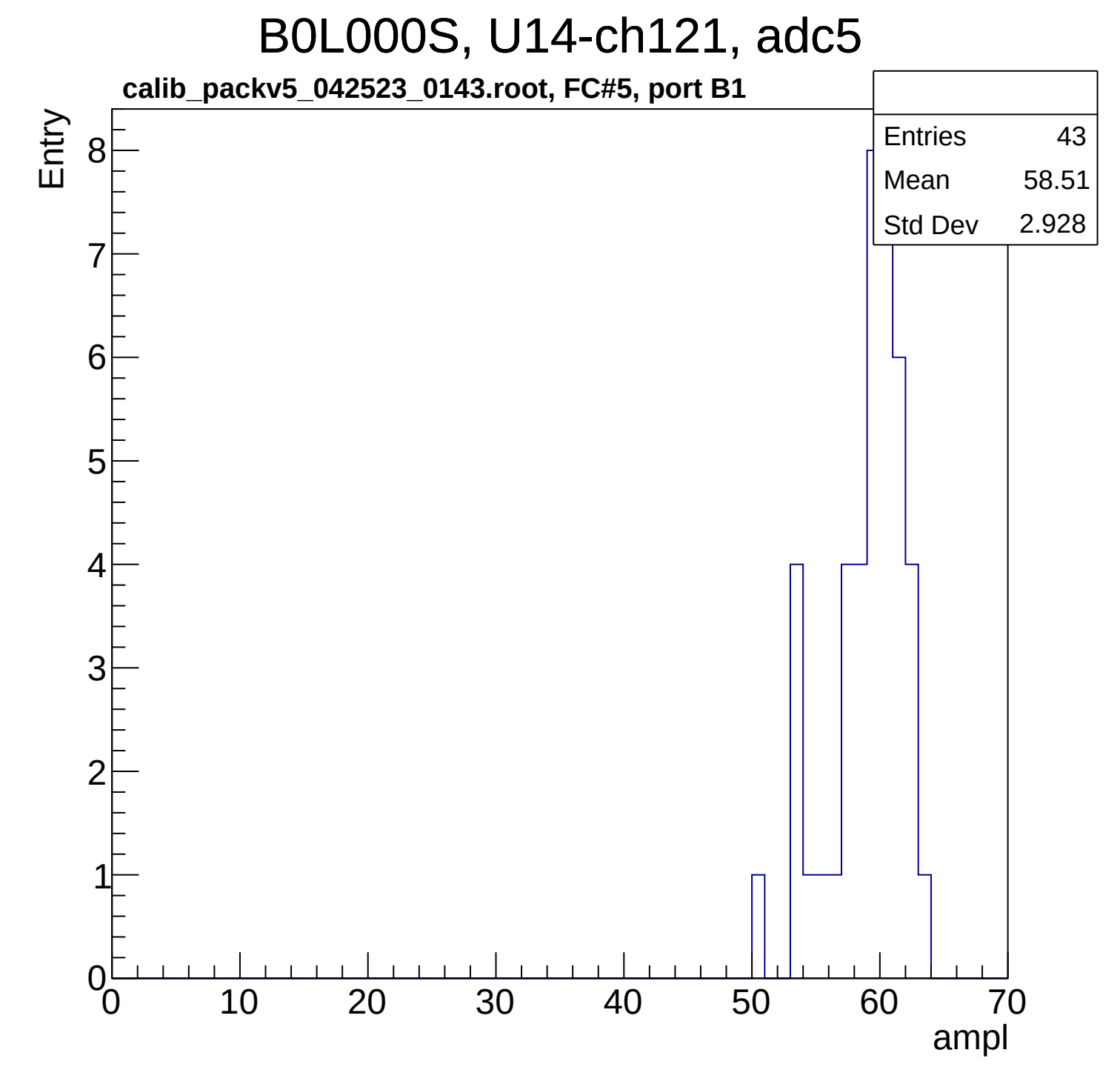
B0L000S, U14-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.51
Std Dev 2.928
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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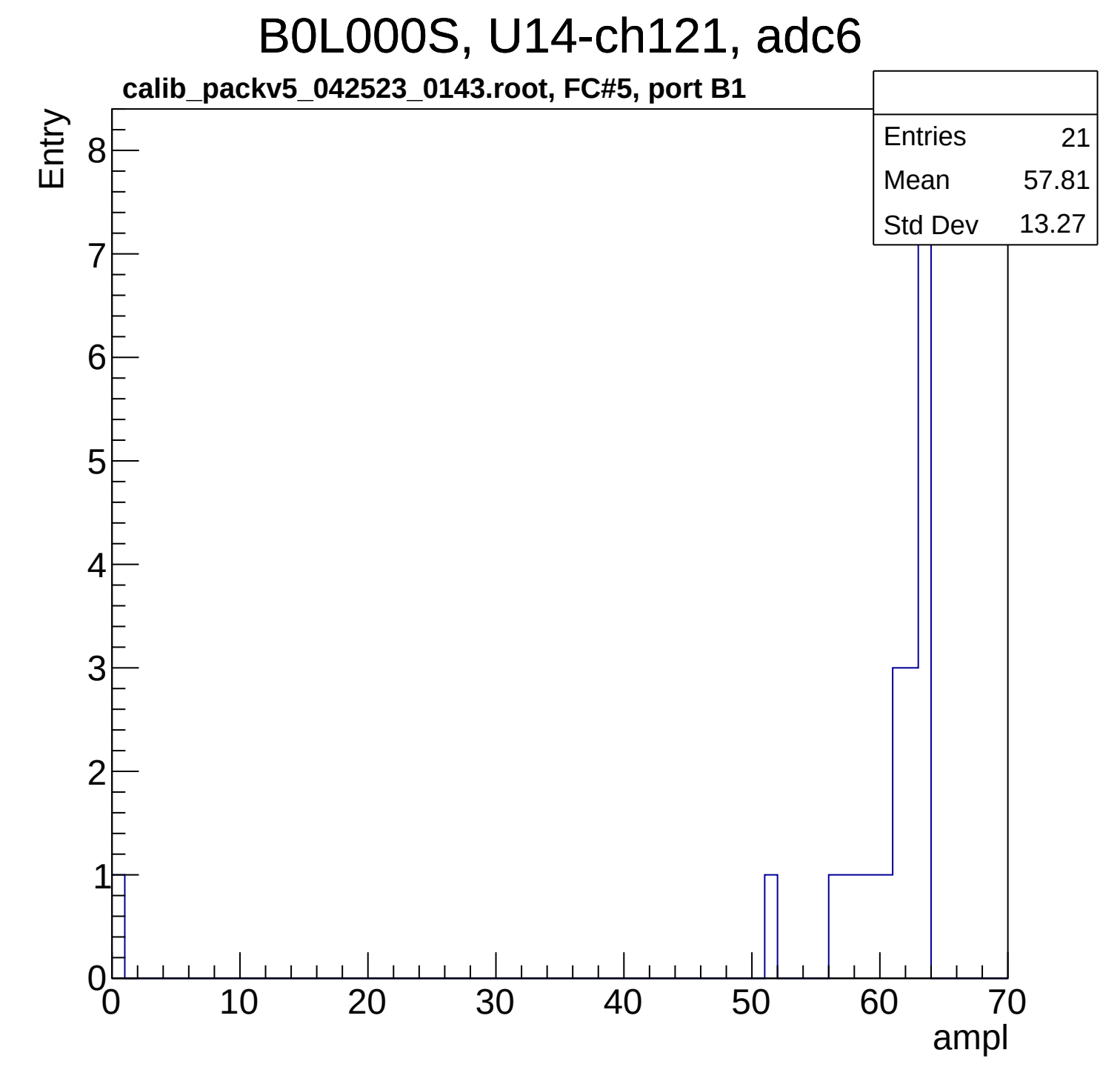
B0L000S, U14-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 21
Mean 57.81
Std Dev 13.27
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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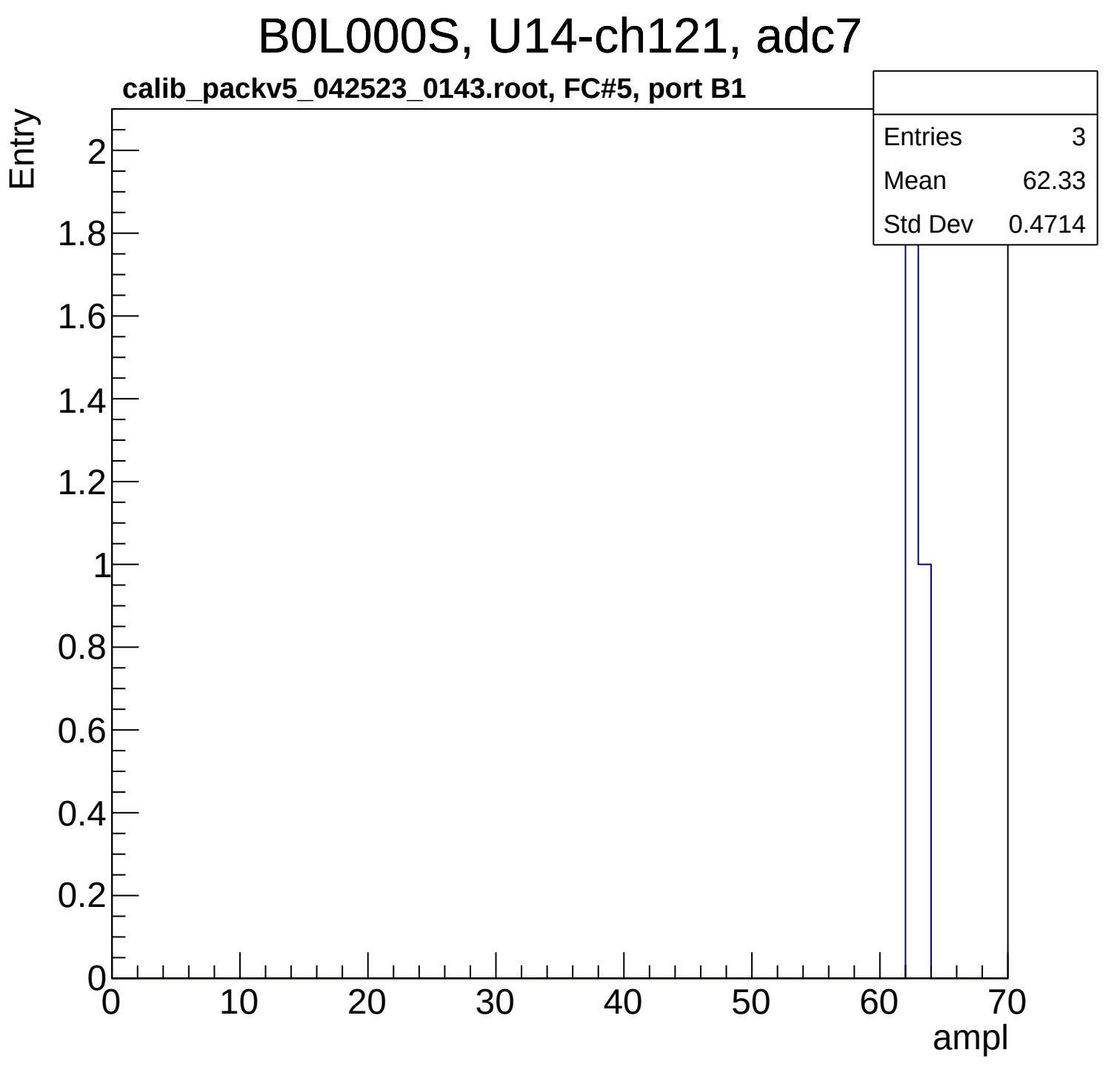
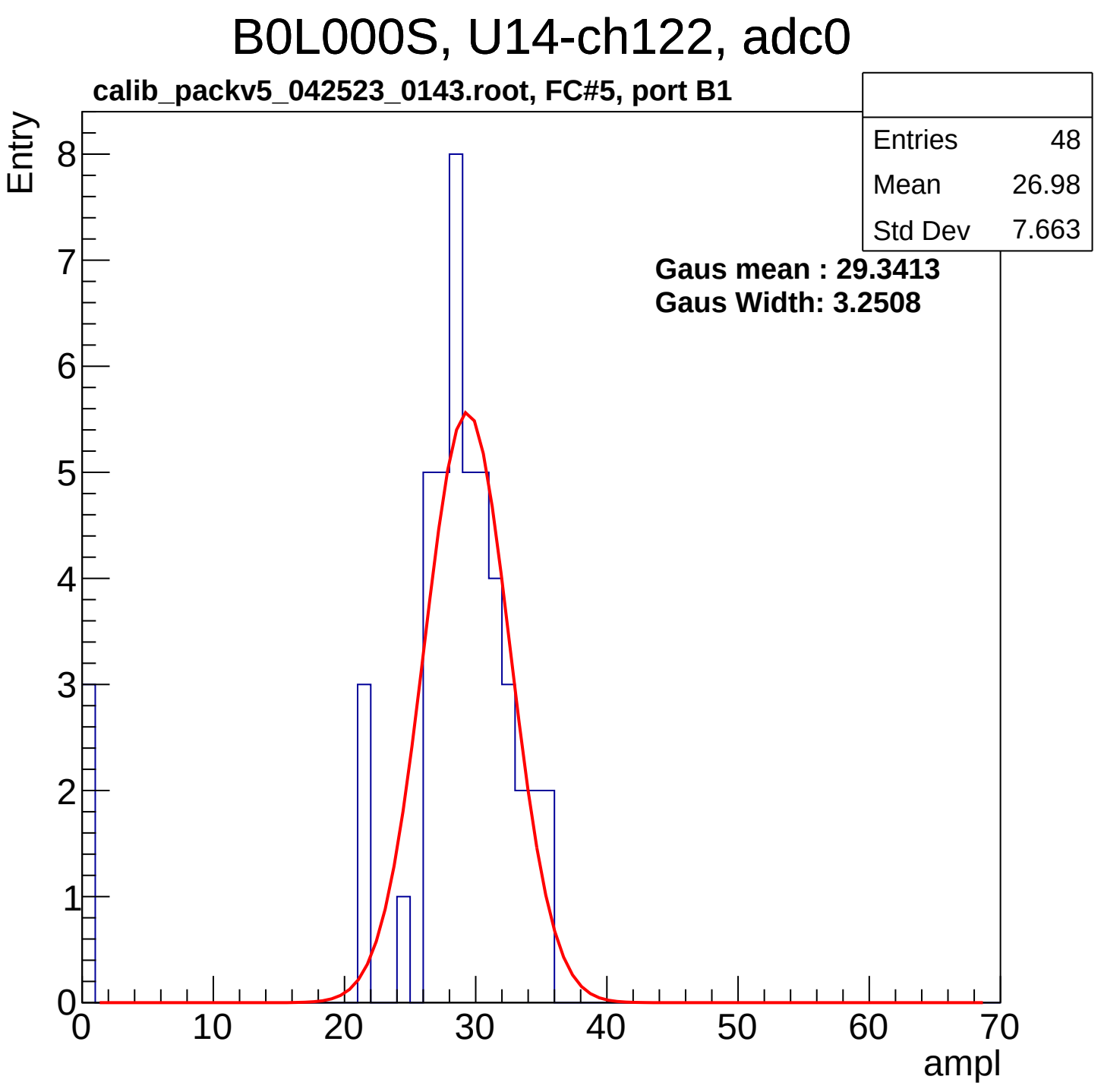

B0L000S, U14-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U14-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 26.98
Std Dev 7.663
Gaus mean : 29.3413
Gaus Width: 3.2508
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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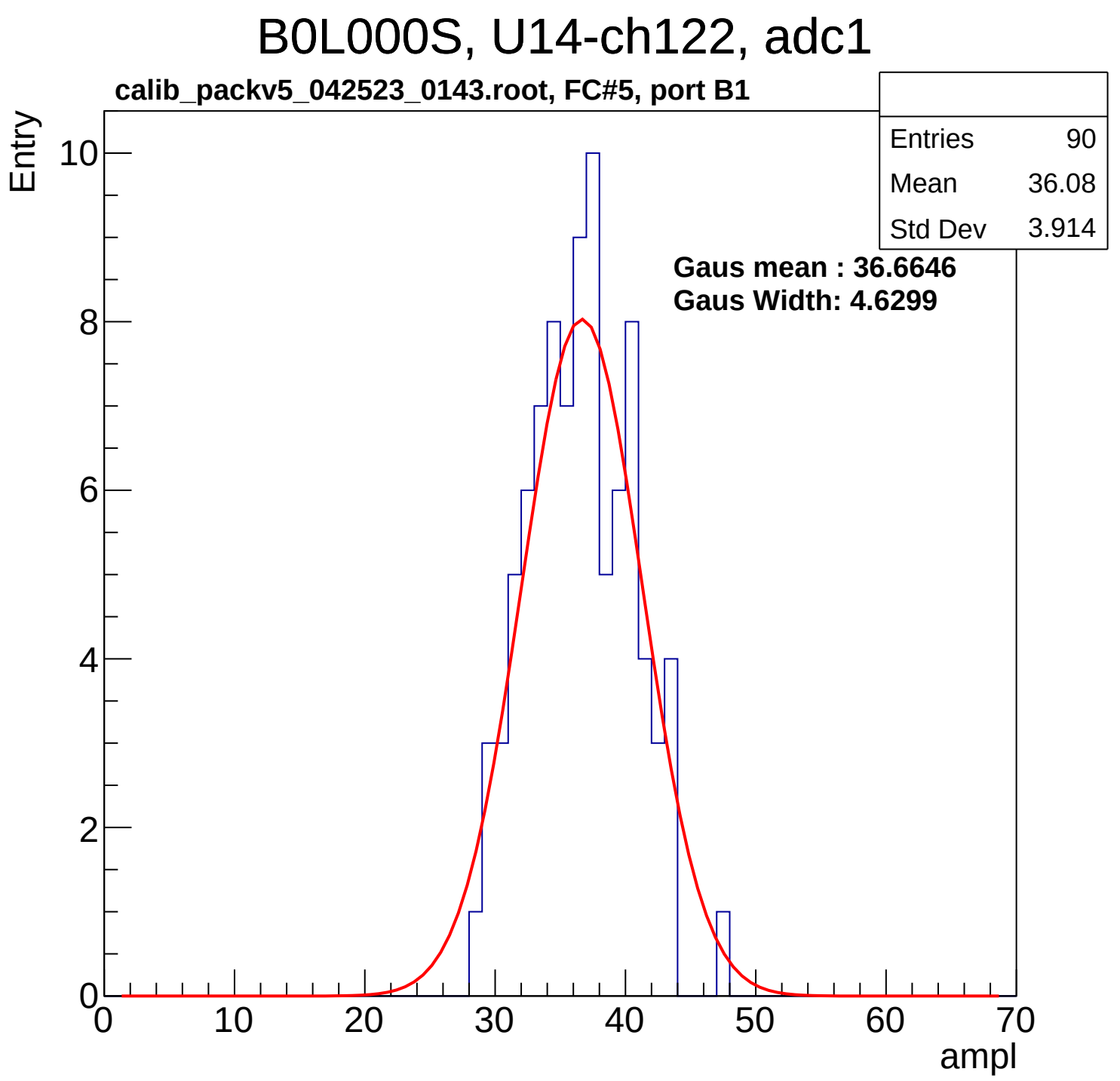
B0L000S, U14-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 90
Mean 36.08
Std Dev 3.914
Gaus mean : 36.6646
Gaus Width: 4.6299
Entry
ampl
0
2
4
6
8
10
0
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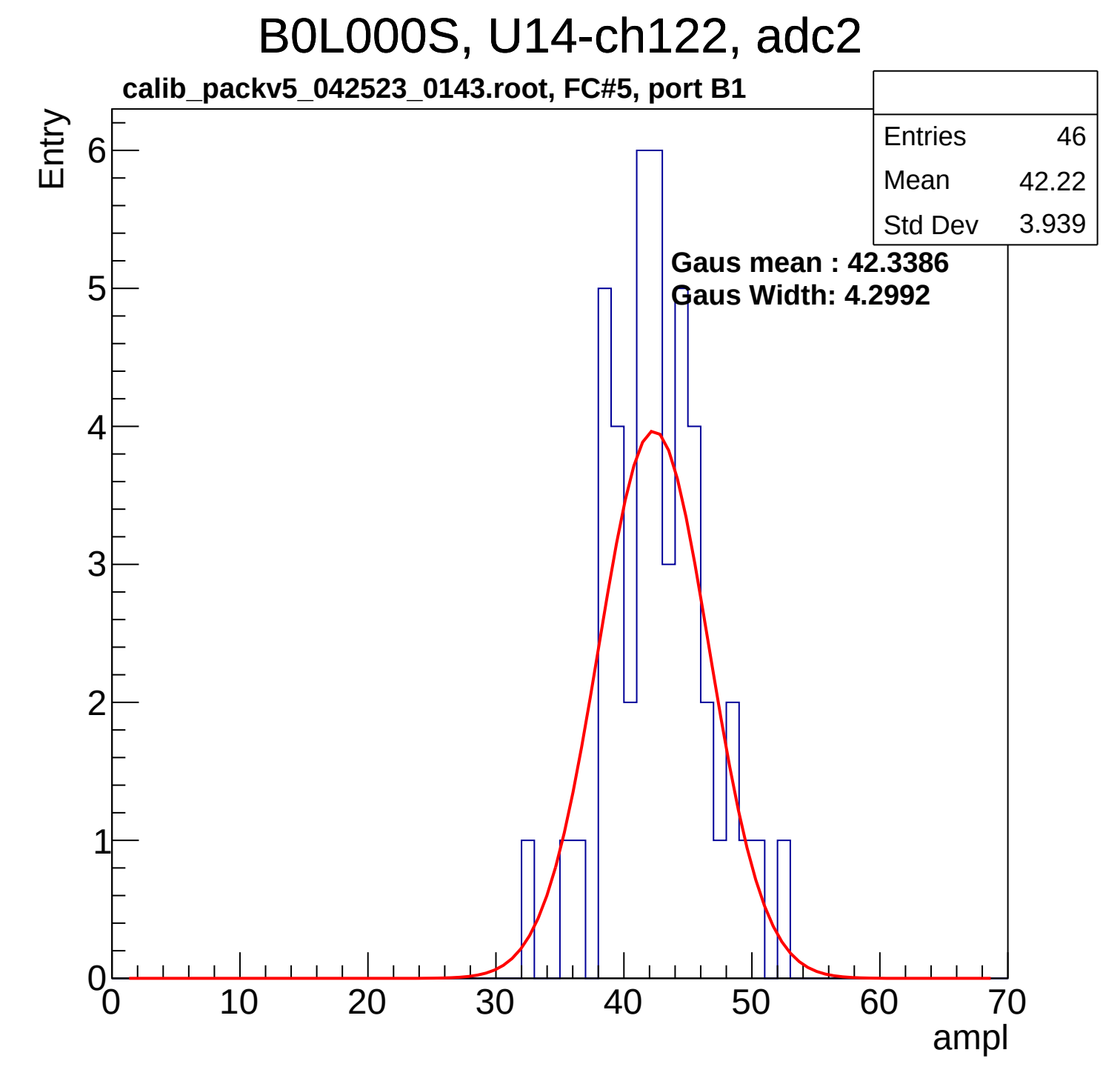
20
30
40
50
60
70


B0L000S, U14-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 42.22
Std Dev 3.939
Gaus mean : 42.3386
Gaus Width: 4.2992
Entry
ampl
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2
3
4
5
6
0
10
20
30
40
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60
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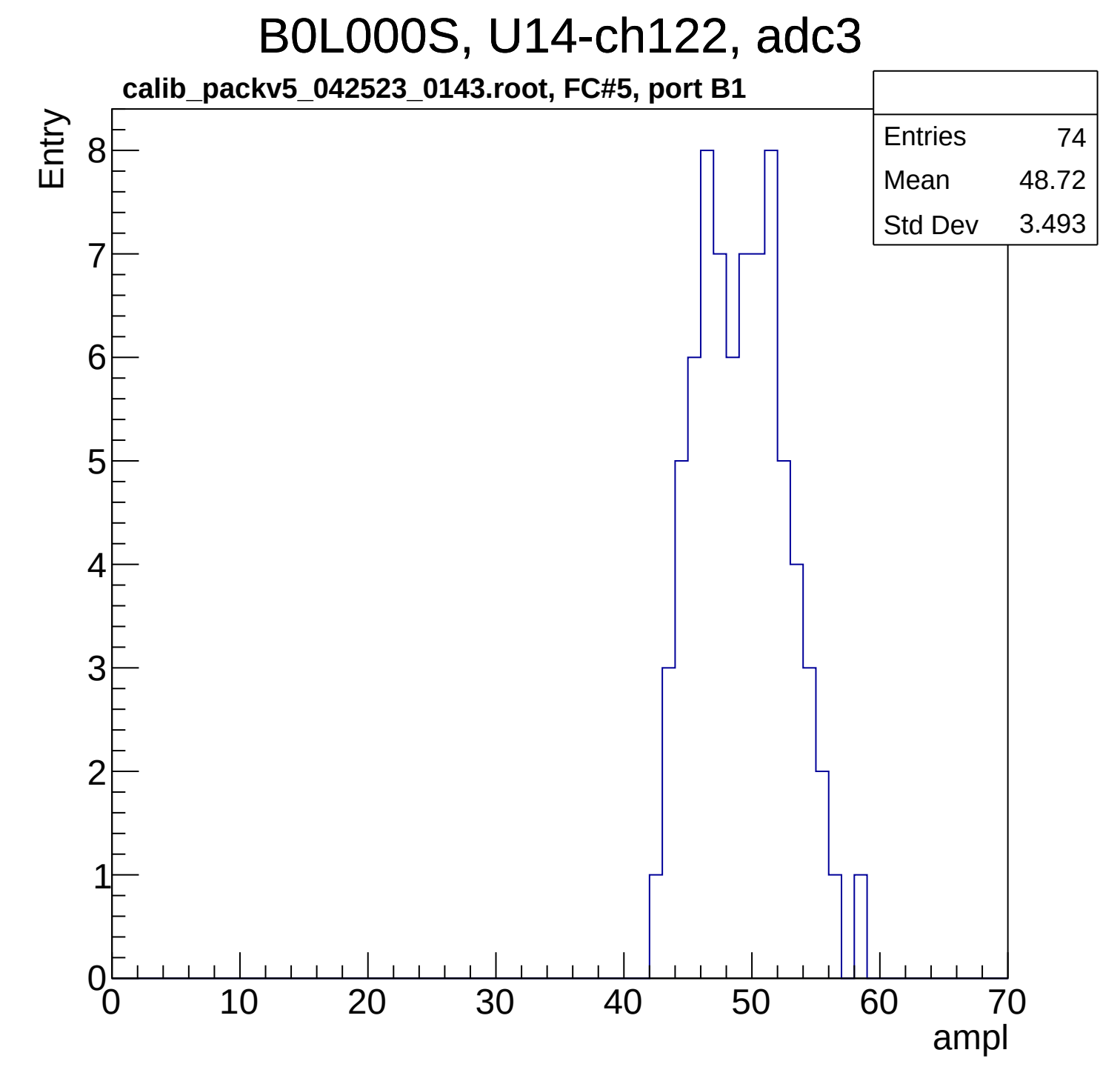
B0L000S, U14-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 48.72
Std Dev 3.493
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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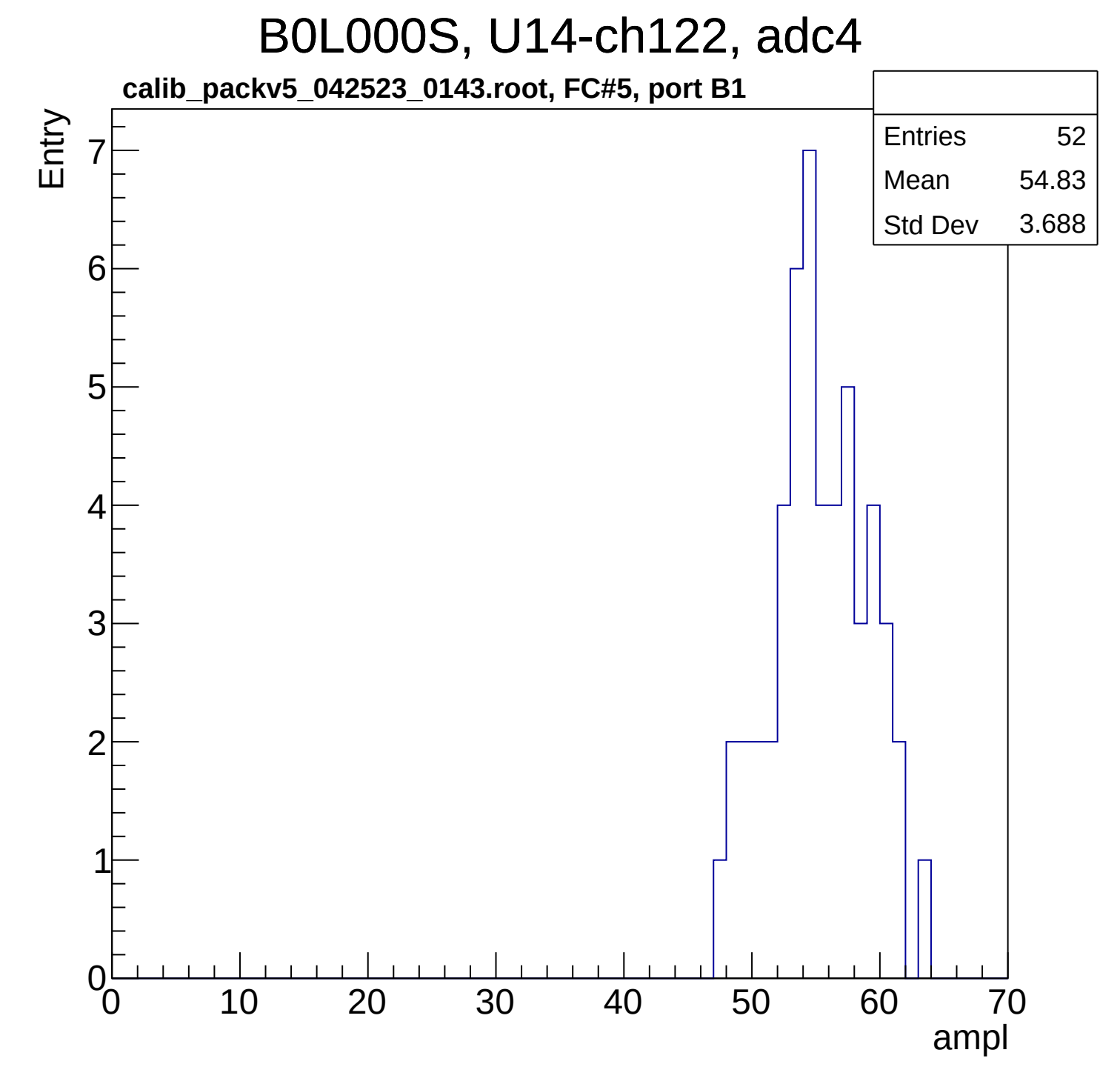
B0L000S, U14-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.83
Std Dev 3.688
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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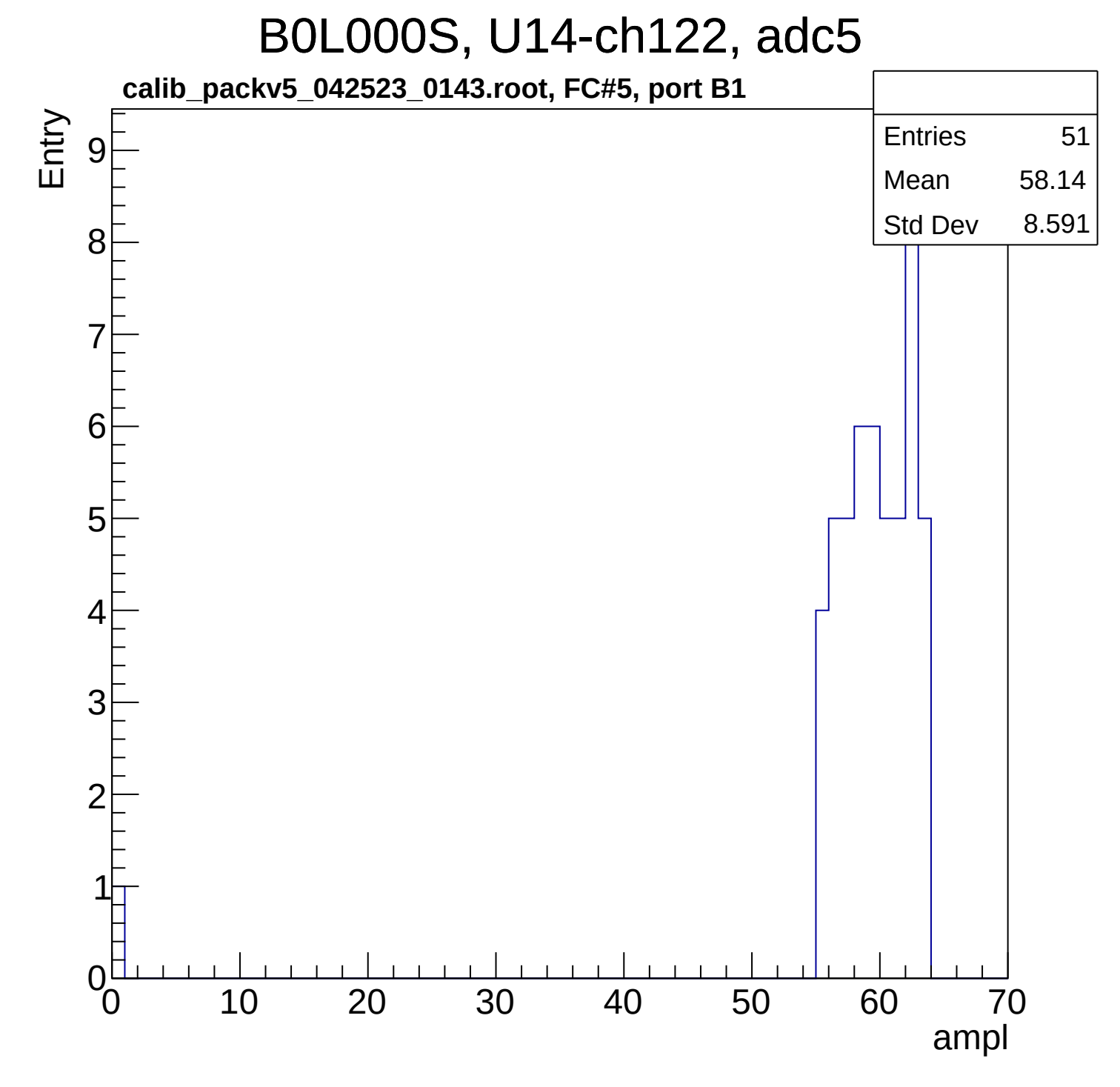
B0L000S, U14-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.14
Std Dev 8.591
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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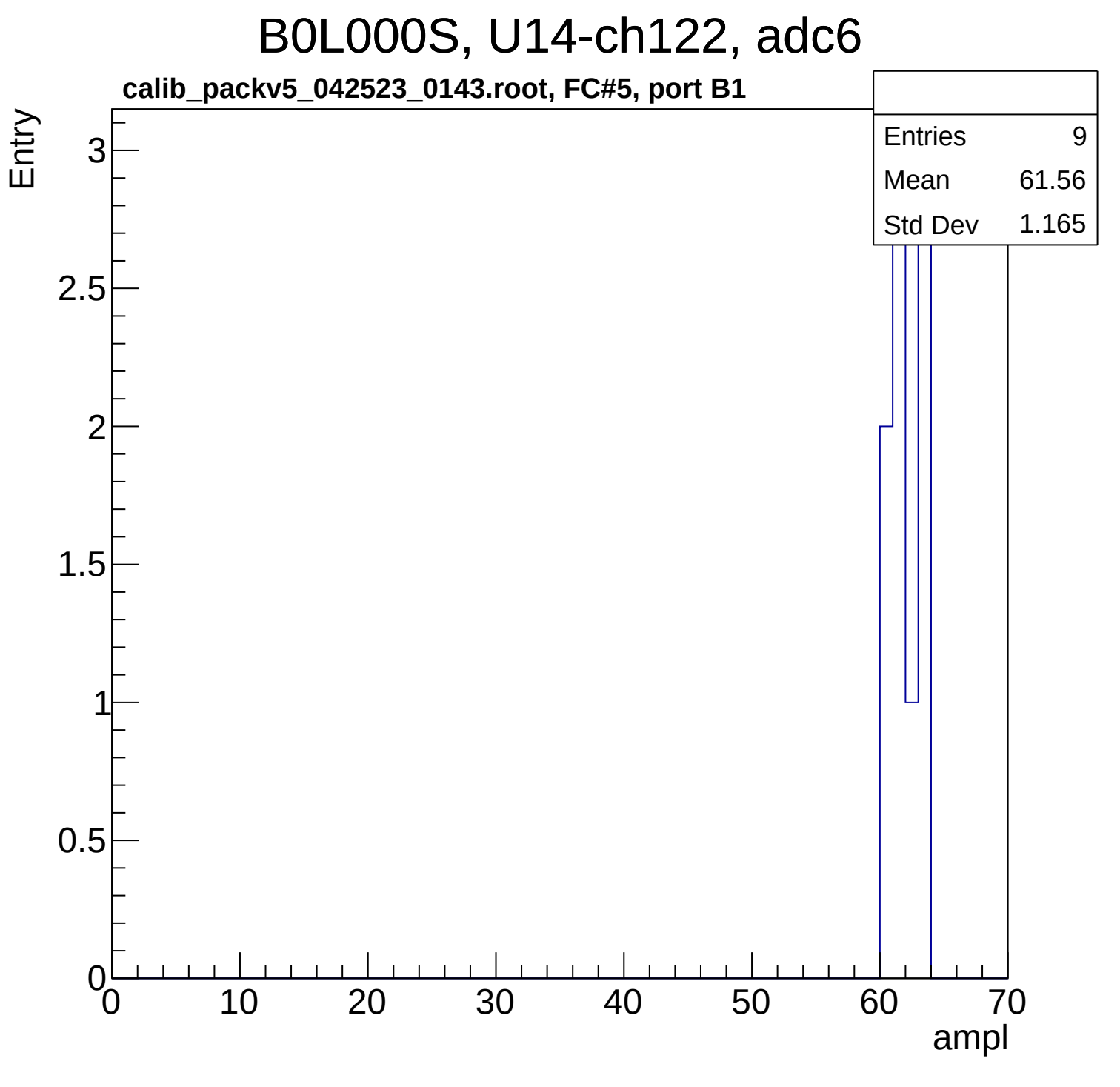
B0L000S, U14-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.56
Std Dev 1.165
Entry
ampl
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1
1.5
2
2.5
3
0
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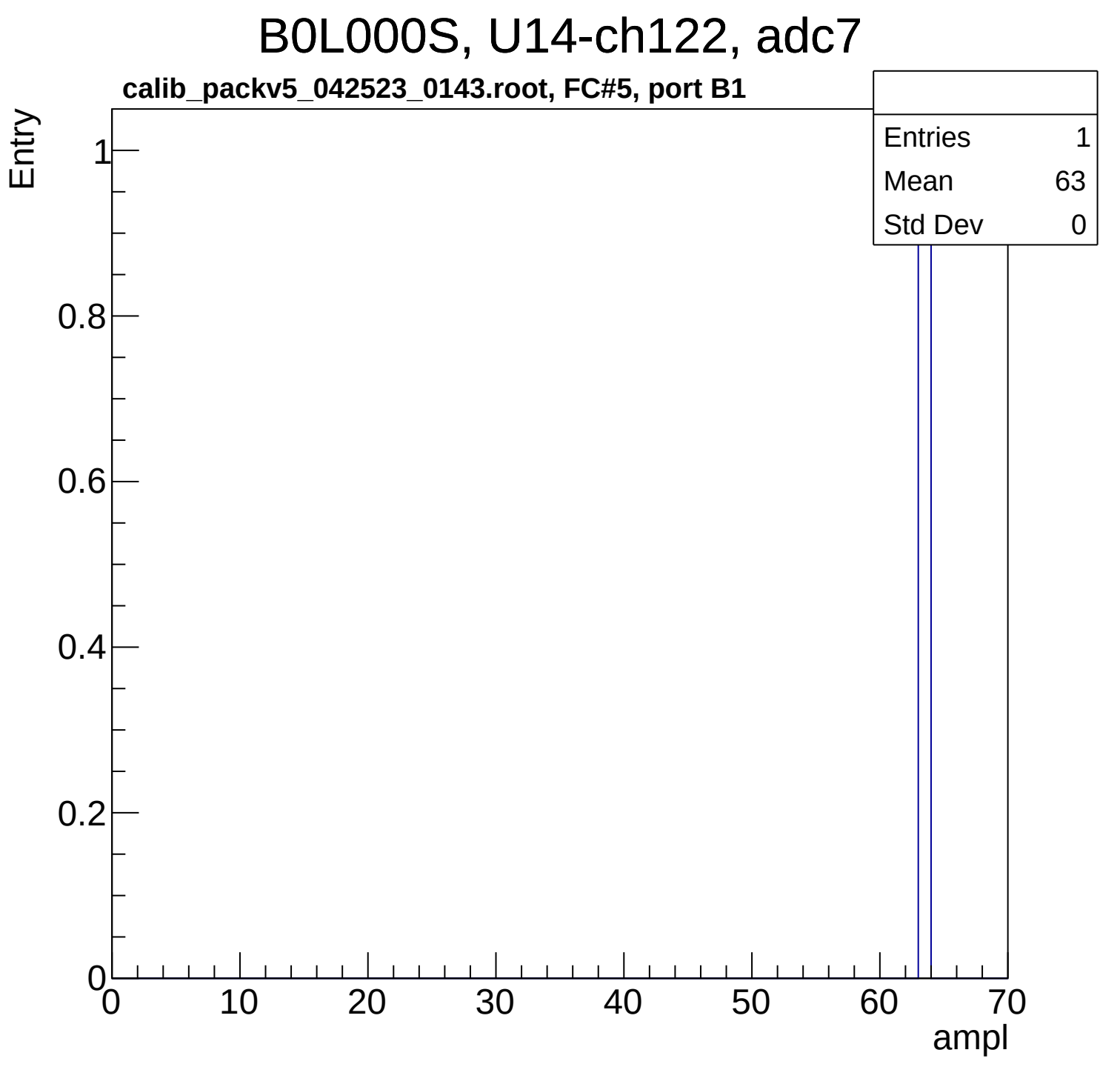
40
50
60
70


B0L000S, U14-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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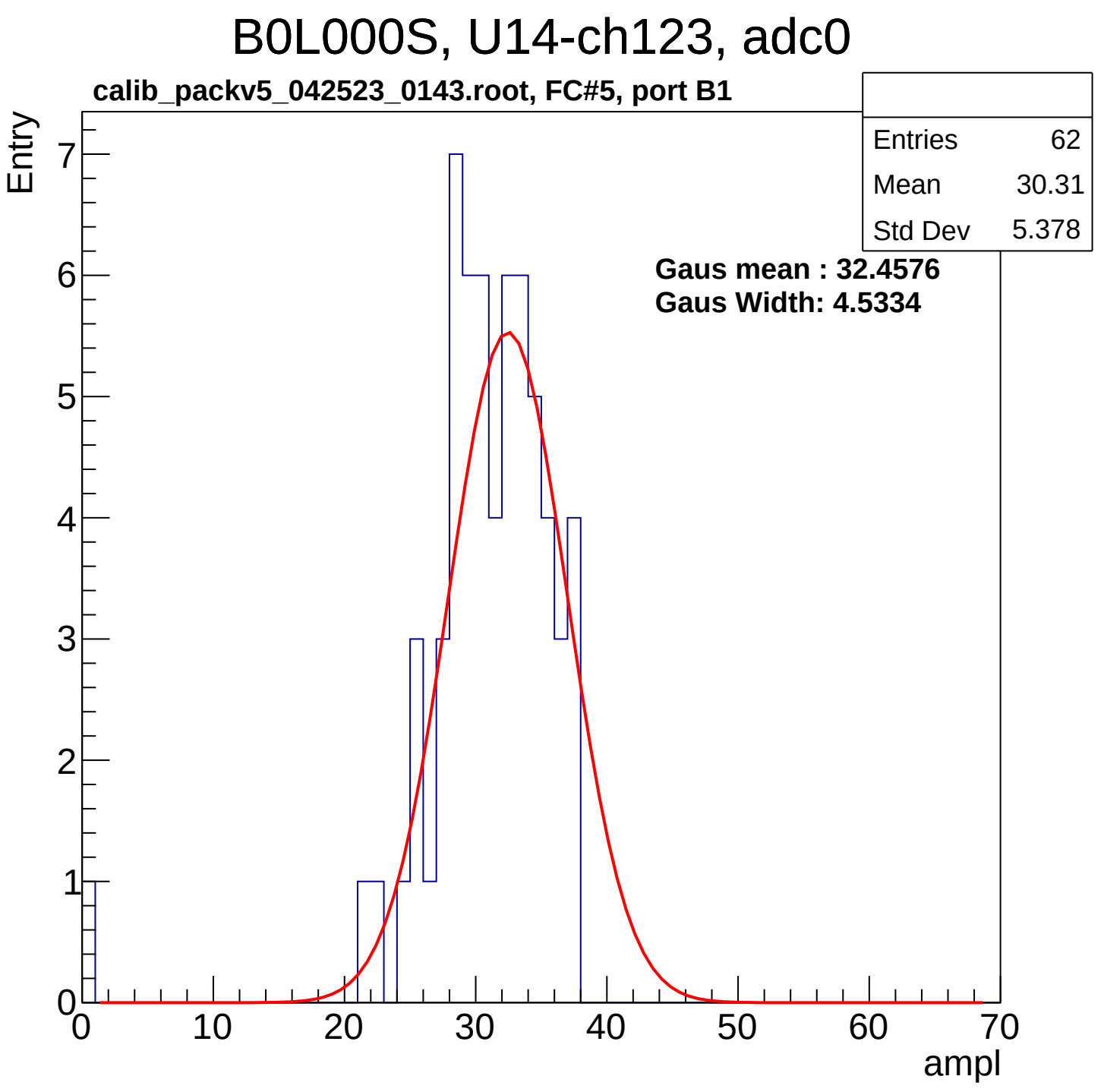
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 30.31
Std Dev 5.378
Gaus mean : 32.4576
Gaus Width: 4.5334
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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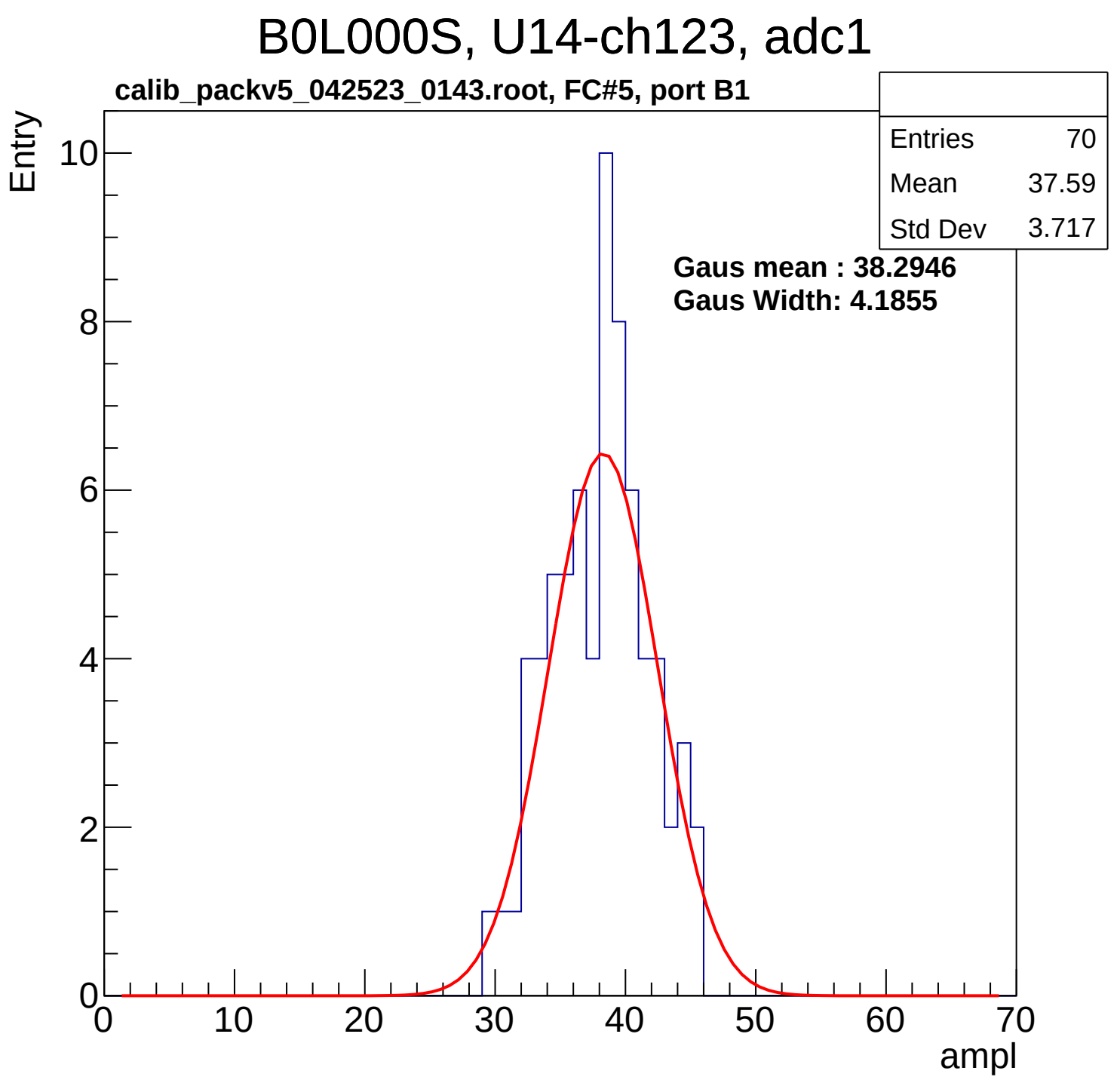
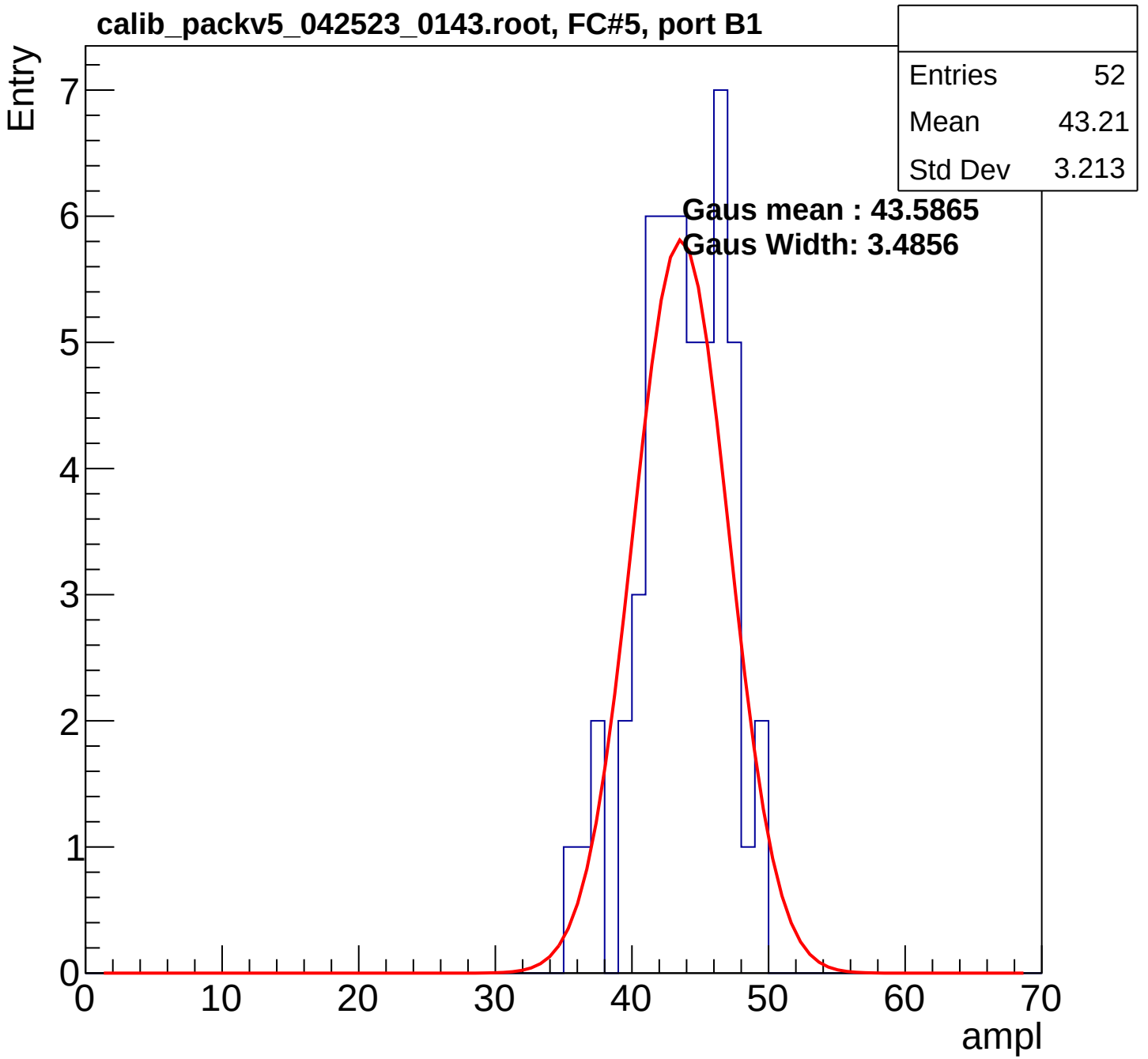

B0L000S, U14-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.59
Std Dev 3.717
Gaus mean : 38.2946
Gaus Width: 4.1855
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 43.21
Std Dev 3.213
Gaus mean : 43.5865
Gaus Width: 3.4856
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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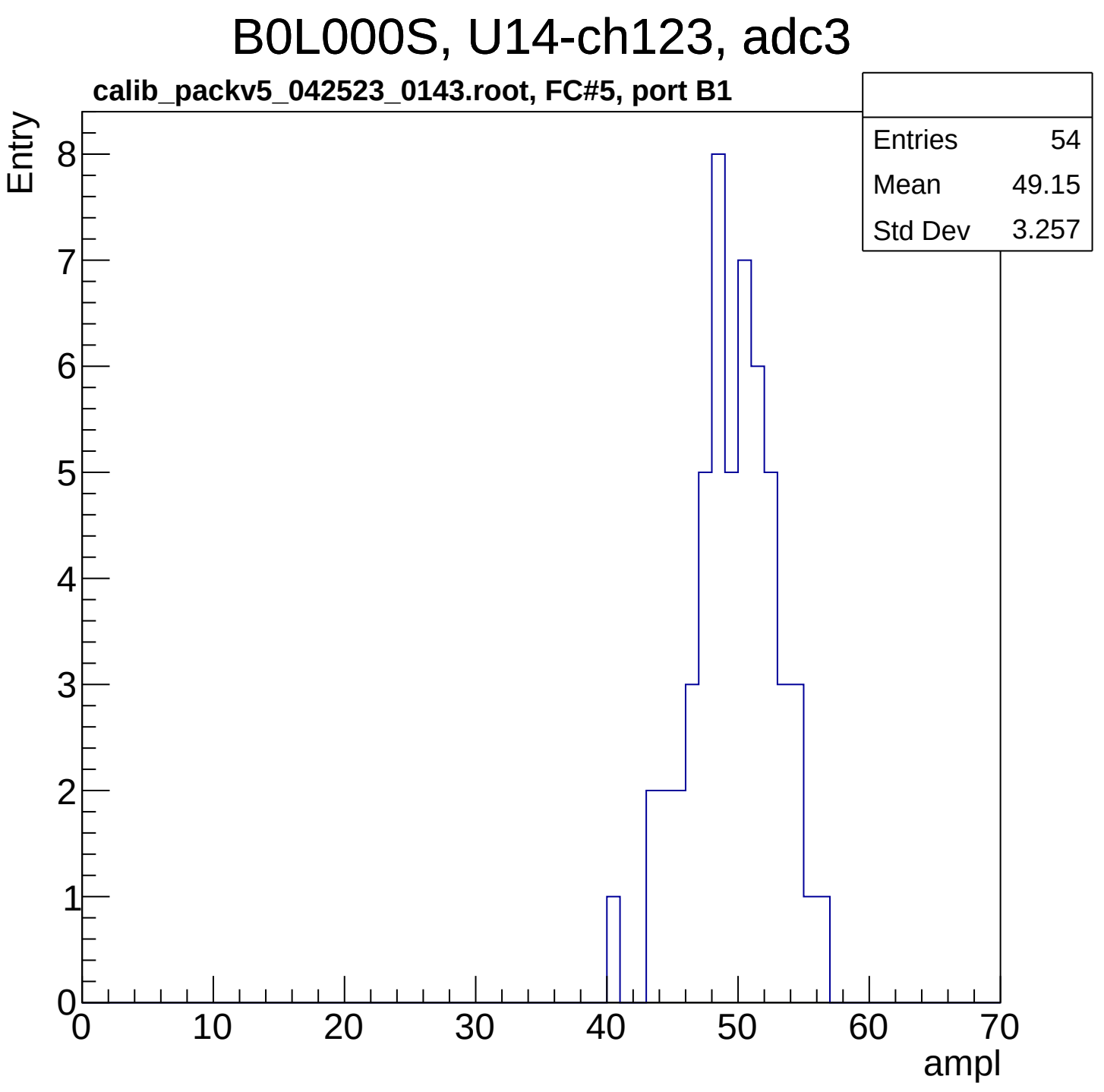
B0L000S, U14-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.15
Std Dev 3.257
Entry
ampl
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2
3
4
5
6
7
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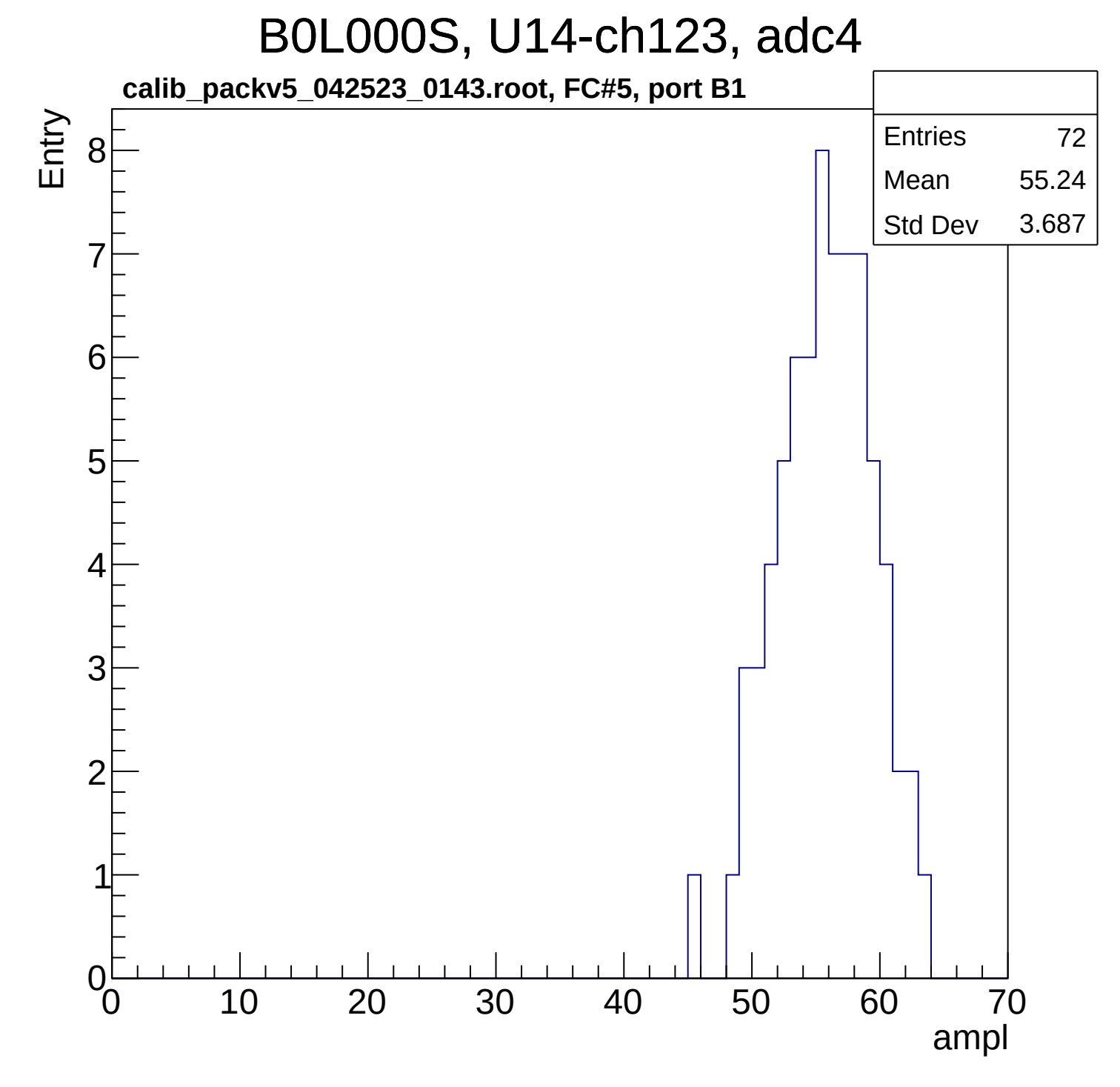
B0L000S, U14-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 55.24
Std Dev 3.687
Entry
ampl
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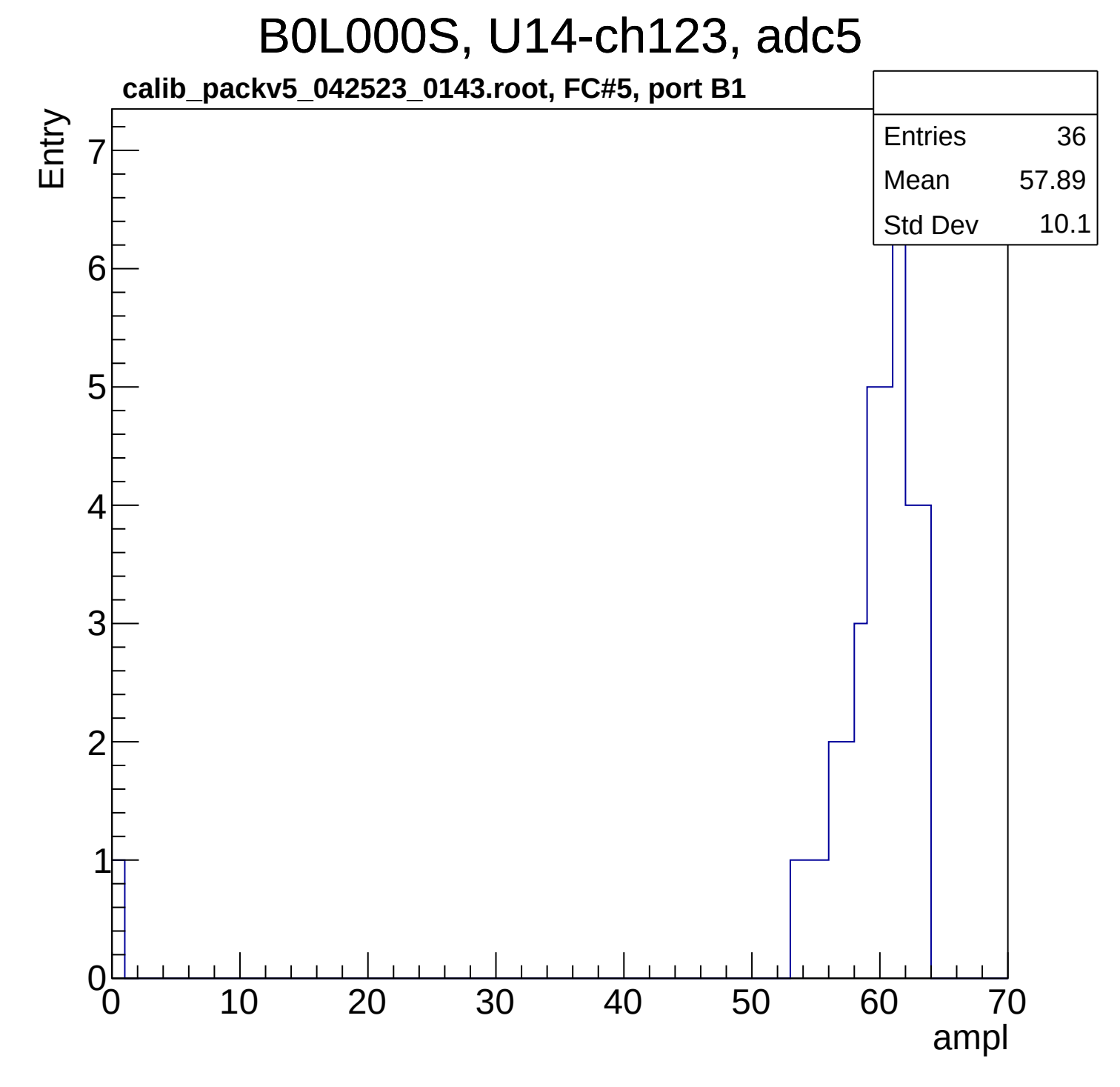
B0L000S, U14-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 57.89
Std Dev 10.1
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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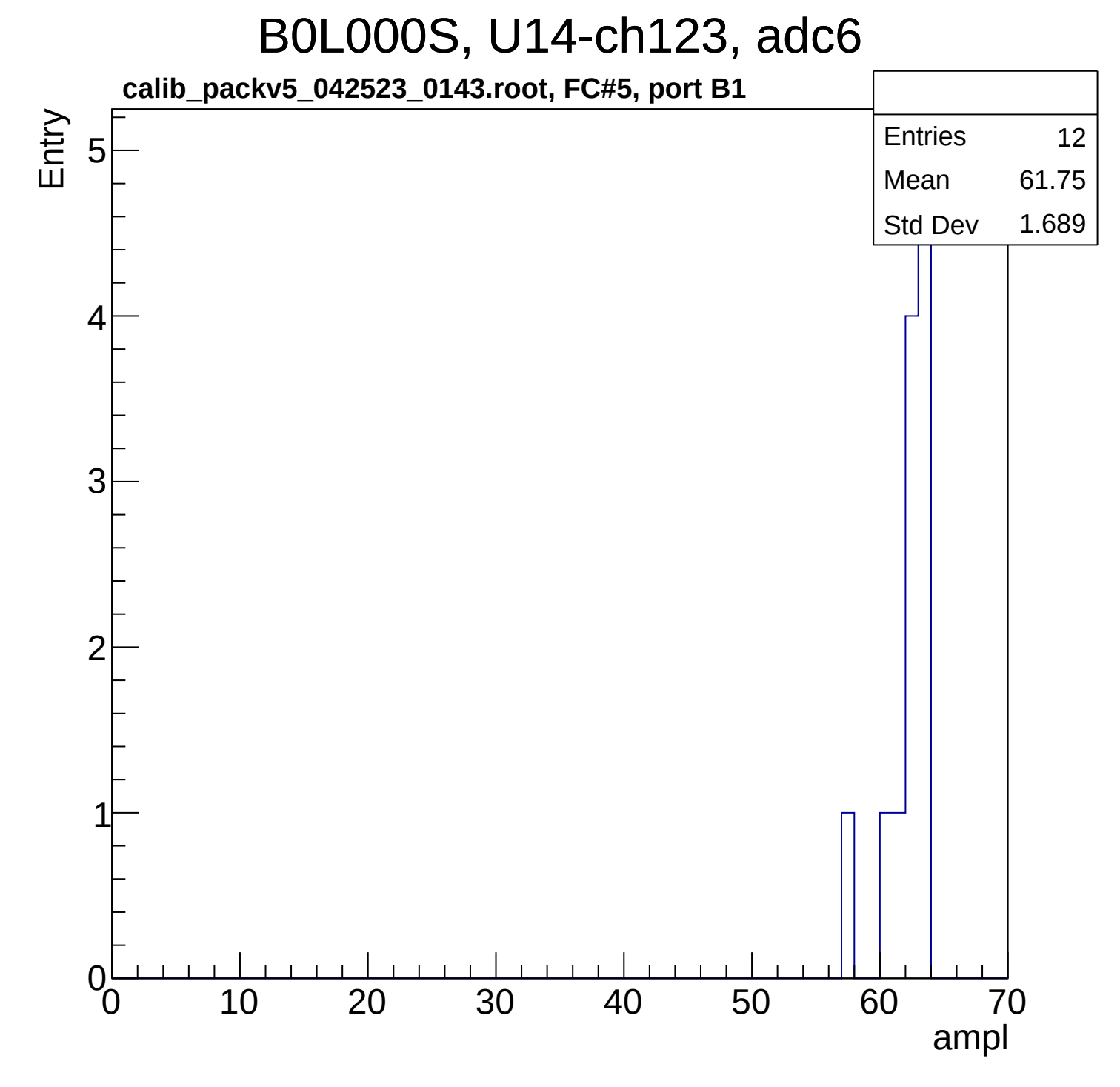
B0L000S, U14-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.75
Std Dev 1.689
Entry
ampl
0
1
2
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4
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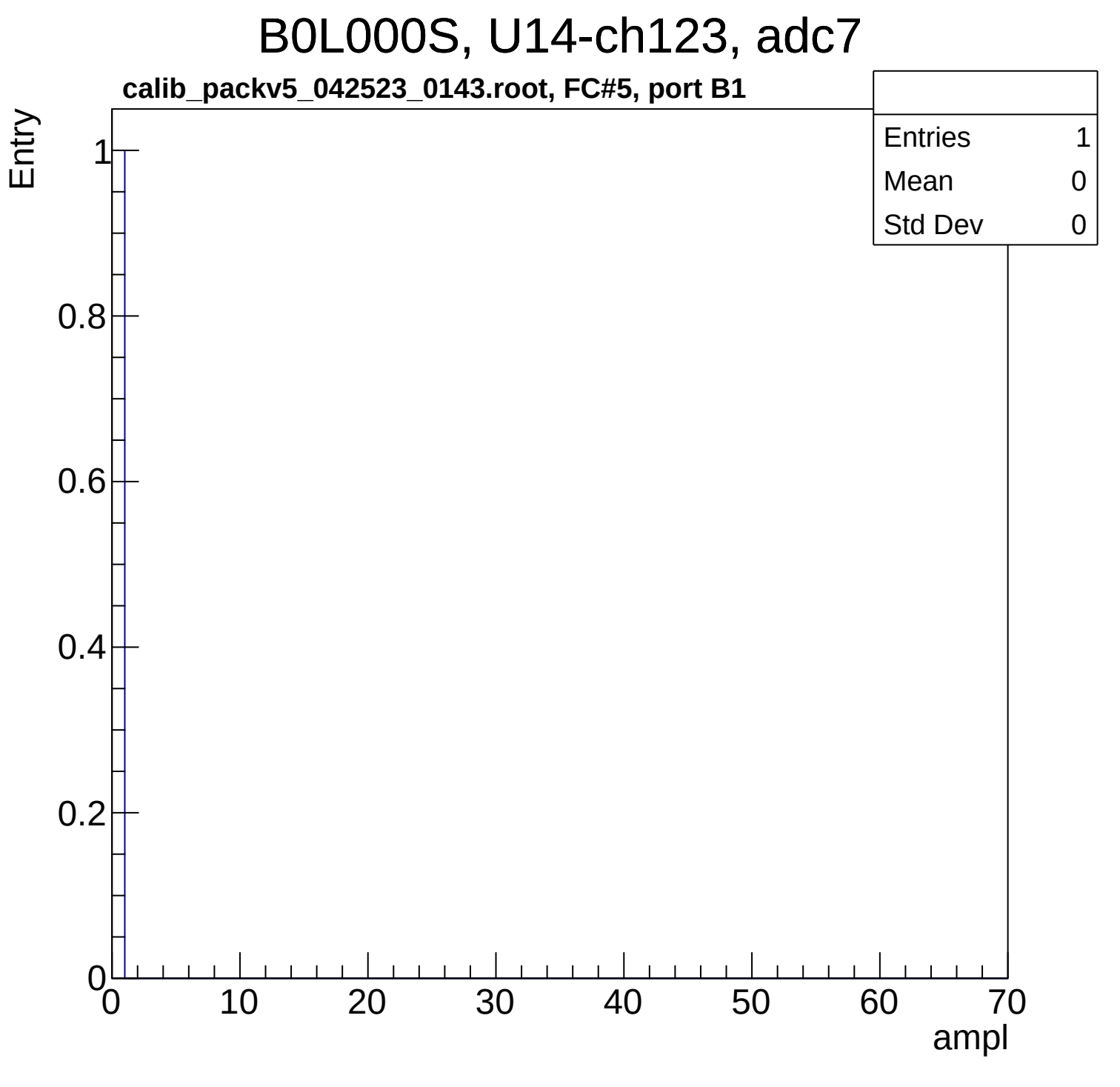
B0L000S, U14-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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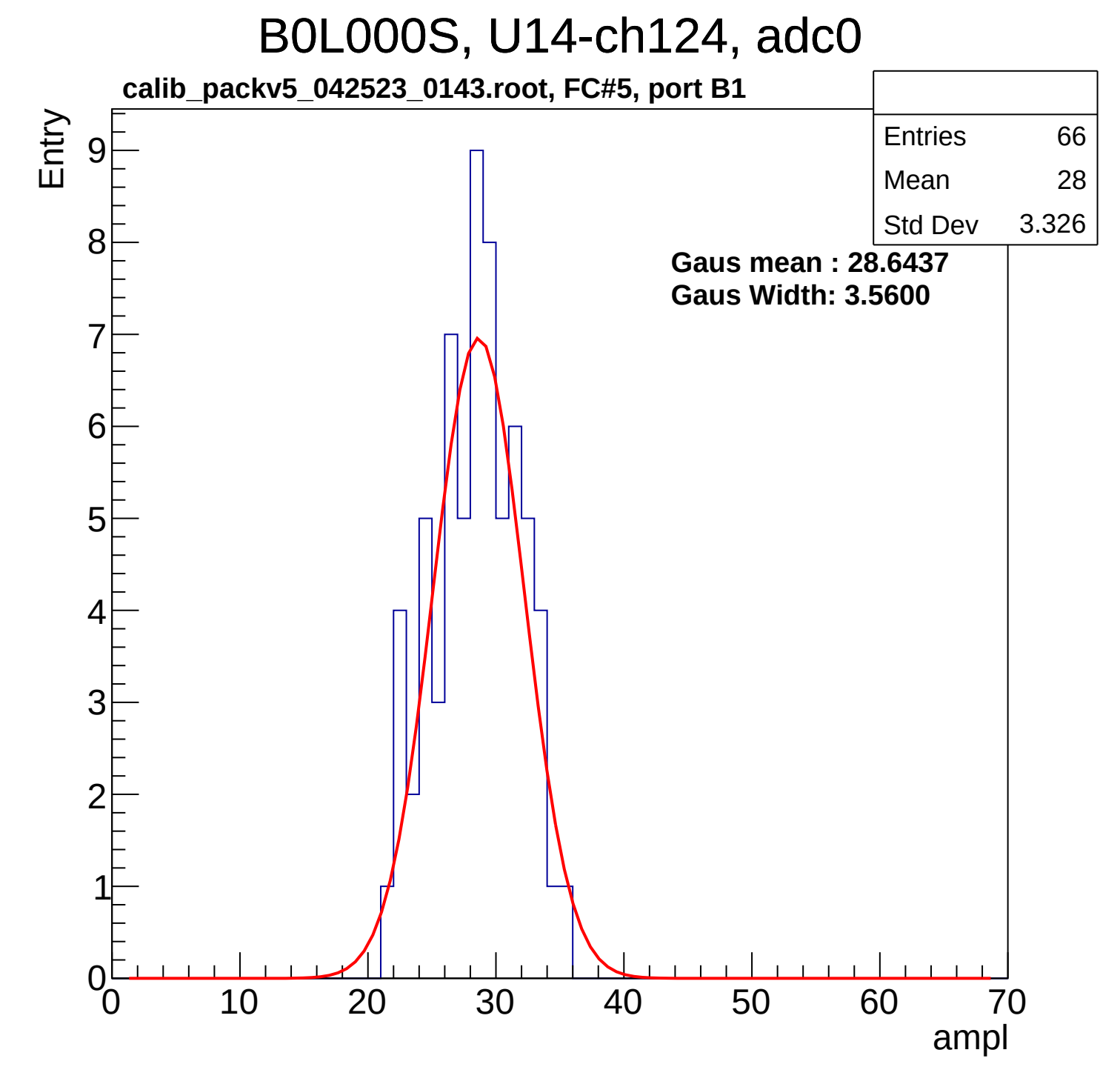
40
50
60
70
ampl


B0L000S, U14-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 28
Std Dev 3.326
Gaus mean : 28.6437
Gaus Width: 3.5600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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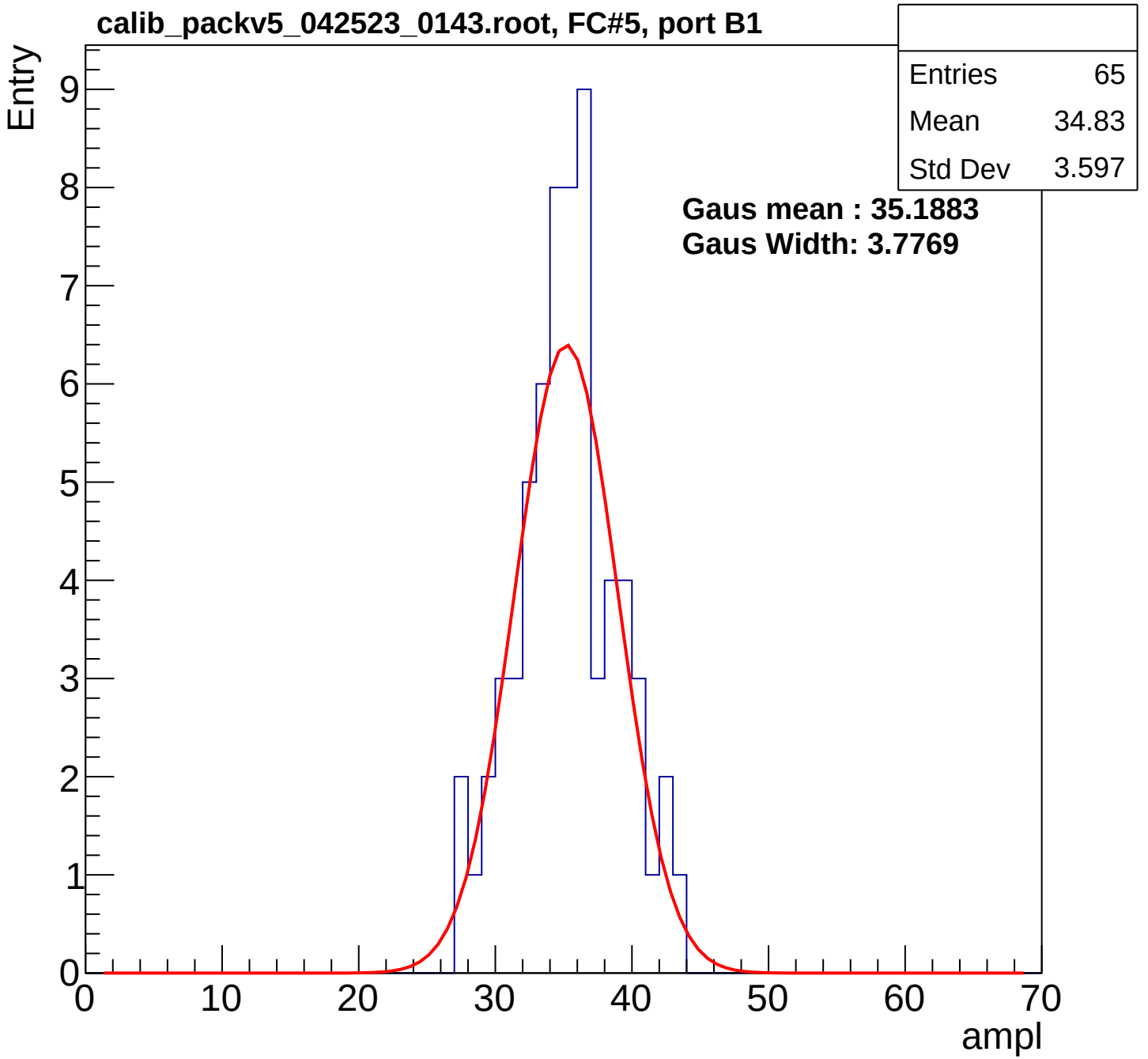
B0L000S, U14-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 34.83
Std Dev 3.597
Gaus mean : 35.1883
Gaus Width: 3.7769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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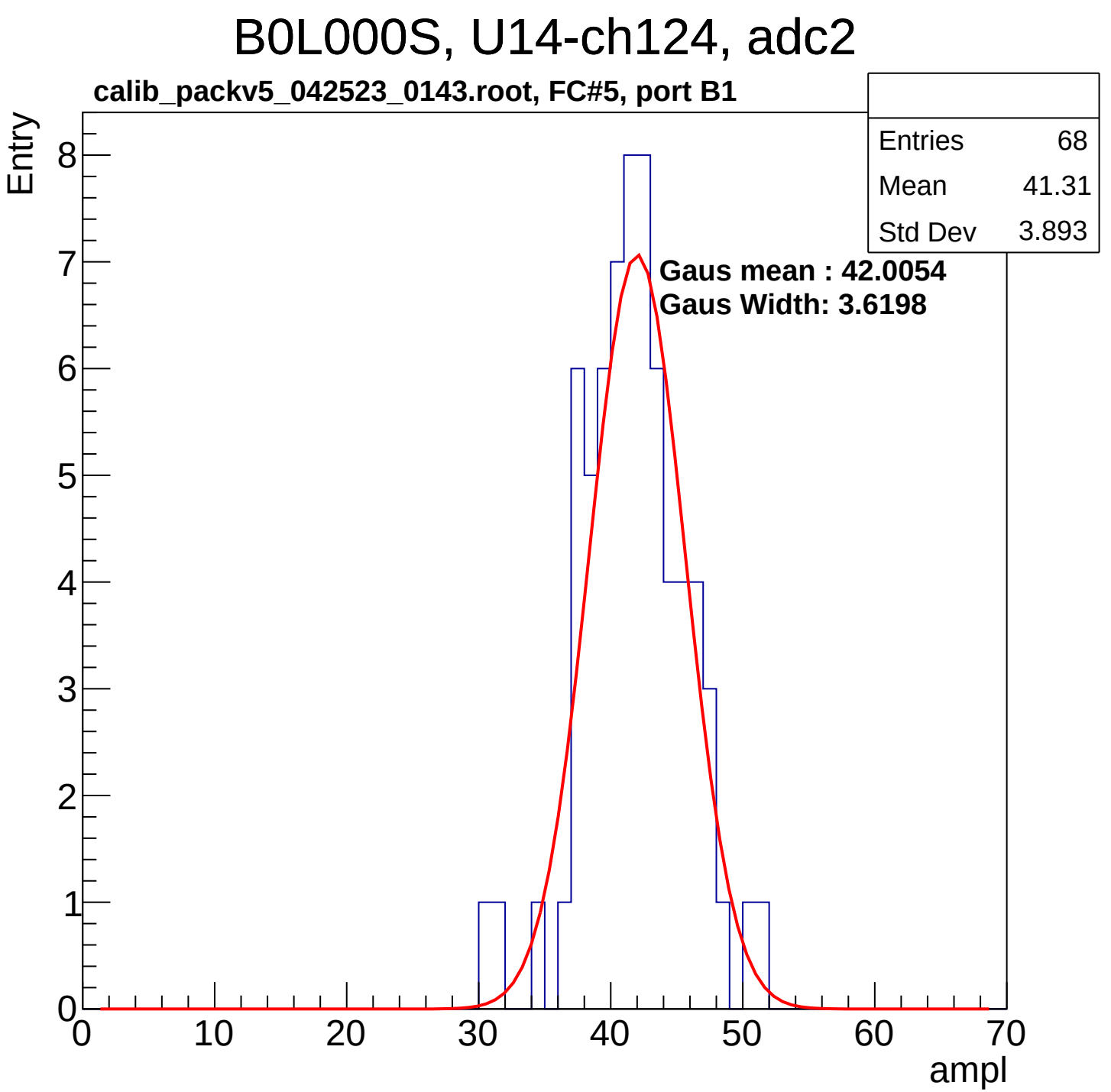
B0L000S, U14-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 41.31
Std Dev 3.893
Gaus mean : 42.0054
Gaus Width: 3.6198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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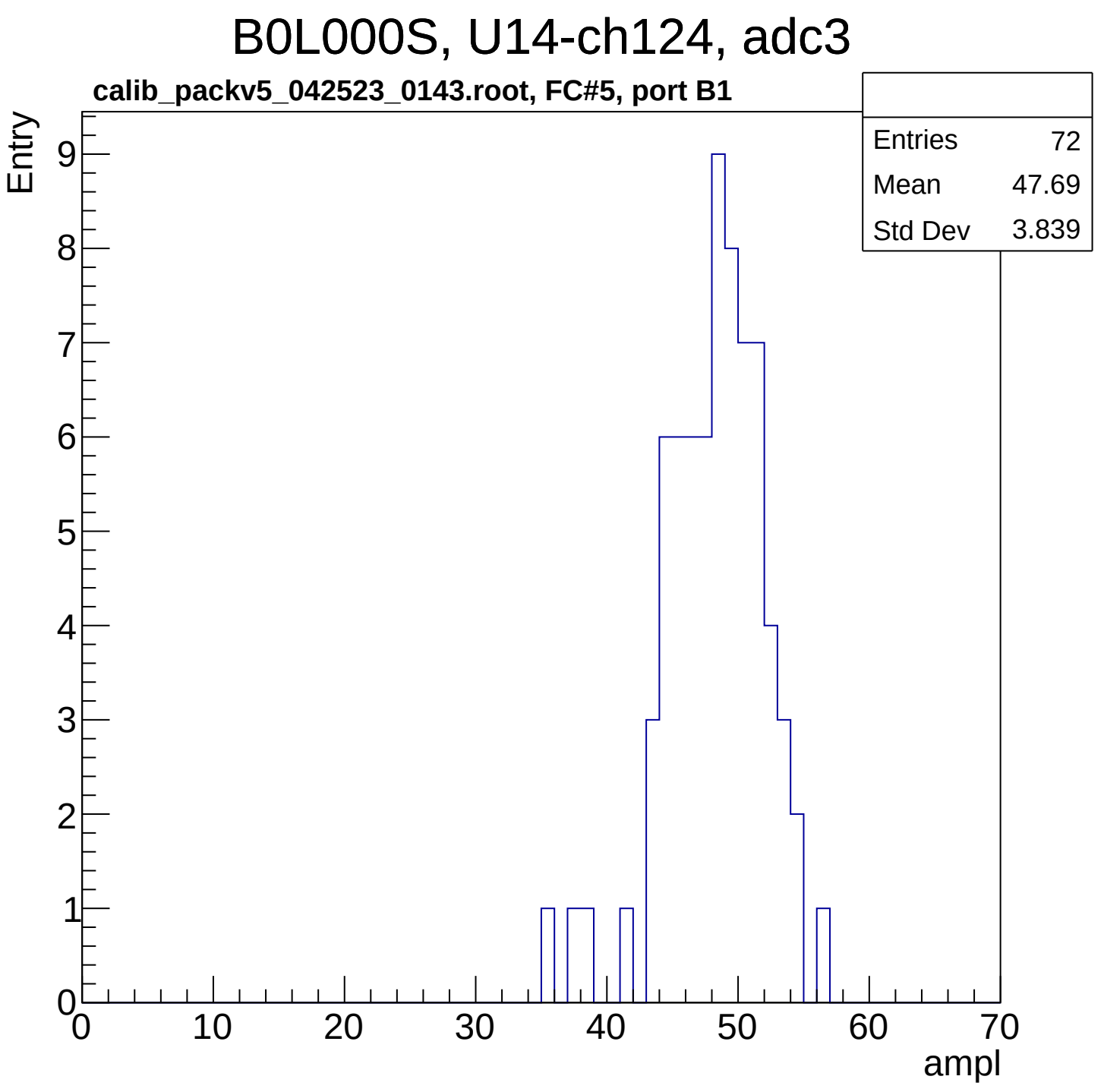
B0L000S, U14-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 47.69
Std Dev 3.839
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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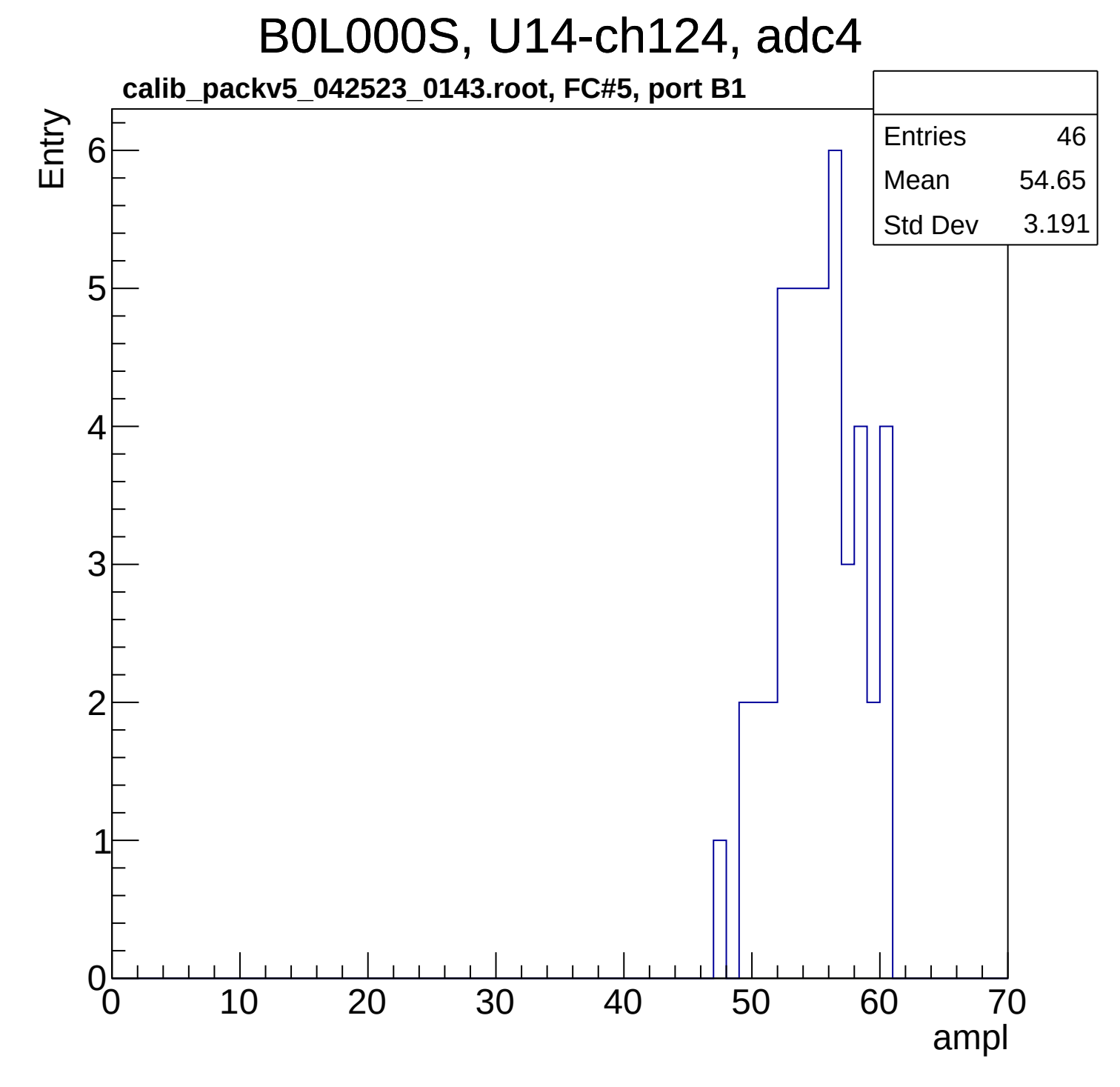
B0L000S, U14-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 54.65
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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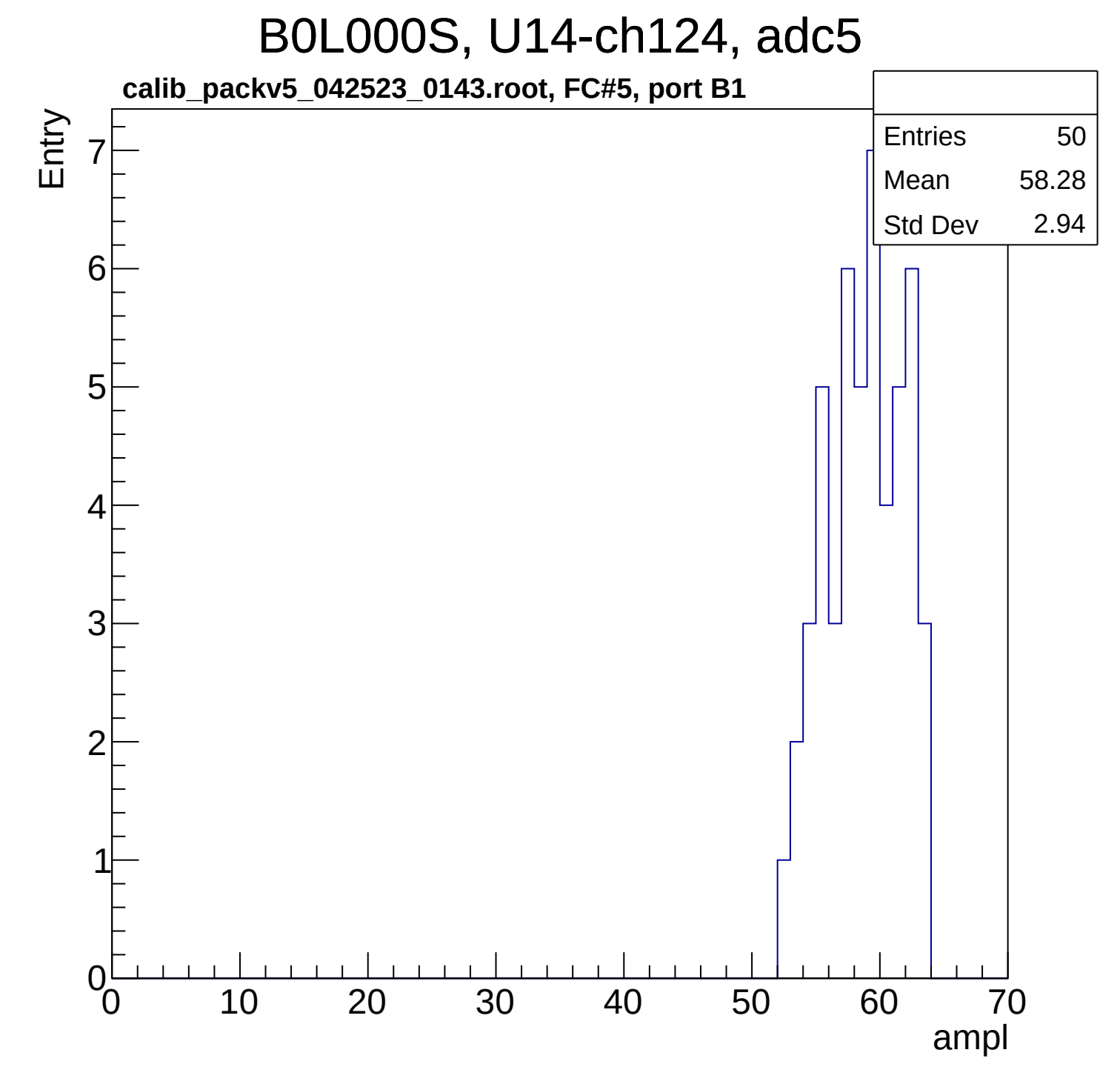
B0L000S, U14-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.28
Std Dev 2.94
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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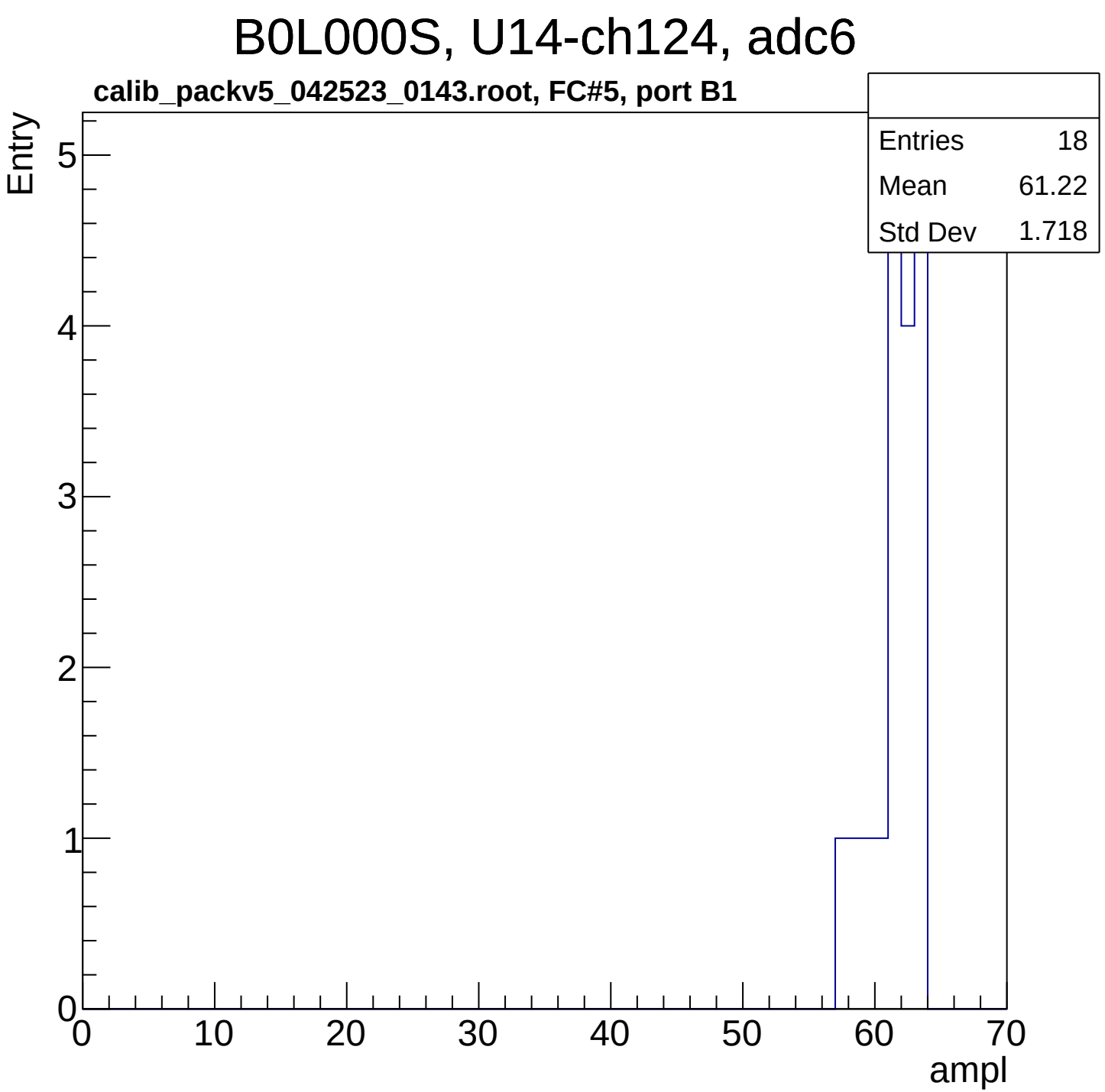
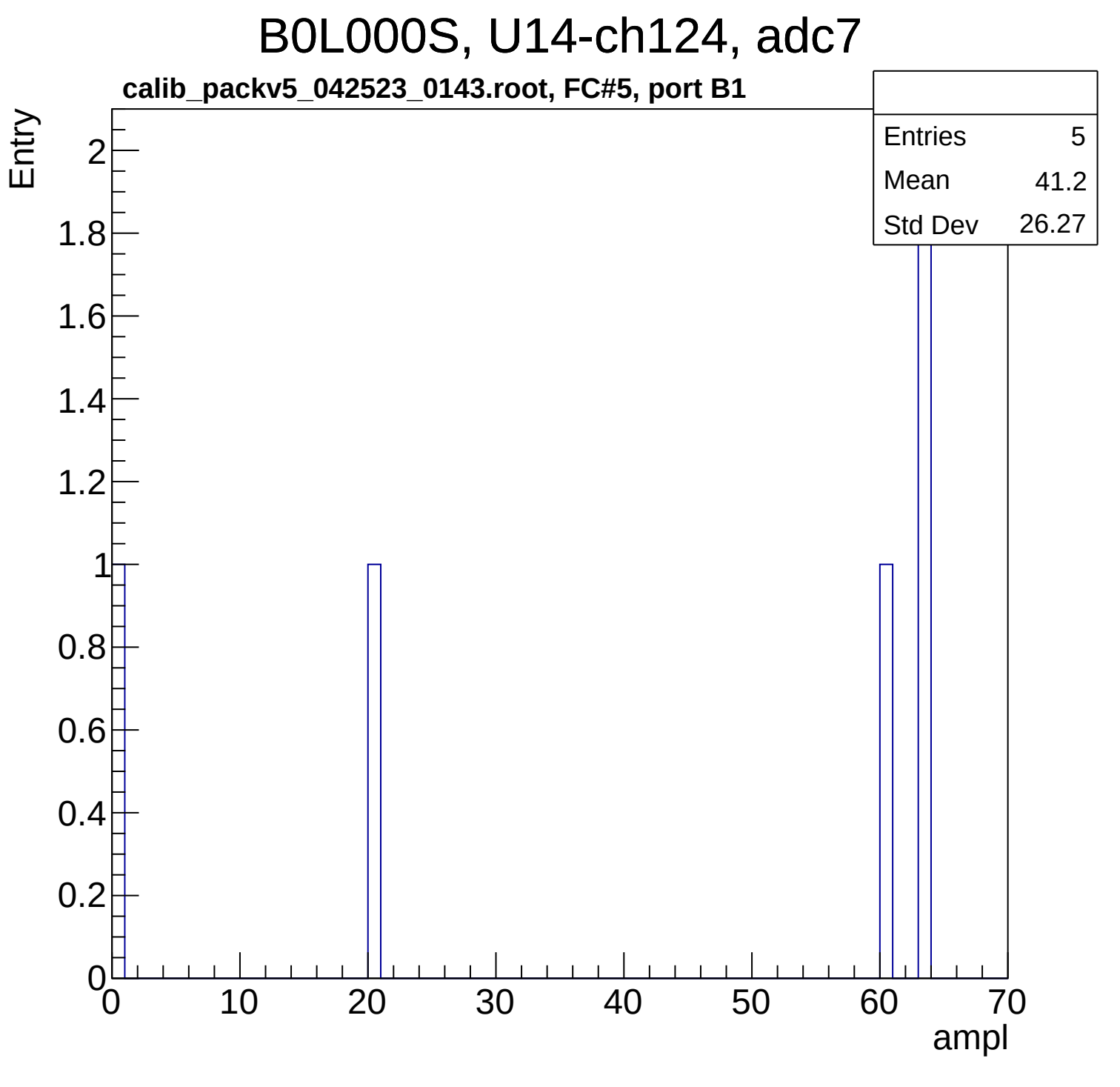

B0L000S, U14-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 61.22
Std Dev 1.718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 41.2
Std Dev 26.27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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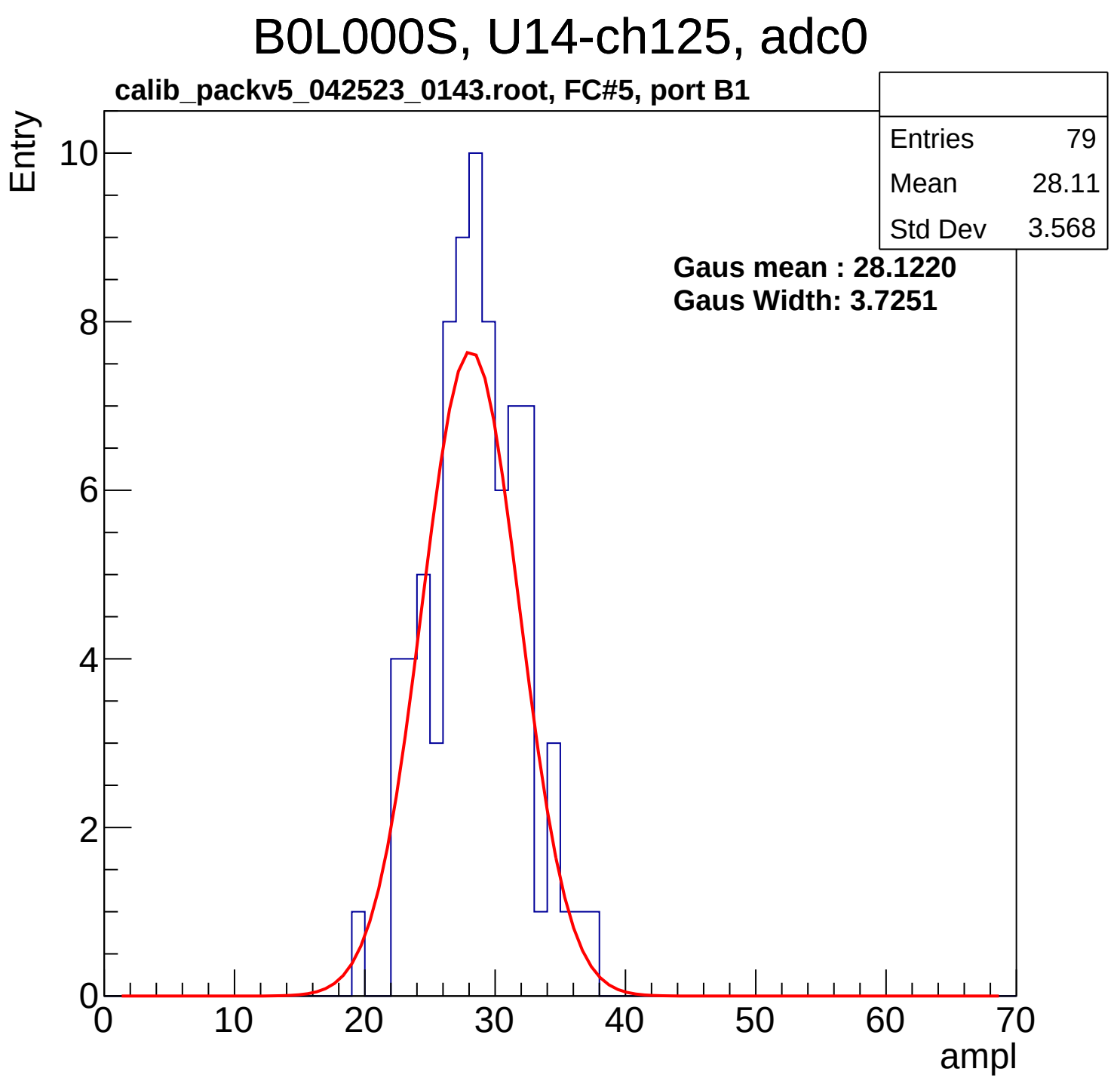
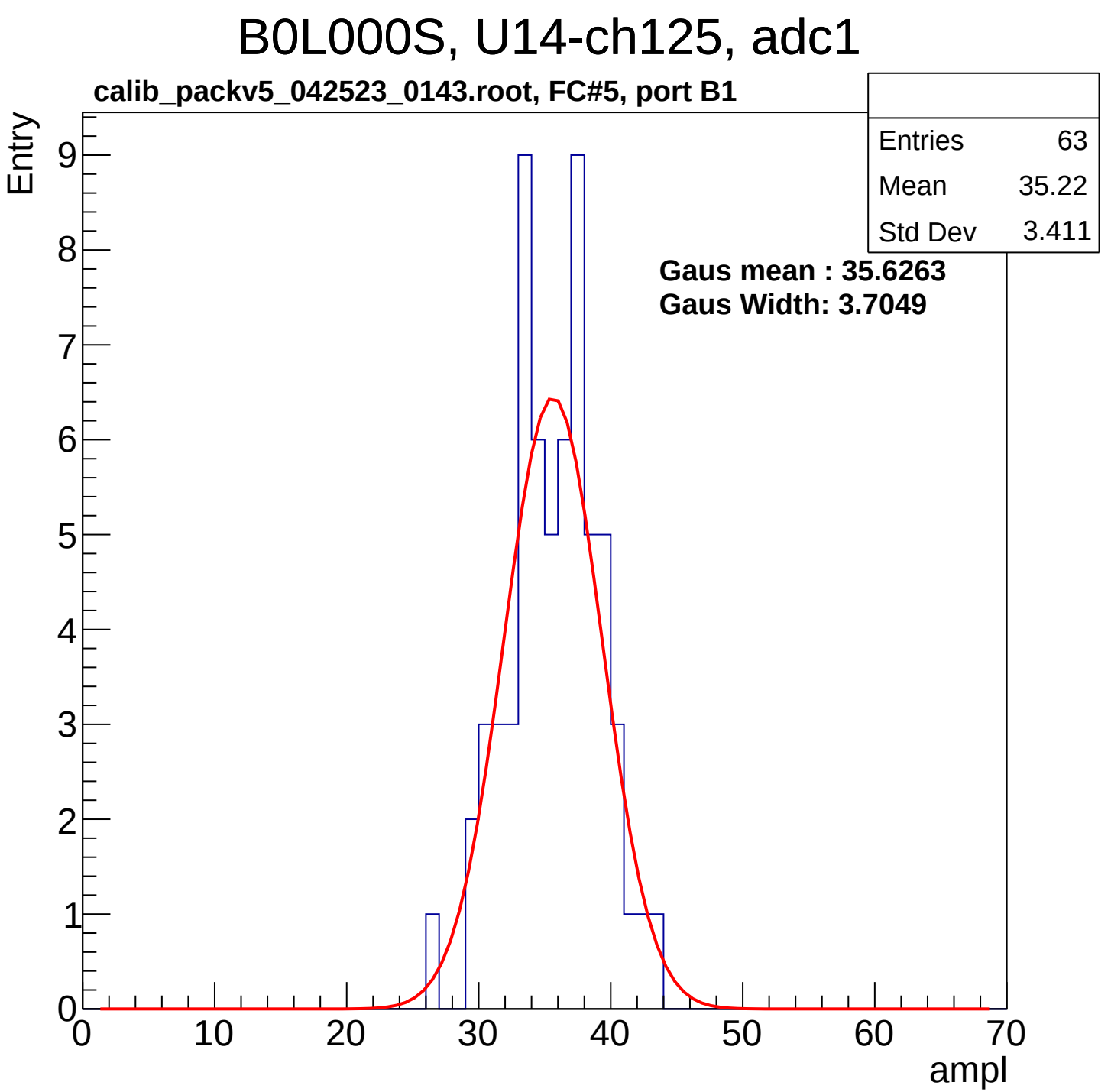
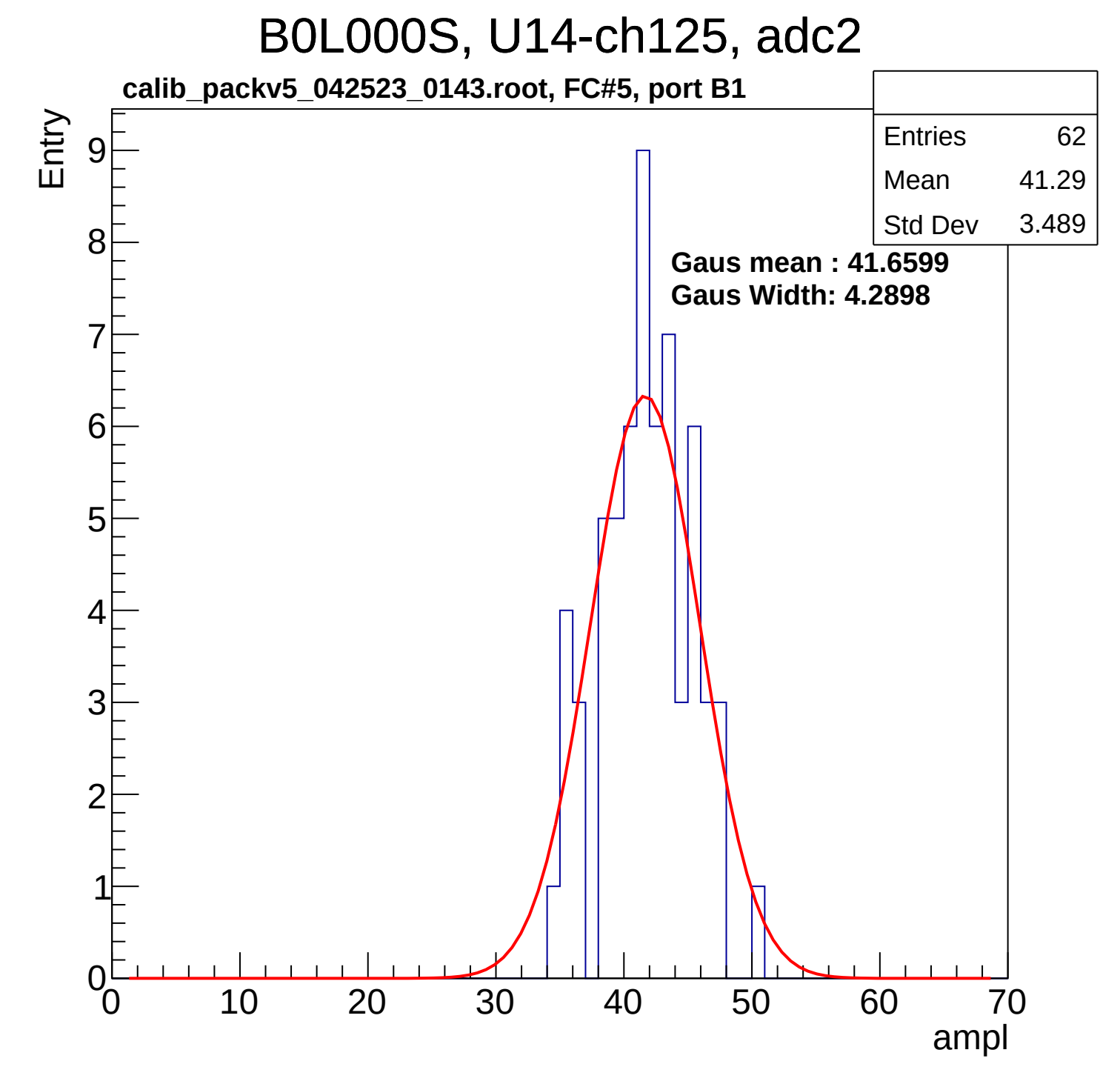

B0L000S, U14-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 28.11
Std Dev 3.568
Gaus mean : 28.1220
Gaus Width: 3.7251
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U14-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.22
Std Dev 3.411
Gaus mean : 35.6263
Gaus Width: 3.7049
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U14-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 41.29
Std Dev 3.489
Gaus mean : 41.6599
Gaus Width: 4.2898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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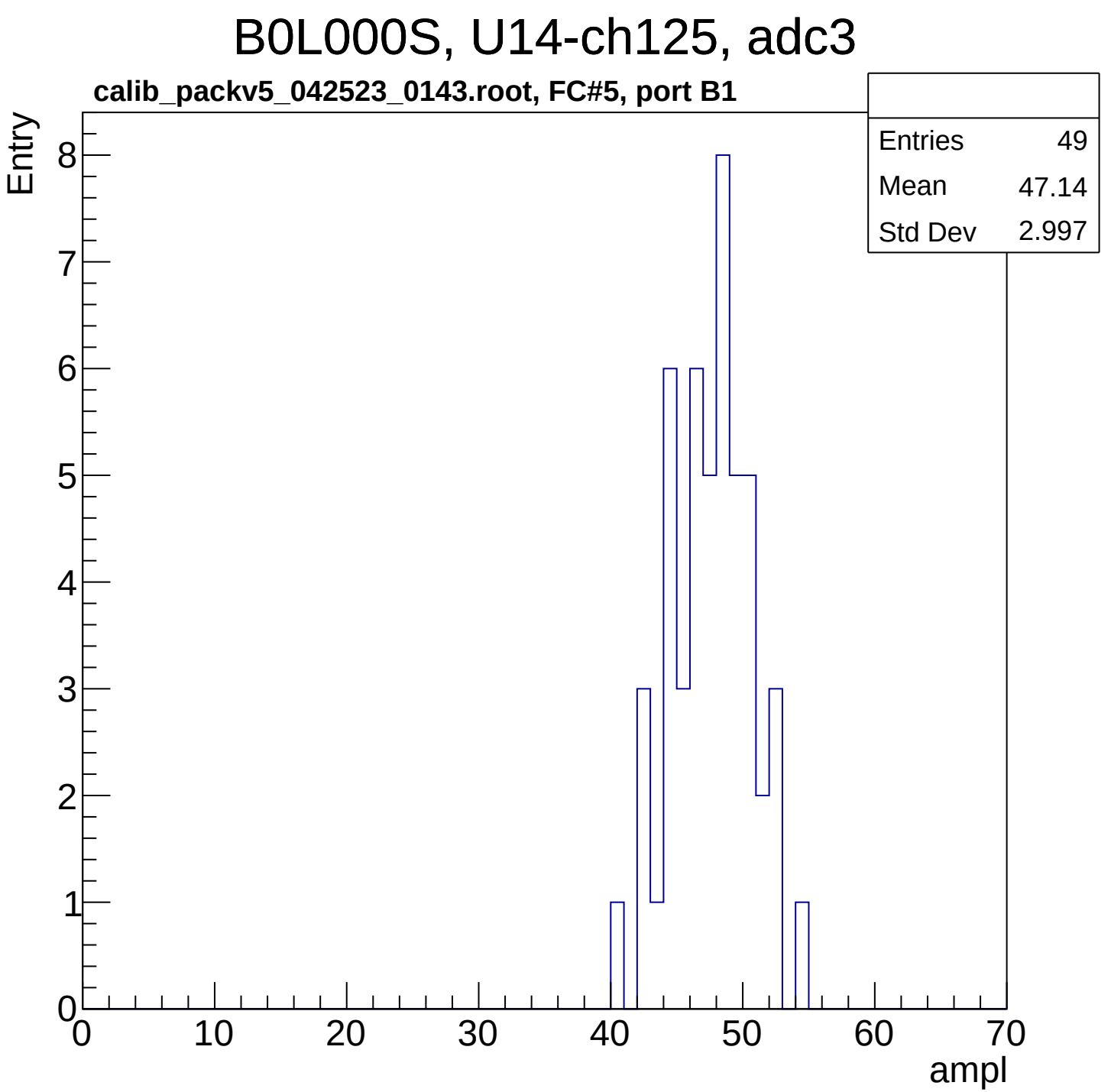
B0L000S, U14-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 47.14
Std Dev 2.997
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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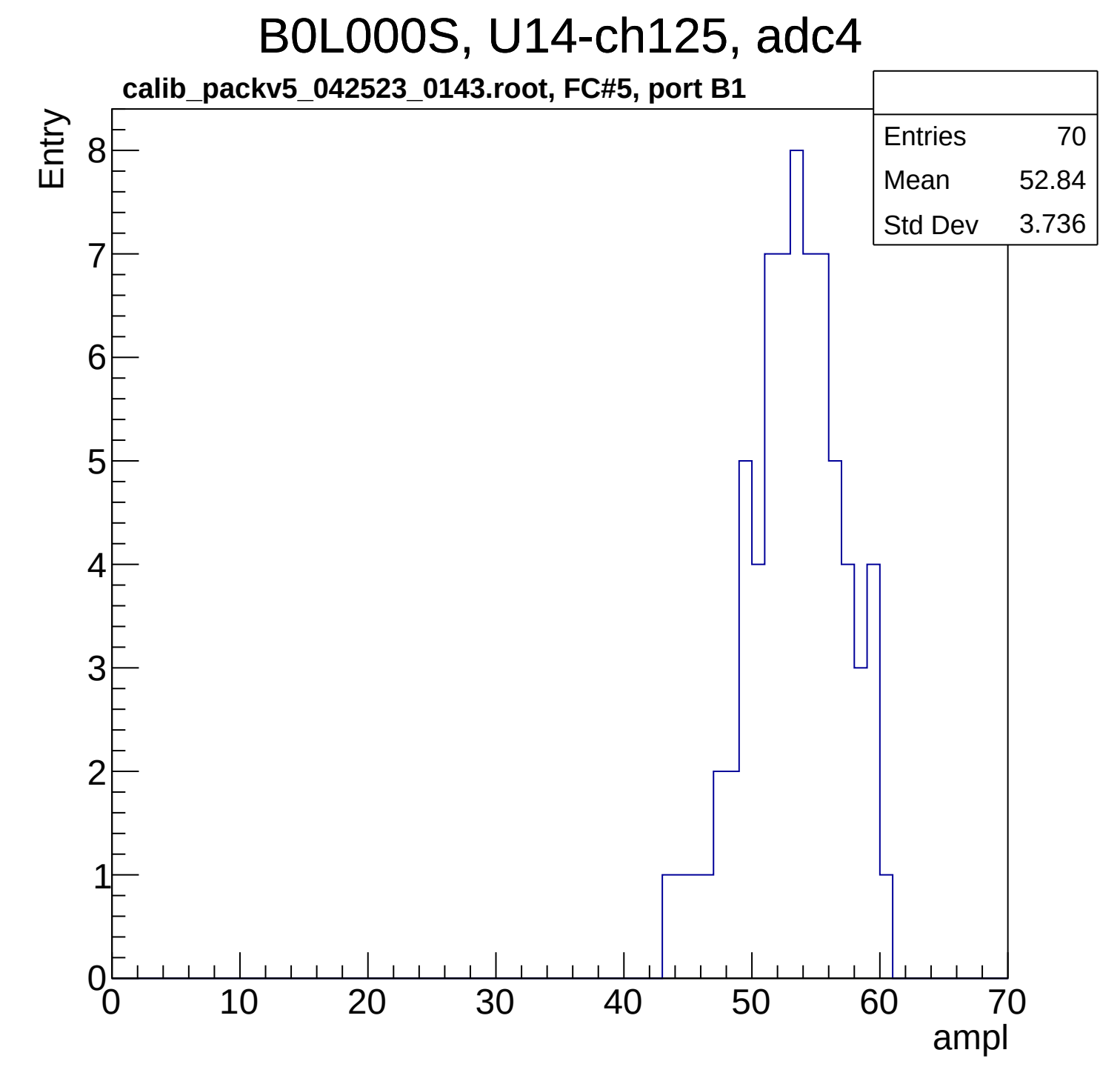
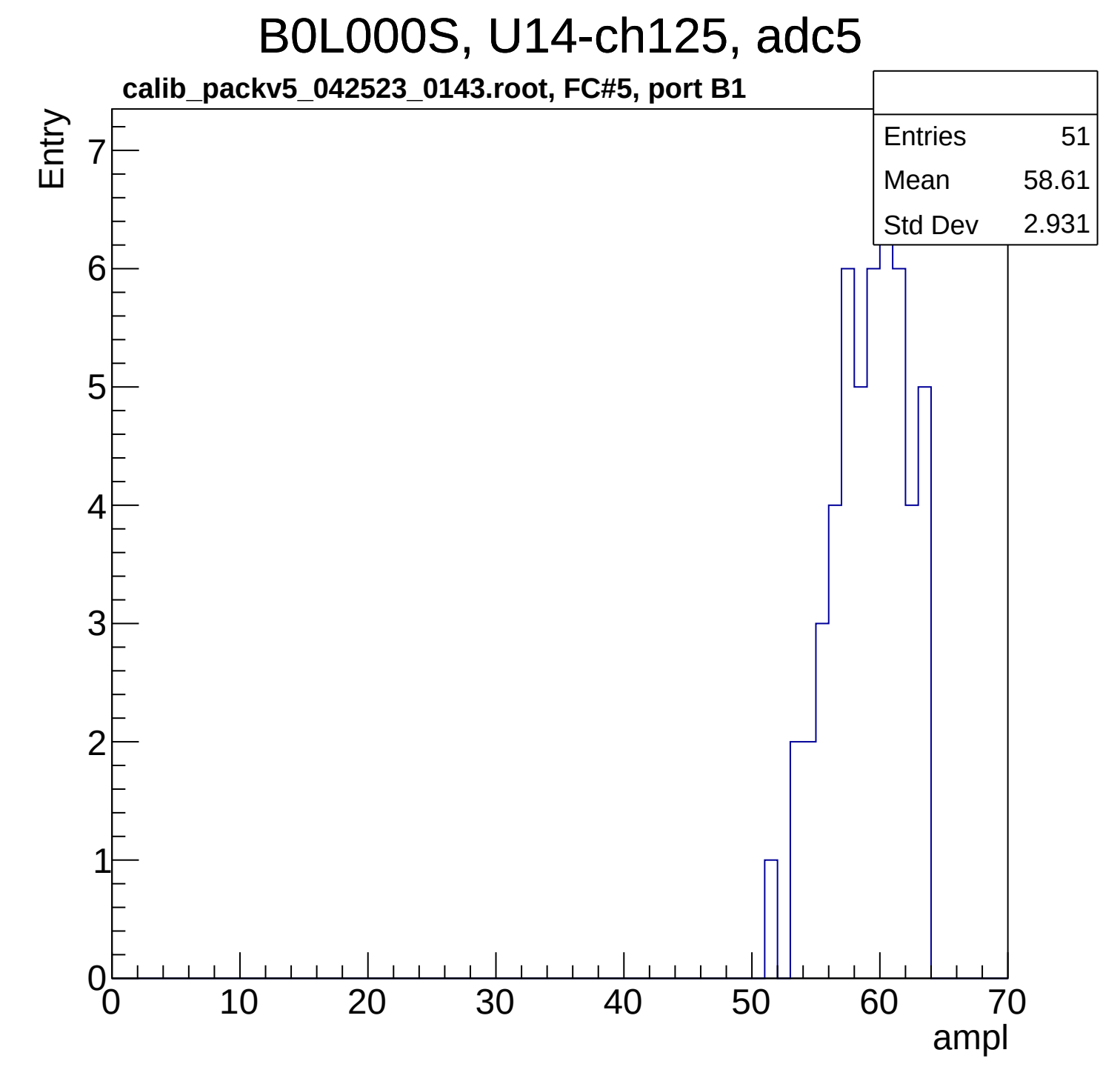
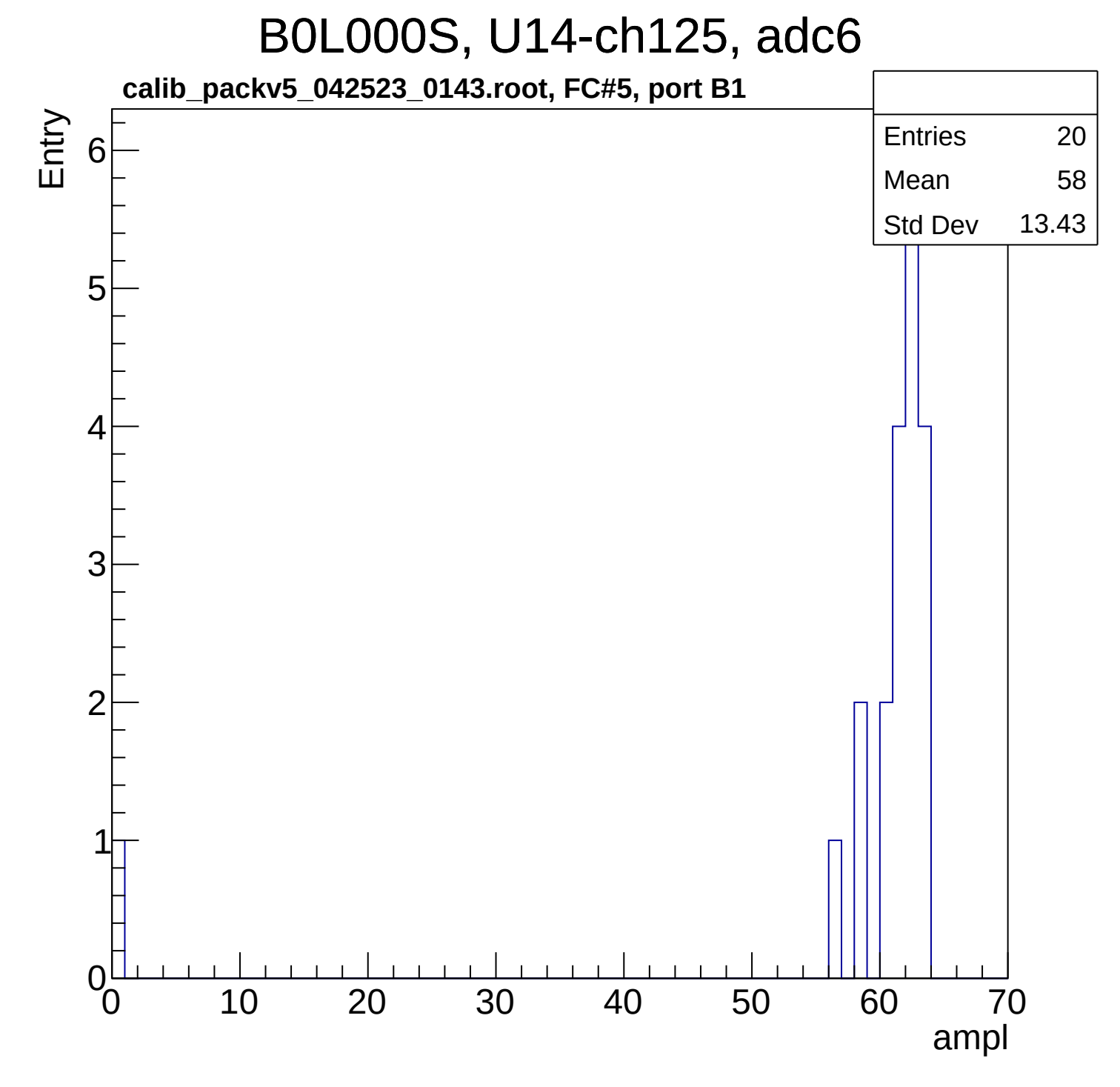
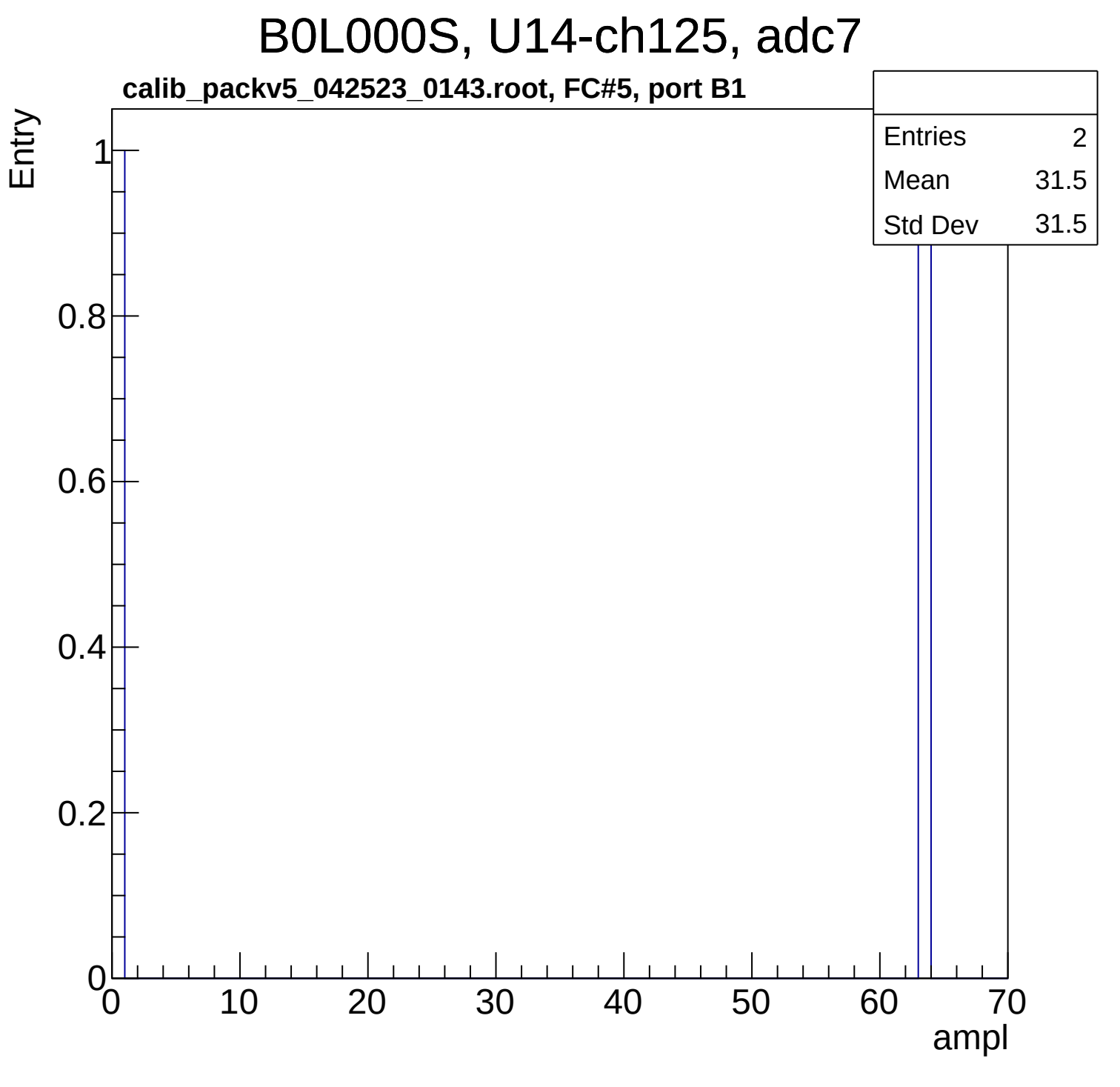

B0L000S, U14-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 52.84
Std Dev 3.736
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.61
Std Dev 2.931
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 20
Mean 58
Std Dev 13.43
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
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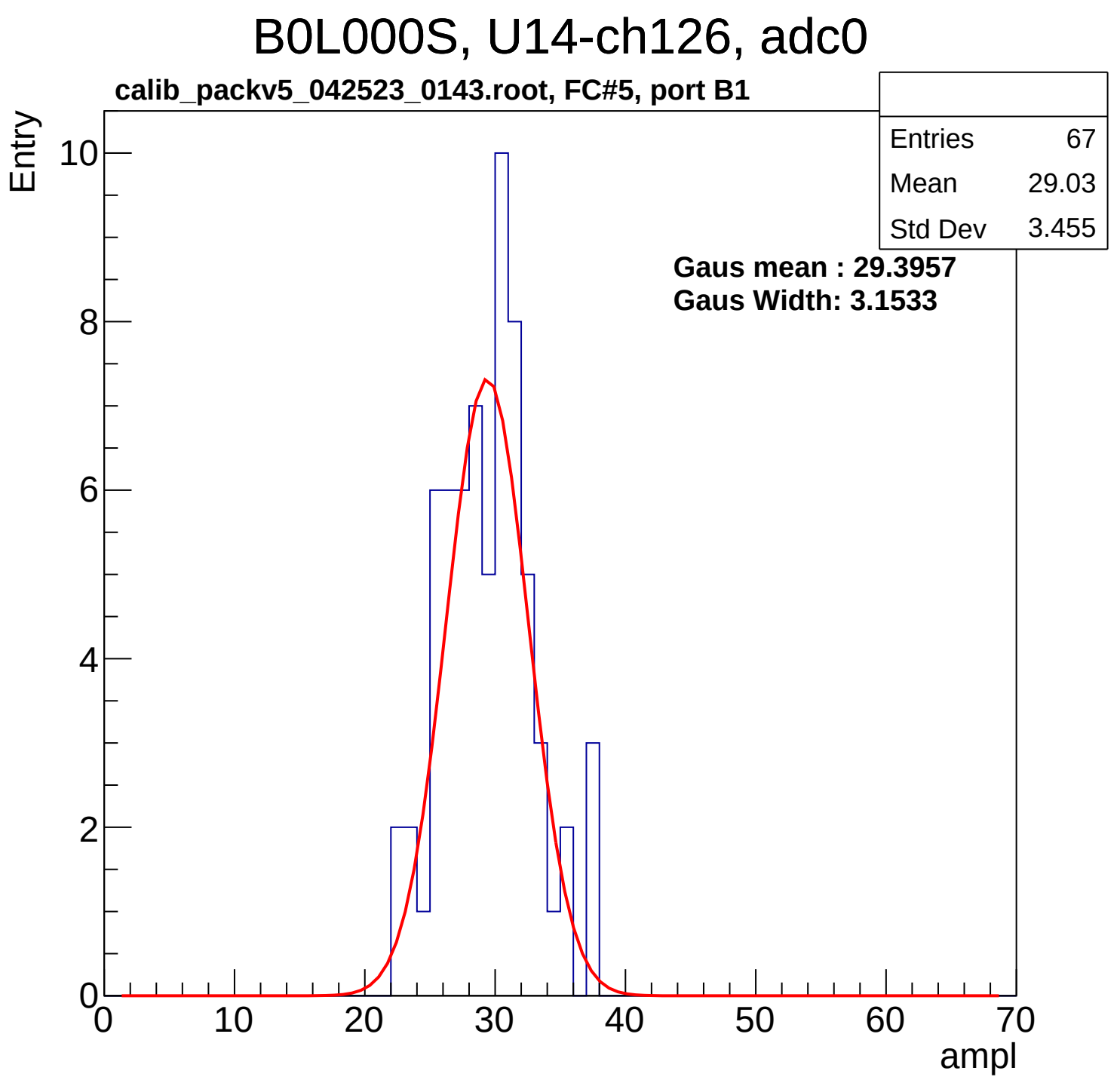
50
60
70
ampl


B0L000S, U14-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.03
Std Dev 3.455
Gaus mean : 29.3957
Gaus Width: 3.1533
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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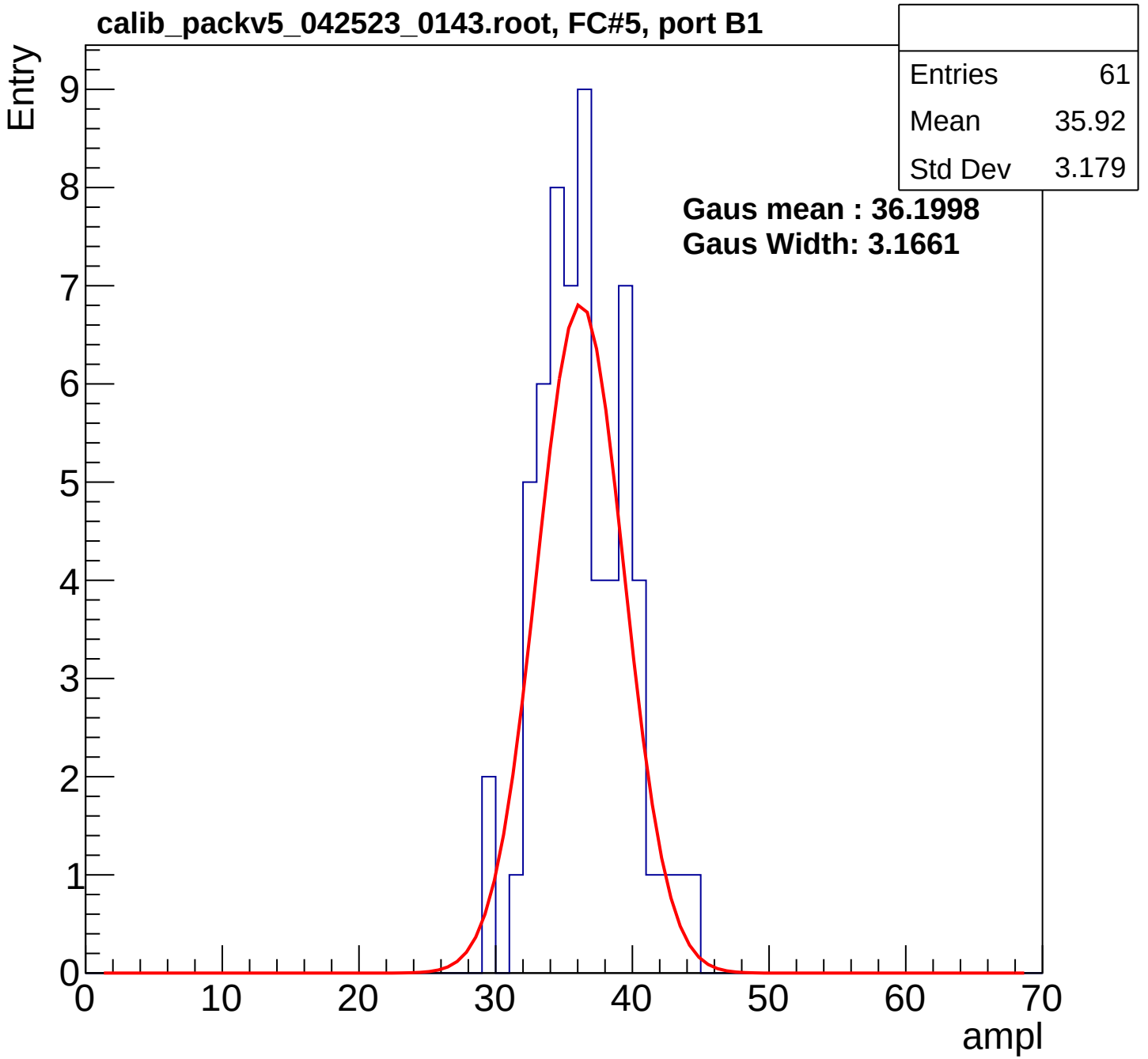
B0L000S, U14-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 35.92
Std Dev 3.179
Gaus mean : 36.1998
Gaus Width: 3.1661
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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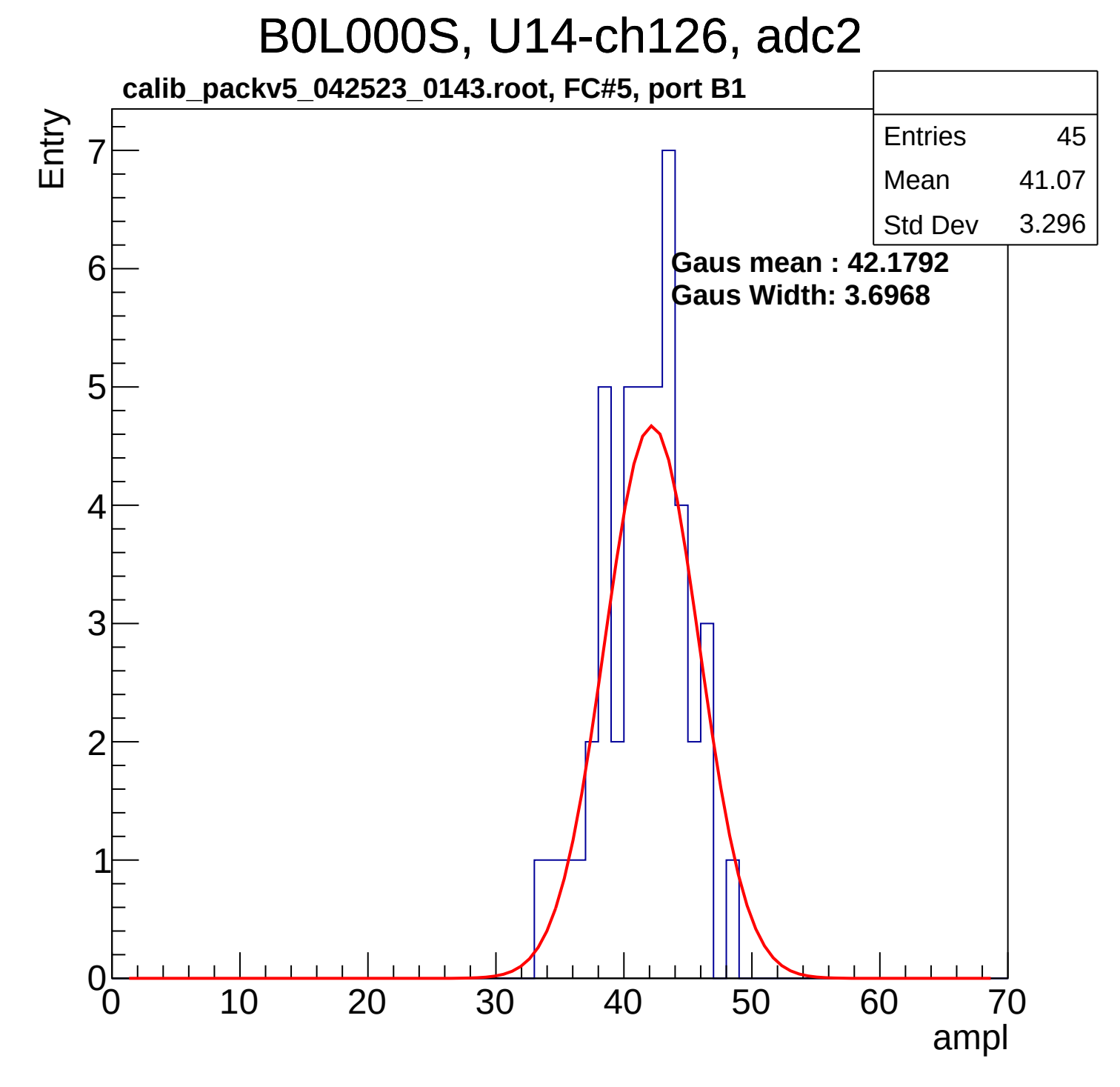
B0L000S, U14-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 41.07
Std Dev 3.296
Gaus mean : 42.1792
Gaus Width: 3.6968
Entry
ampl
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1
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3
4
5
6
7
0
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60
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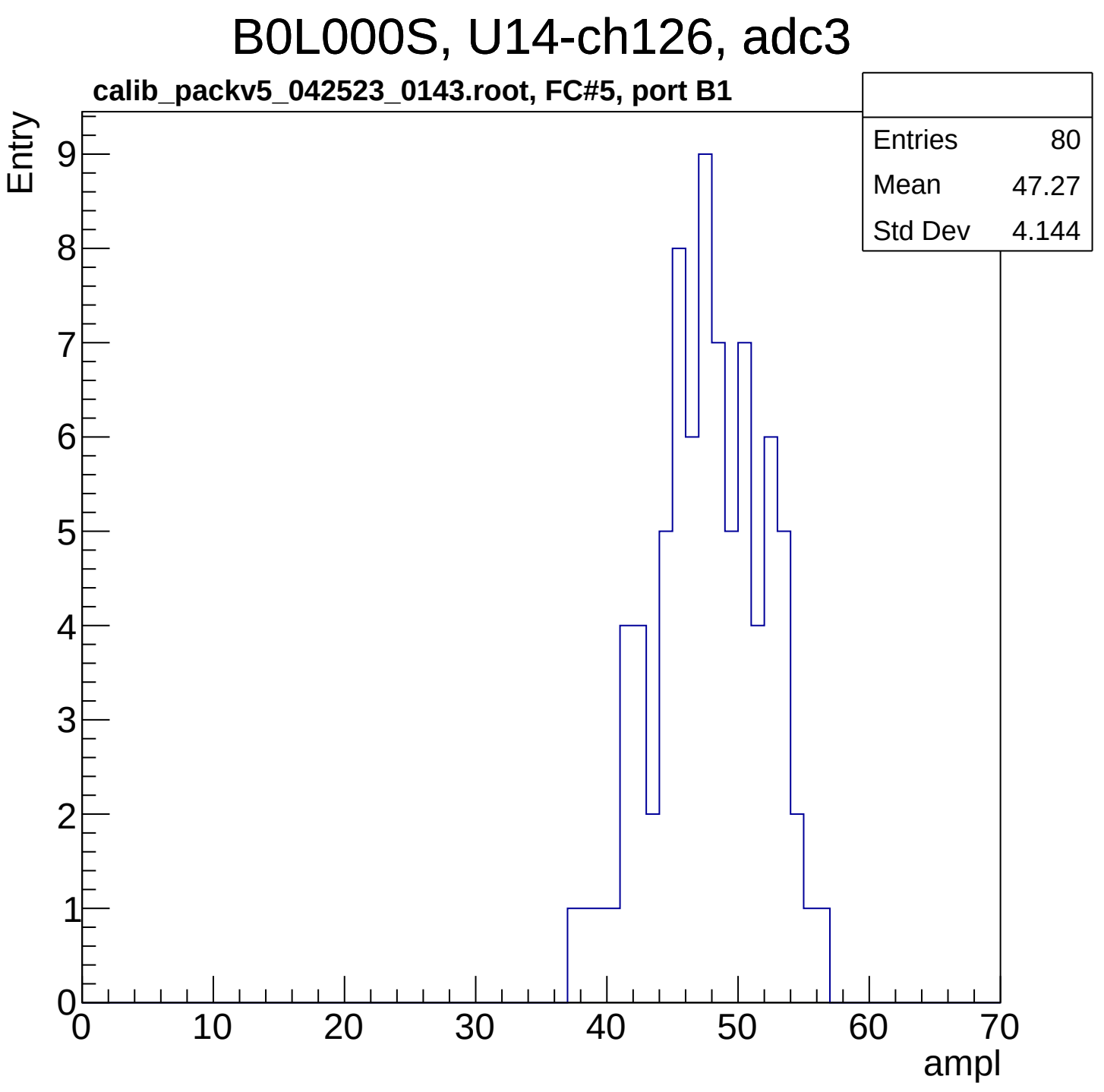
B0L000S, U14-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 47.27
Std Dev 4.144
Entry
ampl
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4
5
6
7
8
9
0
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60
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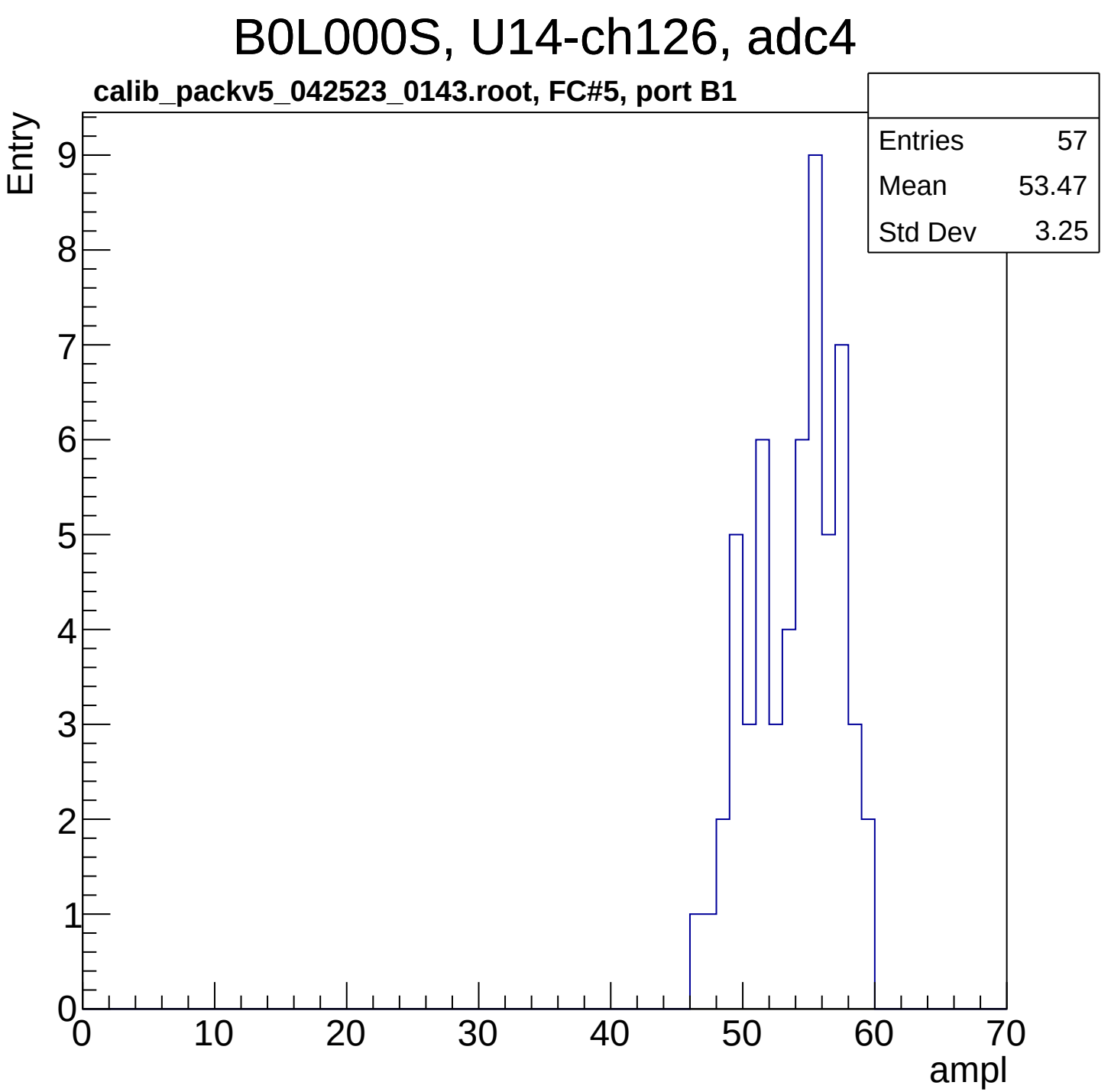
B0L000S, U14-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 53.47
Std Dev 3.25
Entry
ampl
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2
3
4
5
6
7
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0
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60
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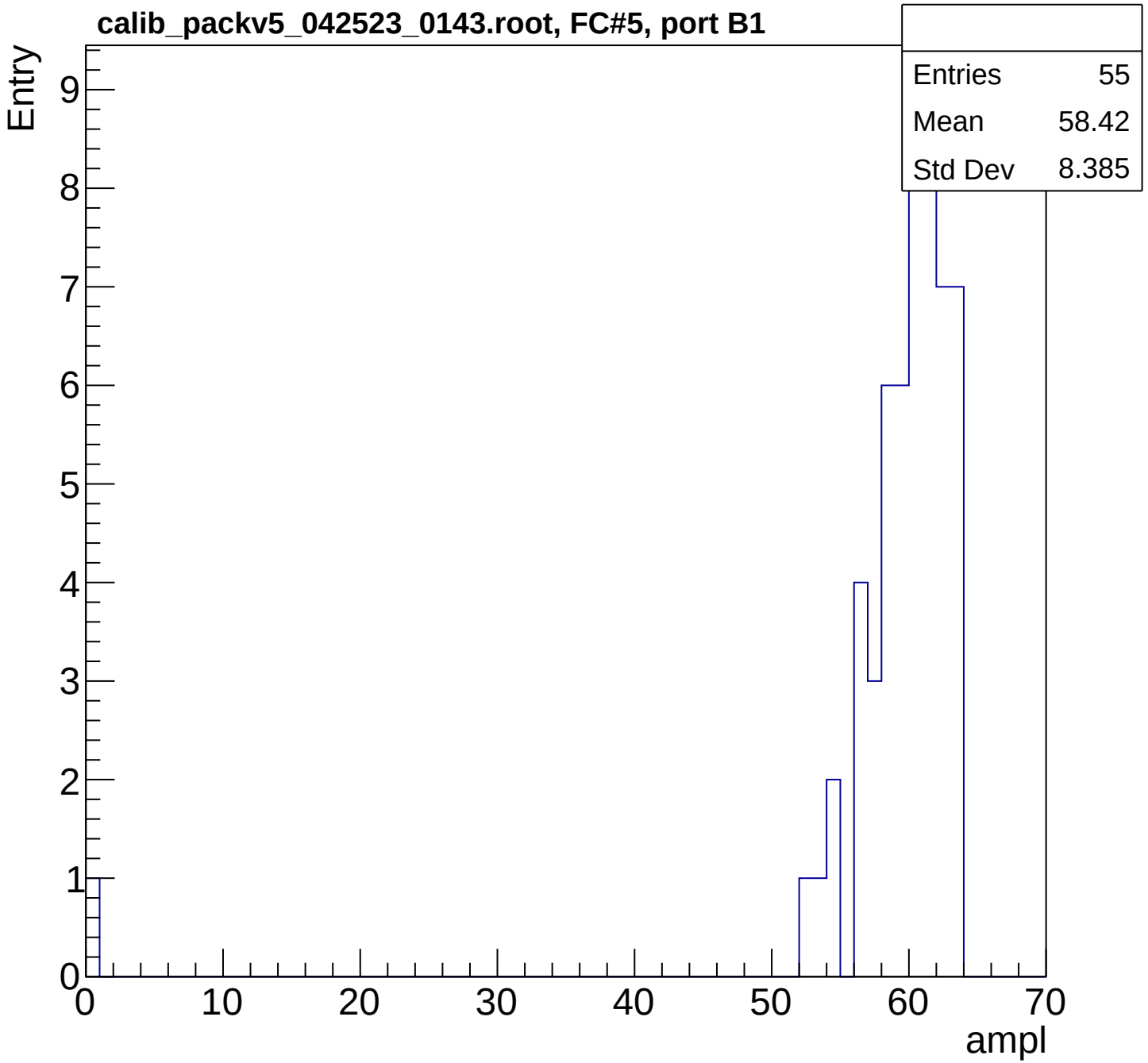
B0L000S, U14-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.42
Std Dev 8.385
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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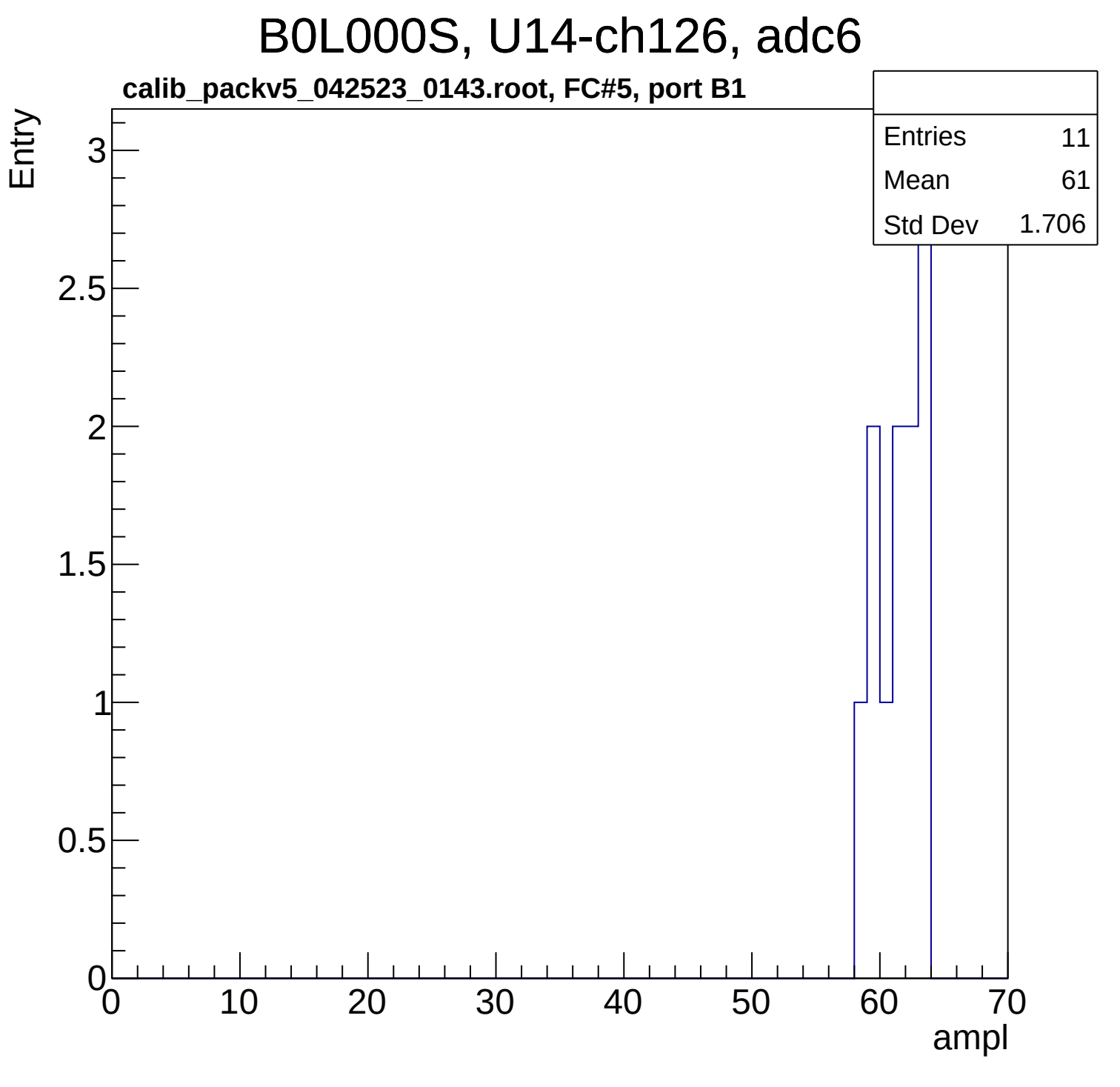
B0L000S, U14-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 11
Mean 61
Std Dev 1.706
Entry
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1
1.5
2
2.5
3
0
10
20
30
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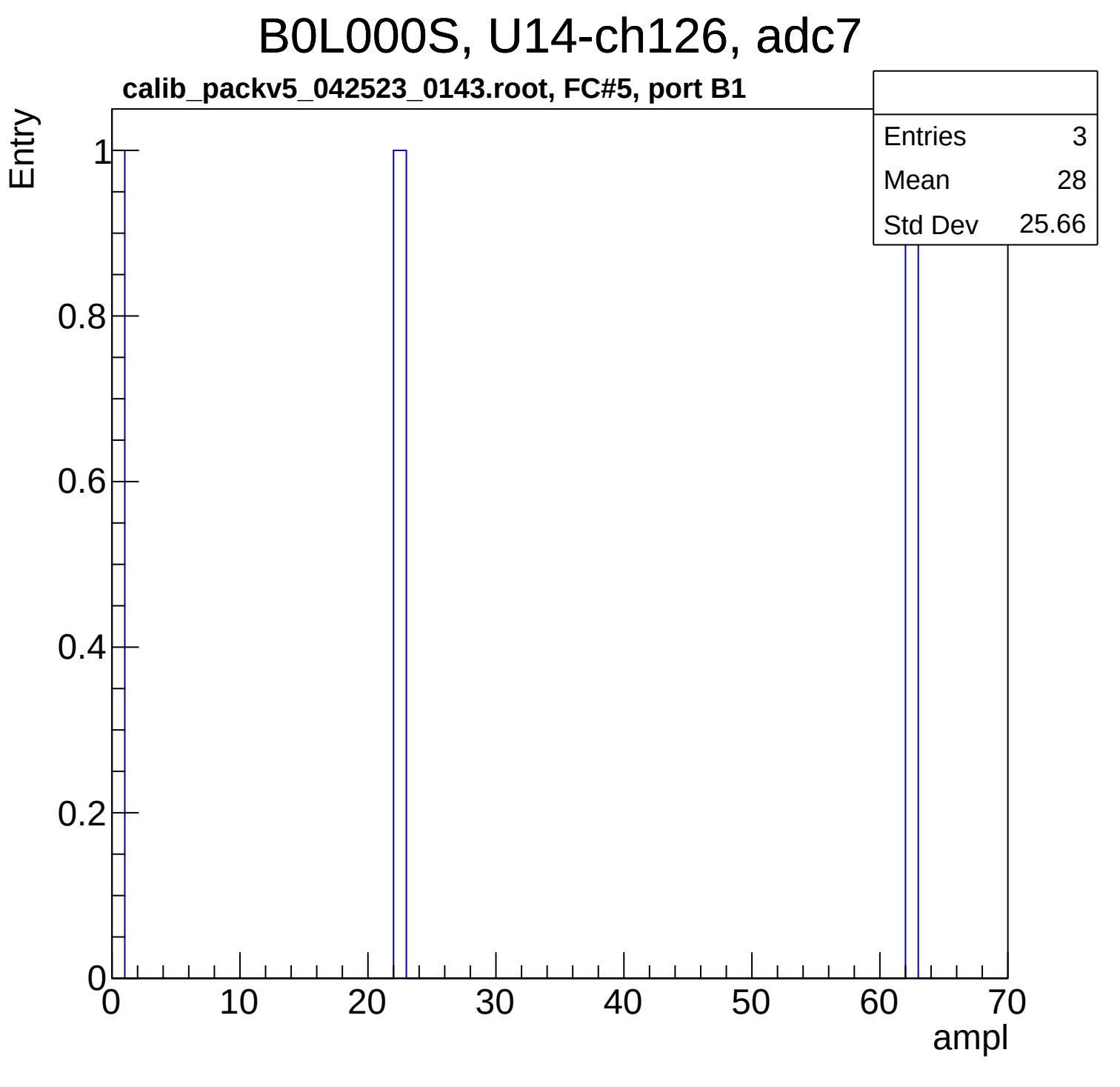
50
60
70
ampl


B0L000S, U14-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
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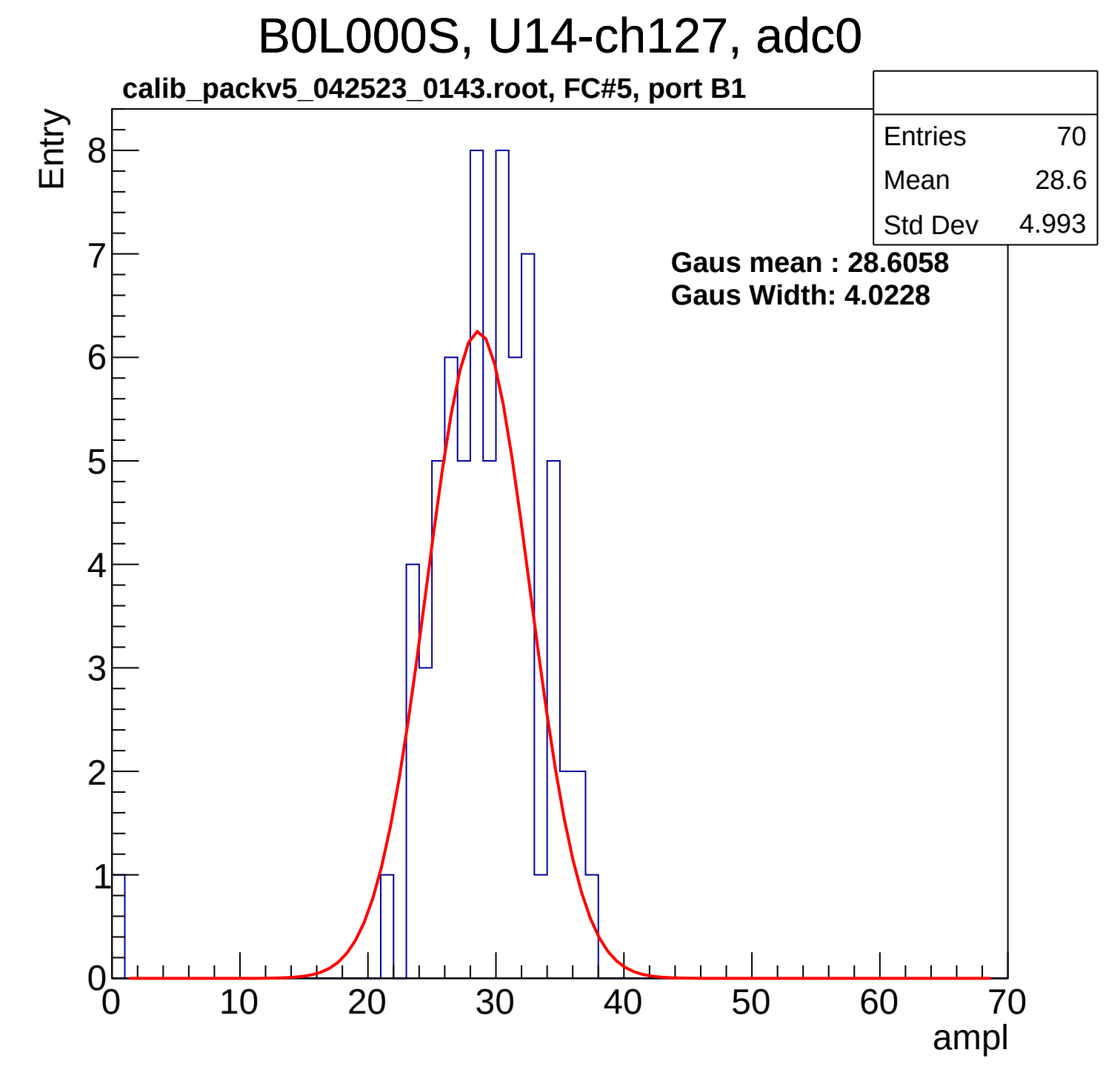
60
70


B0L000S, U14-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 28.6
Std Dev 4.993
Gaus mean : 28.6058
Gaus Width: 4.0228
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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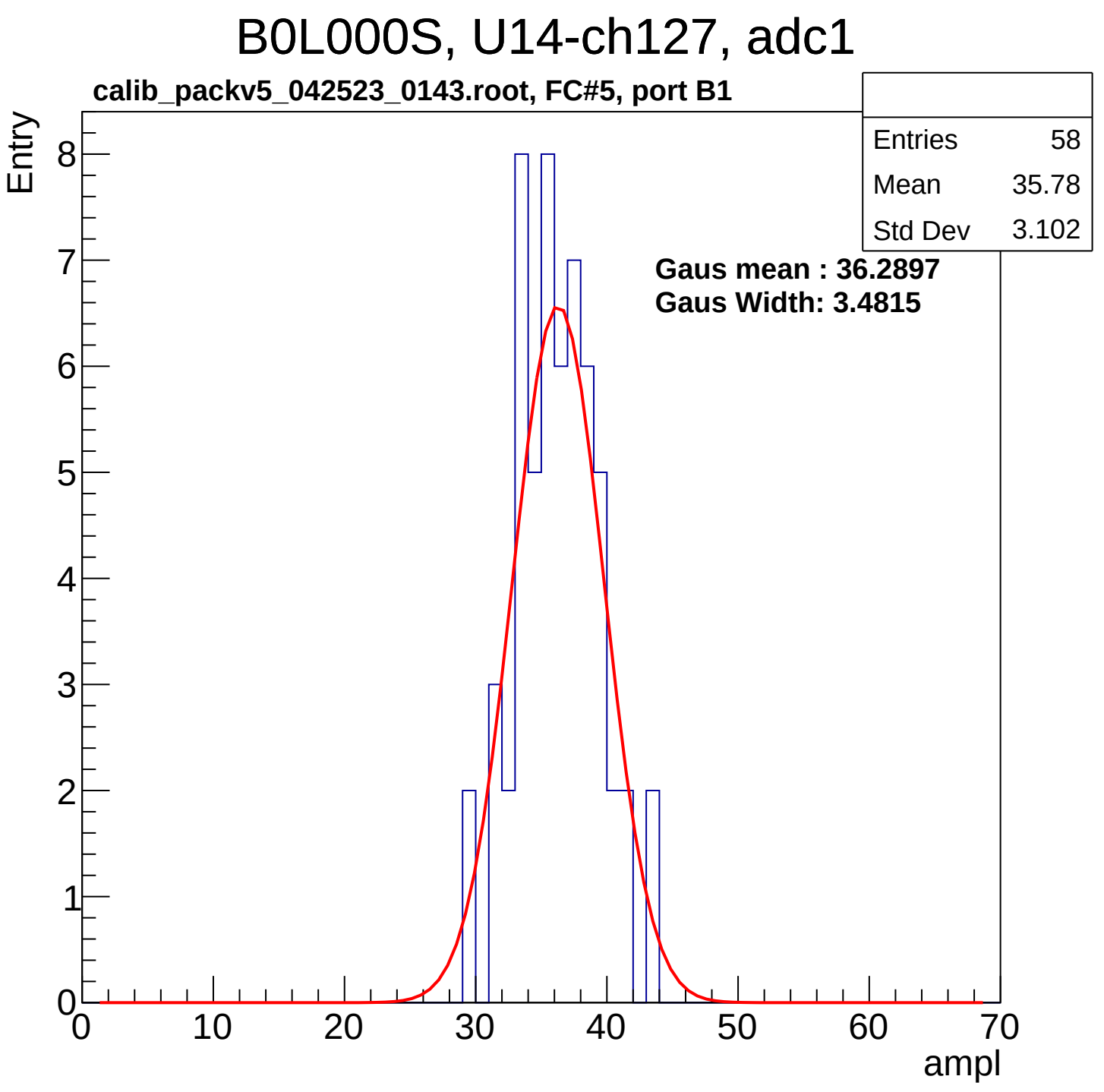
B0L000S, U14-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 35.78
Std Dev 3.102
Gaus mean : 36.2897
Gaus Width: 3.4815
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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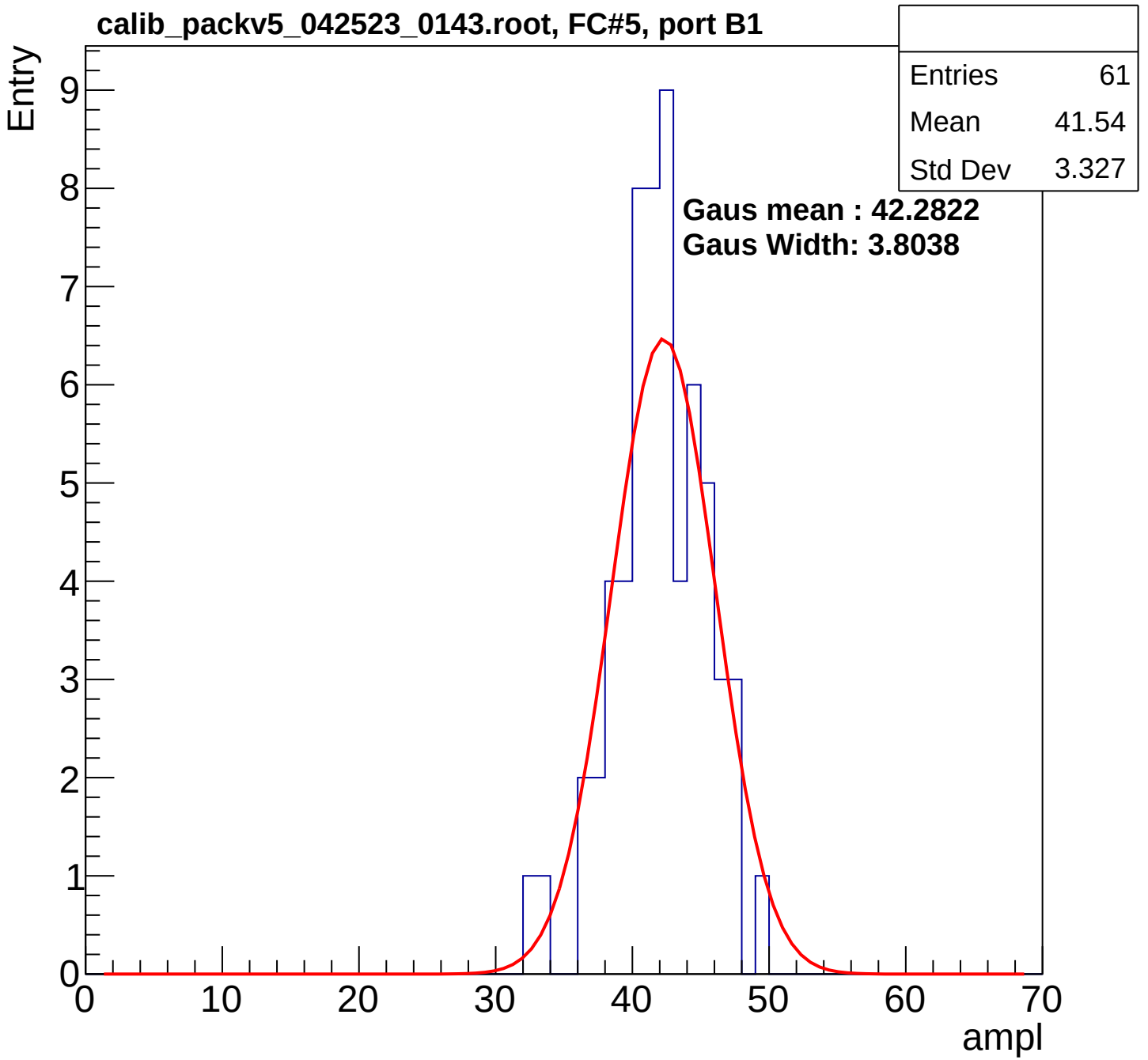
B0L000S, U14-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 41.54
Std Dev 3.327
Gaus mean : 42.2822
Gaus Width: 3.8038
Entry
ampl
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3
4
5
6
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0
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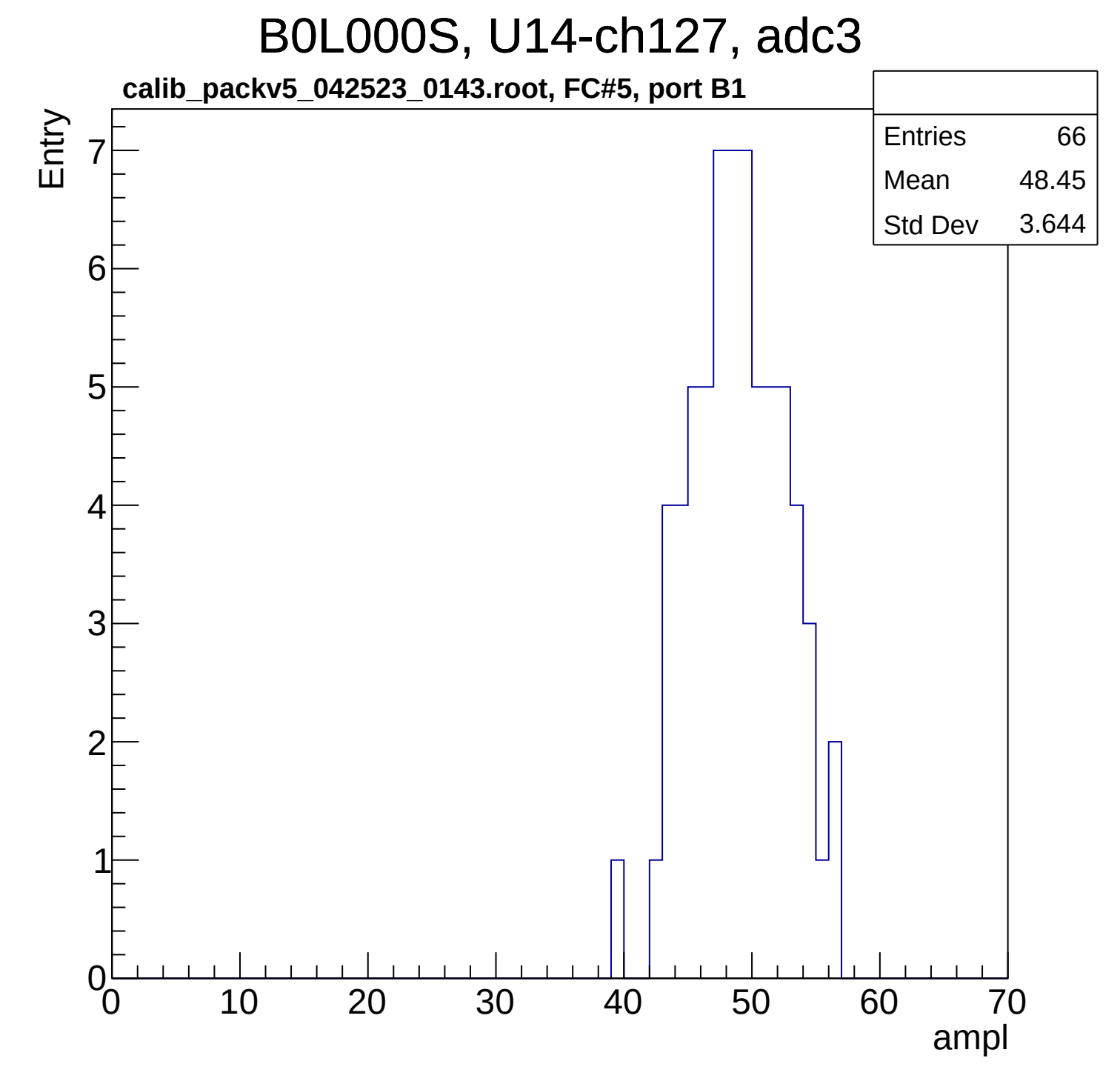
B0L000S, U14-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 48.45
Std Dev 3.644
Entry
ampl
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3
4
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7
0
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20
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40
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60
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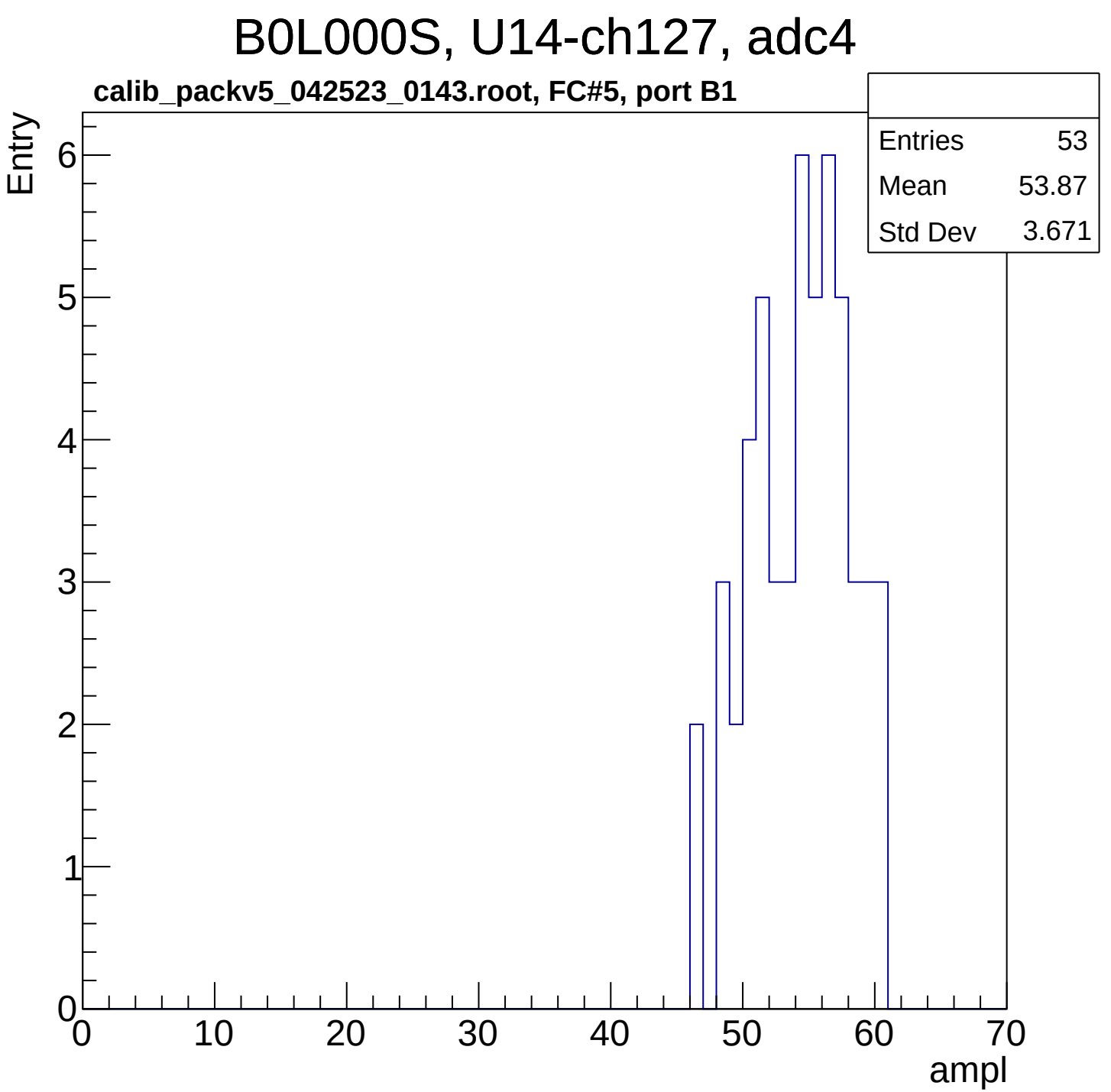
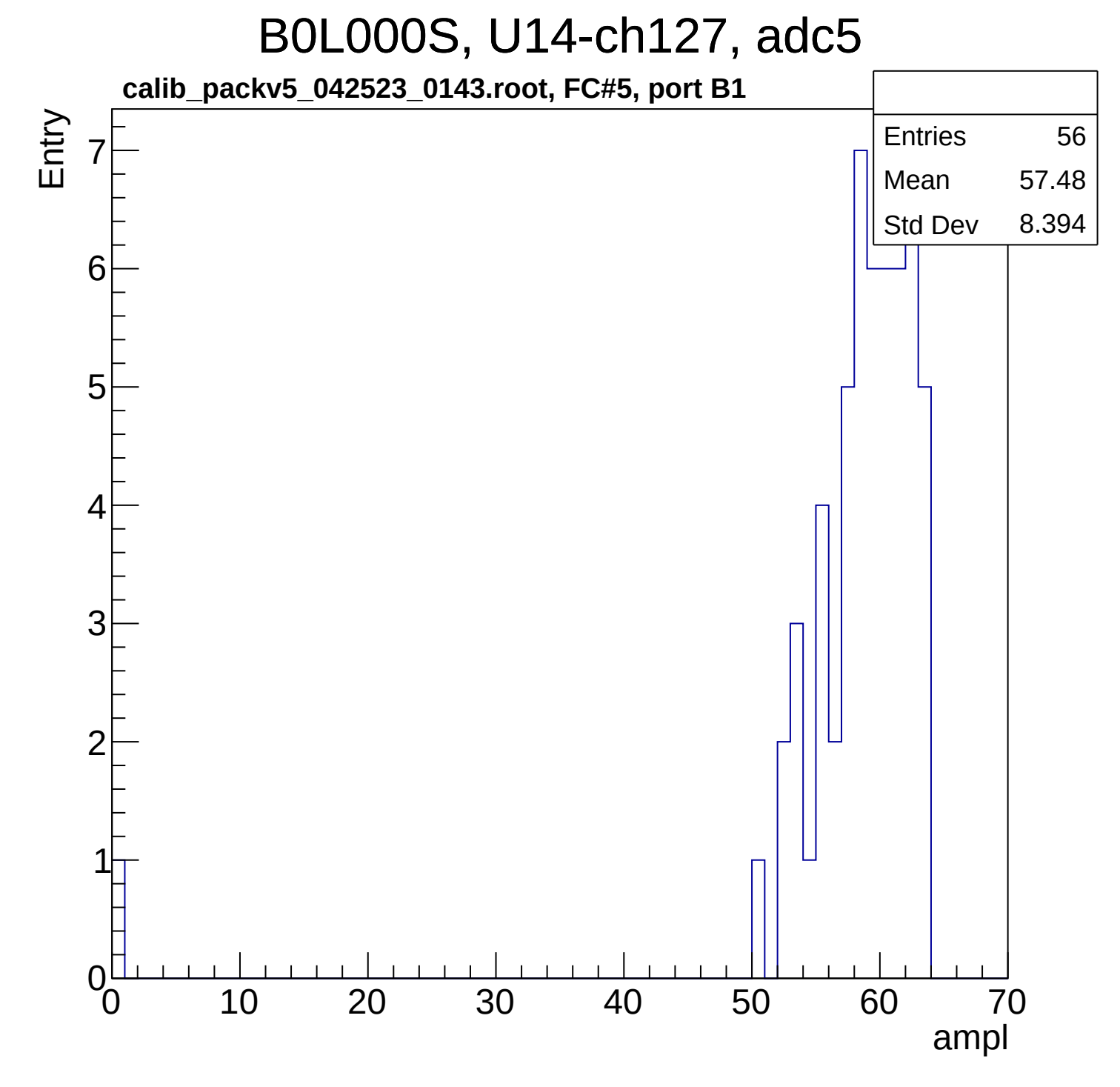
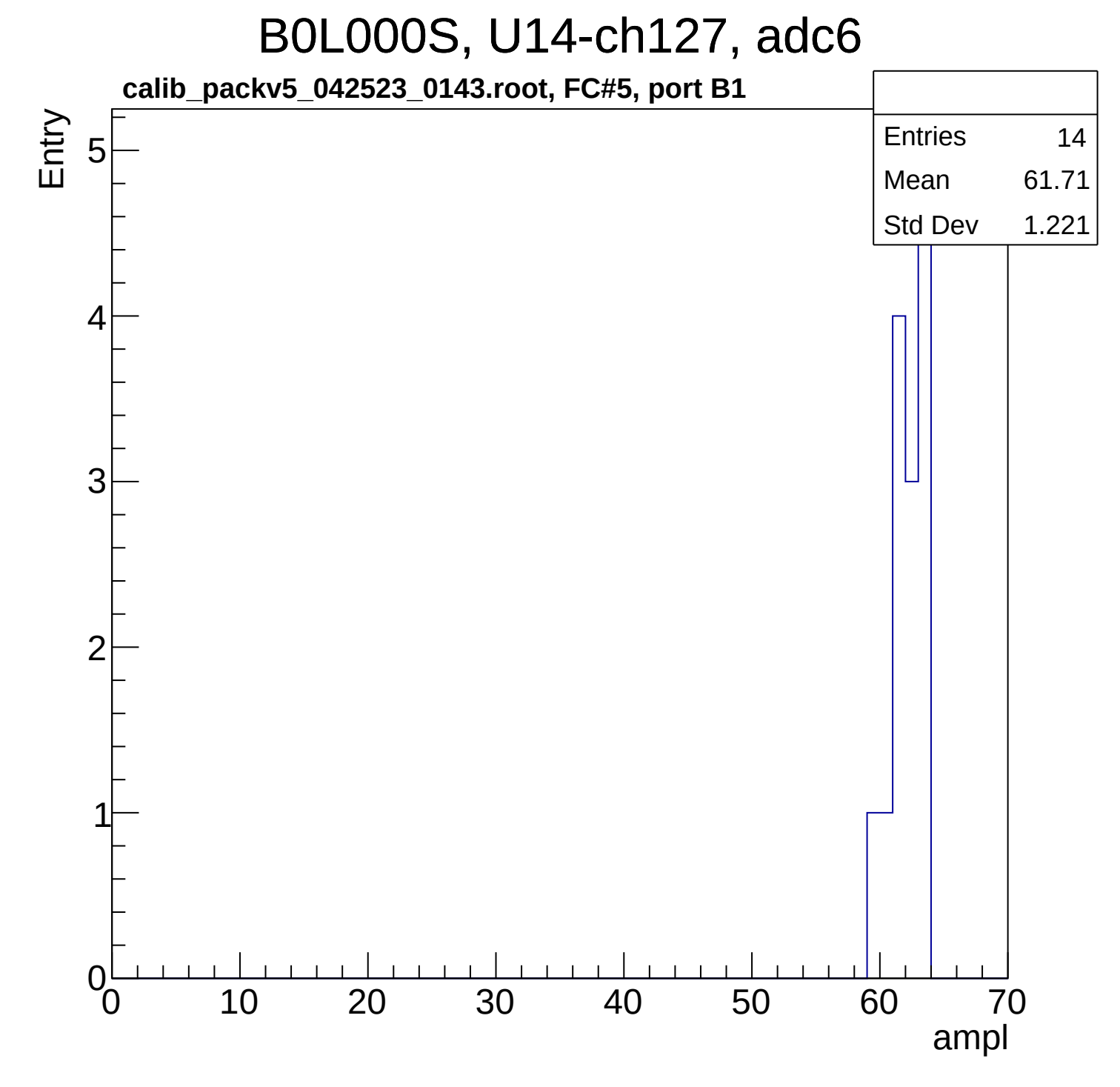
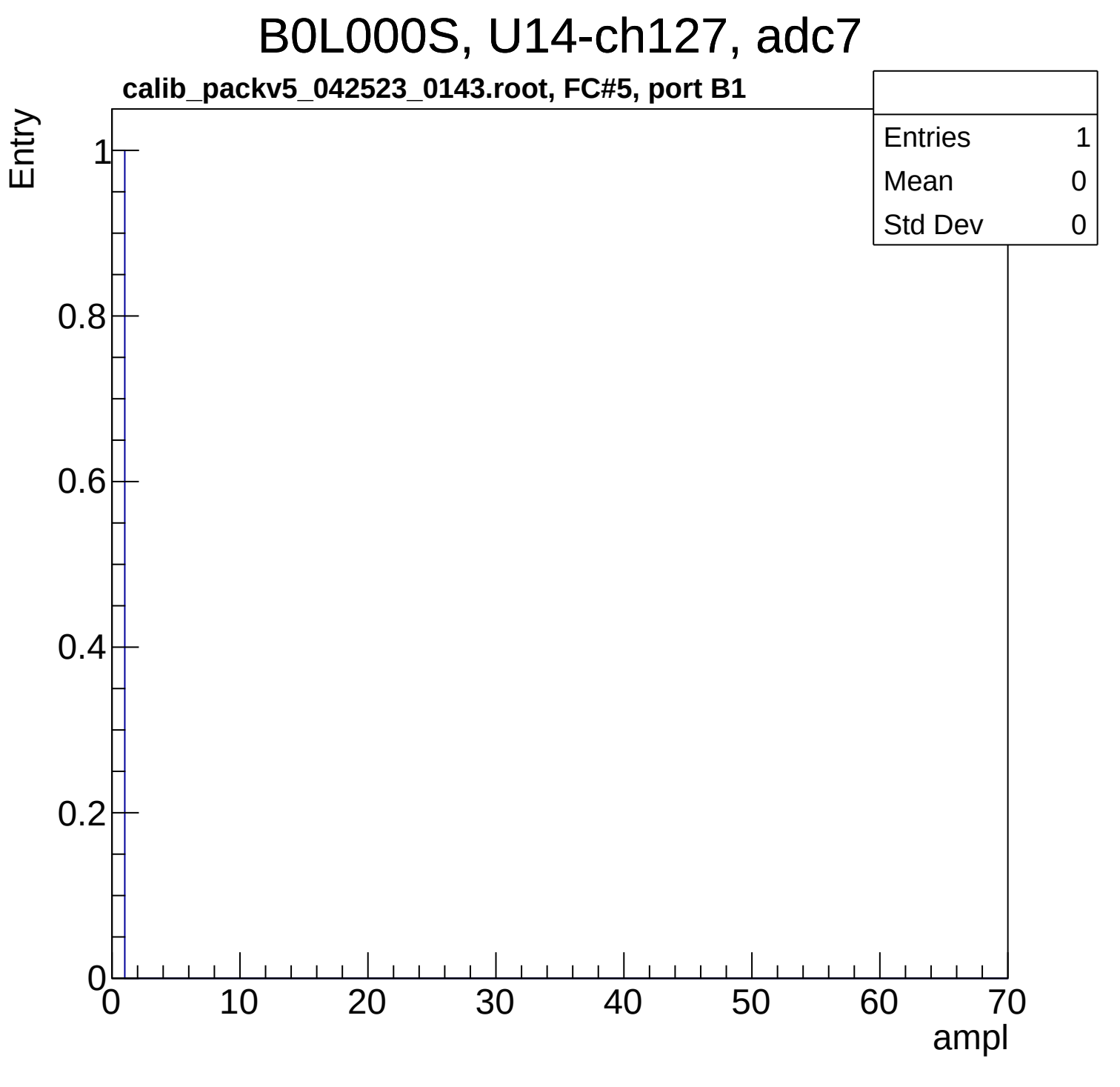
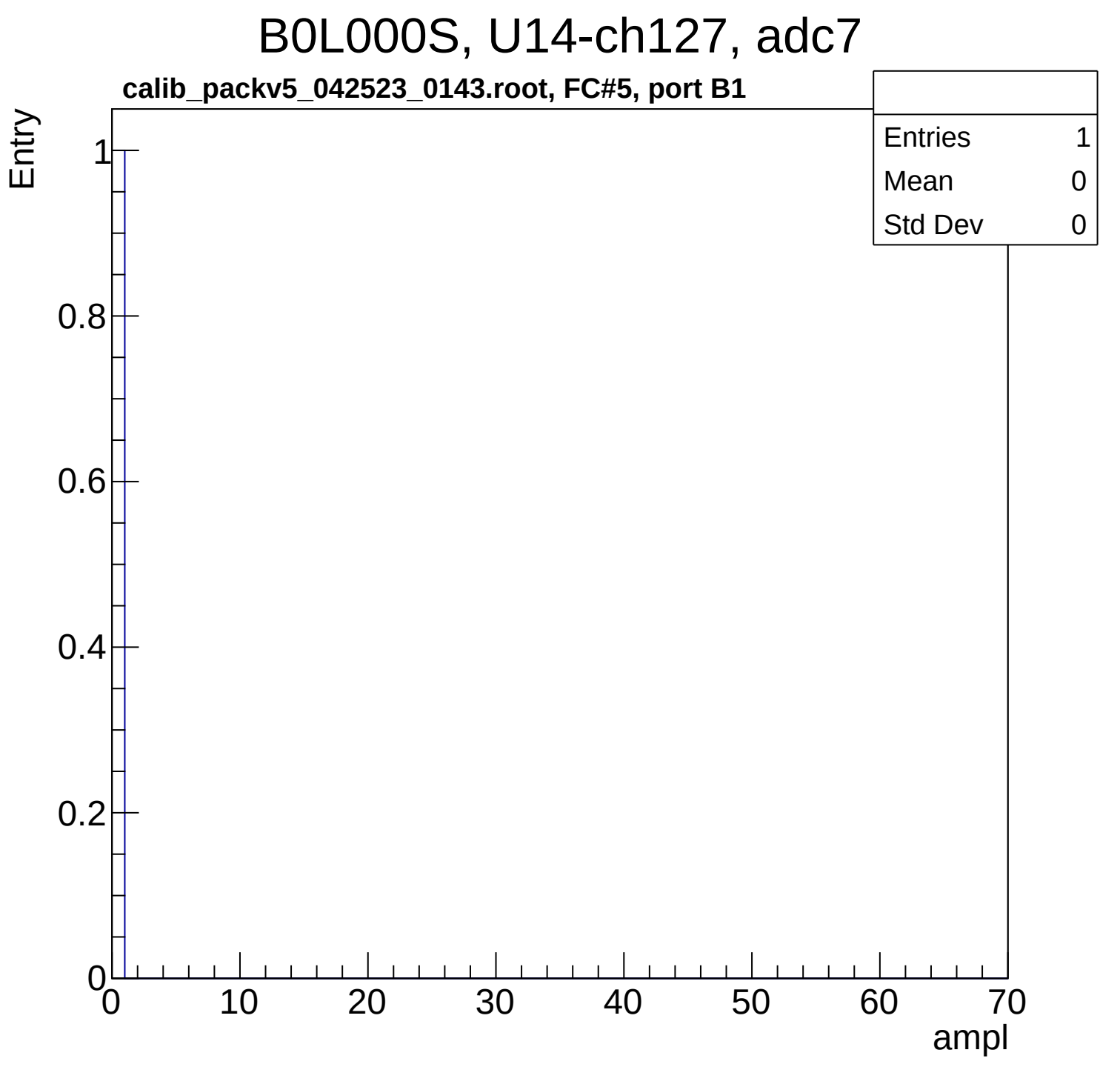

B0L000S, U14-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 53.87
Std Dev 3.671
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U14-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.48
Std Dev 8.394
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U14-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 14
Mean 61.71
Std Dev 1.221
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U14-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U14-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl