

B1L002S, U22-ch0

calib_packv5_042523_0143.root, FC#10, port B3

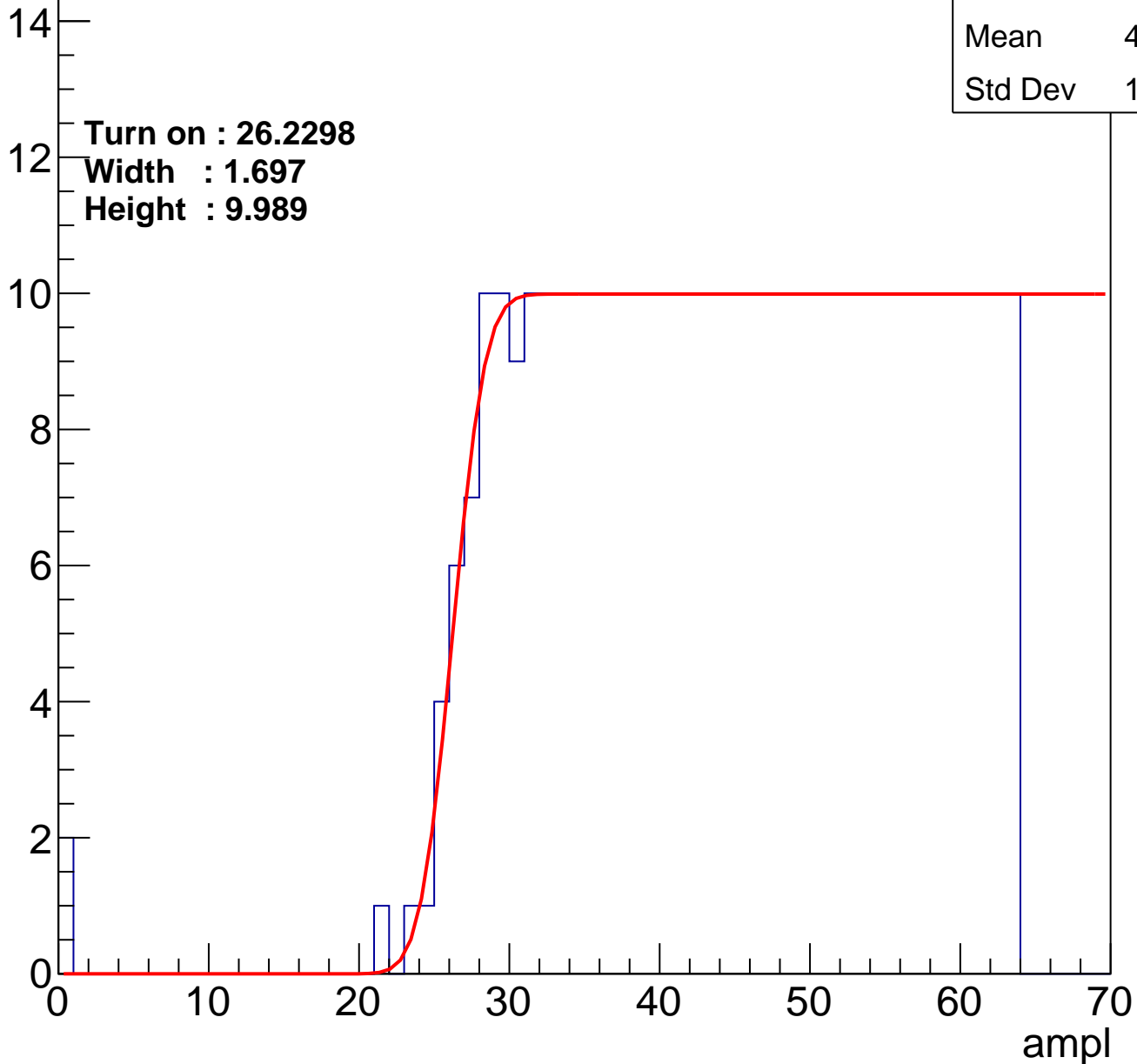
Entries	381
Mean	44.26
Std Dev	11.47

Turn on : 26.2298

Width : 1.697

Height : 9.989

Entry



B1L002S, U22-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 26.7799

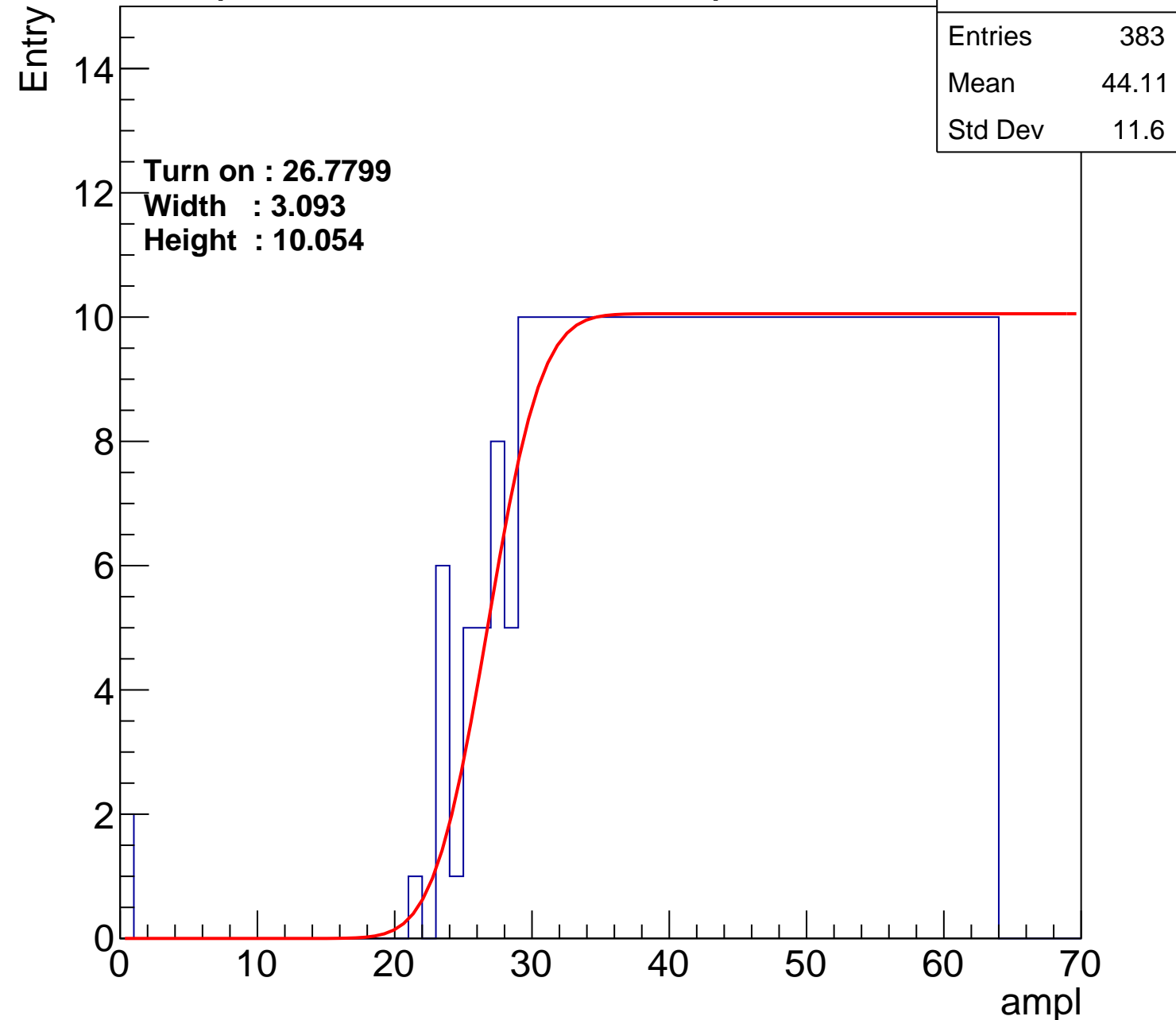
Width : 3.093

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.88
Std Dev	12.52

Turn on : 23.8770

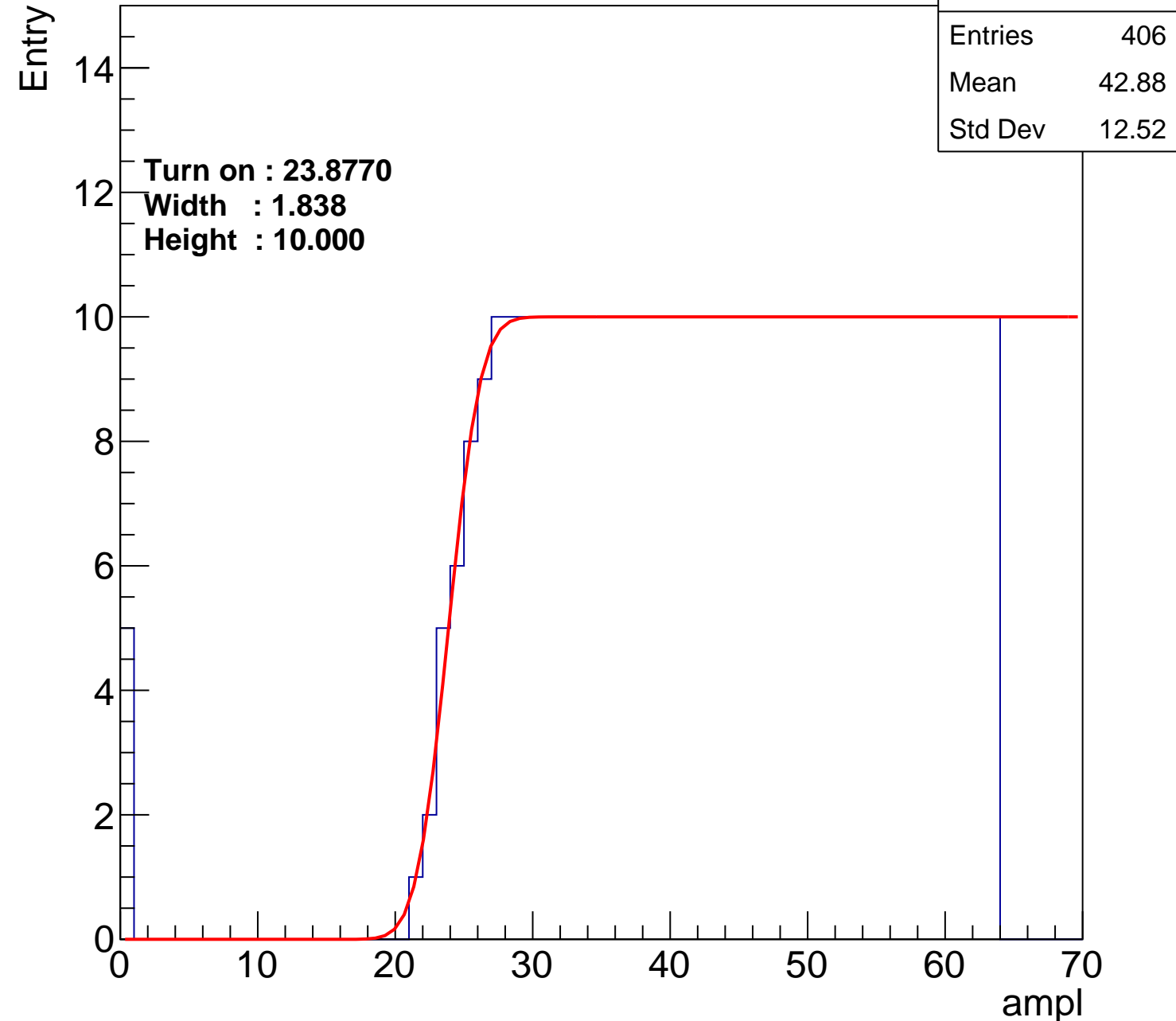
Width : 1.838

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.62
Std Dev	12.27

Turn on : 26.4285

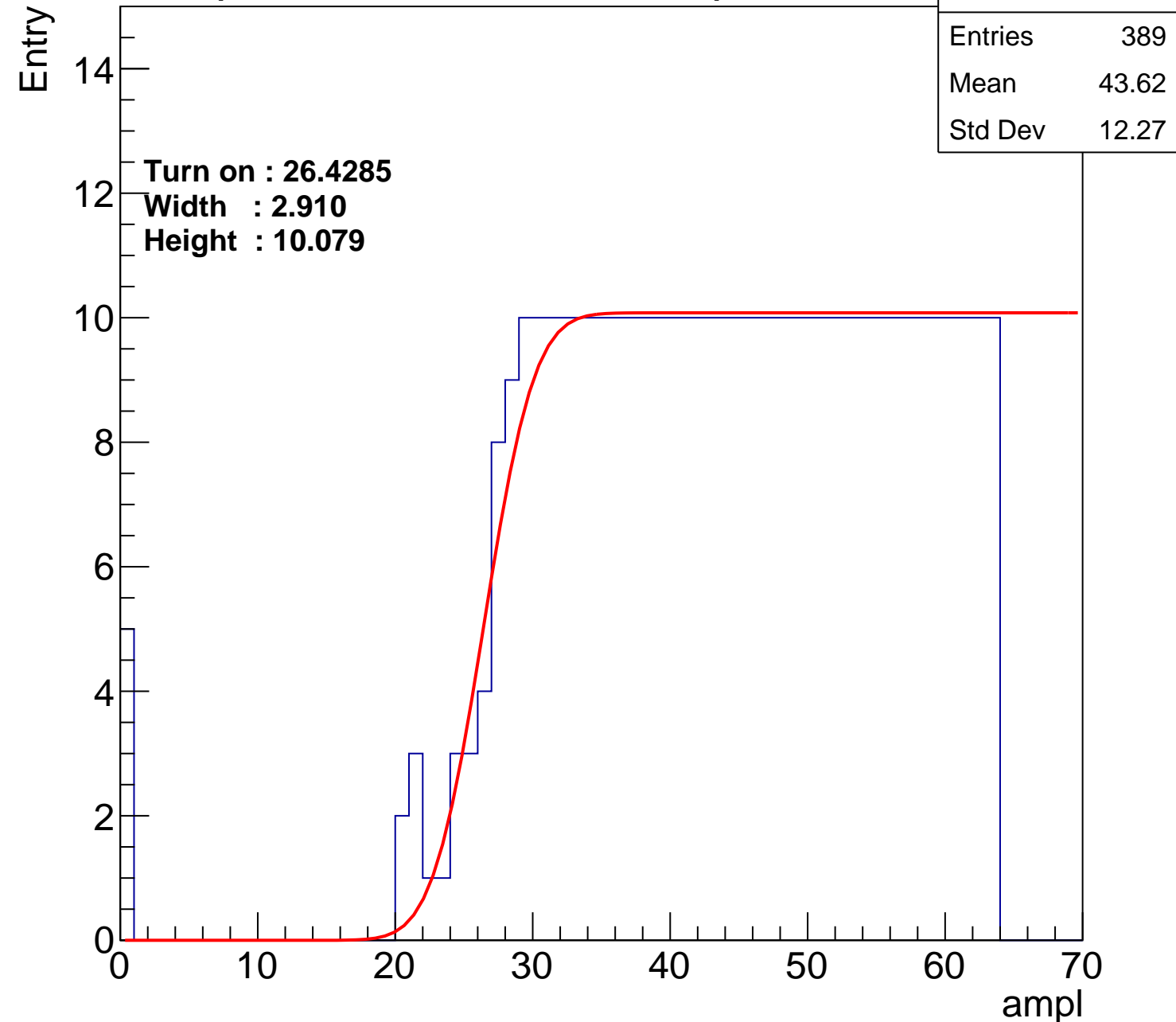
Width : 2.910

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 25.7014

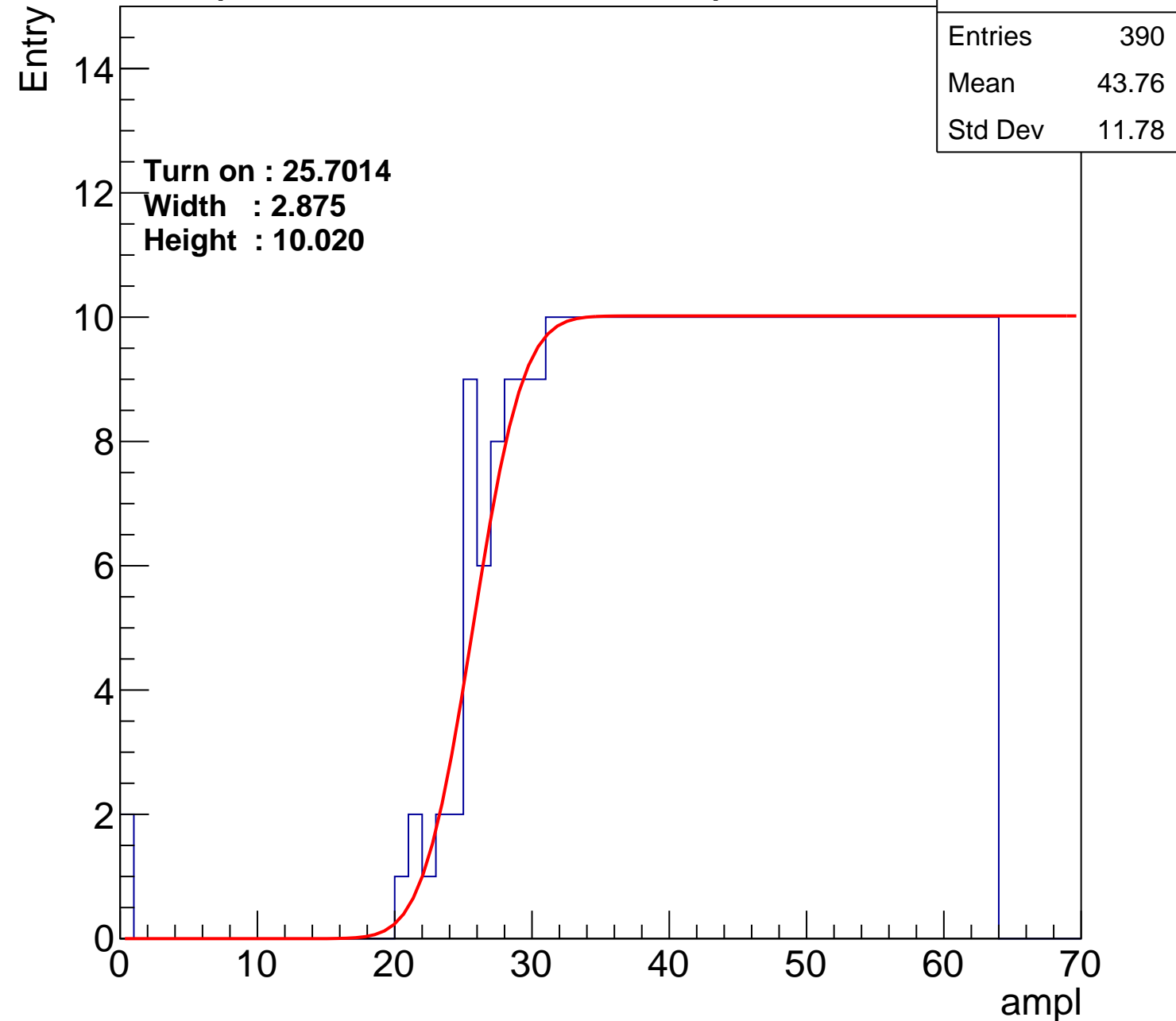
Width : 2.875

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.22
Std Dev	12.08

Turn on : 24.7838

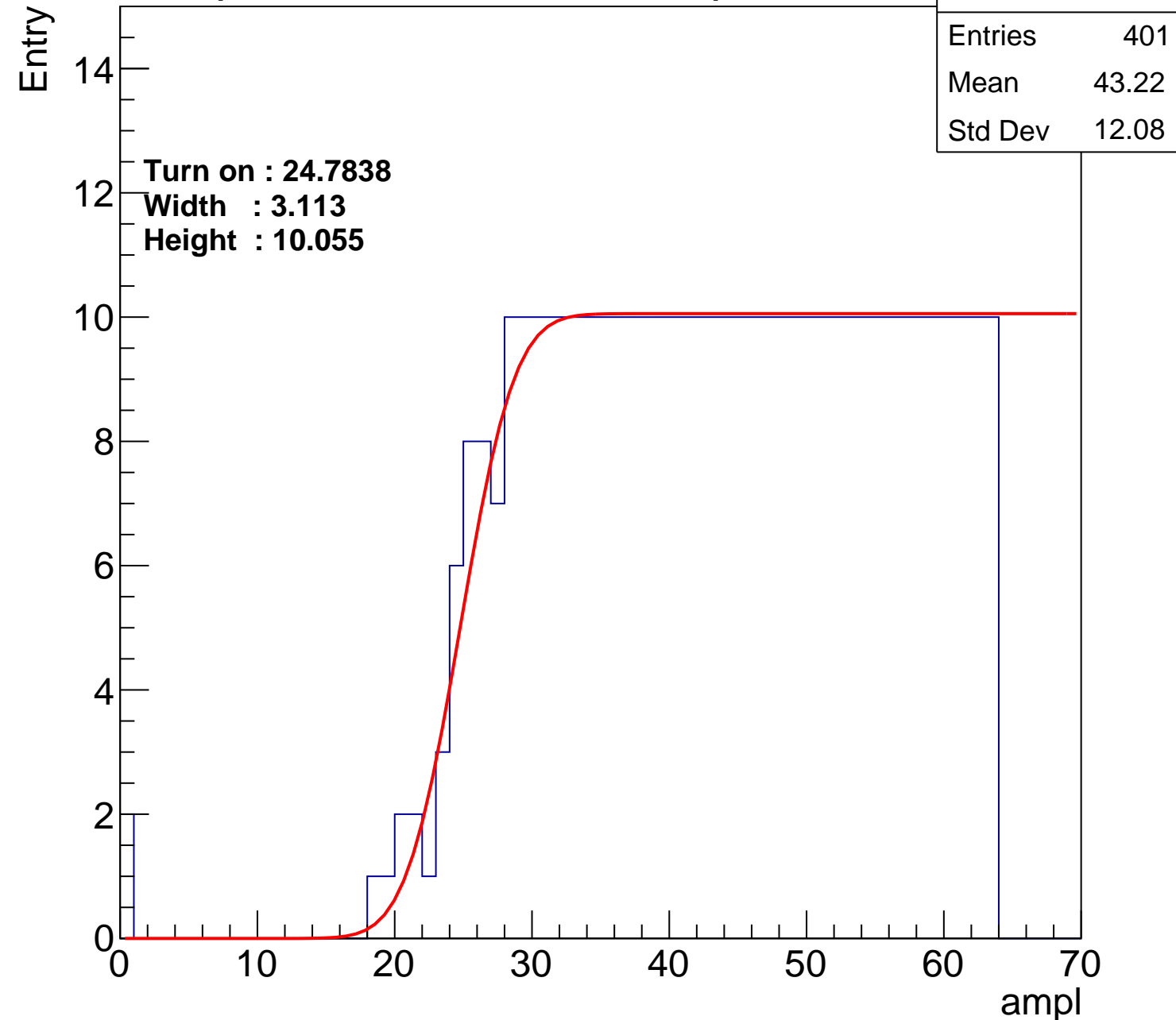
Width : 3.113

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.63
Std Dev	11.5

Turn on : 27.5374

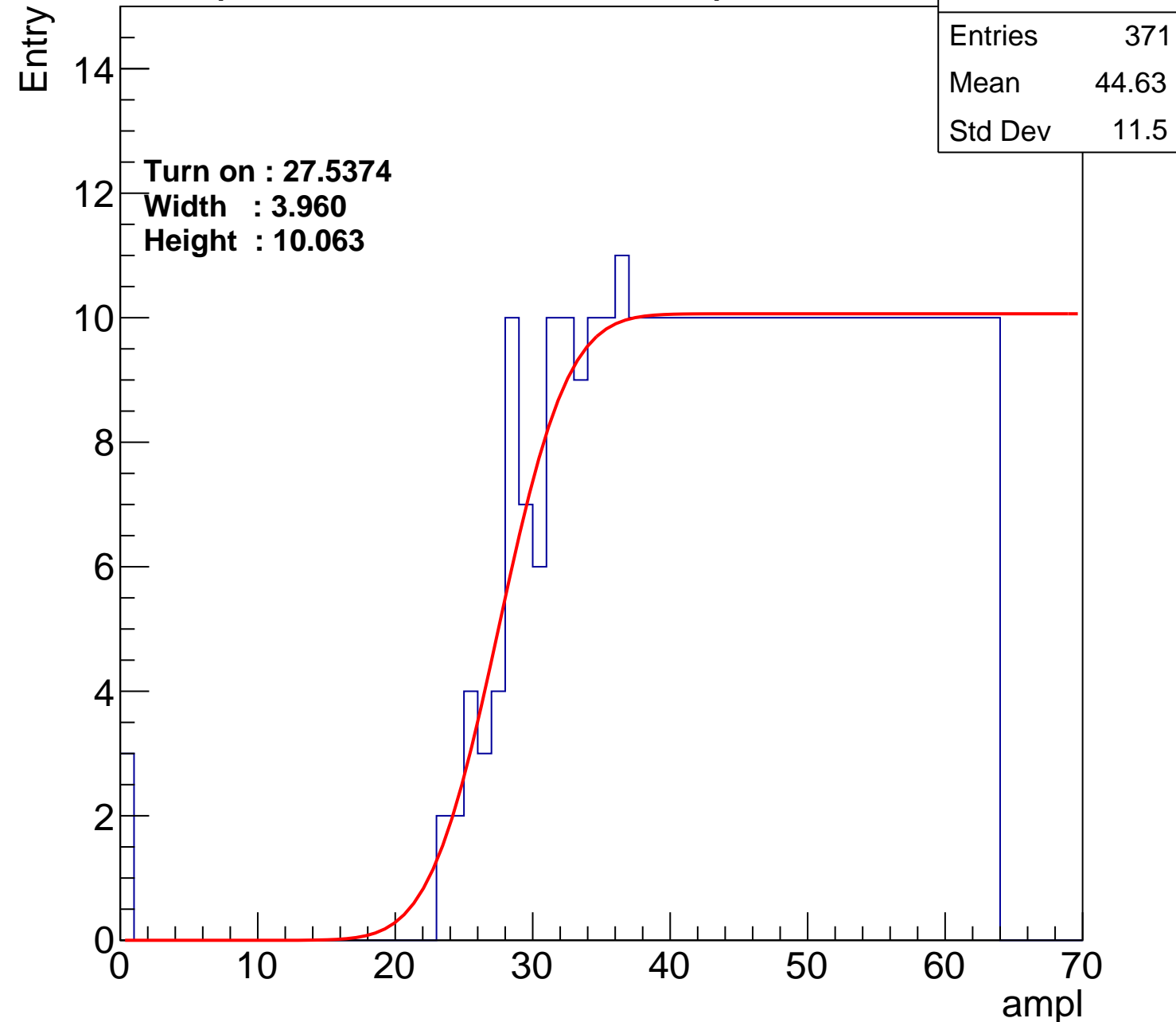
Width : 3.960

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch7

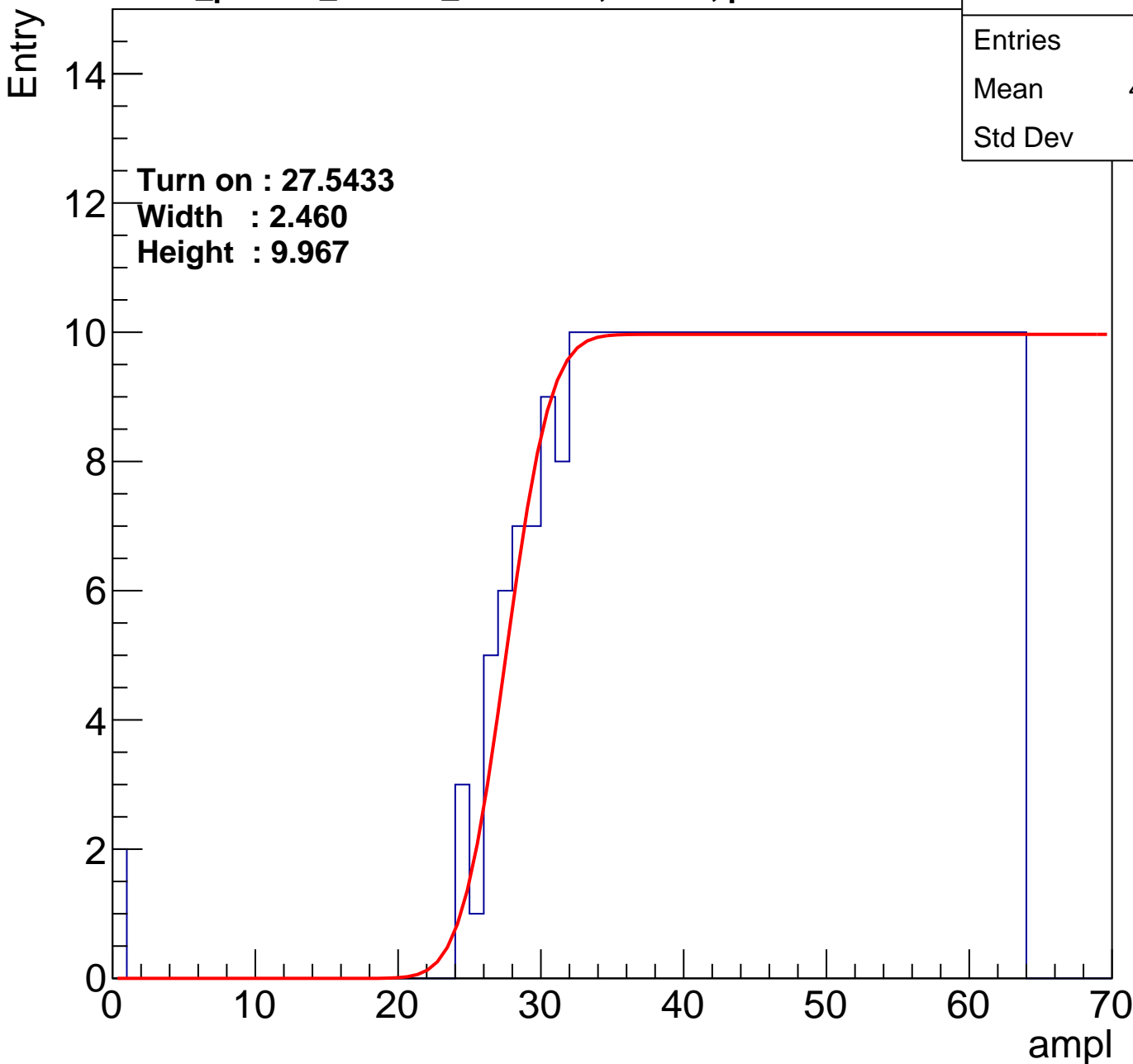
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 27.5433

Width : 2.460

Height : 9.967

Entries	368
Mean	44.85
Std Dev	11.2



B1L002S, U22-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 26.6629

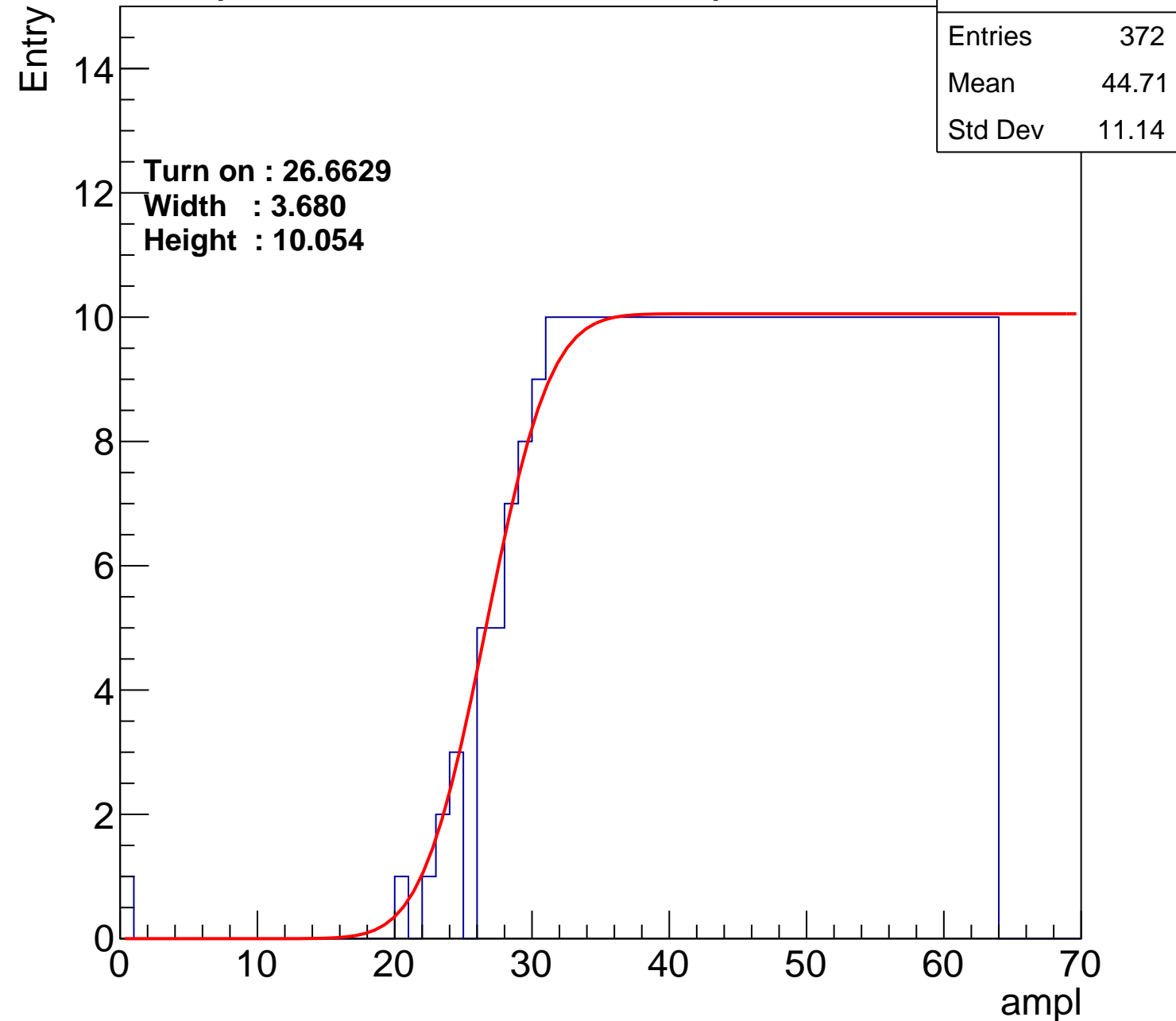
Width : 3.680

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.4560

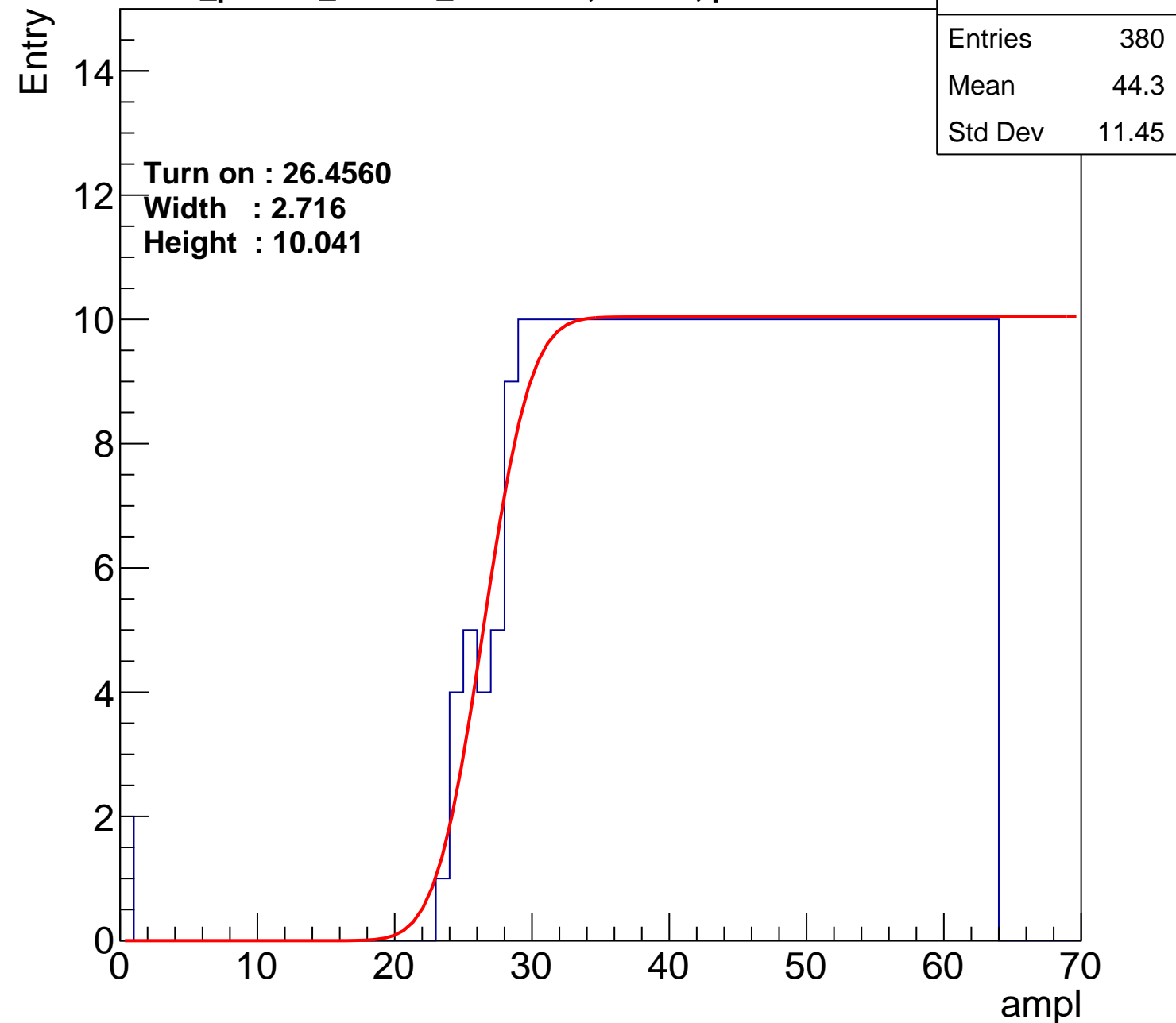
Width : 2.716

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.8

Turn on : 25.7445

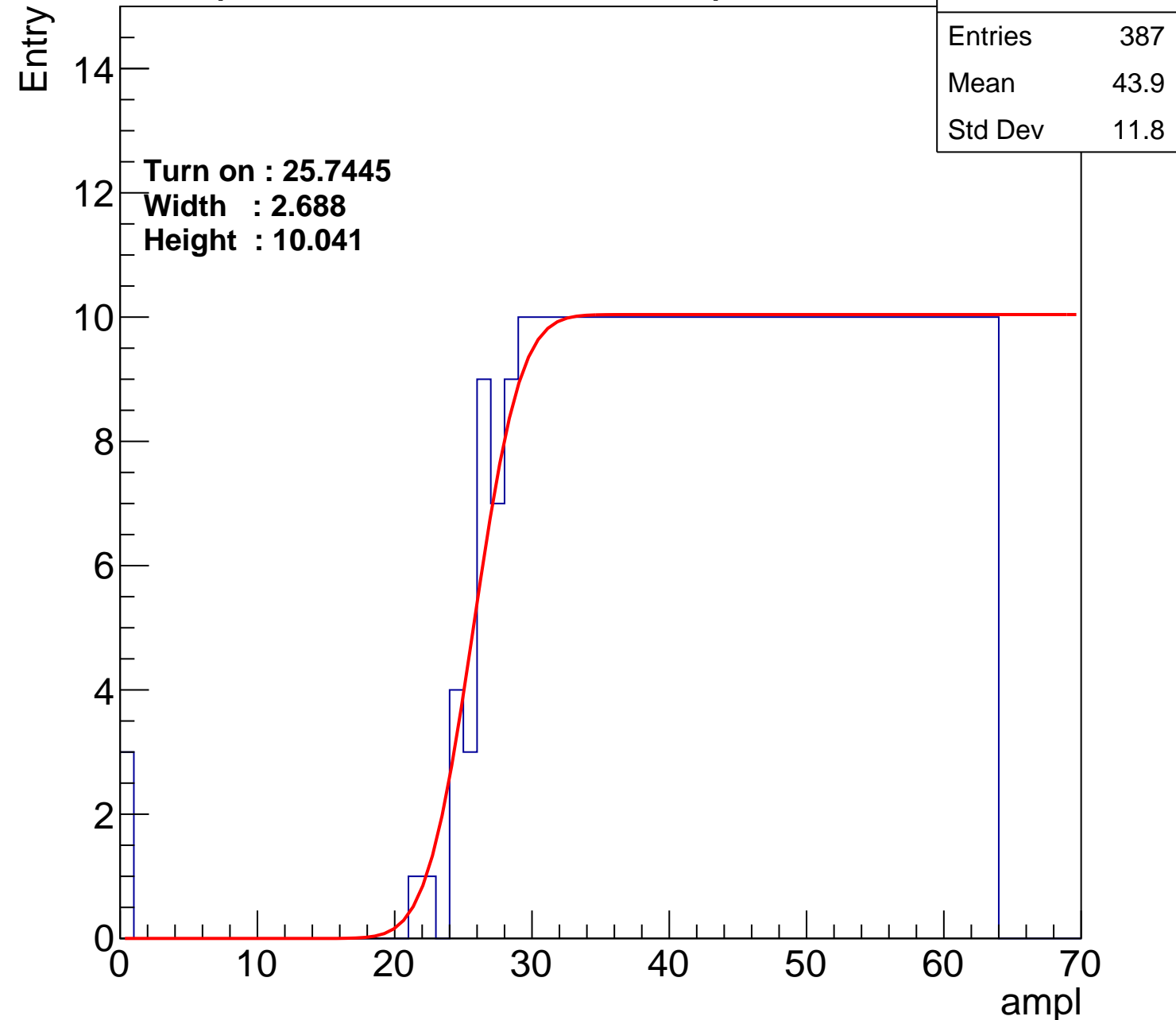
Width : 2.688

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch11

calib_packv5_042523_0143.root, FC#10, port B3

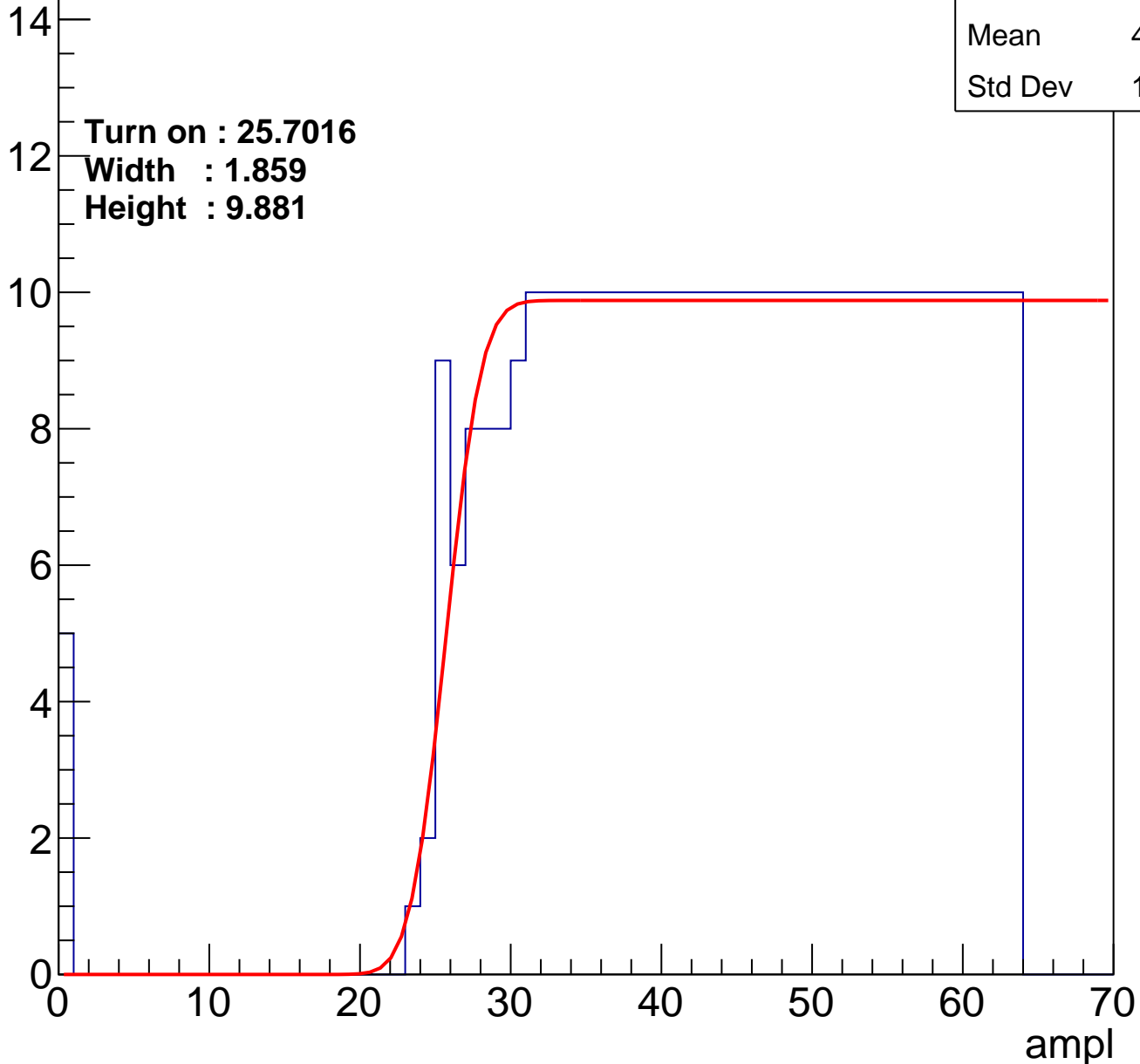
Entries	386
Mean	43.79
Std Dev	12.14

Turn on : 25.7016

Width : 1.859

Height : 9.881

Entry



B1L002S, U22-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.5138

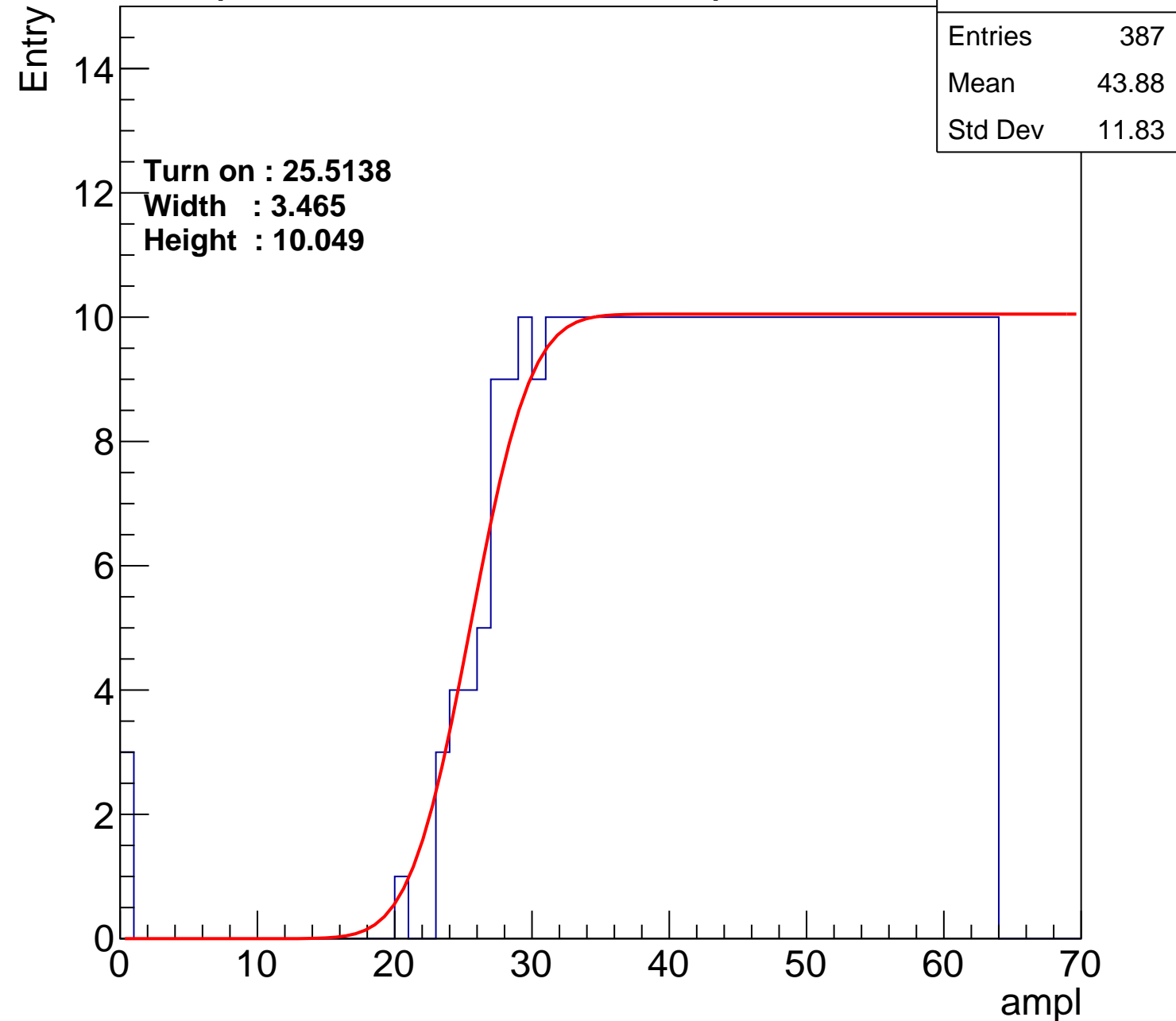
Width : 3.465

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch13

calib_packv5_042523_0143.root, FC#10, port B3

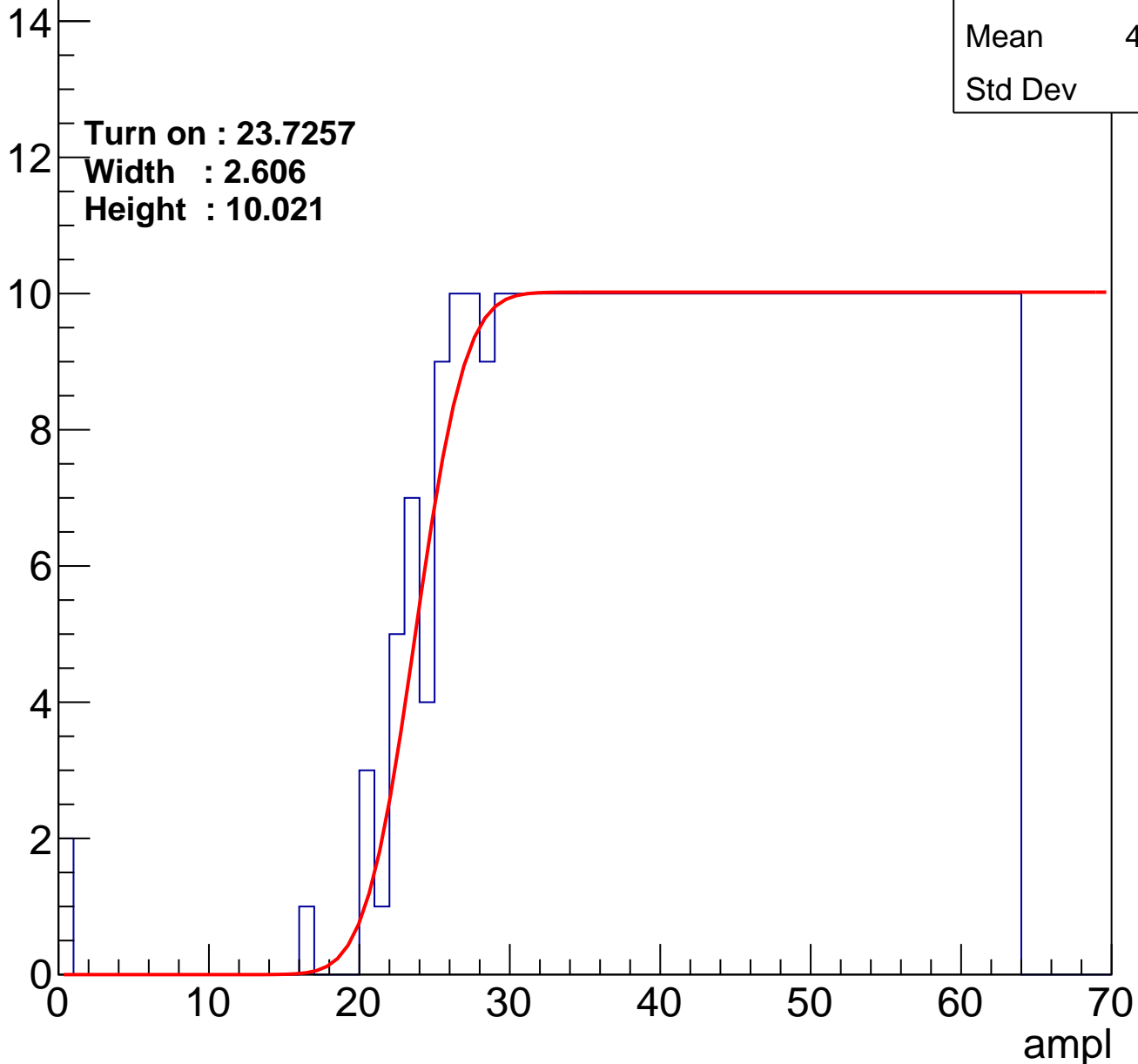
Entries	411
Mean	42.75
Std Dev	12.3

Turn on : 23.7257

Width : 2.606

Height : 10.021

Entry



B1L002S, U22-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.06
Std Dev	12.22

Turn on : 23.8747

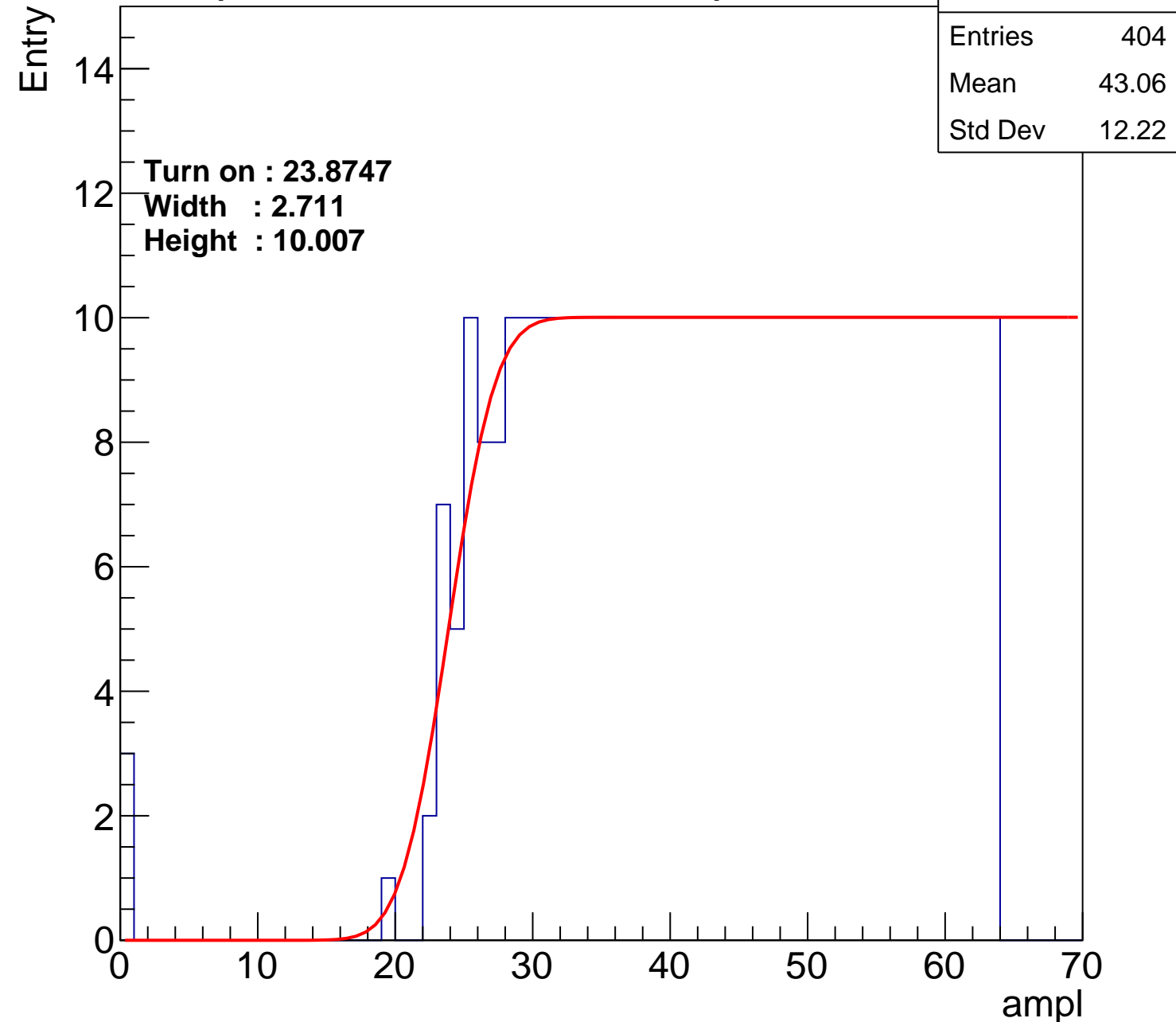
Width : 2.711

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch15

calib_packv5_042523_0143.root, FC#10, port B3

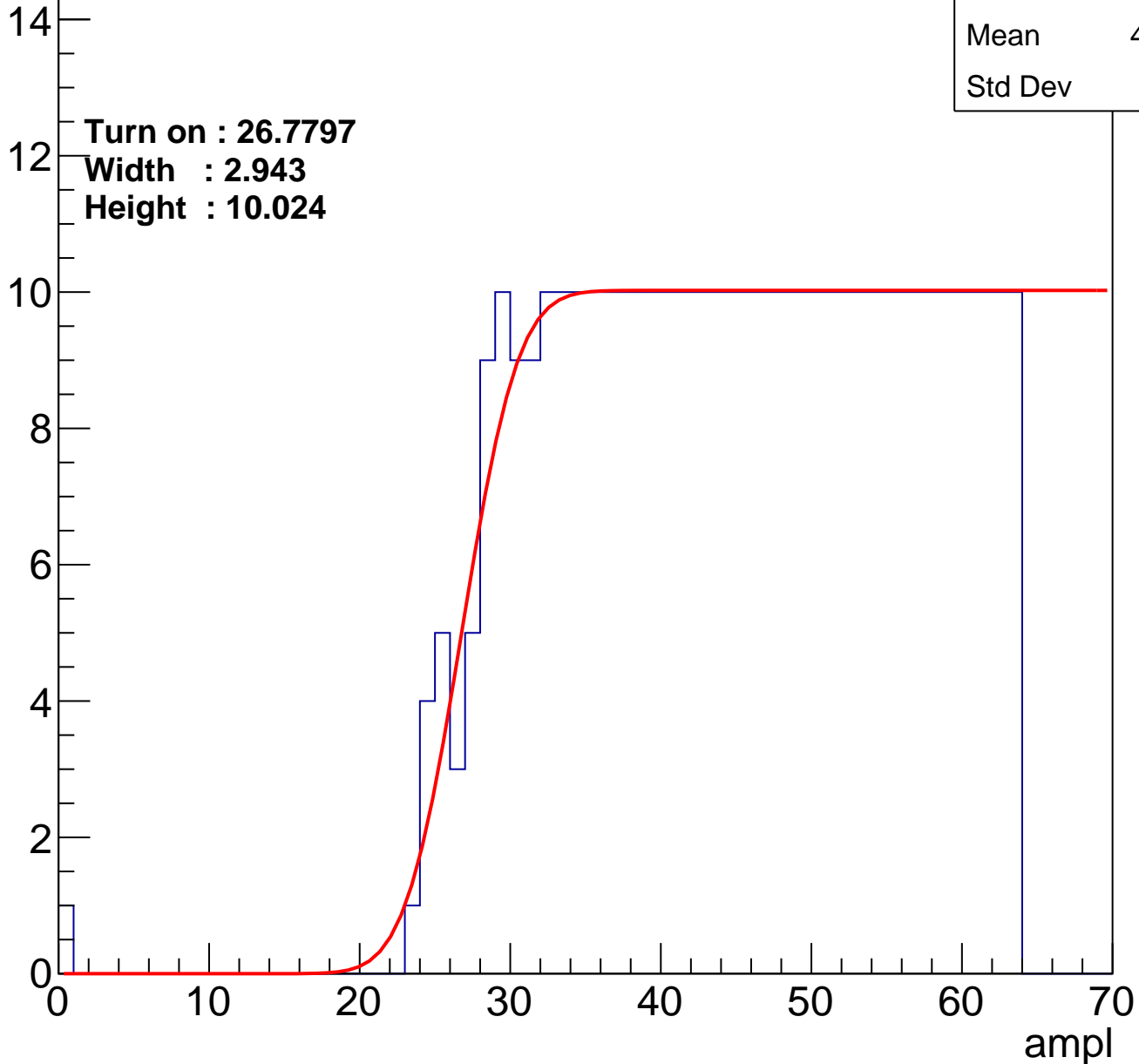
Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.7797

Width : 2.943

Height : 10.024

Entry



B1L002S, U22-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.18
Std Dev	12.03

Turn on : 23.9680

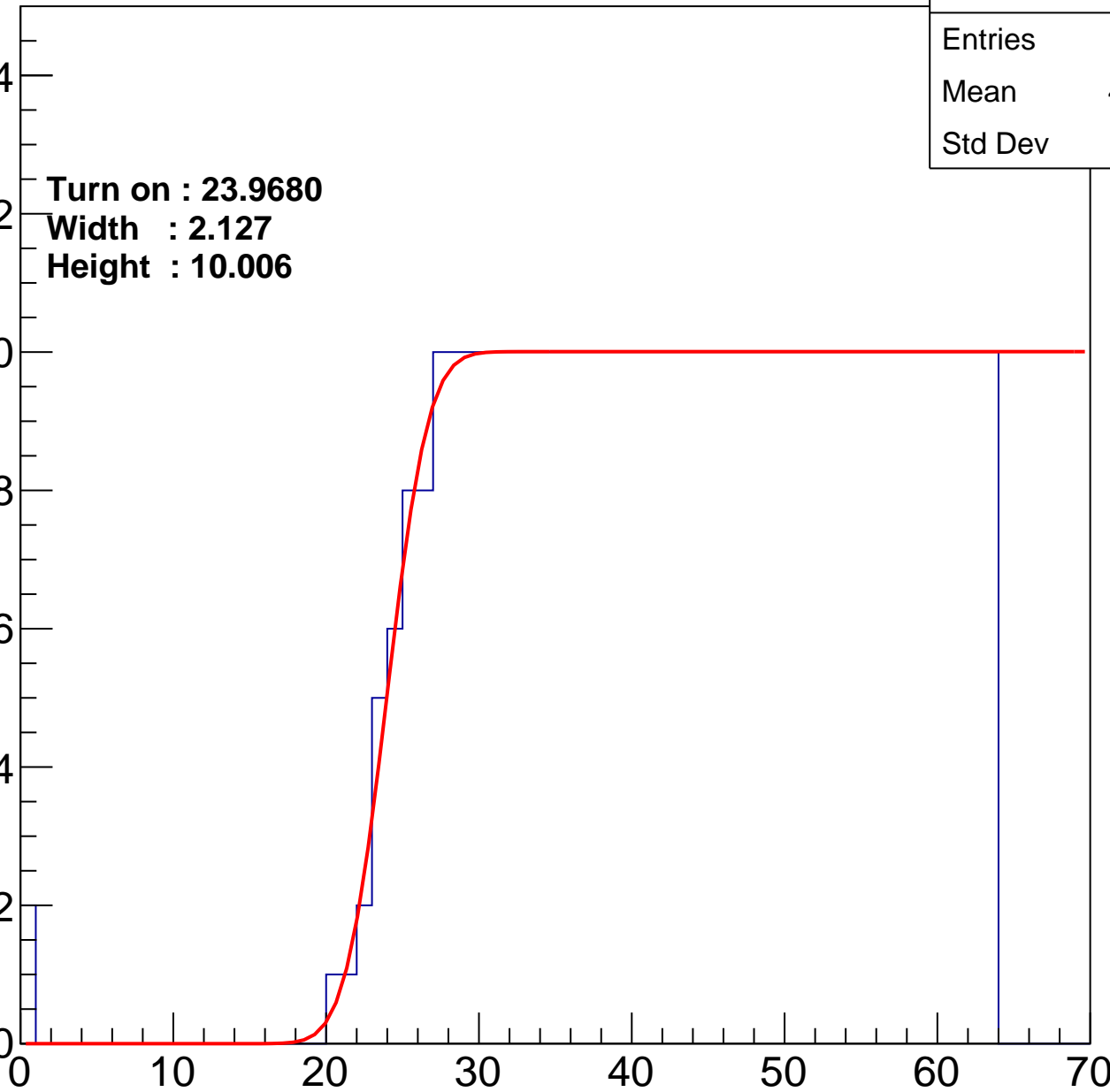
Width : 2.127

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch17

calib_packv5_042523_0143.root, FC#10, port B3

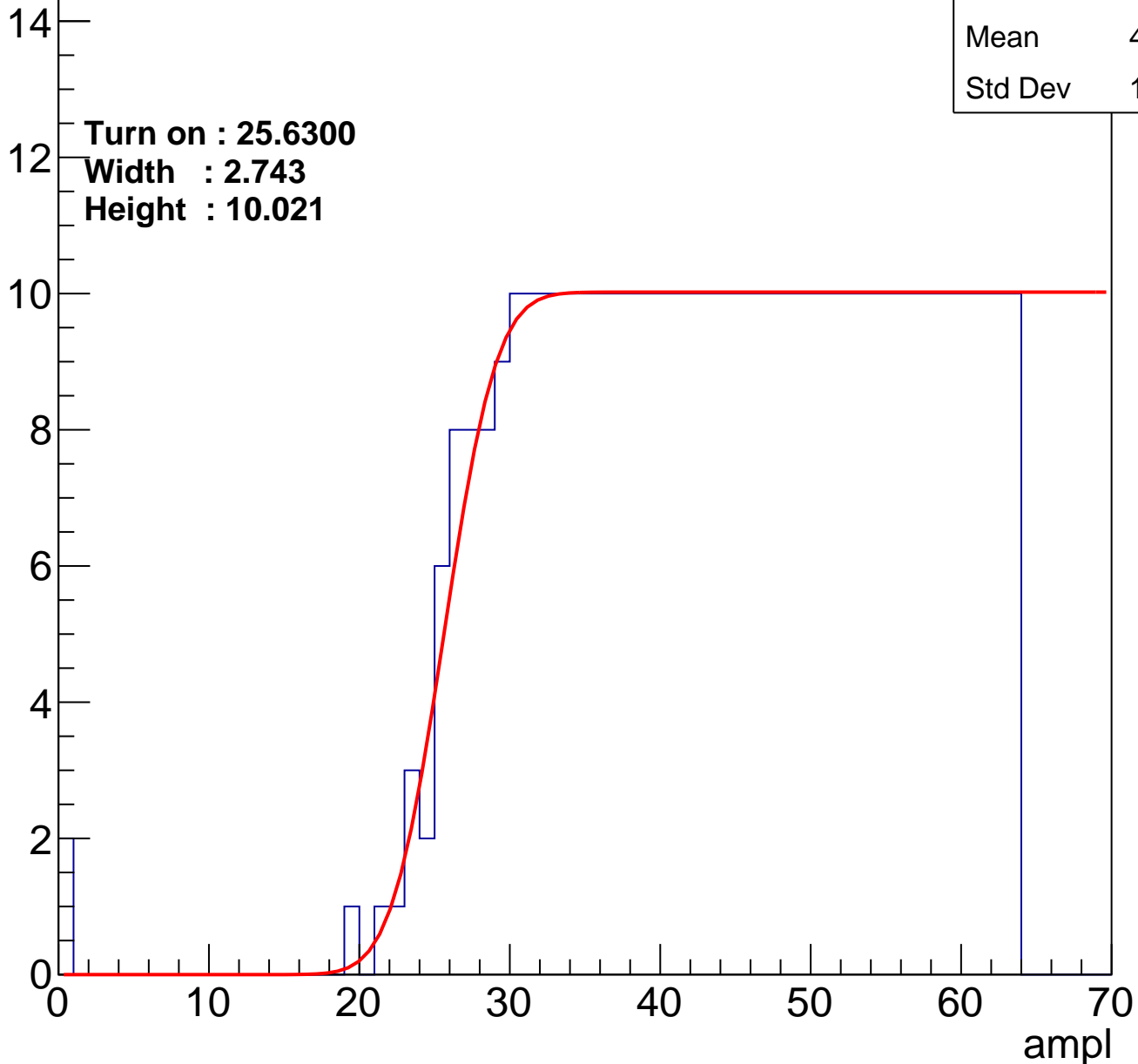
Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.6300

Width : 2.743

Height : 10.021

Entry



B1L002S, U22-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.08
Std Dev	12.25

Turn on : 24.2552

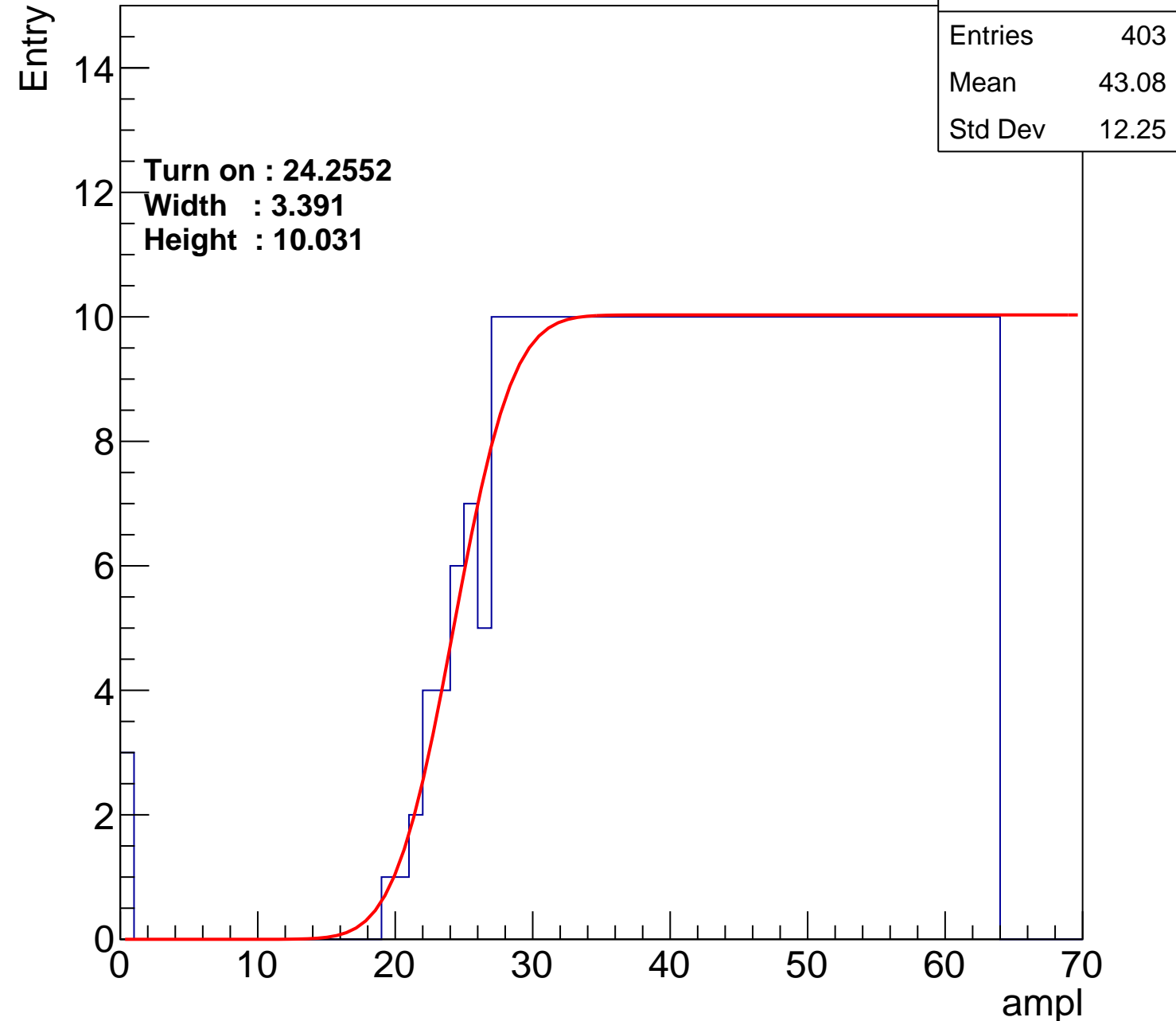
Width : 3.391

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch19

calib_packv5_042523_0143.root, FC#10, port B3

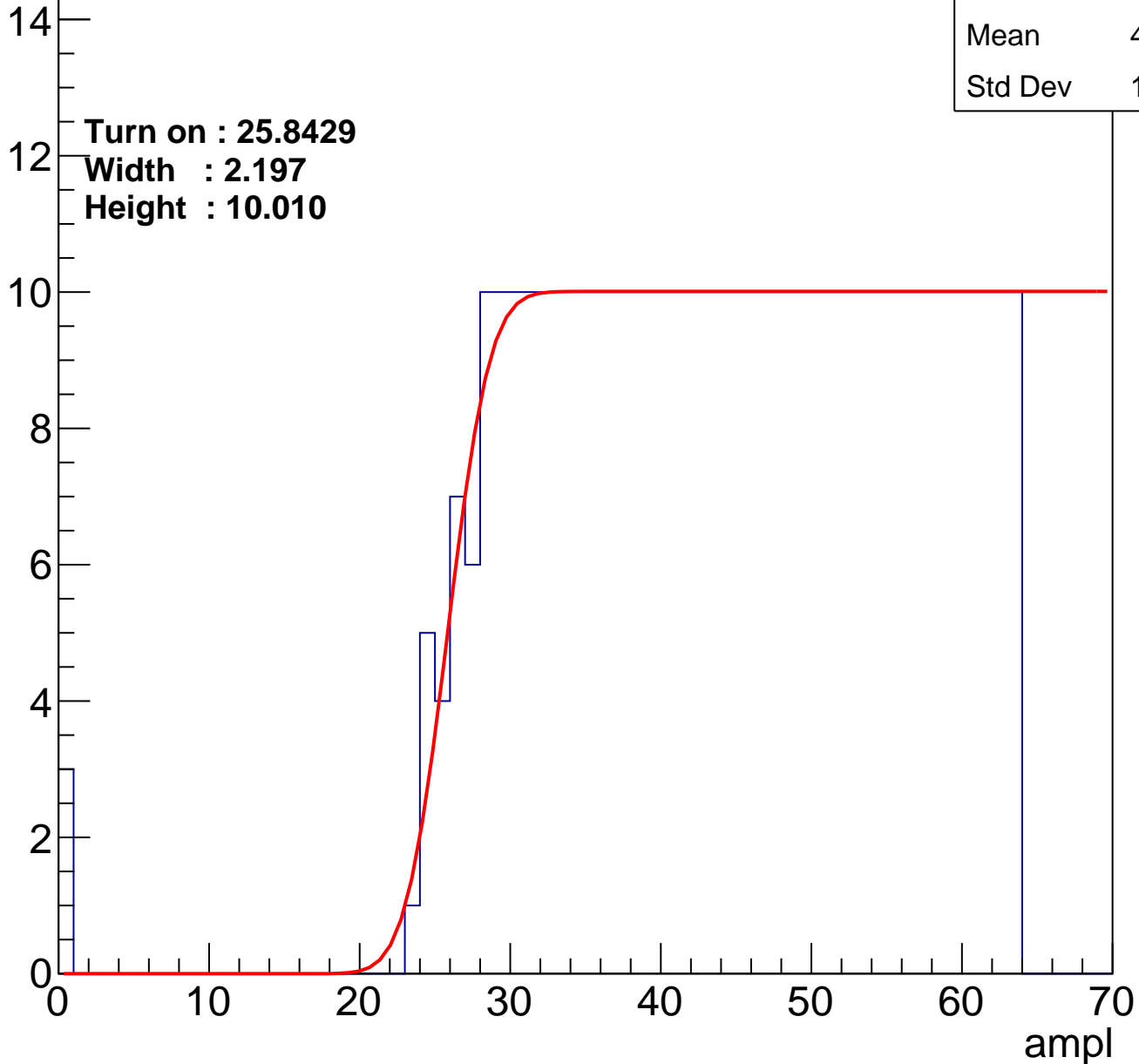
Entries	386
Mean	43.96
Std Dev	11.76

Turn on : 25.8429

Width : 2.197

Height : 10.010

Entry



B1L002S, U22-ch20

calib_packv5_042523_0143.root, FC#10, port B3

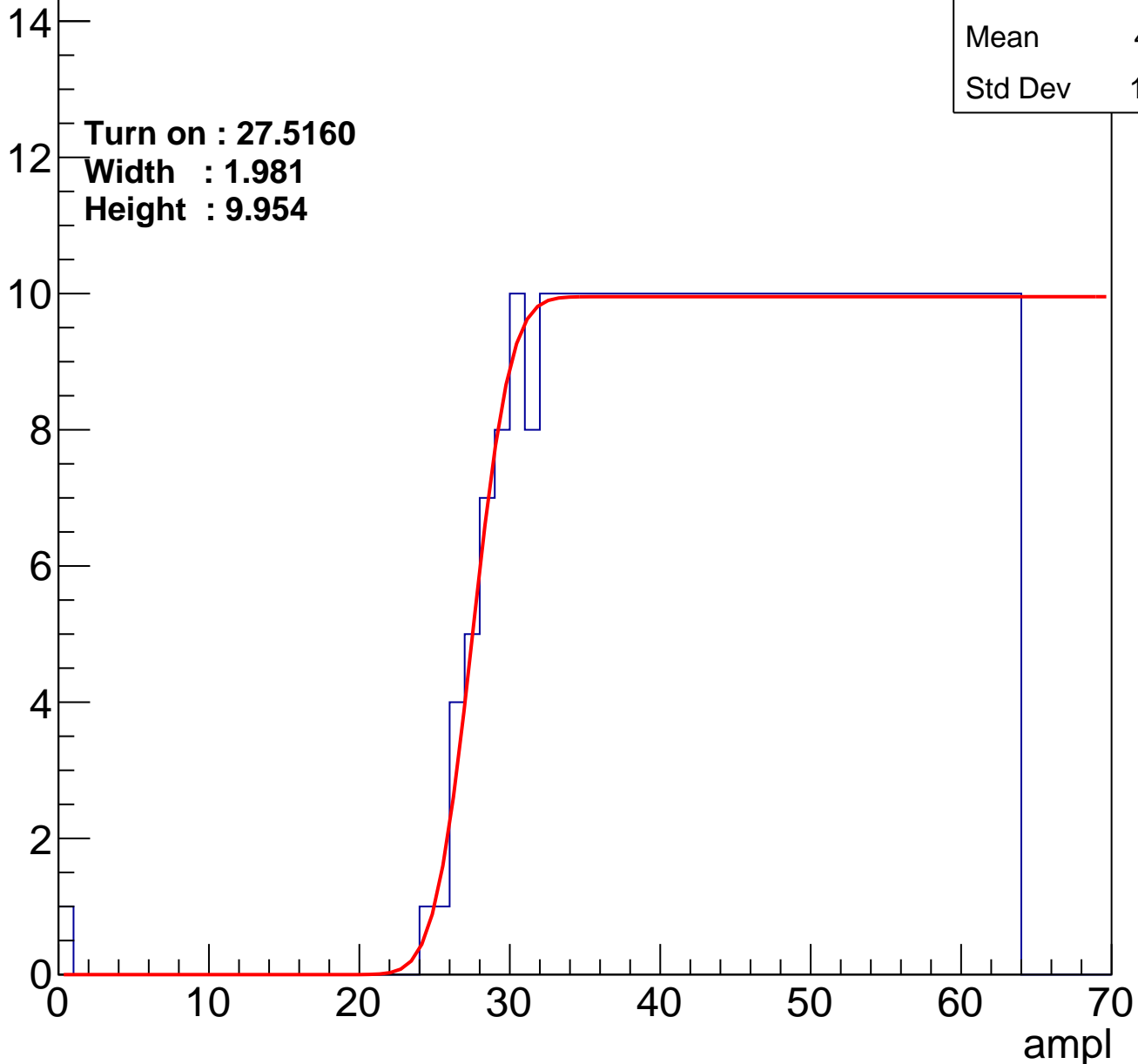
Entry

Entries	365
Mean	45.11
Std Dev	10.87

Turn on : 27.5160

Width : 1.981

Height : 9.954



B1L002S, U22-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.81
Std Dev	11.97

Turn on : 26.2920

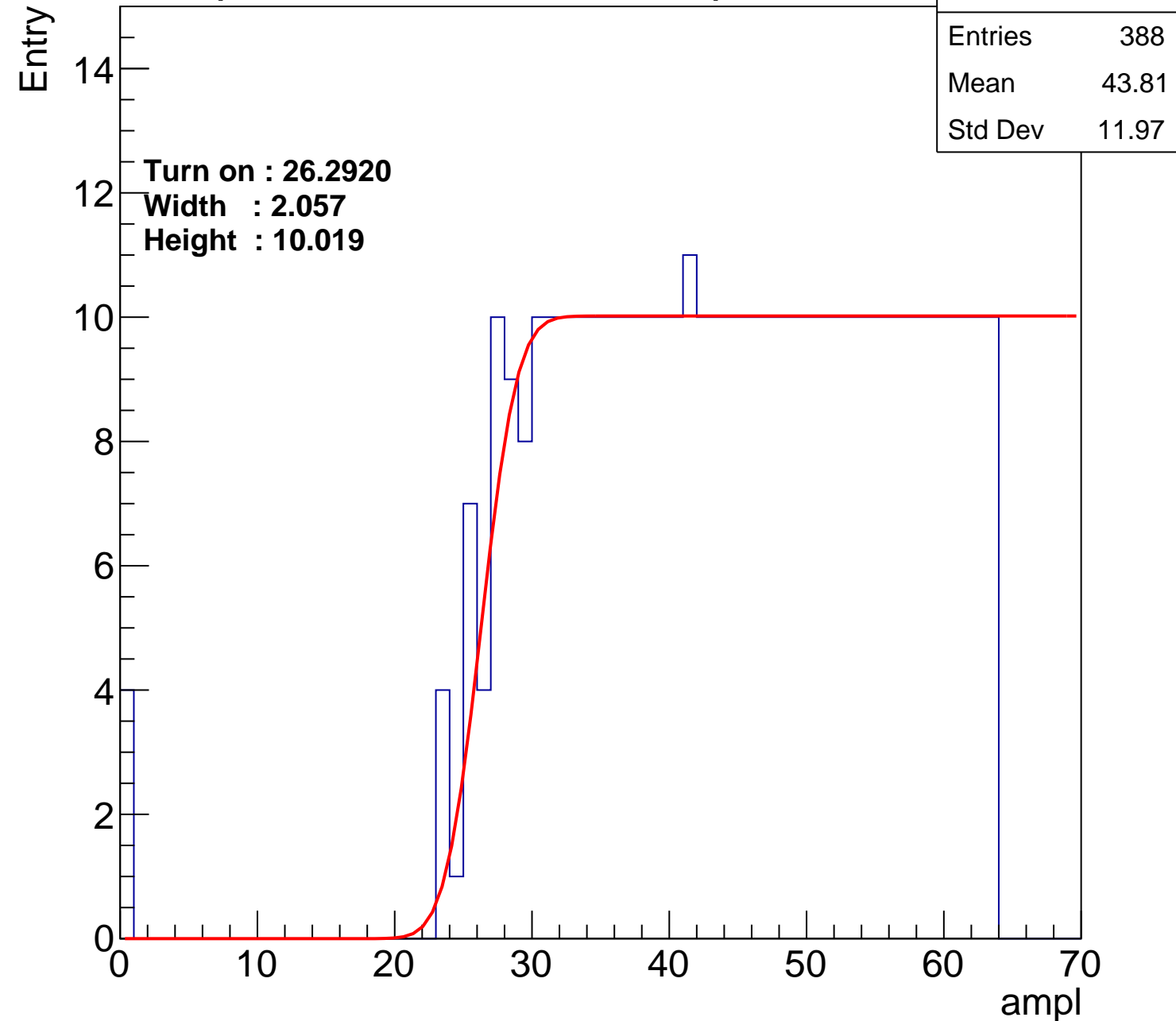
Width : 2.057

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.88
Std Dev	12.25

Turn on : 23.8327

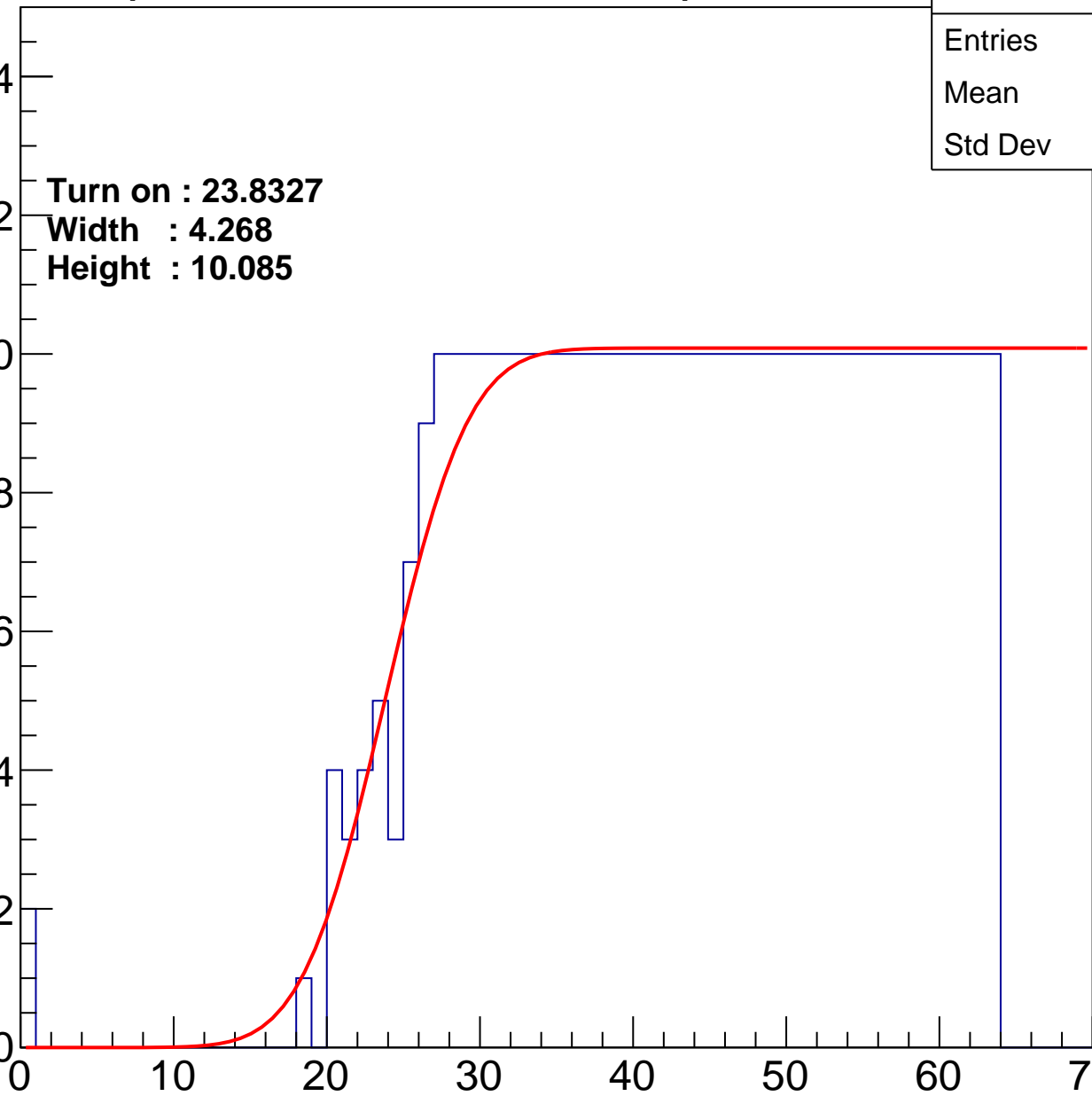
Width : 4.268

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.37
Std Dev	11.46

Turn on : 27.0684

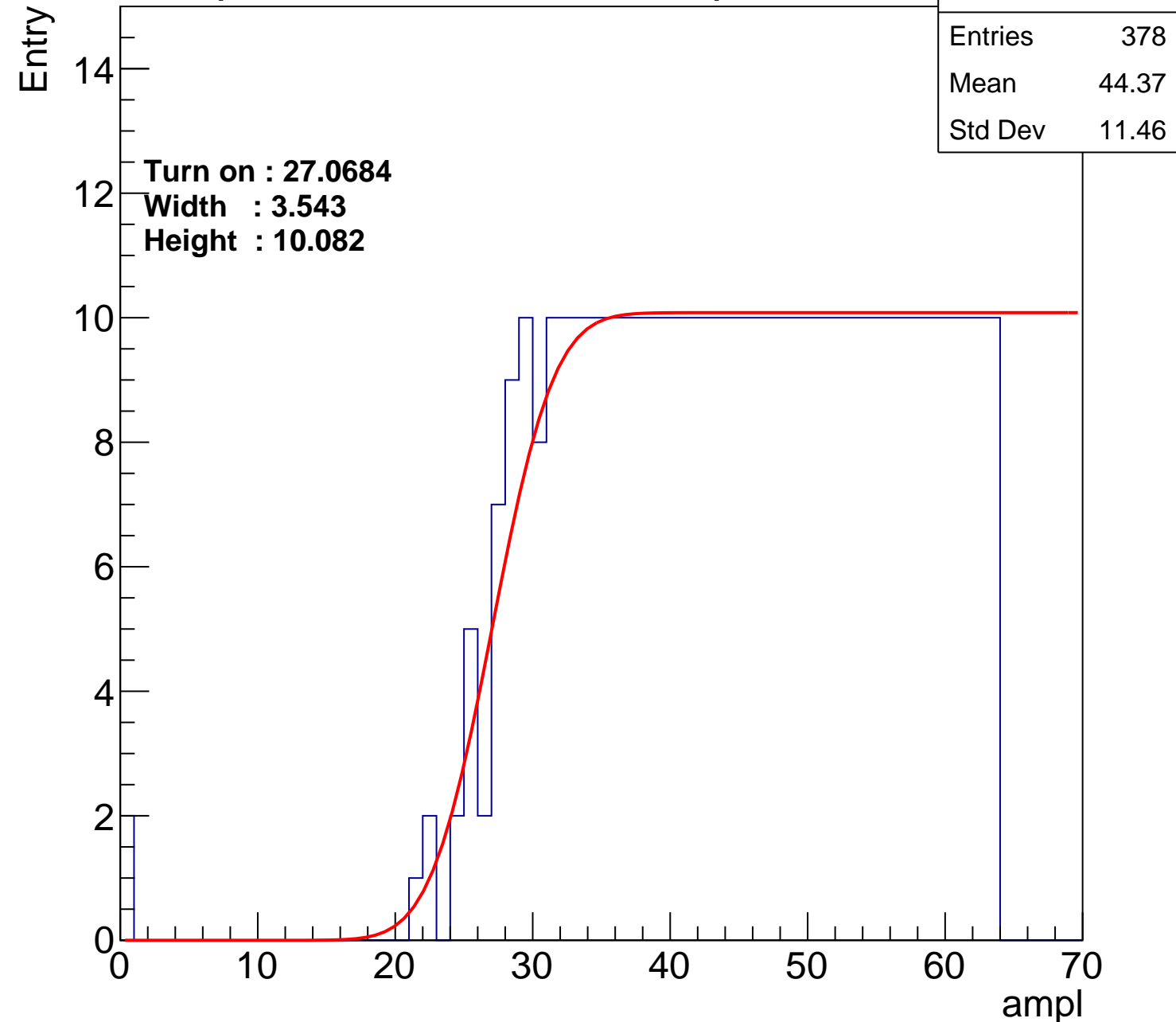
Width : 3.543

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.8194

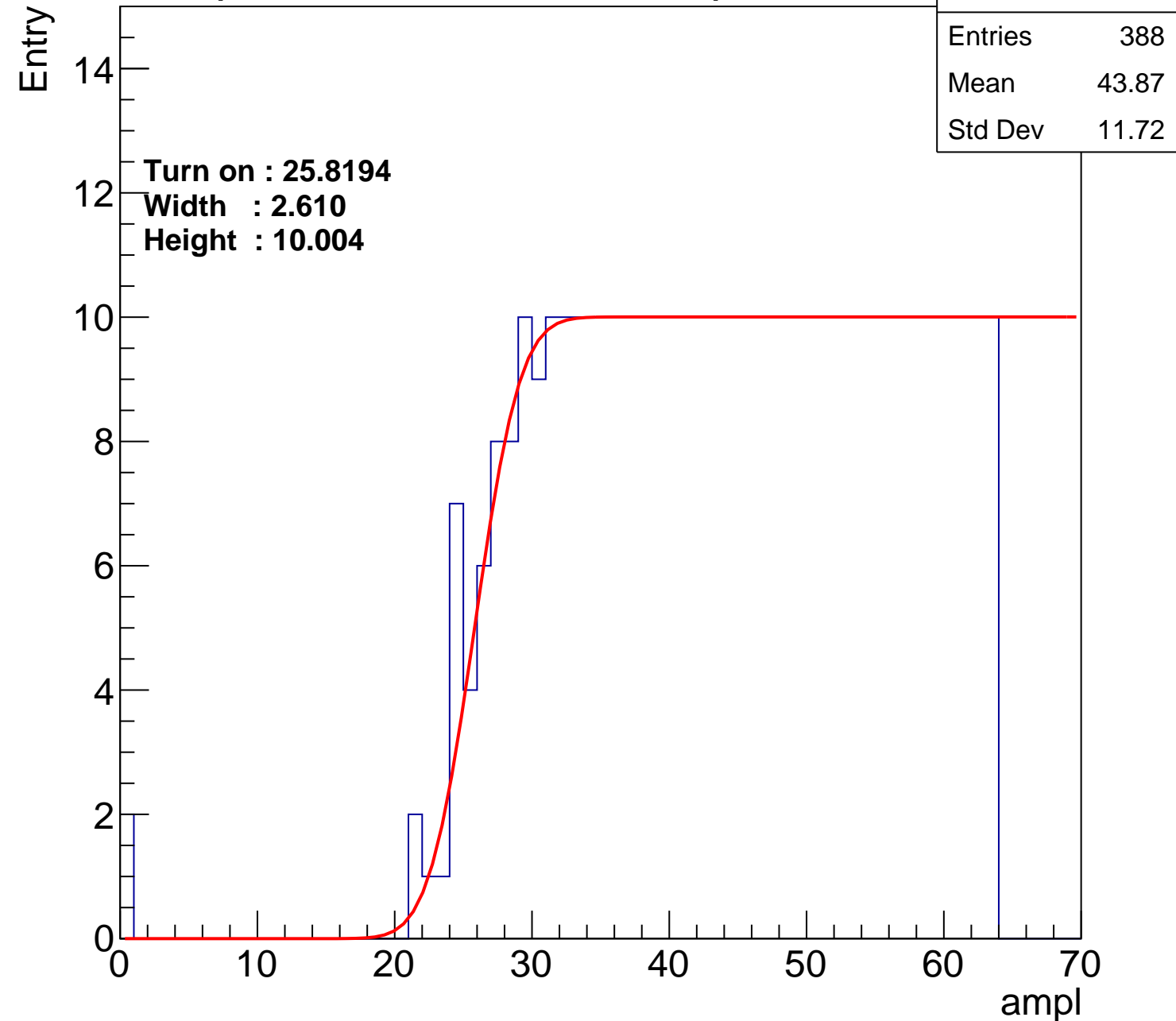
Width : 2.610

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.9315

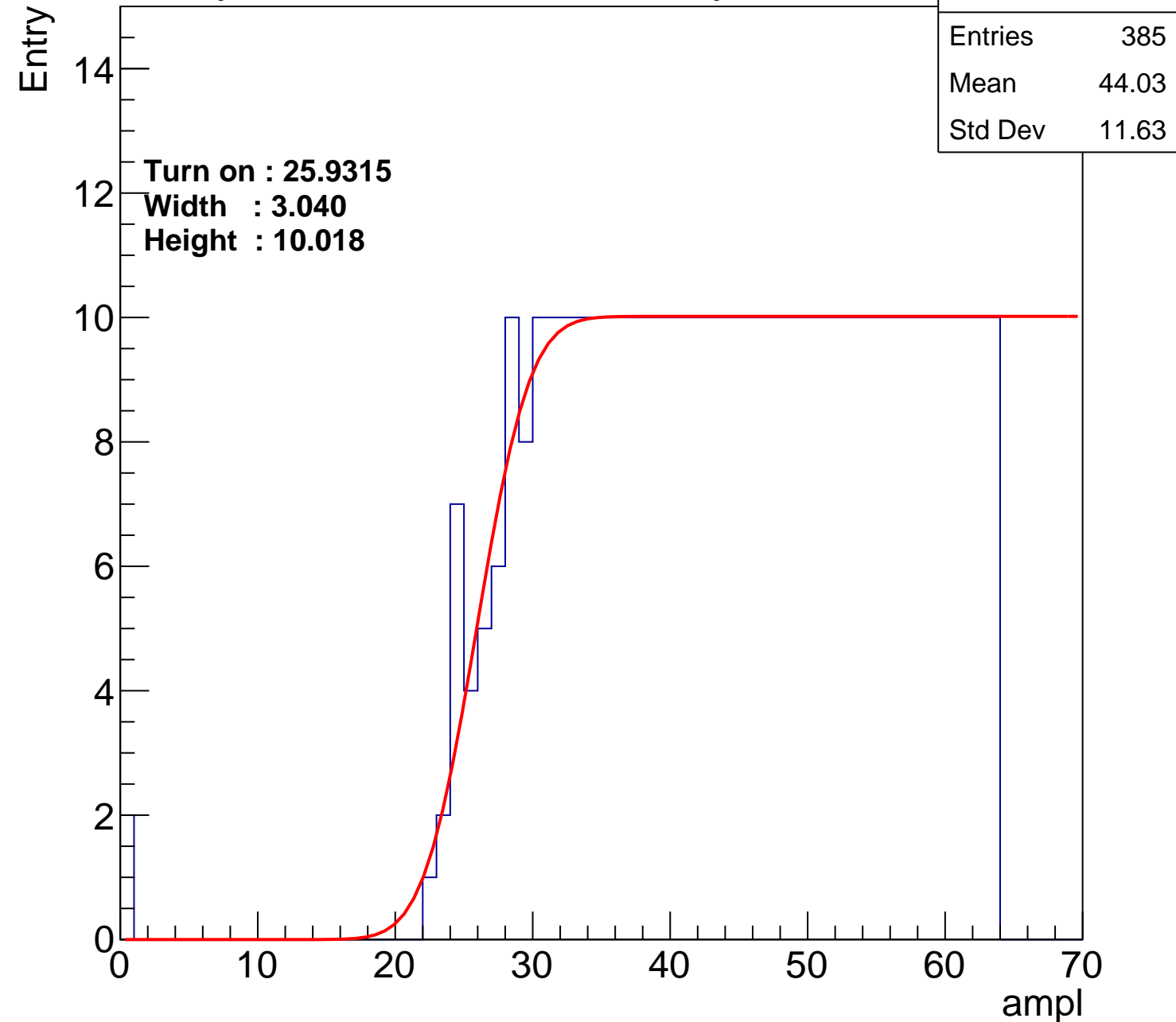
Width : 3.040

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch26

calib_packv5_042523_0143.root, FC#10, port B3

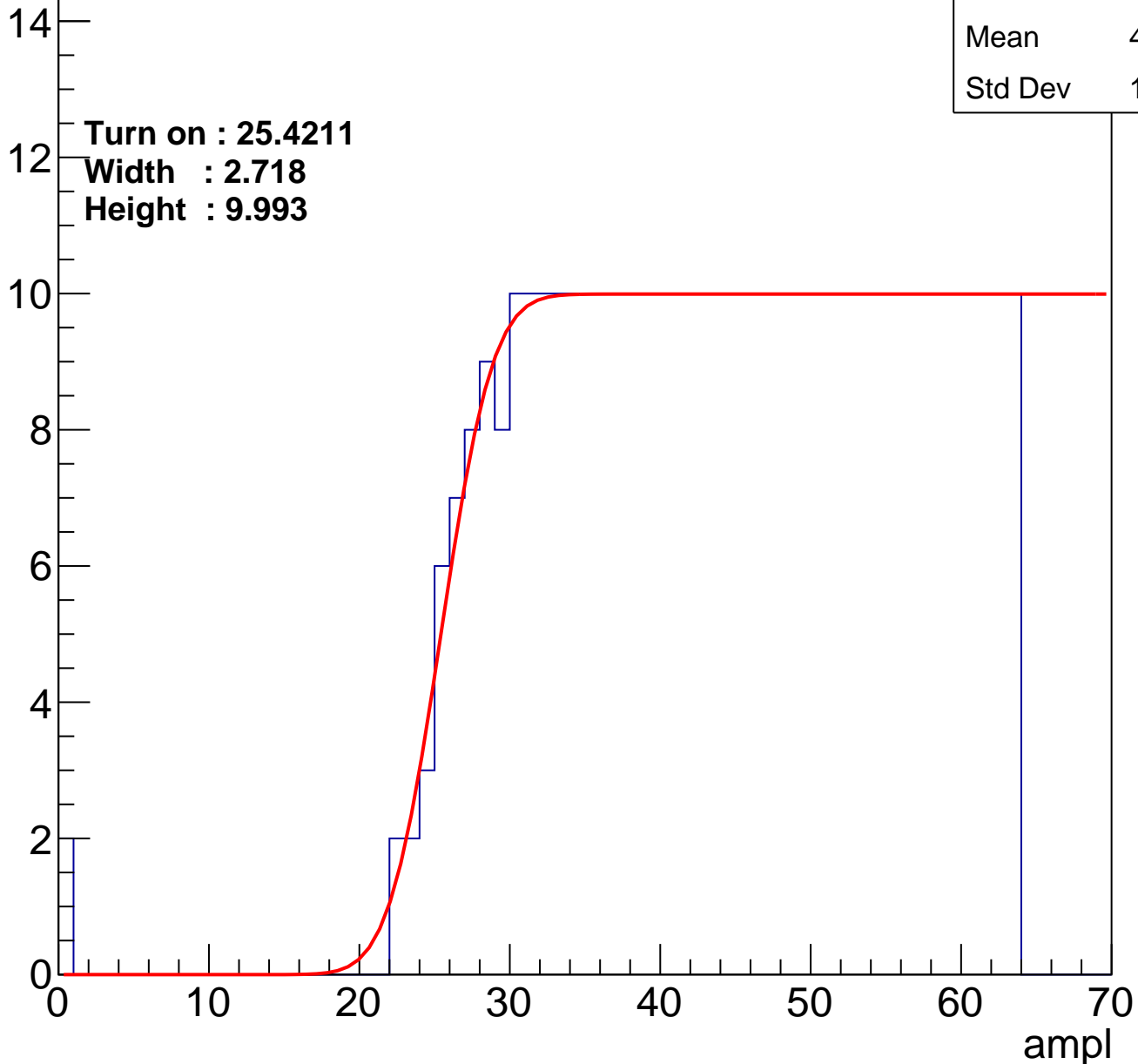
Entries	387
Mean	43.94
Std Dev	11.66

Turn on : 25.4211

Width : 2.718

Height : 9.993

Entry



B1L002S, U22-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.23
Std Dev	11.4

Turn on : 26.1291

Width : 3.175

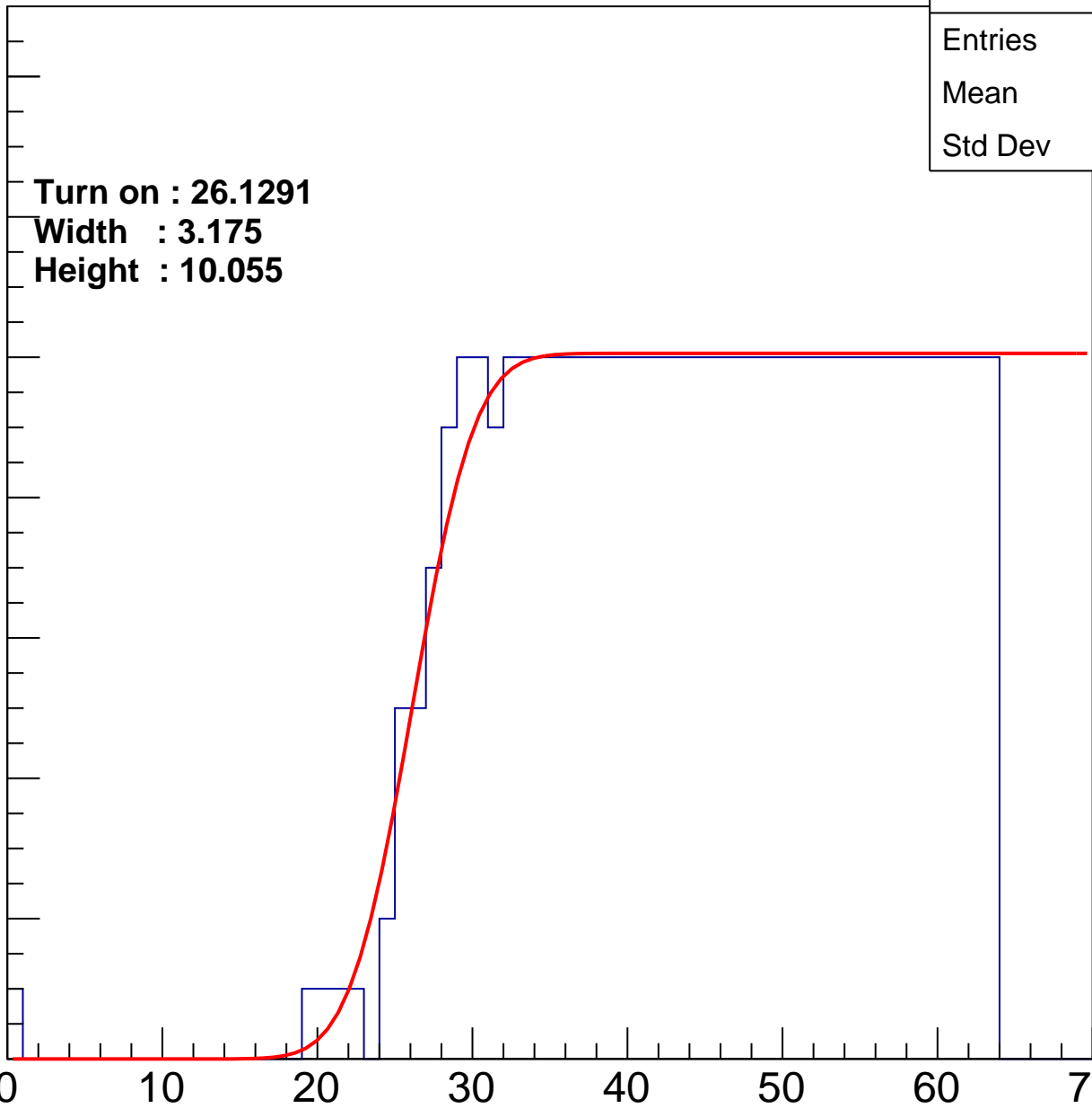
Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U22-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.0022

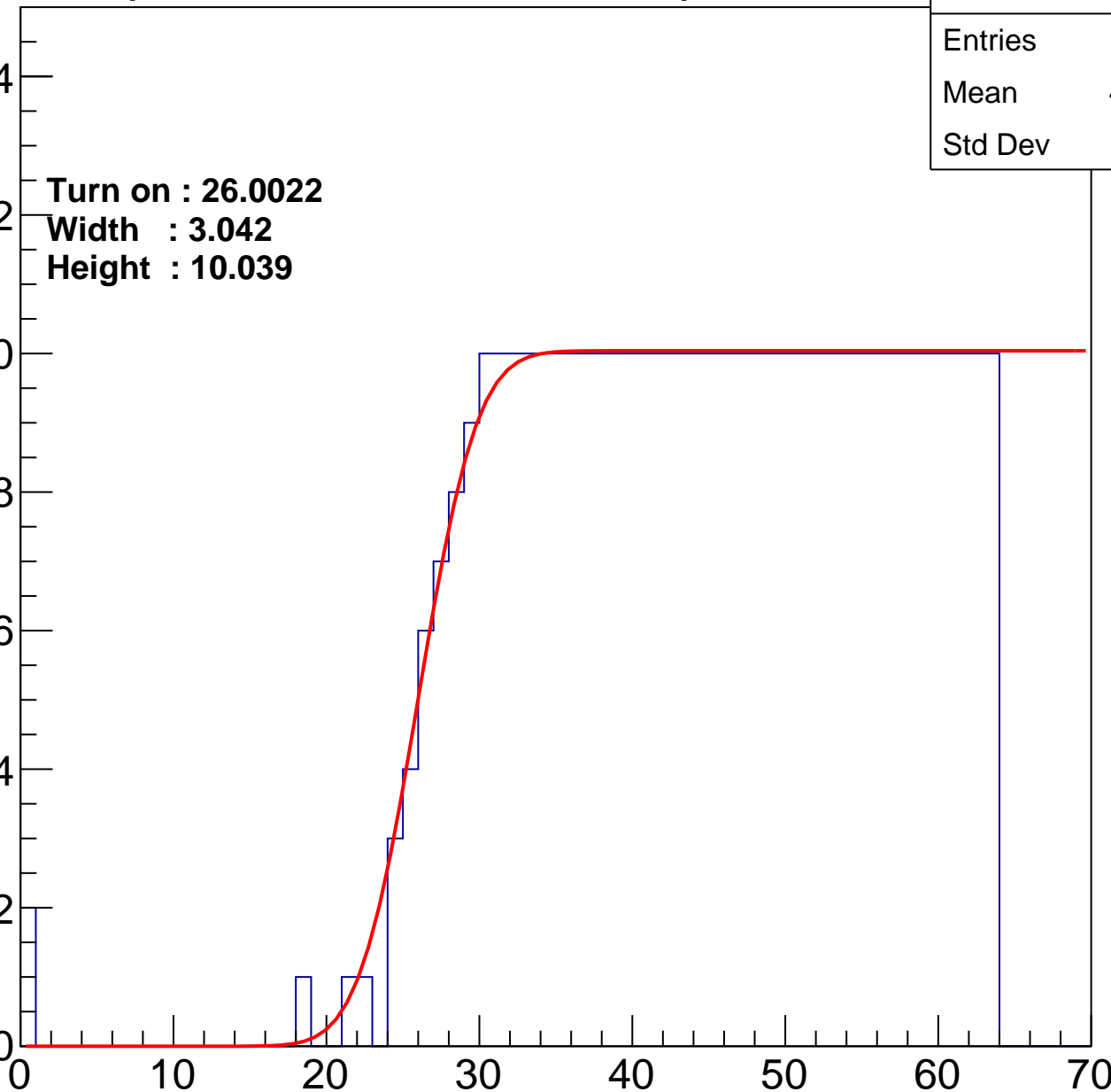
Width : 3.042

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch29

calib_packv5_042523_0143.root, FC#10, port B3

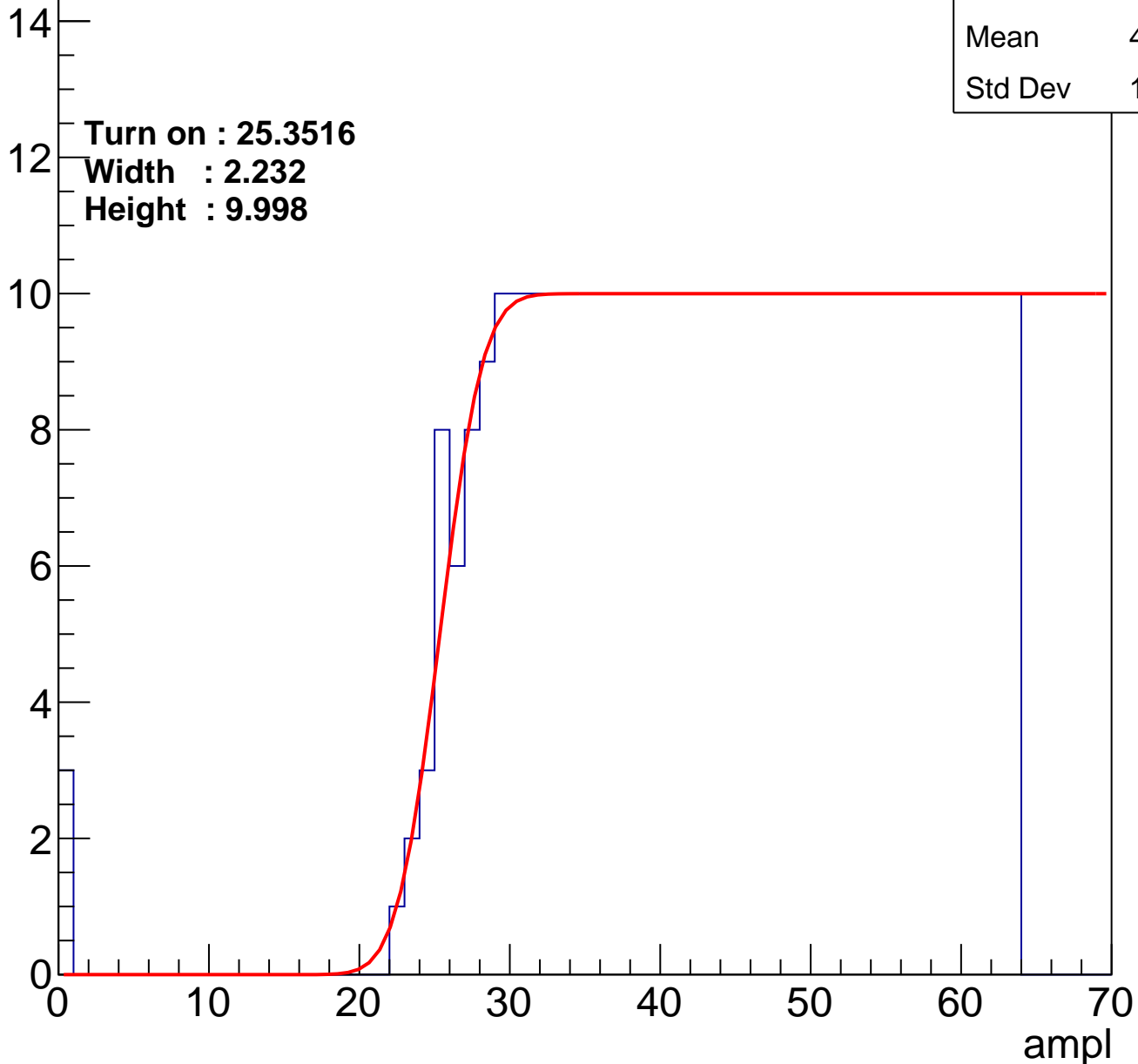
Entries	390
Mean	43.75
Std Dev	11.86

Turn on : 25.3516

Width : 2.232

Height : 9.998

Entry



B1L002S, U22-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.9102

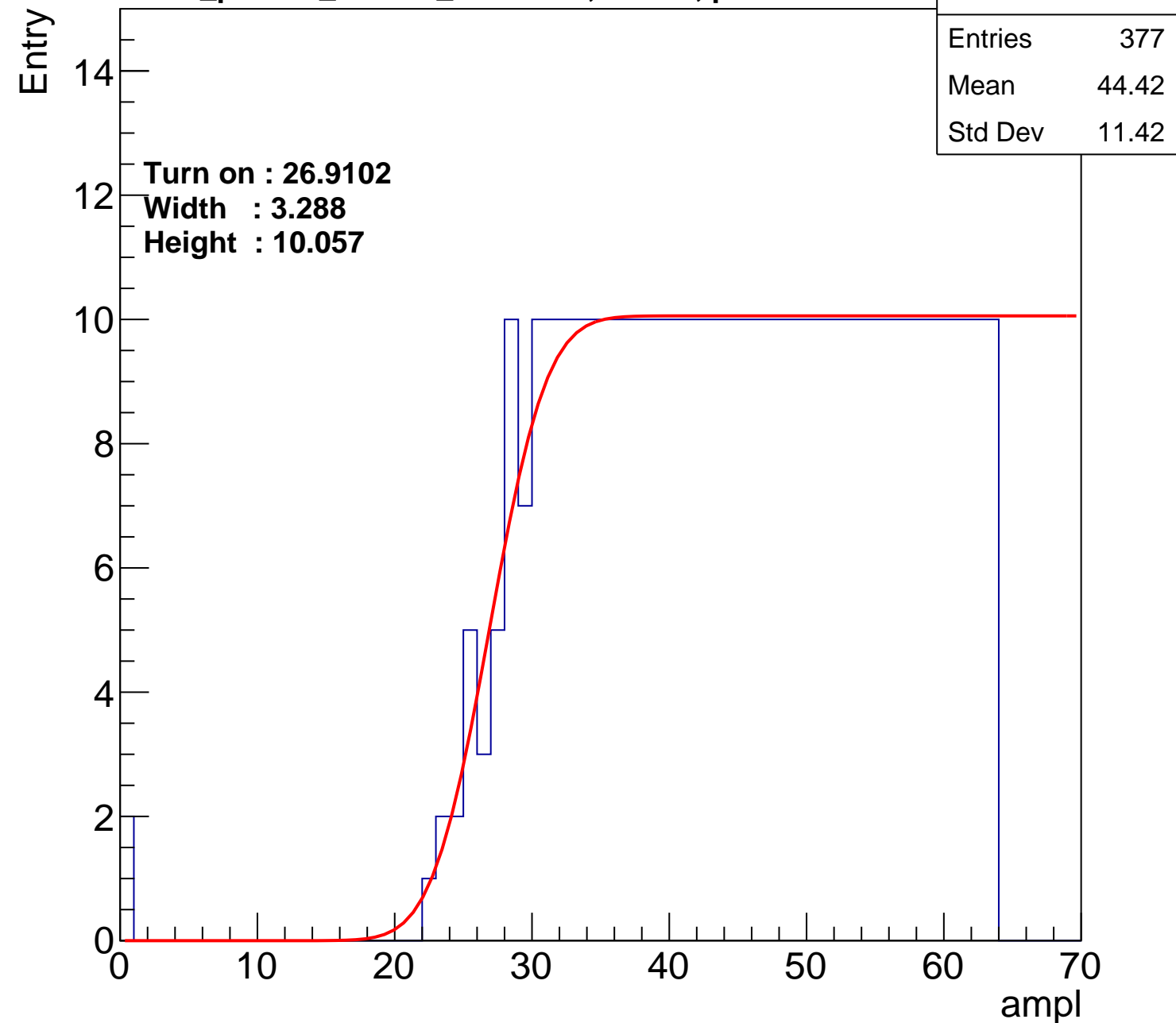
Width : 3.288

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch31

calib_packv5_042523_0143.root, FC#10, port B3

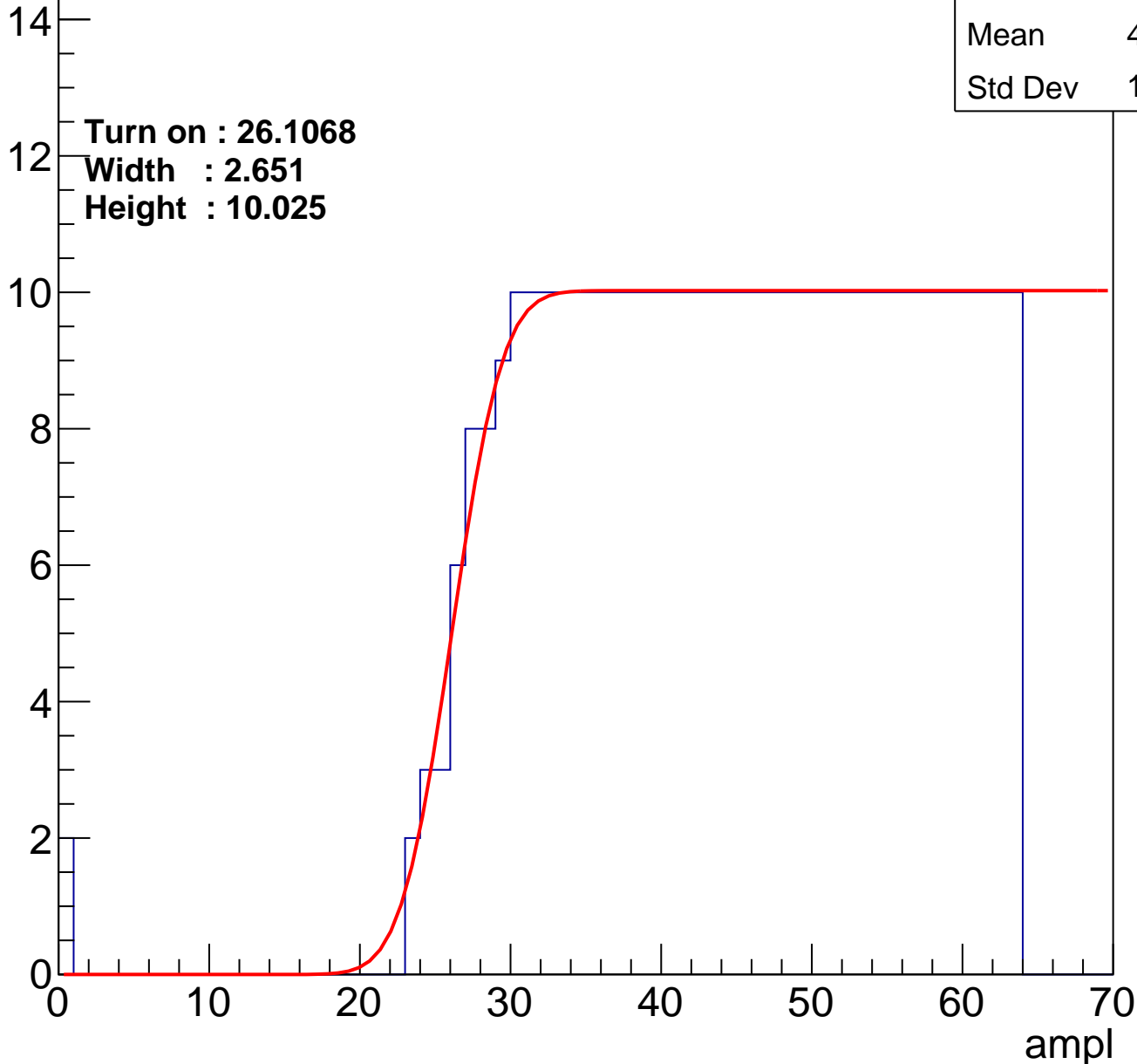
Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.1068

Width : 2.651

Height : 10.025

Entry



B1L002S, U22-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.53

Turn on : 25.5156

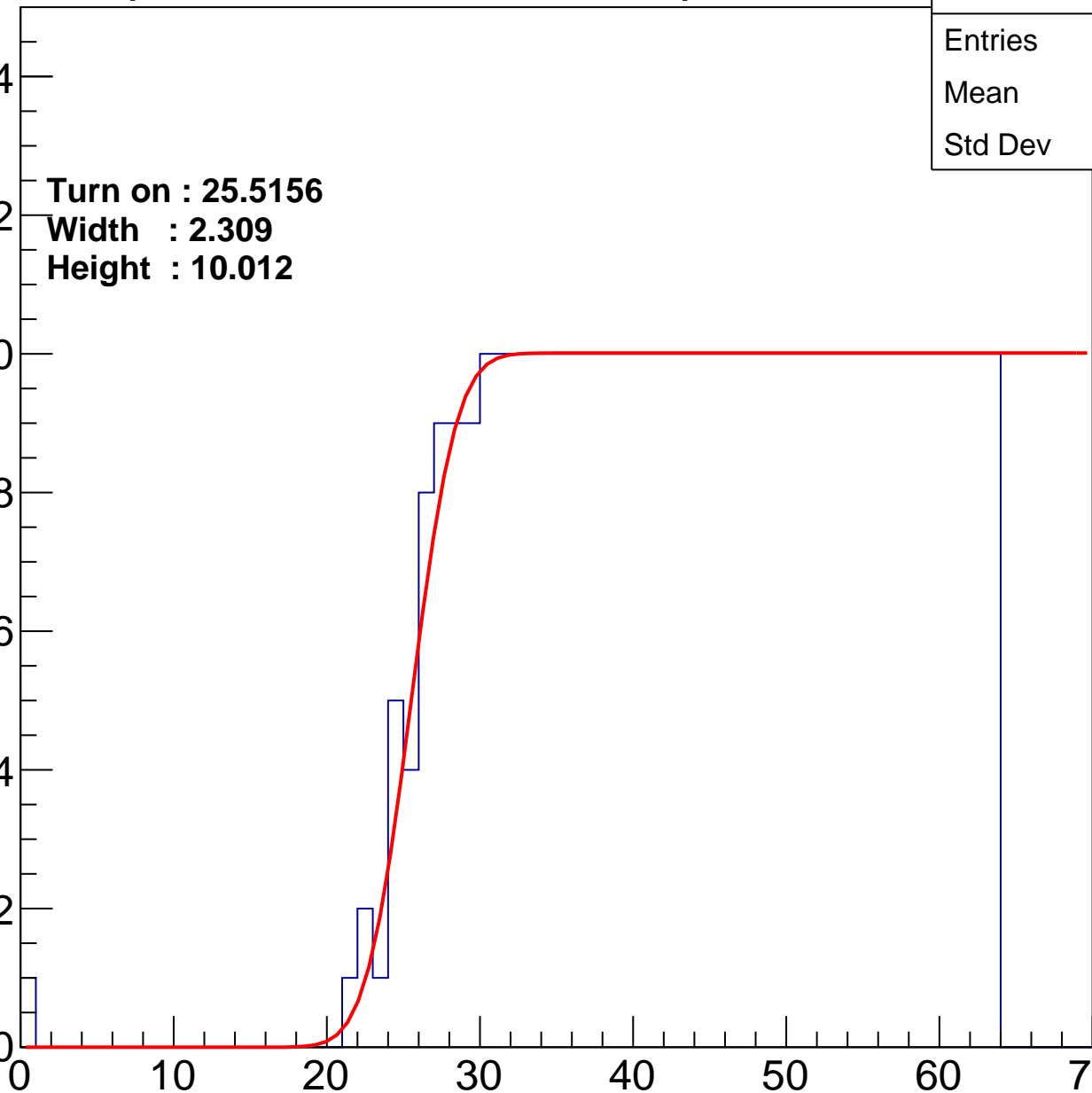
Width : 2.309

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.02

Turn on : 25.0830

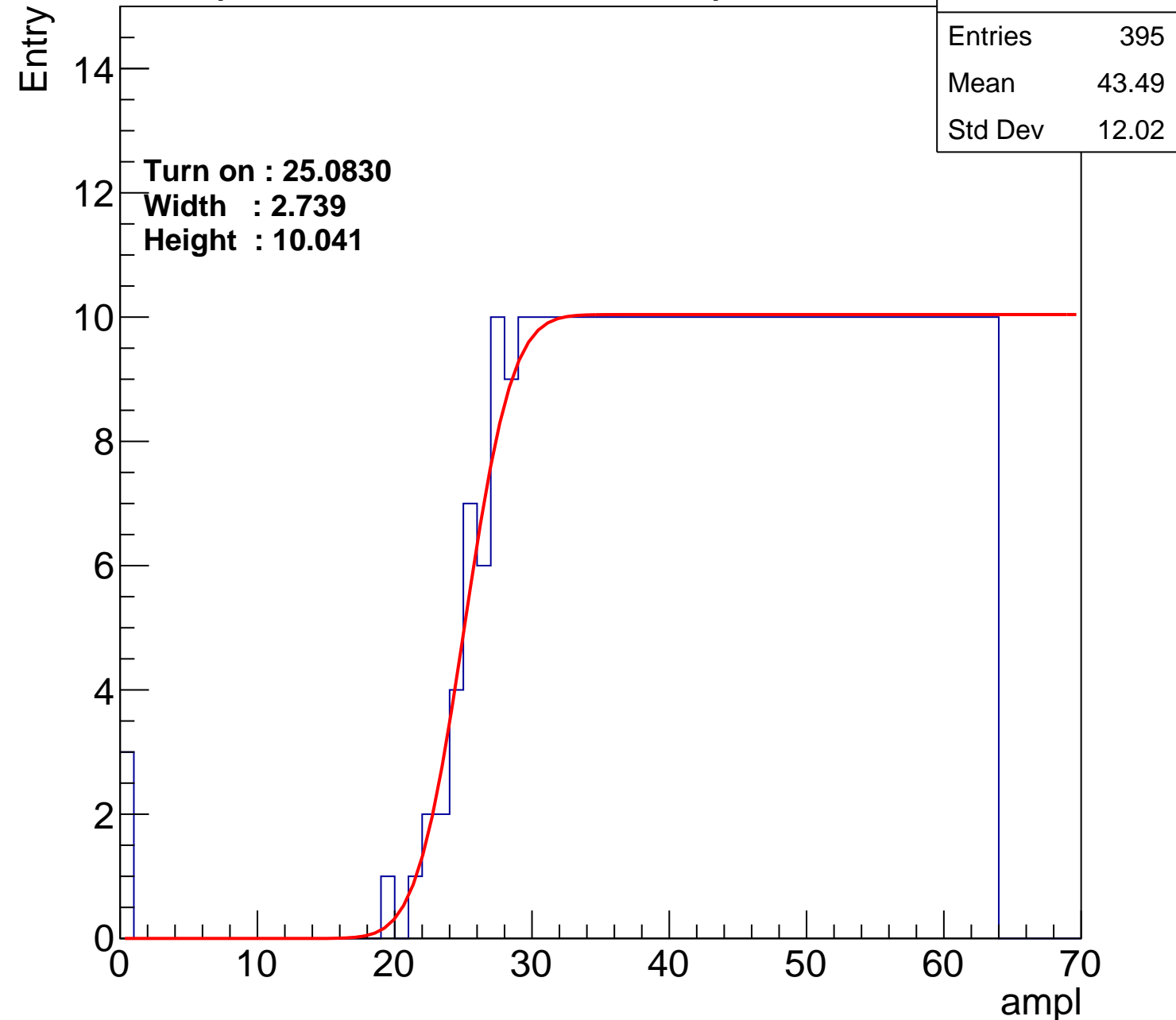
Width : 2.739

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.9
Std Dev	11.96

Turn on : 26.1178

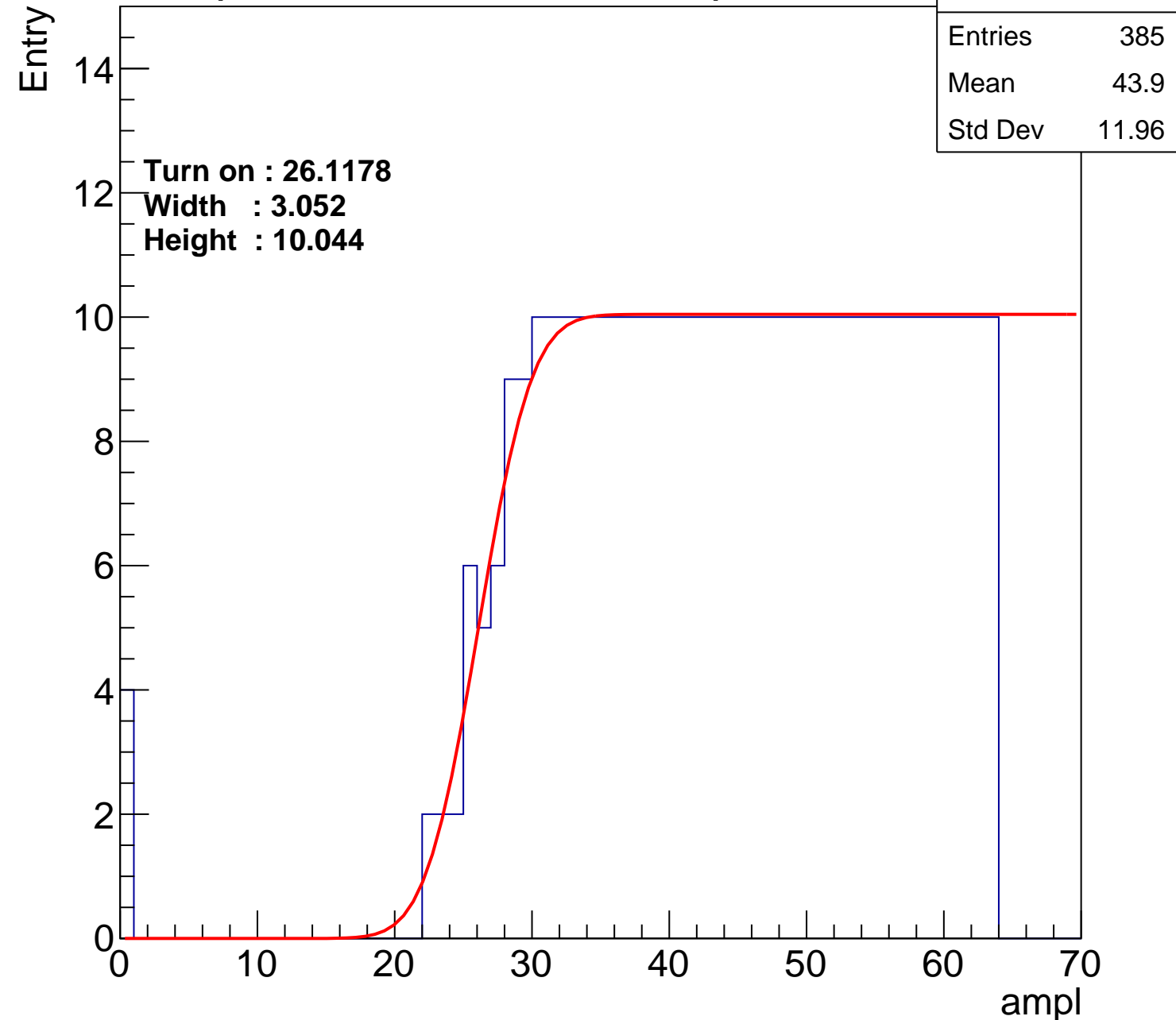
Width : 3.052

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.14
Std Dev	11.42

Turn on : 25.5635

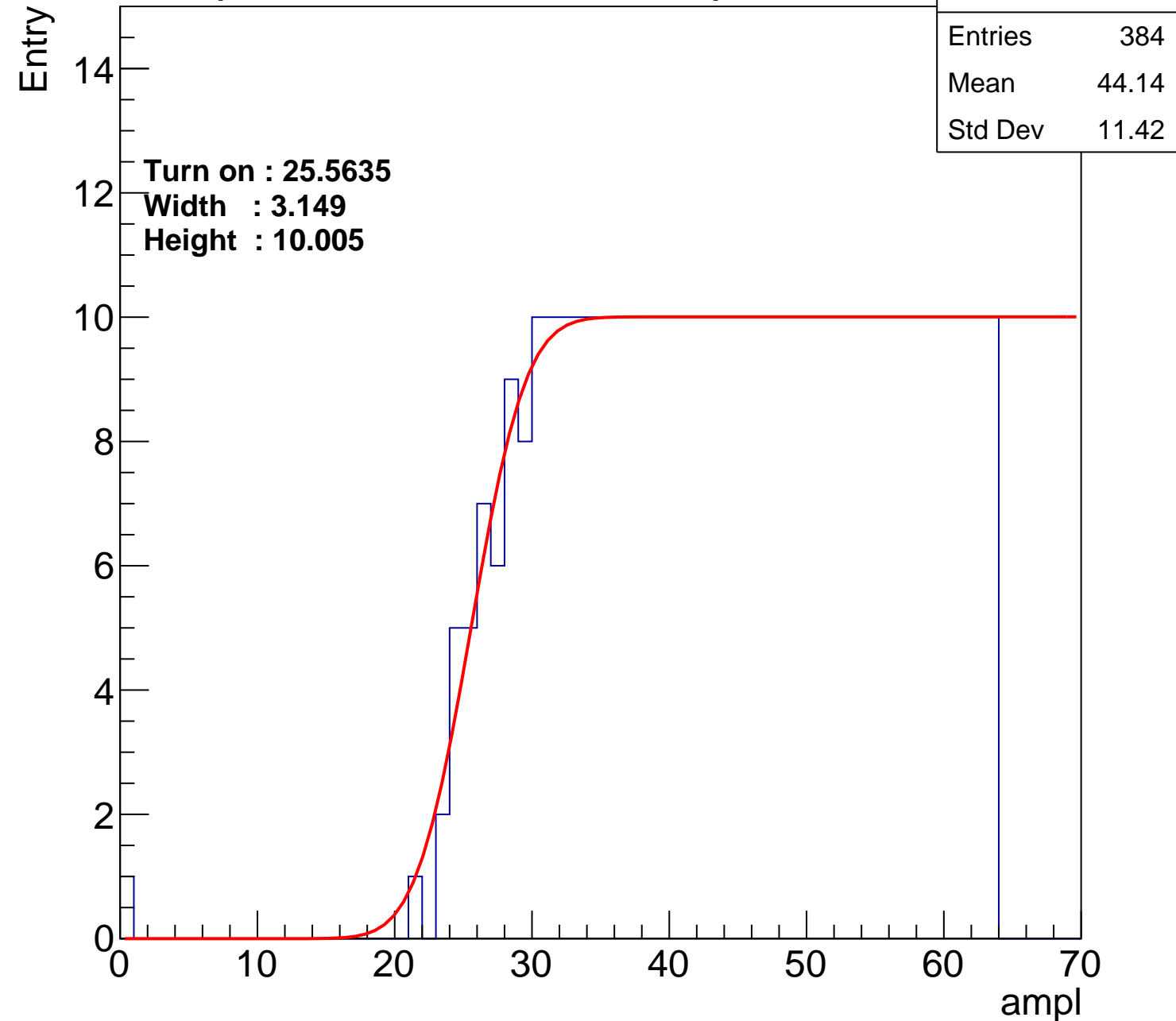
Width : 3.149

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch36

calib_packv5_042523_0143.root, FC#10, port B3

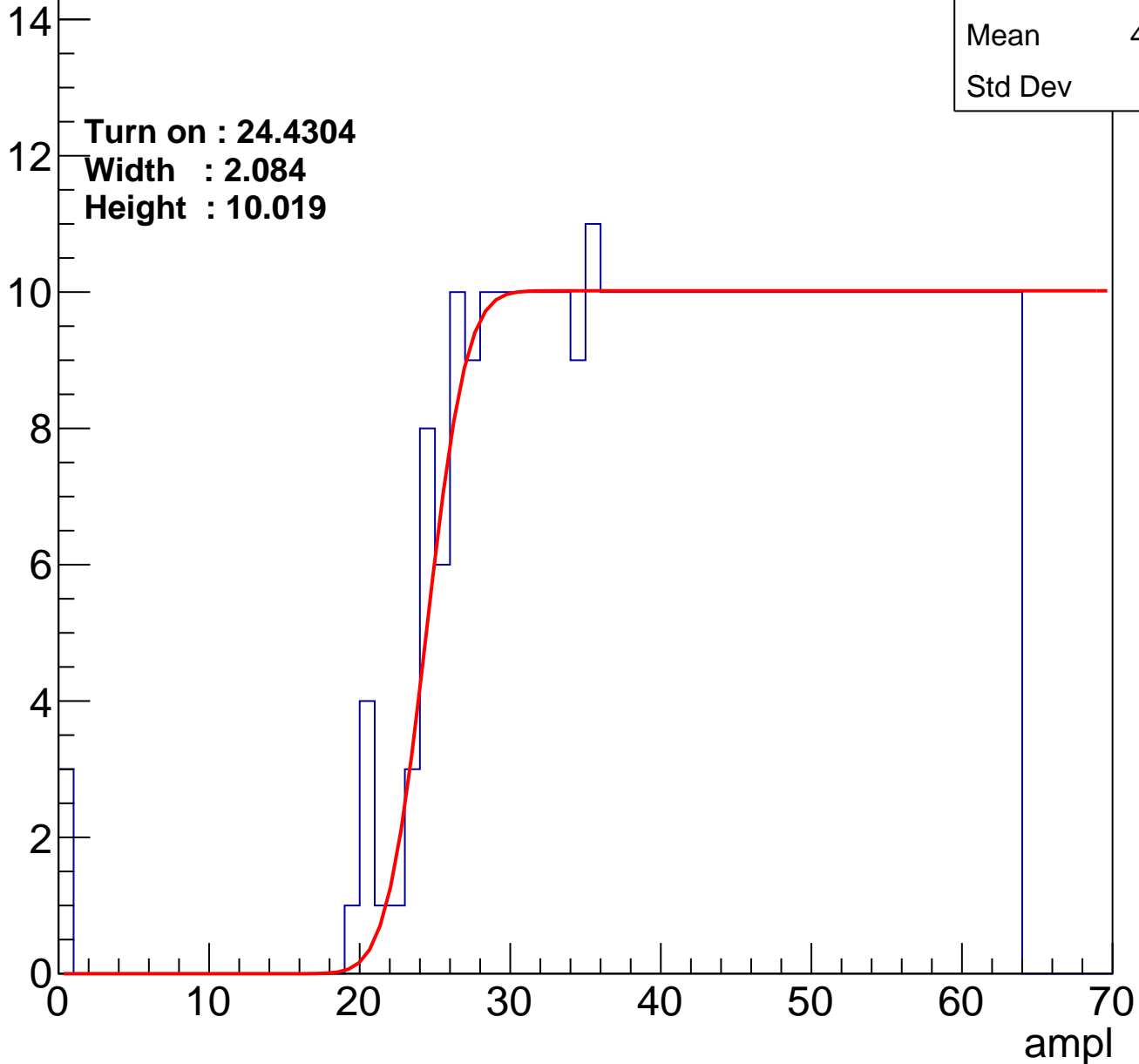
Entries	406
Mean	42.95
Std Dev	12.3

Turn on : 24.4304

Width : 2.084

Height : 10.019

Entry



B1L002S, U22-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.2922

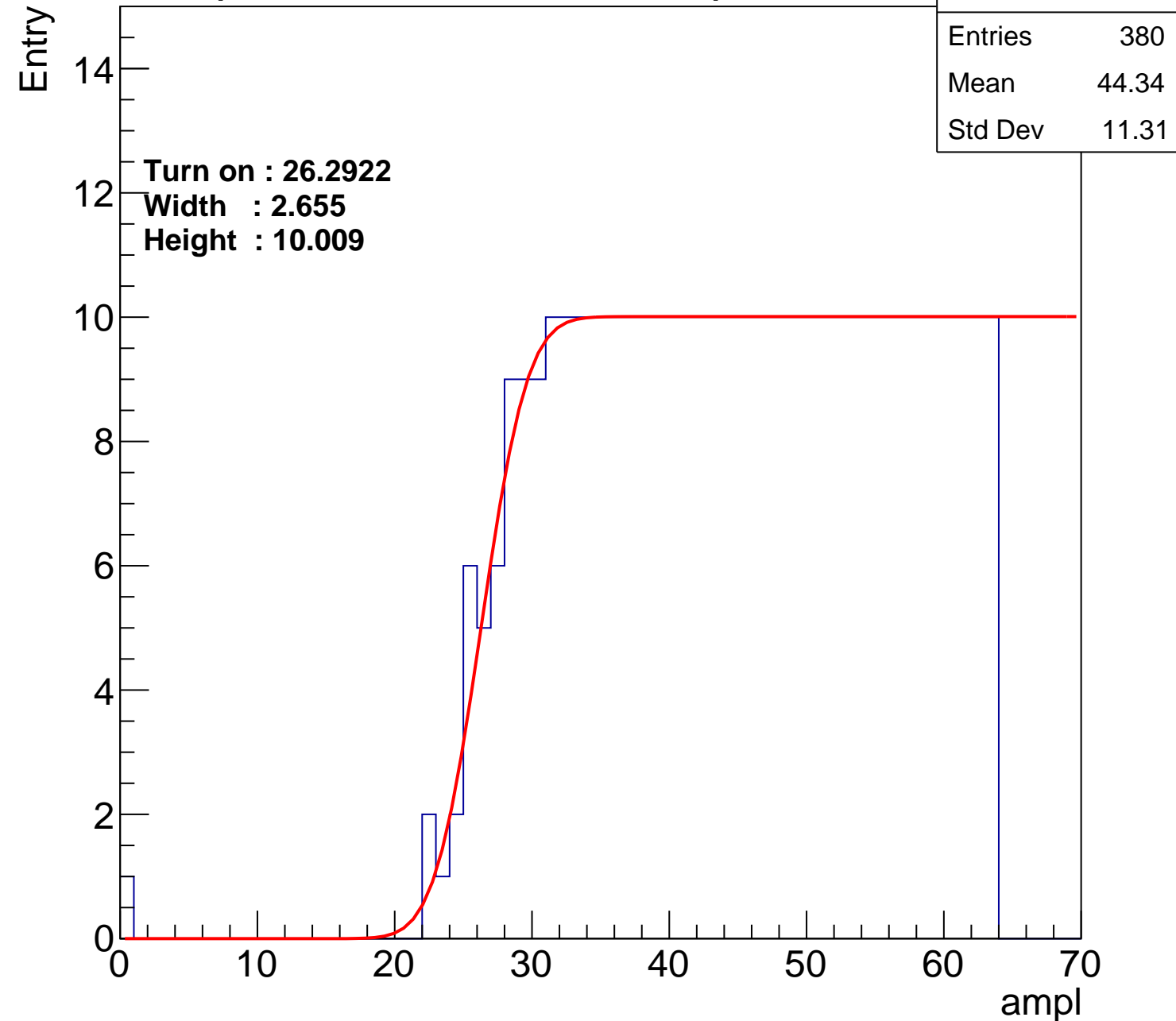
Width : 2.655

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	364
Mean	45.15
Std Dev	10.86

Turn on : 27.9651

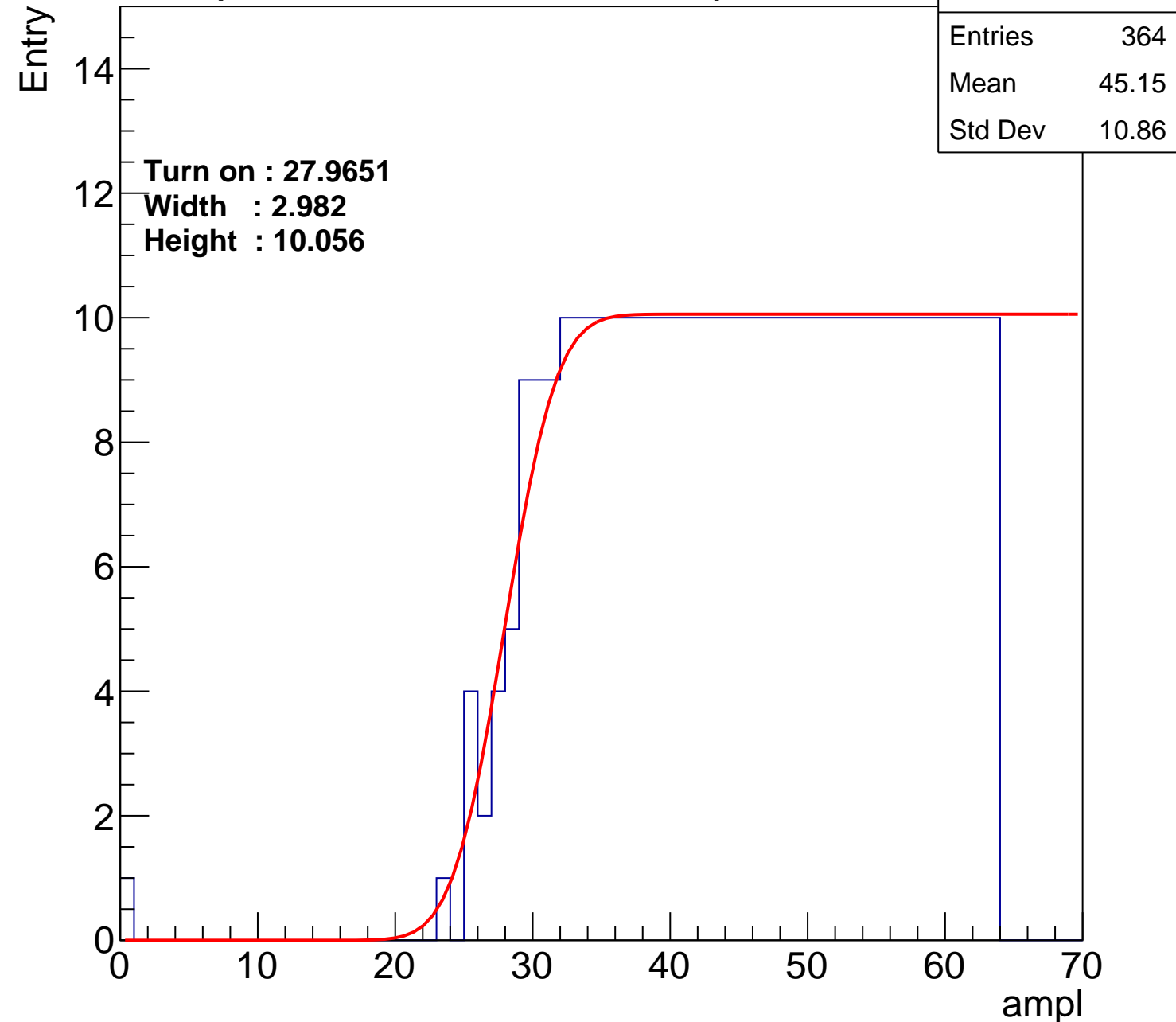
Width : 2.982

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.05
Std Dev	11.59

Turn on : 25.7734

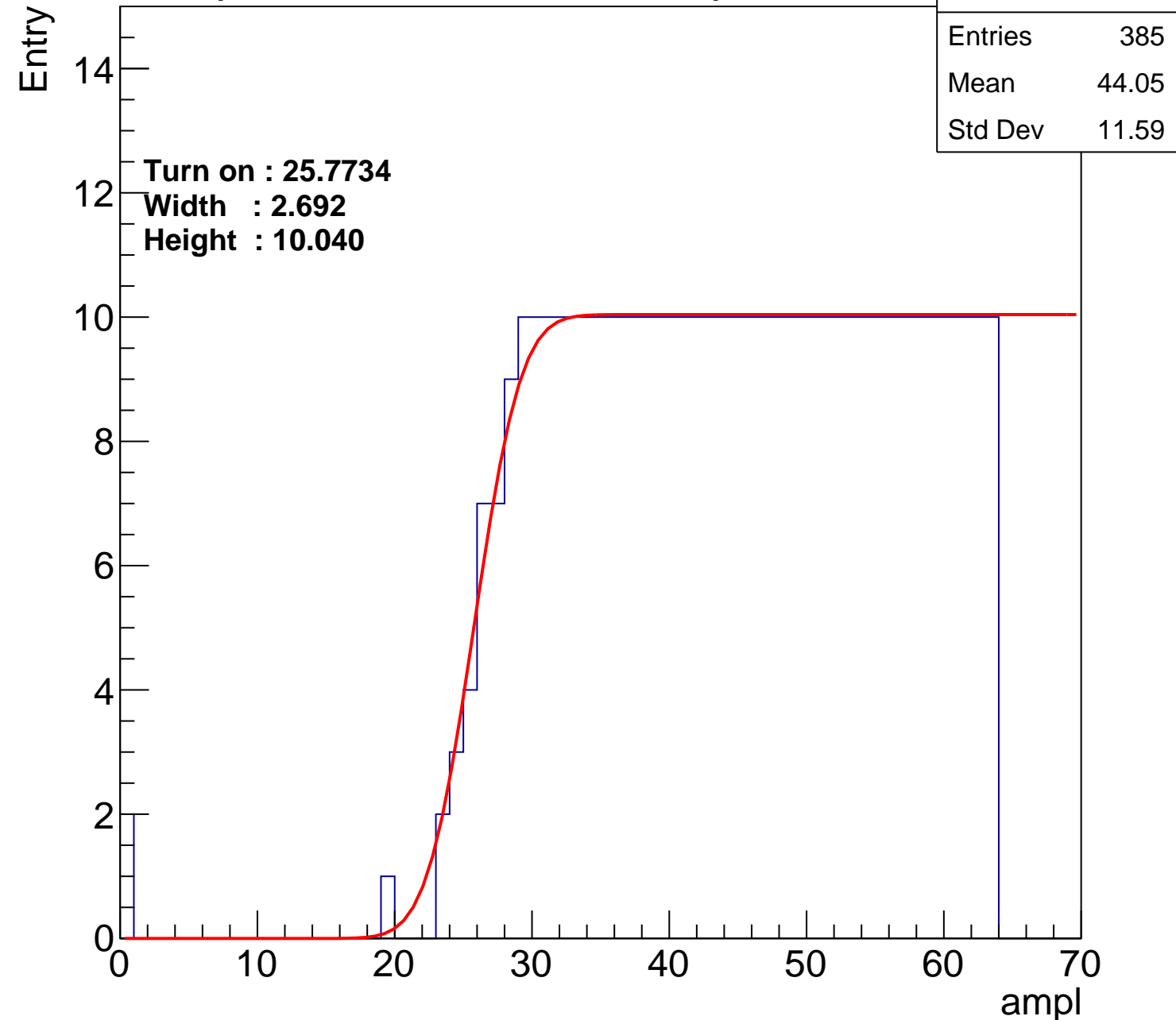
Width : 2.692

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch40

calib_packv5_042523_0143.root, FC#10, port B3

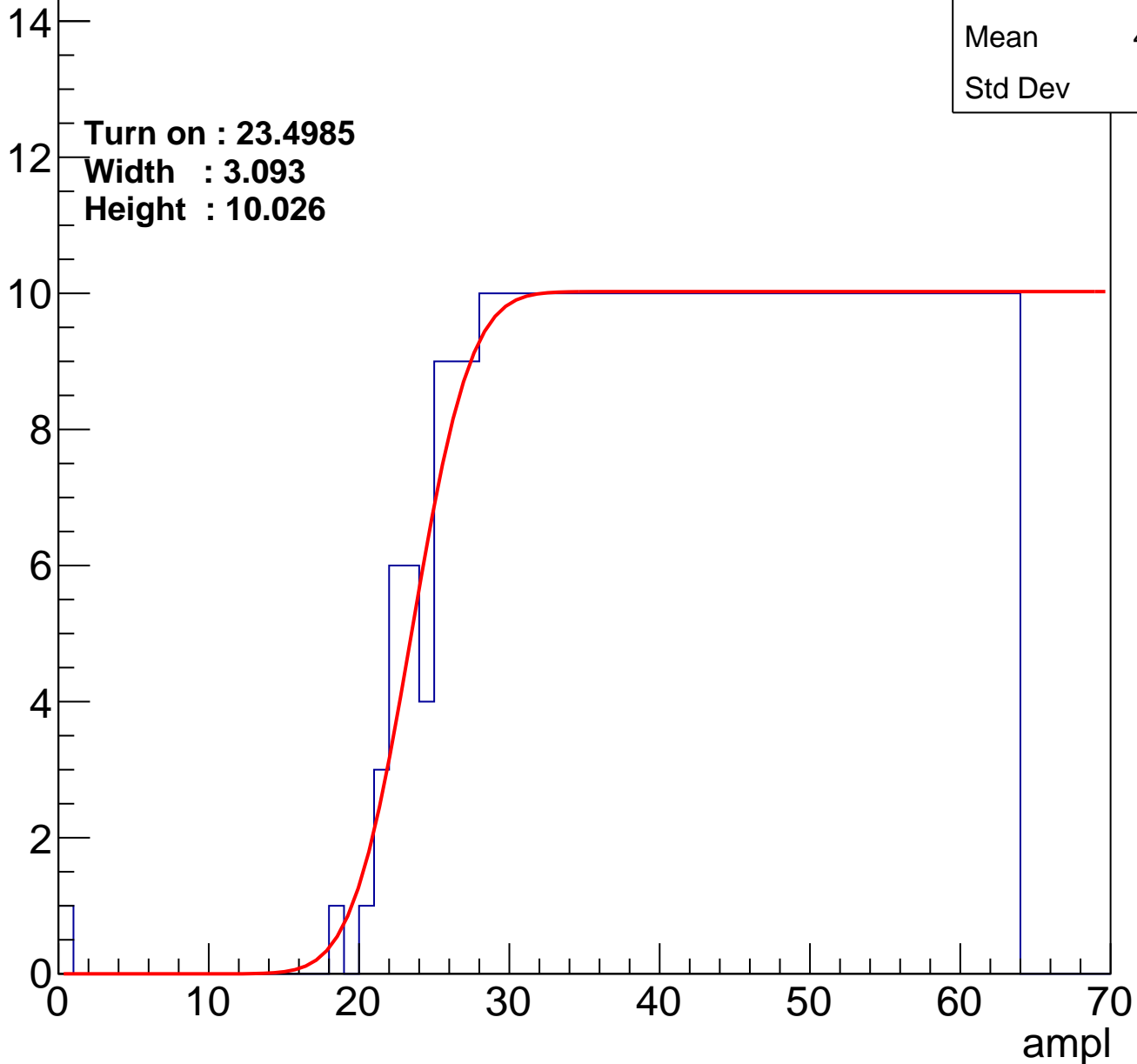
Entries	409
Mean	42.91
Std Dev	12.1

Turn on : 23.4985

Width : 3.093

Height : 10.026

Entry



B1L002S, U22-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.94
Std Dev	11.48

Turn on : 25.5643

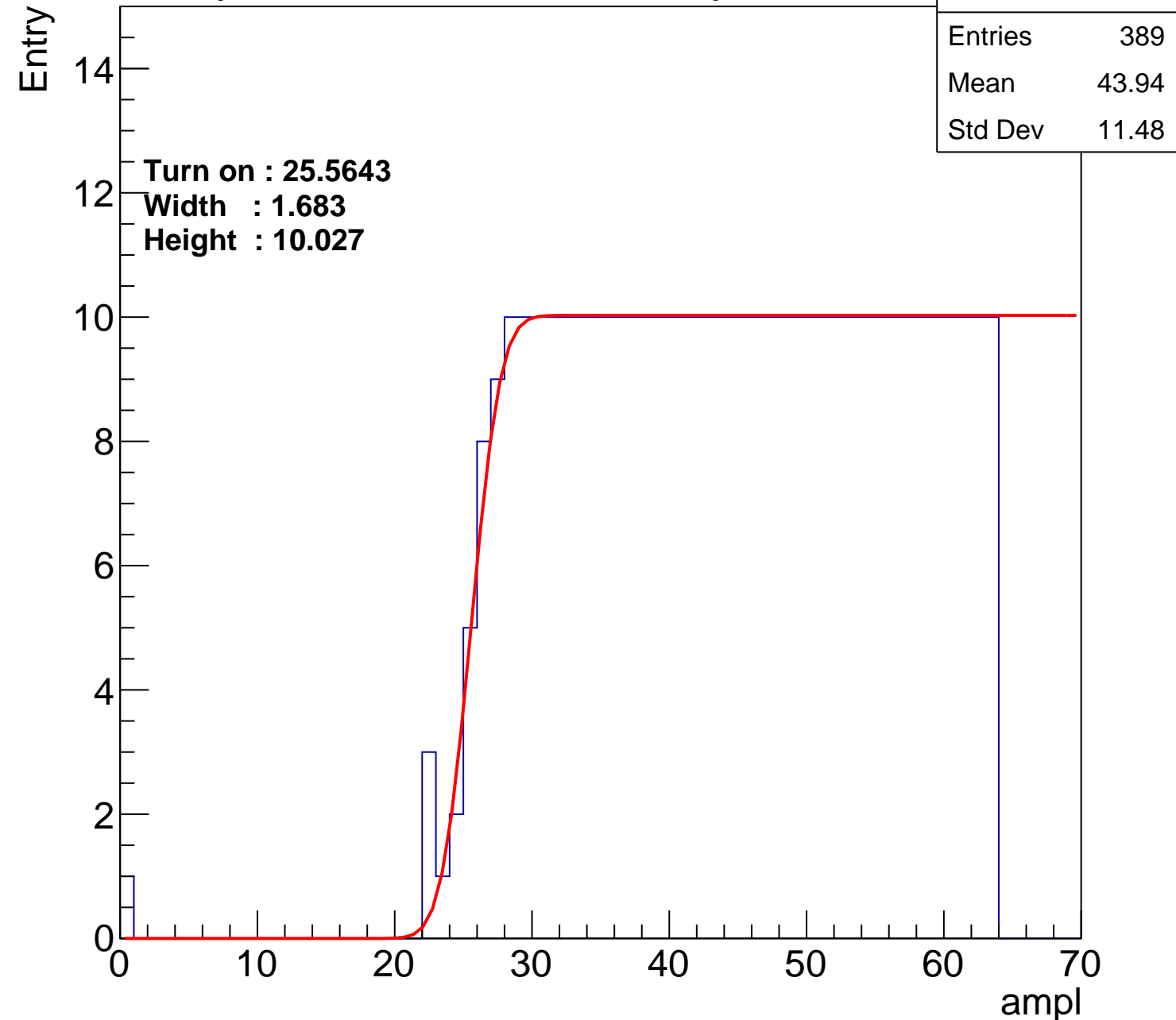
Width : 1.683

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.41
Std Dev	12.14

Turn on : 25.3766

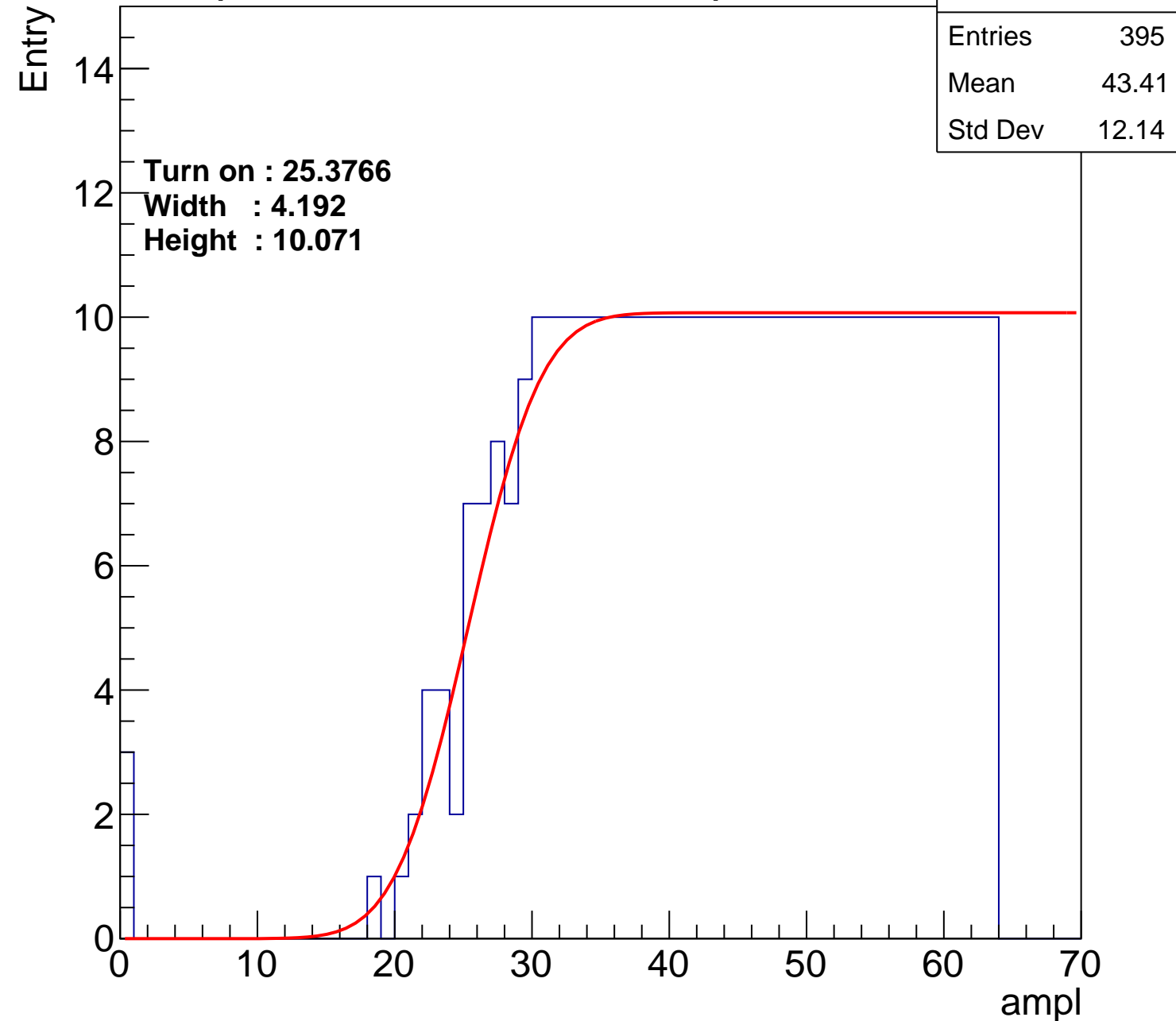
Width : 4.192

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.78
Std Dev	11.21

Turn on : 27.4214

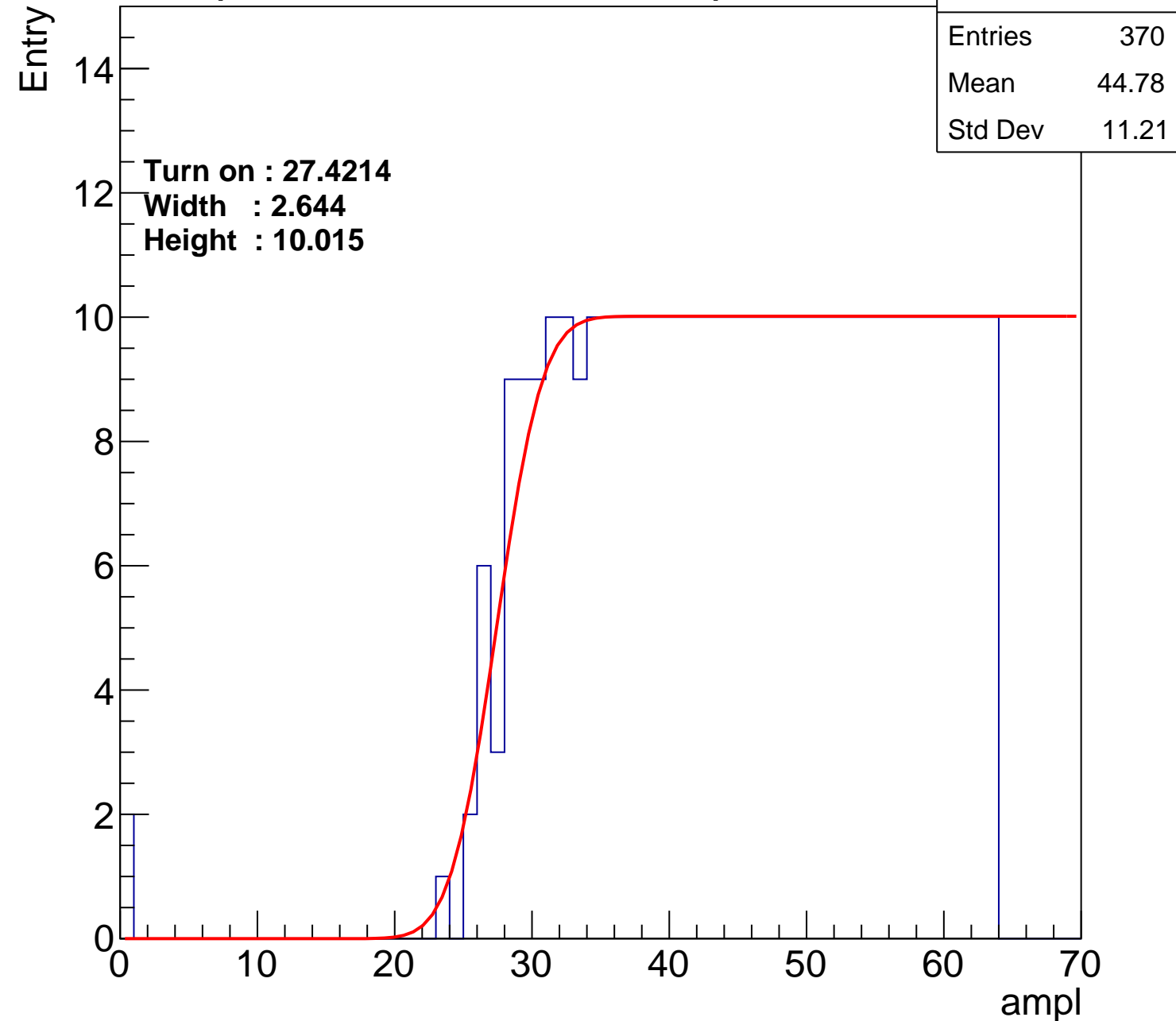
Width : 2.644

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch44

calib_packv5_042523_0143.root, FC#10, port B3

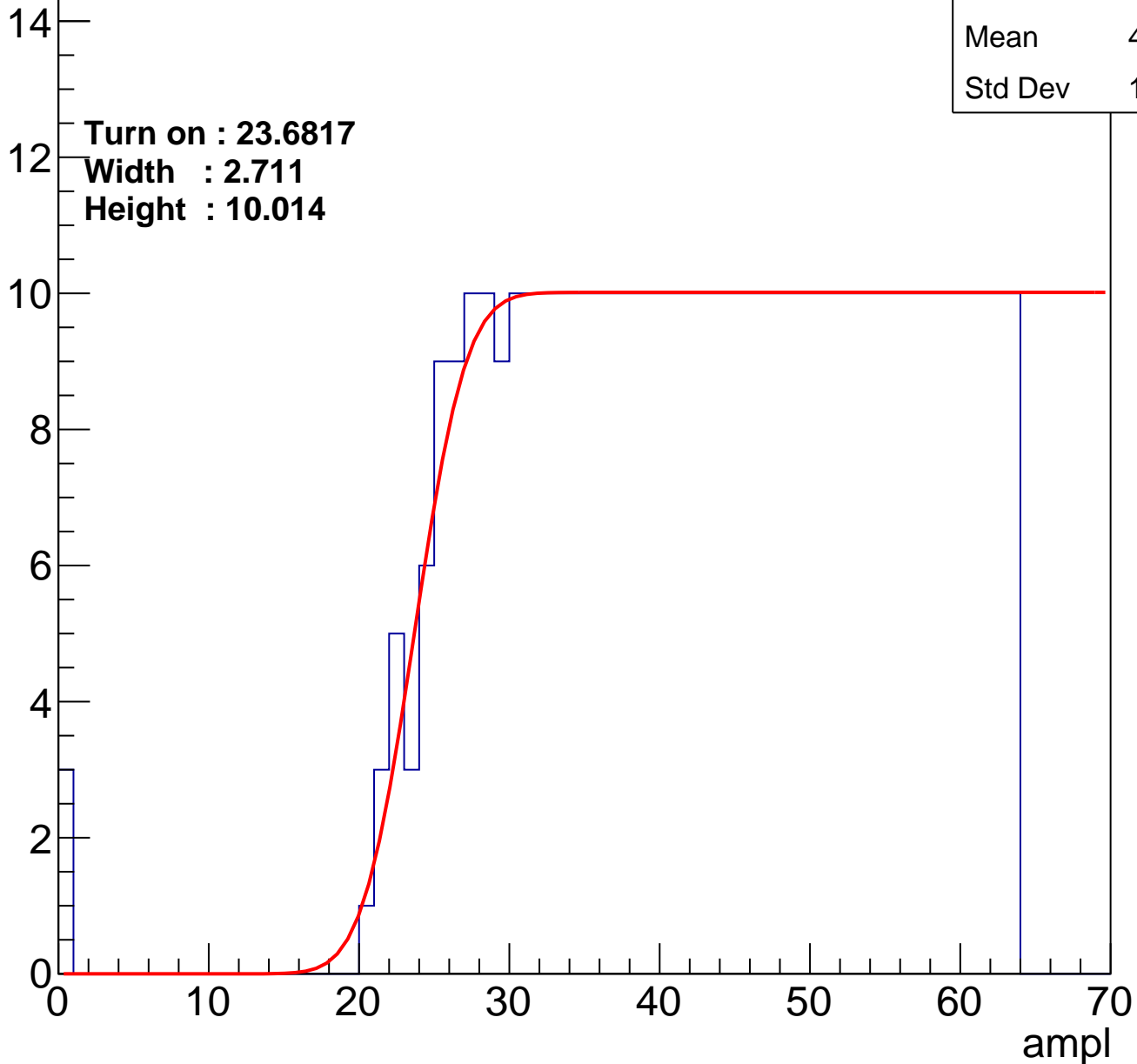
Entries	408
Mean	42.86
Std Dev	12.33

Turn on : 23.6817

Width : 2.711

Height : 10.014

Entry



B1L002S, U22-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.82
Std Dev	12.03

Turn on : 26.1465

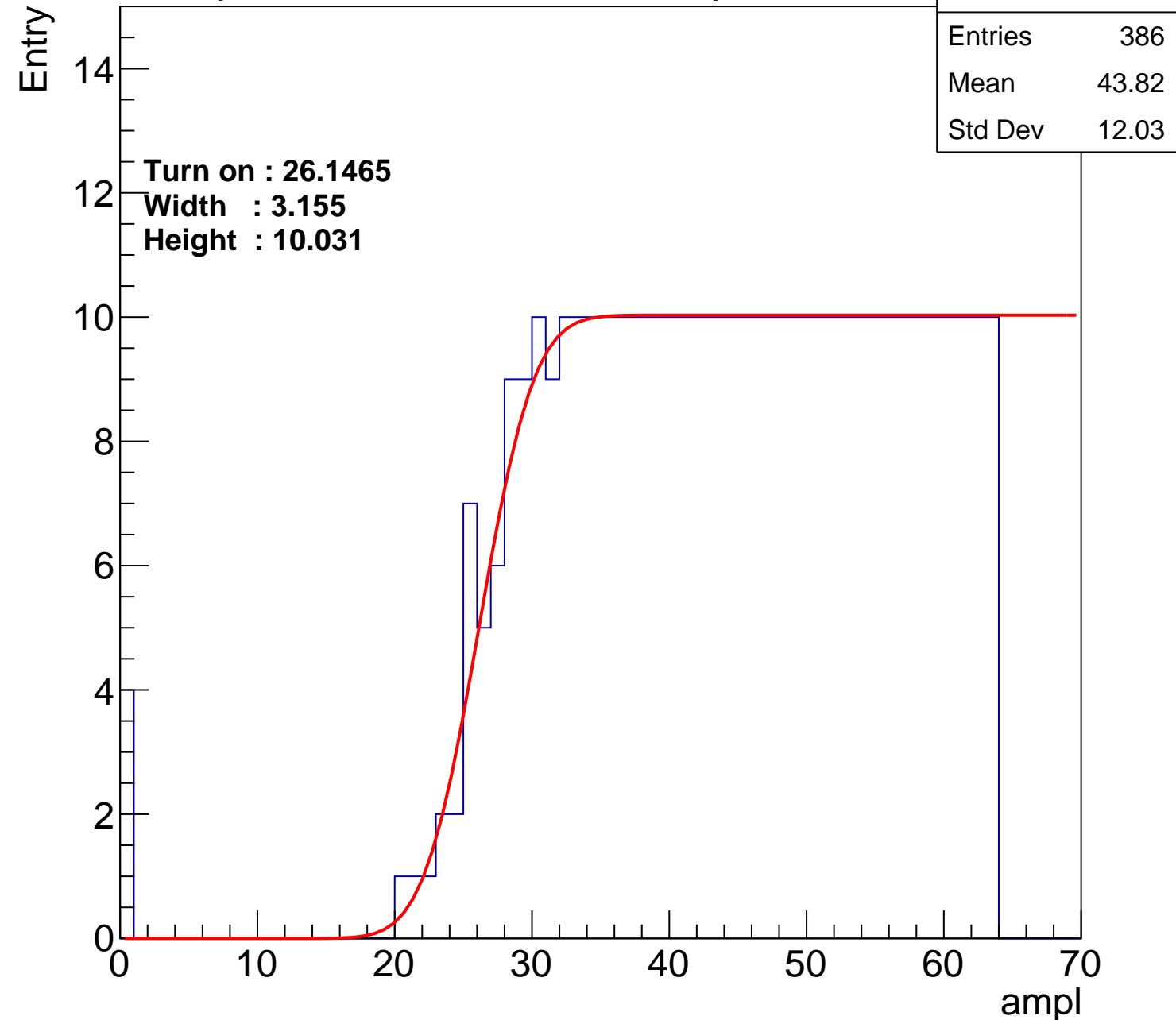
Width : 3.155

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.4244

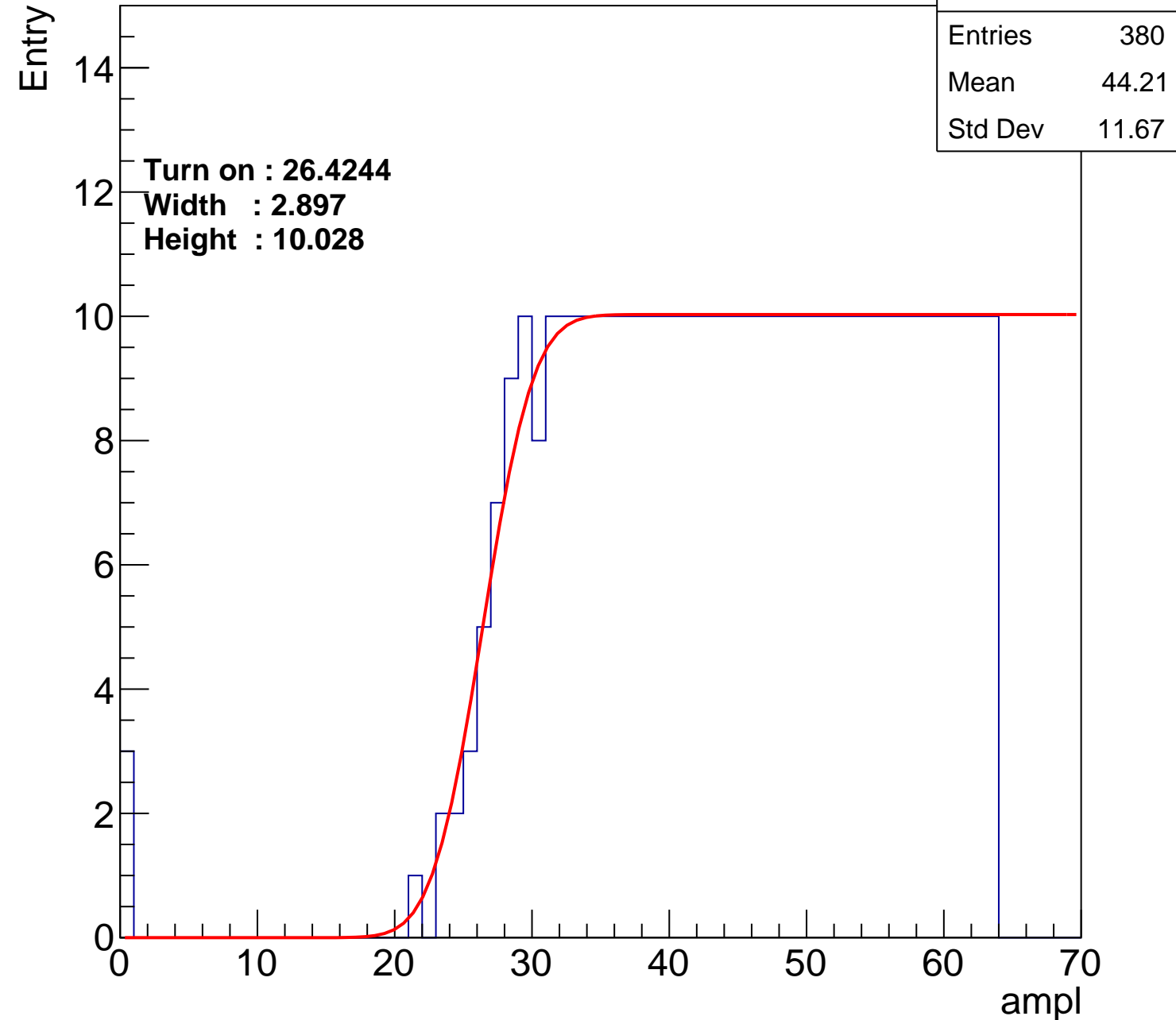
Width : 2.897

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.56

Turn on : 26.5458

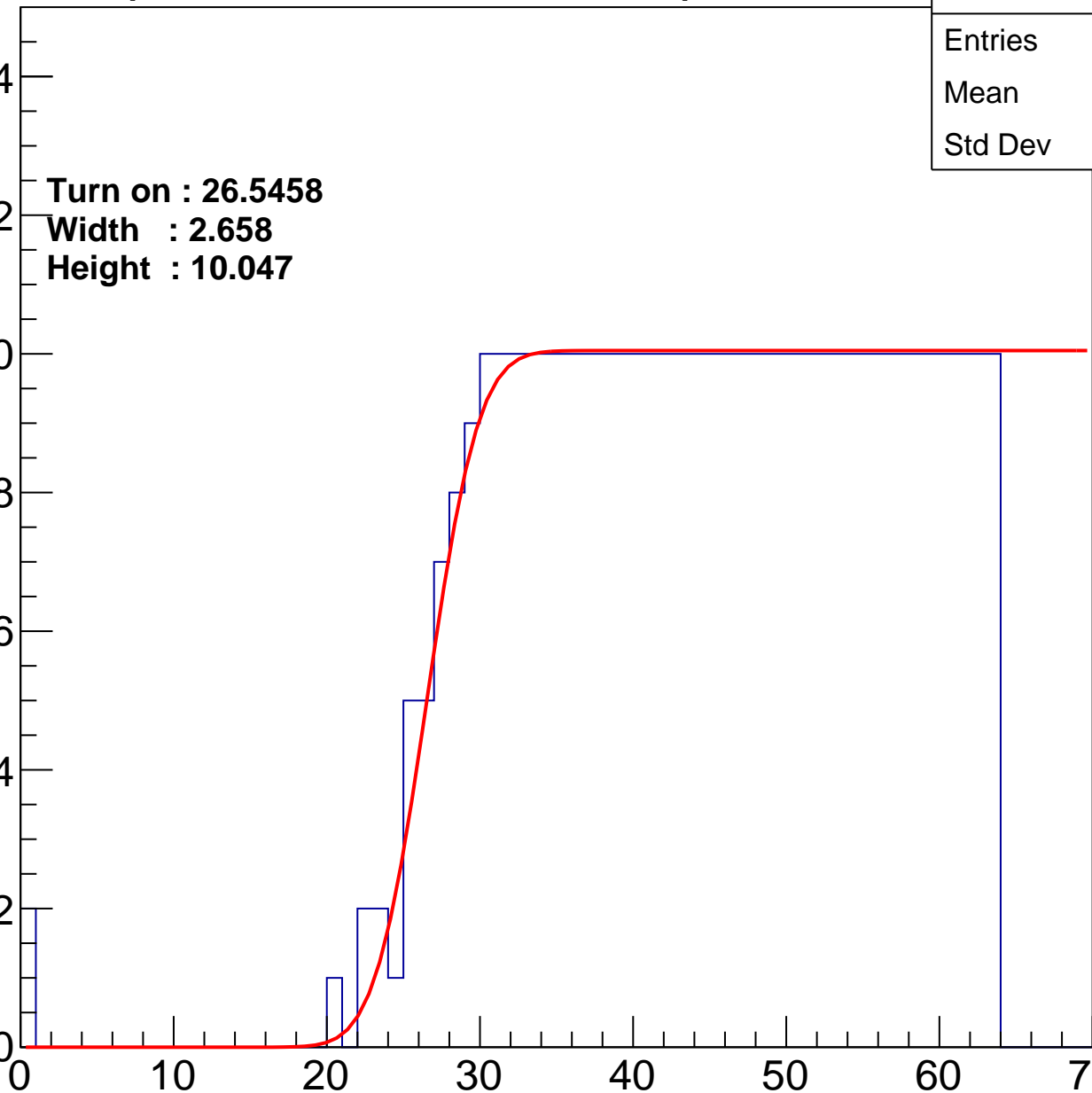
Width : 2.658

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 25.9066

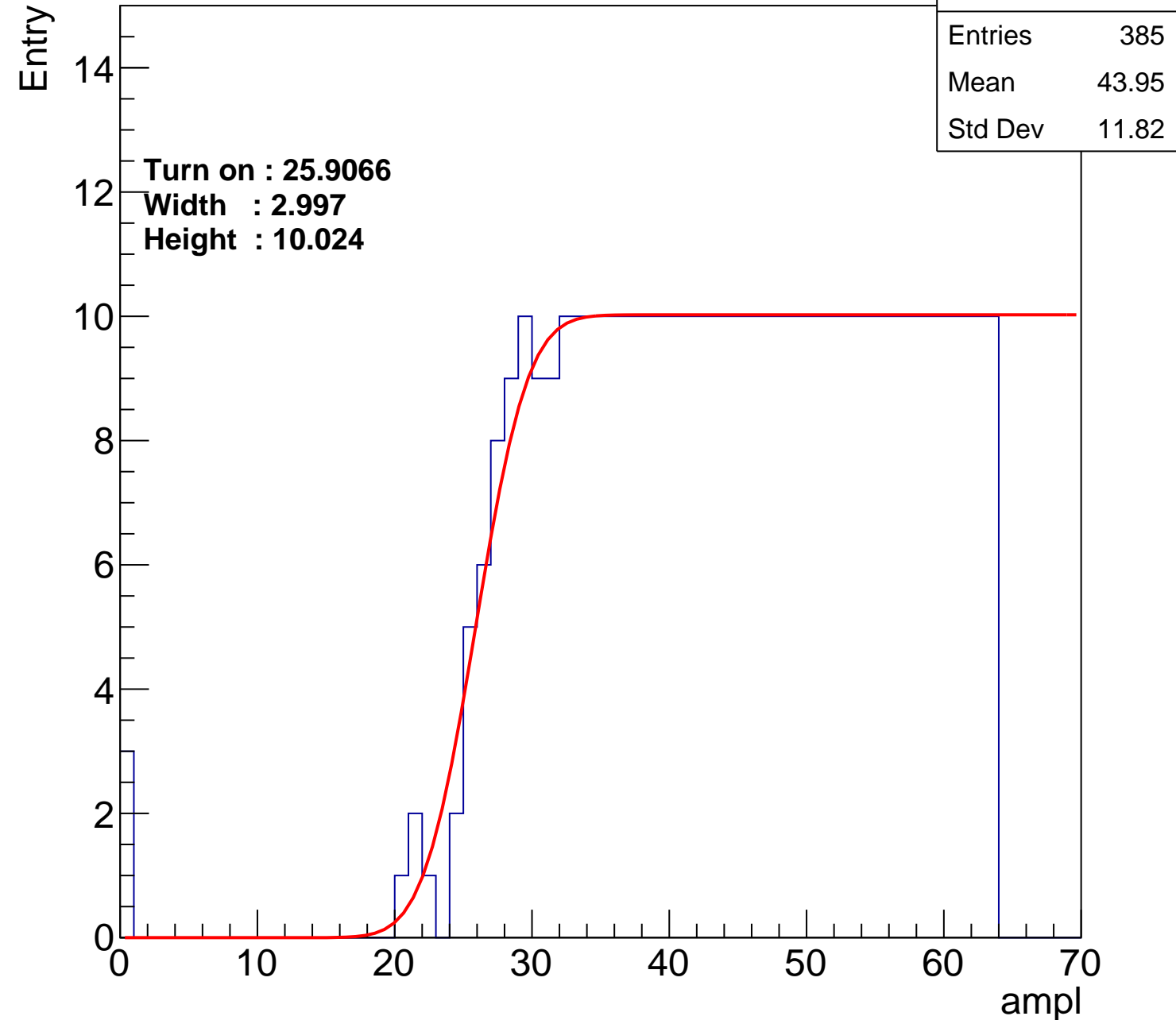
Width : 2.997

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 27.3560

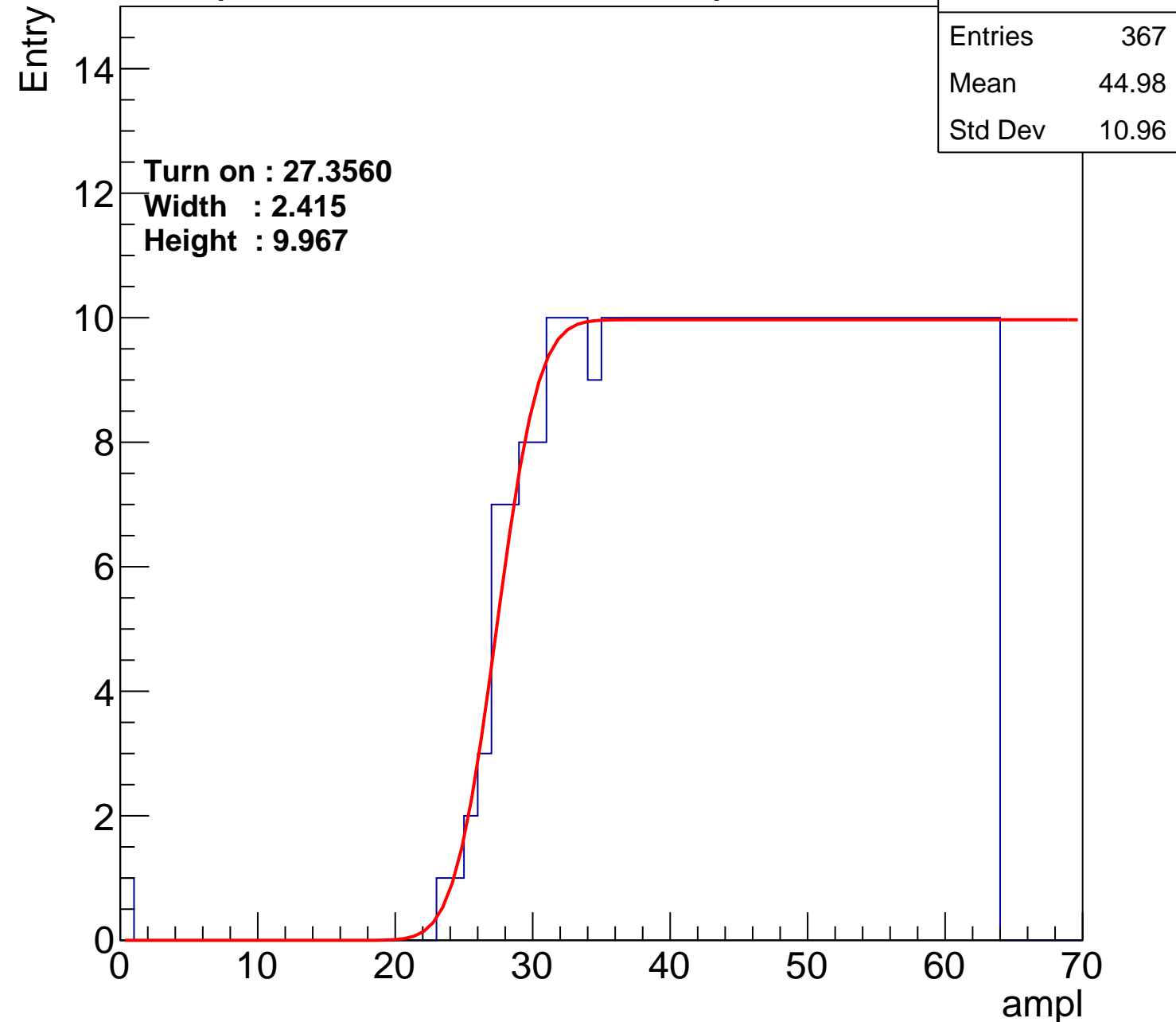
Width : 2.415

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch50

calib_packv5_042523_0143.root, FC#10, port B3

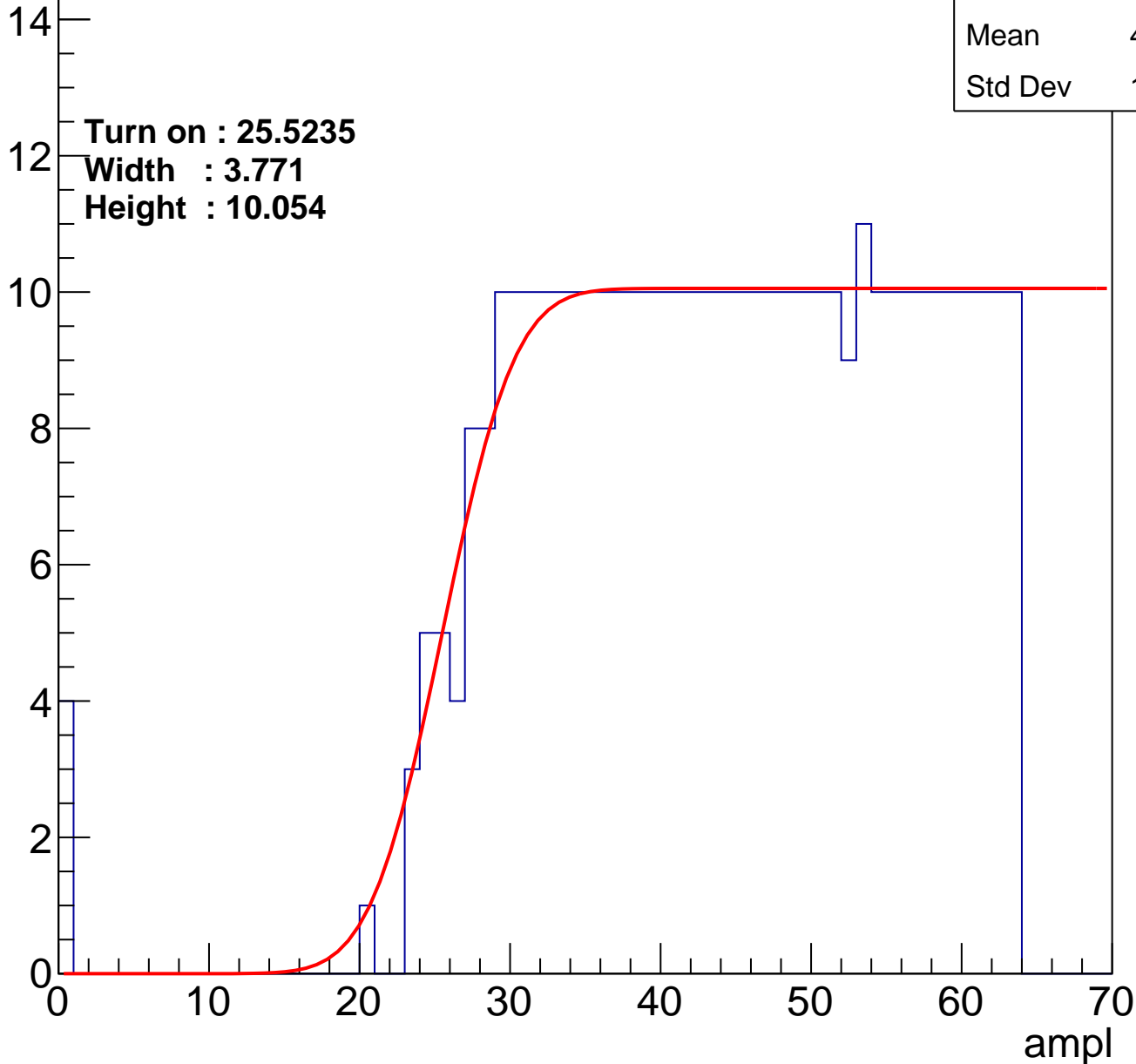
Entries	388
Mean	43.76
Std Dev	12.04

Turn on : 25.5235

Width : 3.771

Height : 10.054

Entry



B1L002S, U22-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.67
Std Dev	12.9

Turn on : 24.8805

Width : 1.507

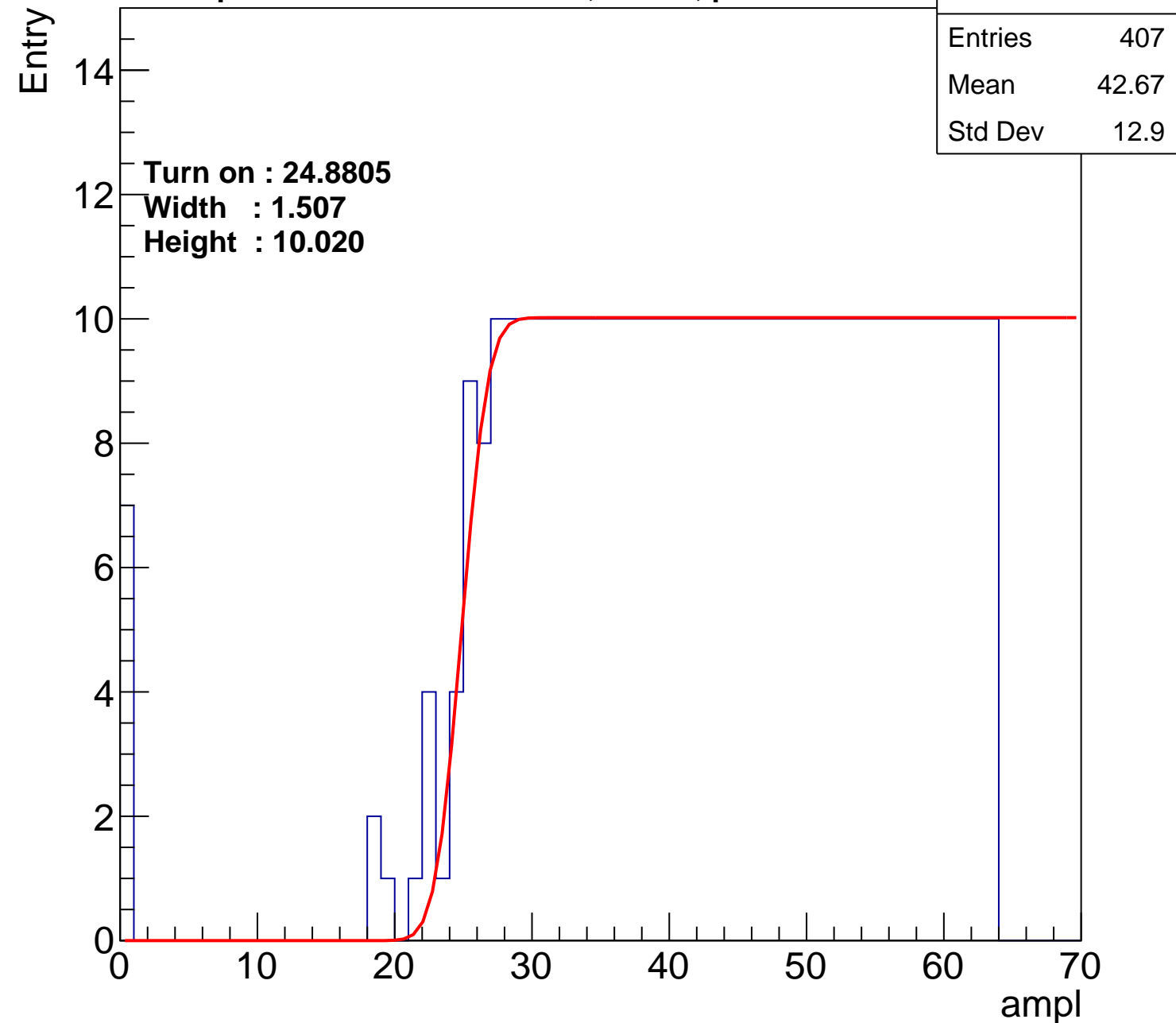
Height : 10.020

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L002S, U22-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 24.9187

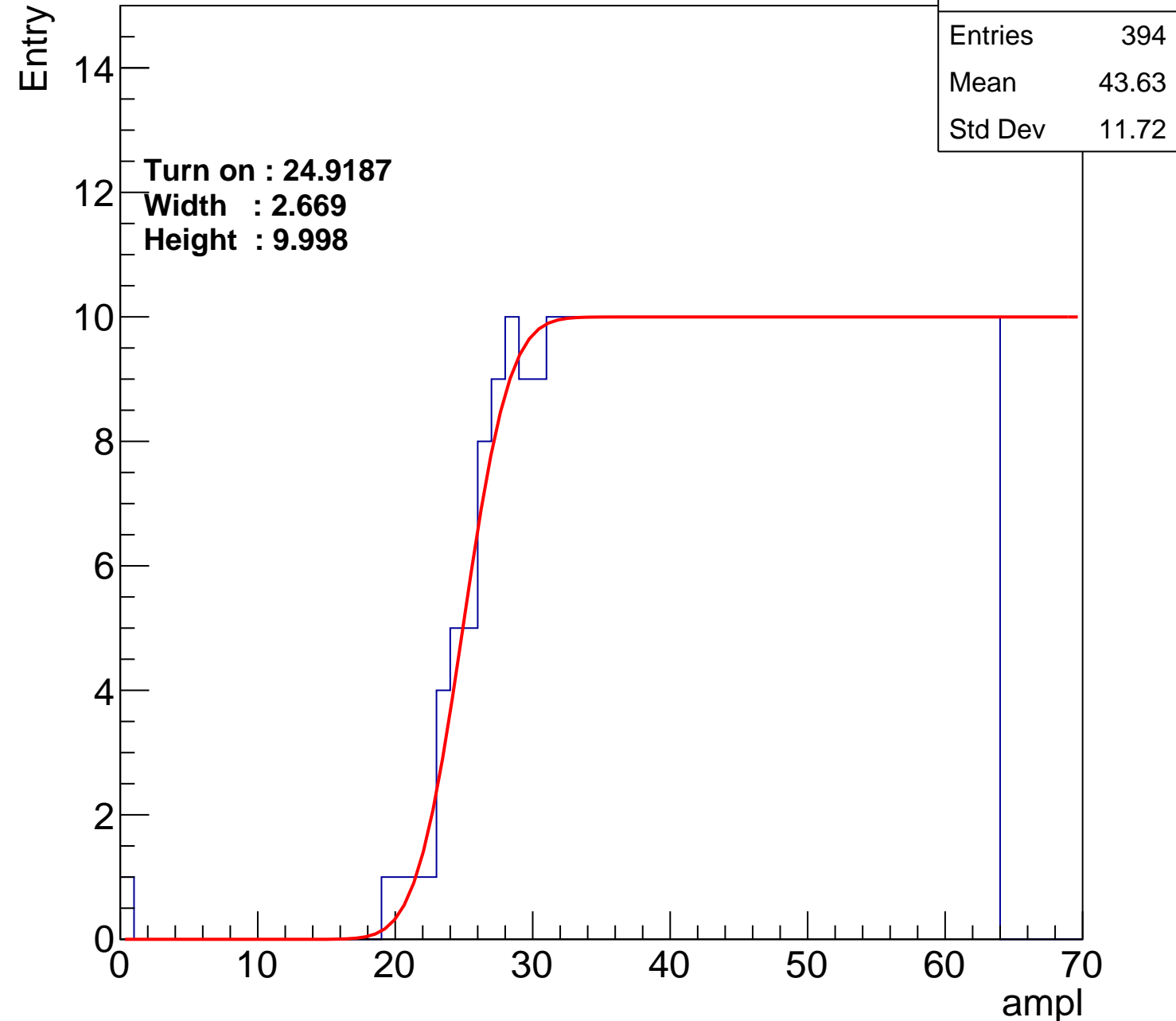
Width : 2.669

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.9279

Width : 2.452

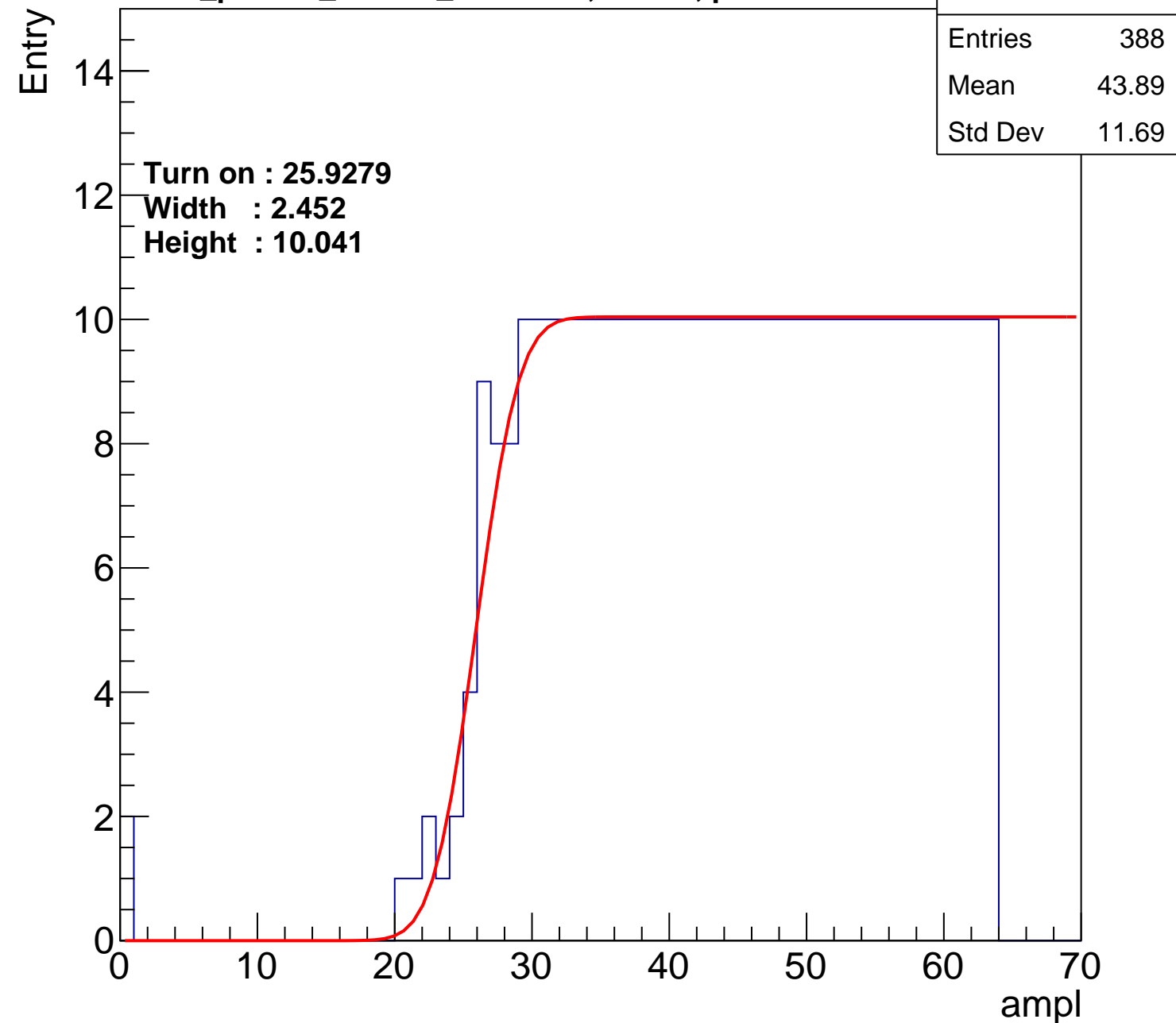
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



calib_packv5_042523_0143.root, FC#10, port B3

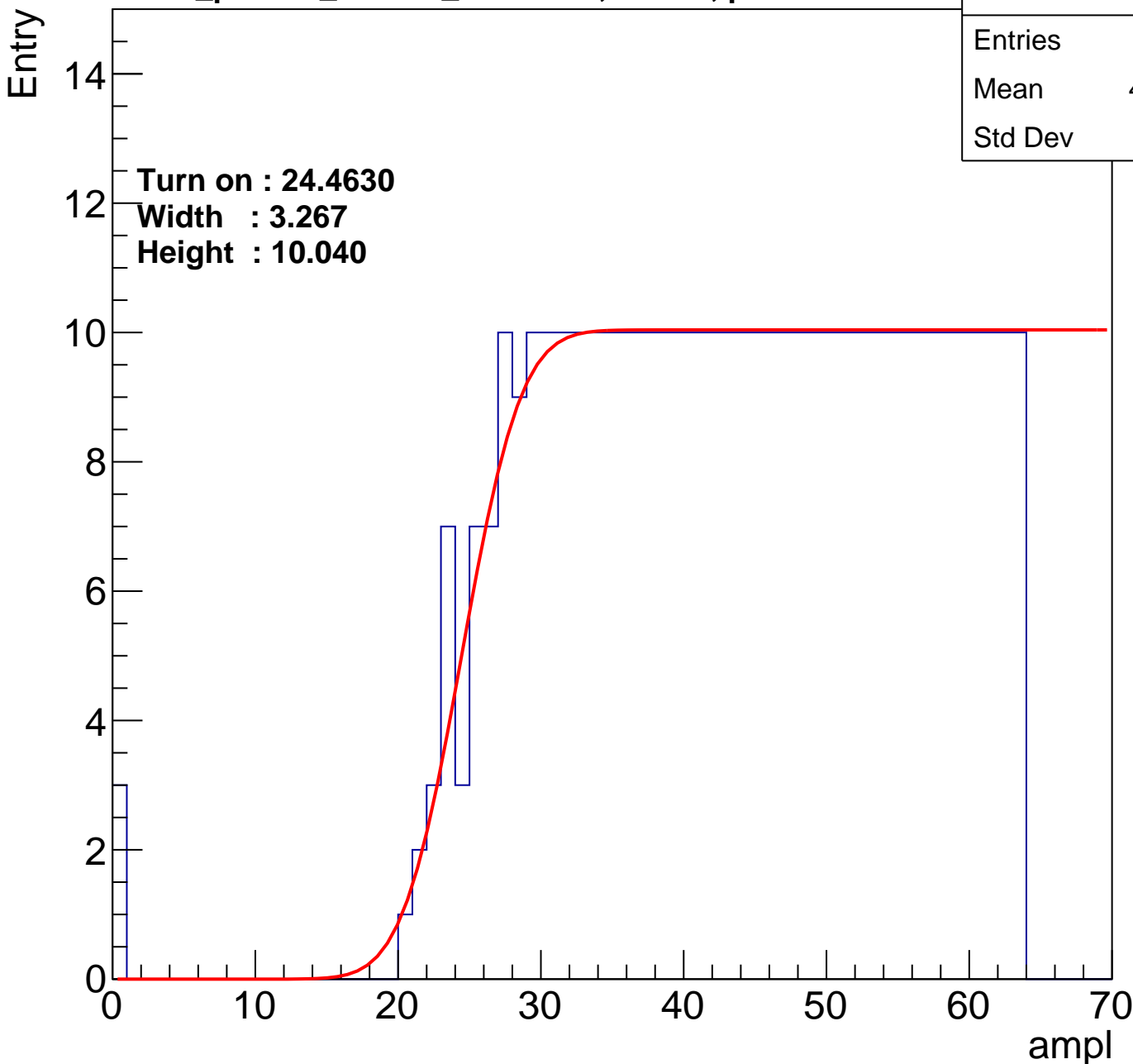
Entries	402
Mean	43.13
Std Dev	12.21

Mean	43.13
------	-------

Std Dev	12.21
---------	-------

Width : 3.267

Height : 10.040



B1L002S, U22-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.5788

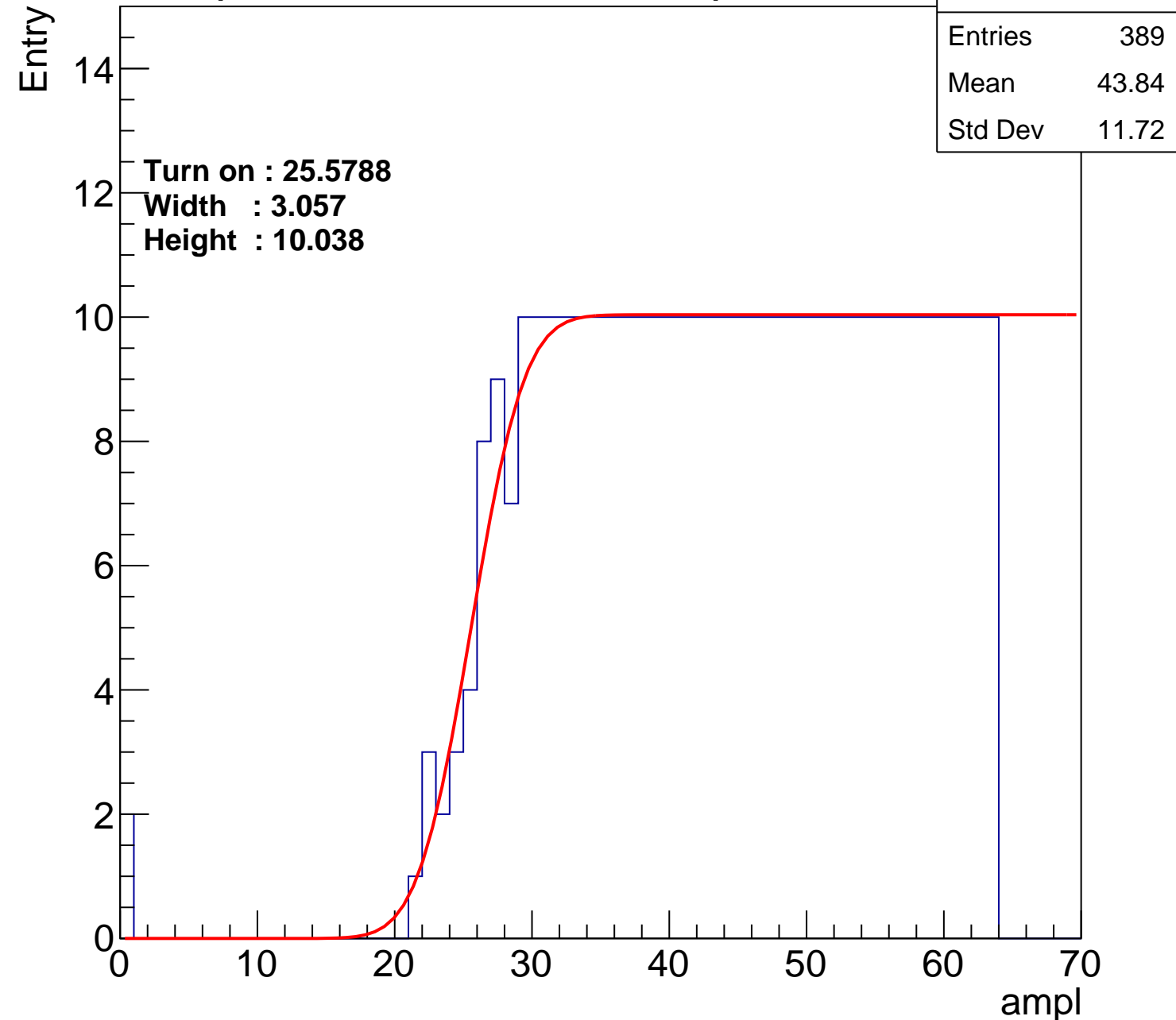
Width : 3.057

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch56

calib_packv5_042523_0143.root, FC#10, port B3

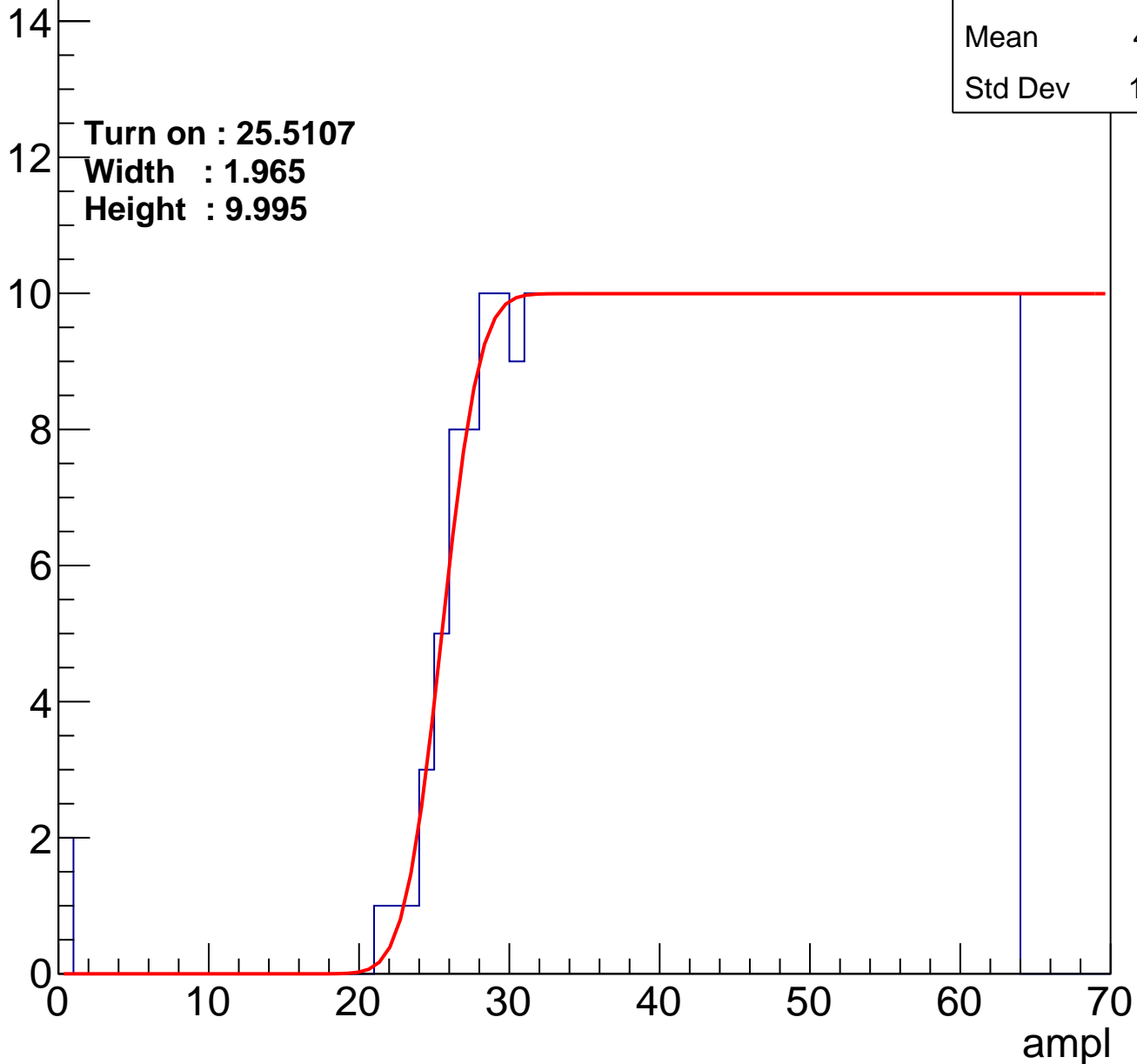
Entries	388
Mean	43.91
Std Dev	11.65

Turn on : 25.5107

Width : 1.965

Height : 9.995

Entry



B1L002S, U22-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.34
Std Dev	11.35

Turn on : 25.9067

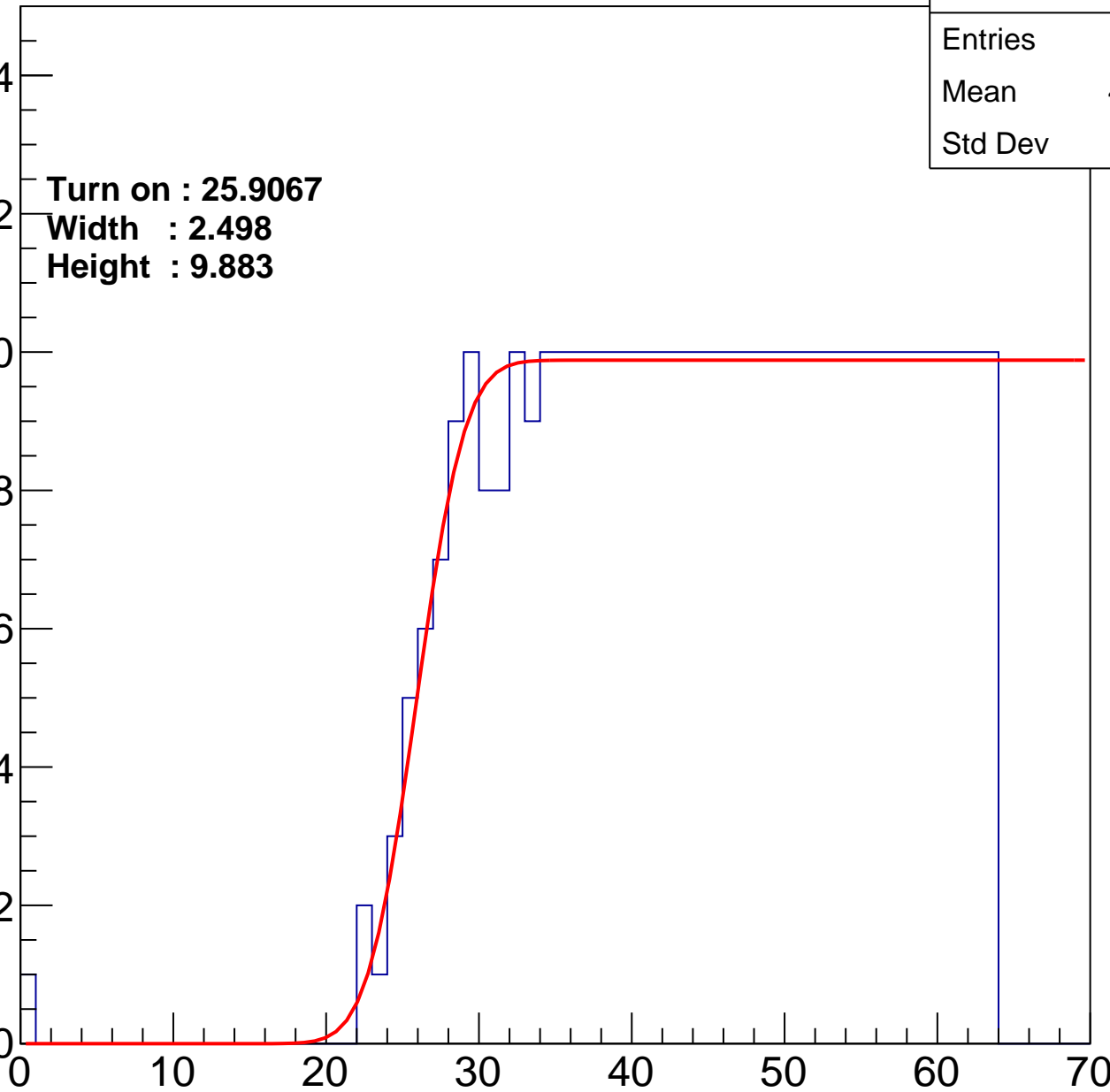
Width : 2.498

Height : 9.883

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch58

calib_packv5_042523_0143.root, FC#10, port B3

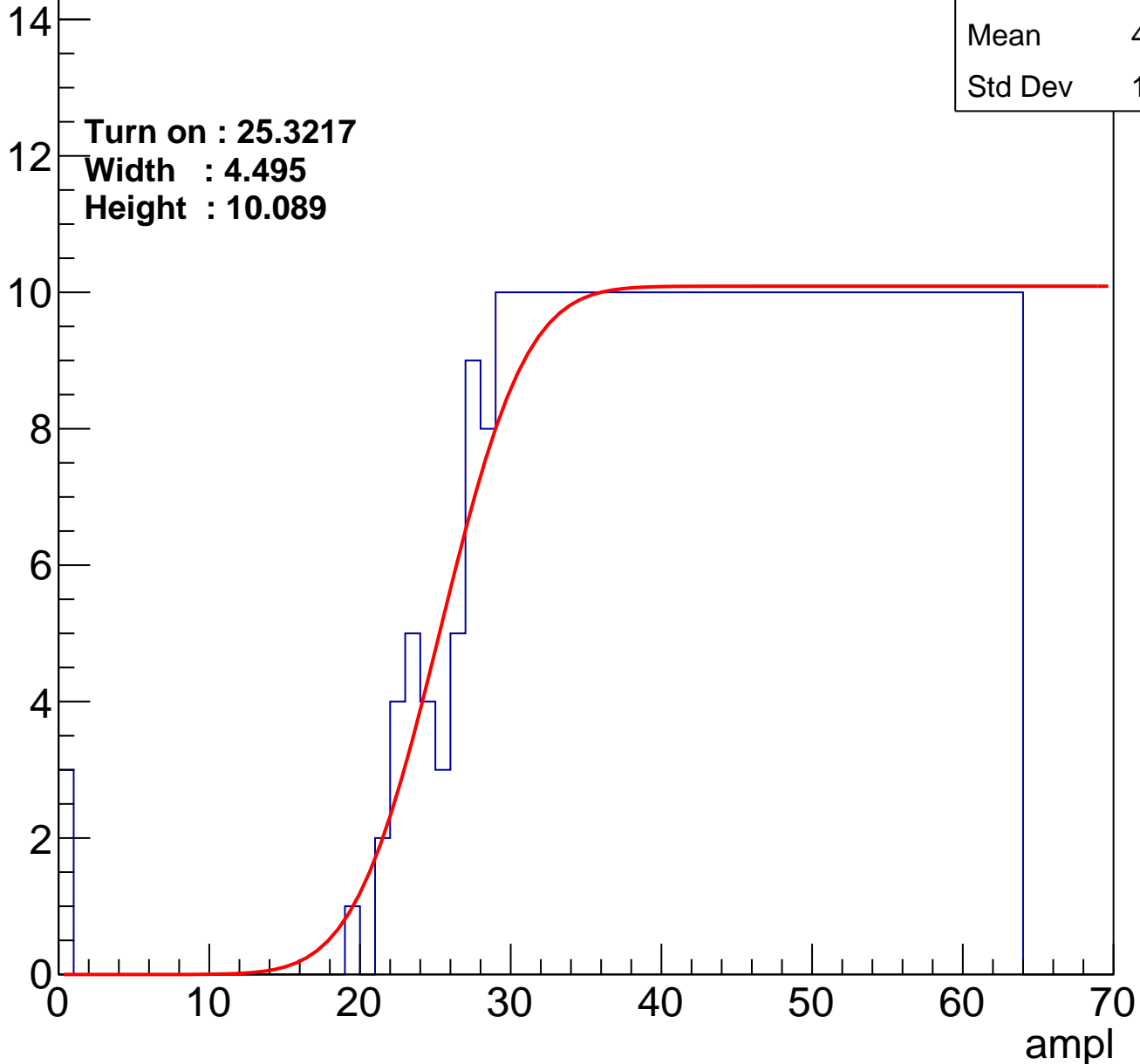
Entries	394
Mean	43.48
Std Dev	12.09

Turn on : 25.3217

Width : 4.495

Height : 10.089

Entry



B1L002S, U22-ch59

calib_packv5_042523_0143.root, FC#10, port B3

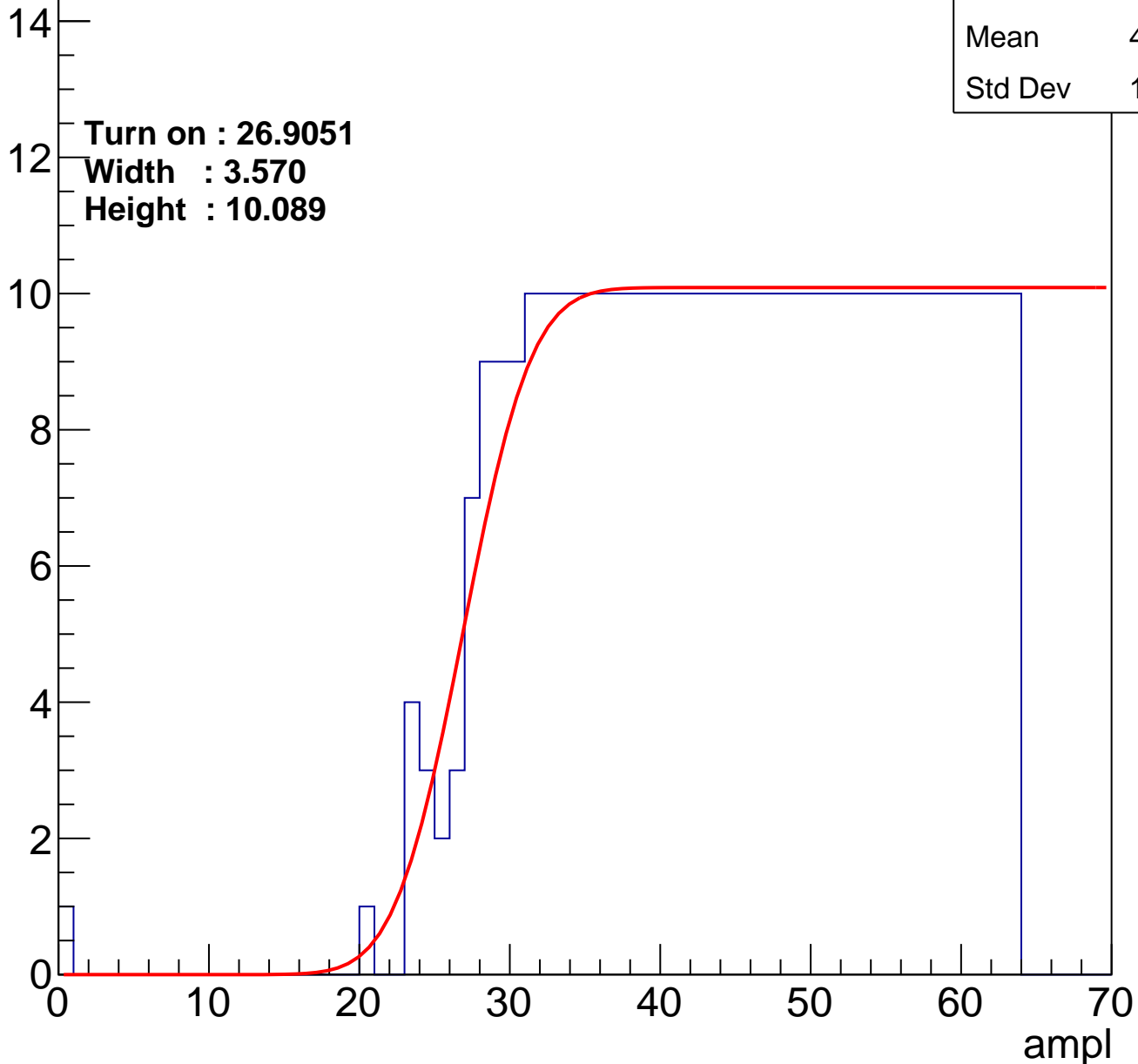
Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.9051

Width : 3.570

Height : 10.089

Entry



B1L002S, U22-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.7172

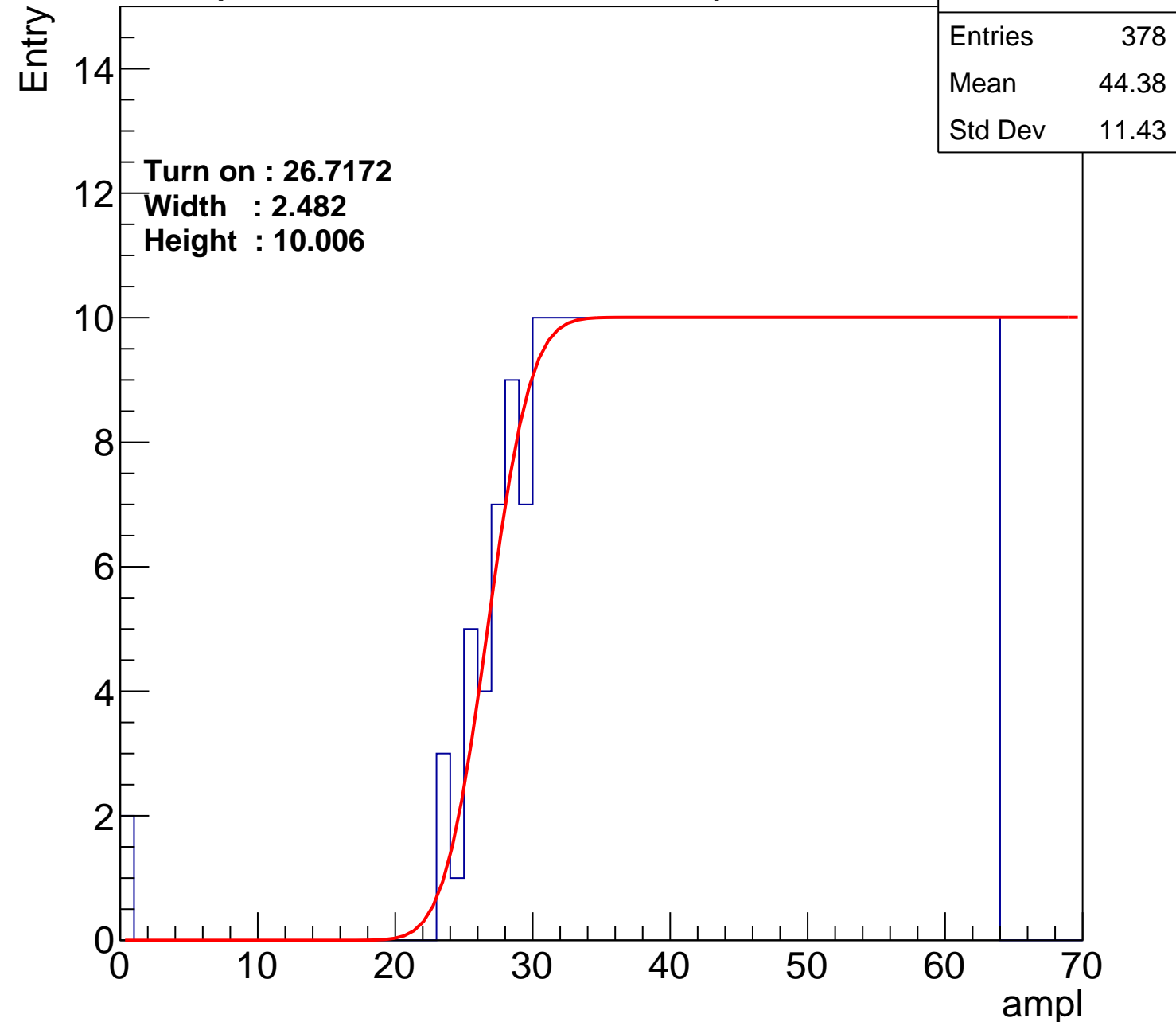
Width : 2.482

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.4539

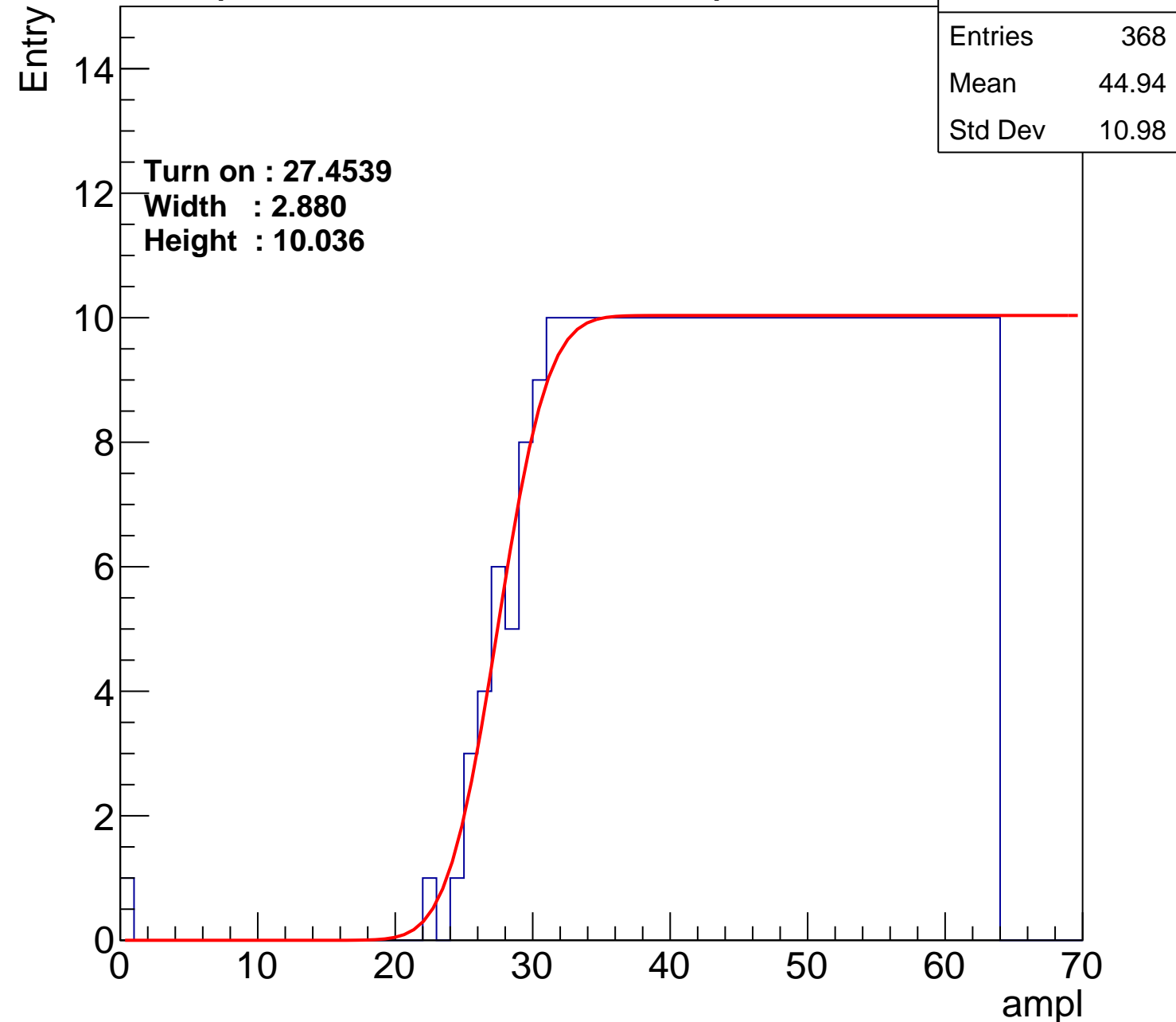
Width : 2.880

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.7
Std Dev	11.67

Turn on : 24.7581

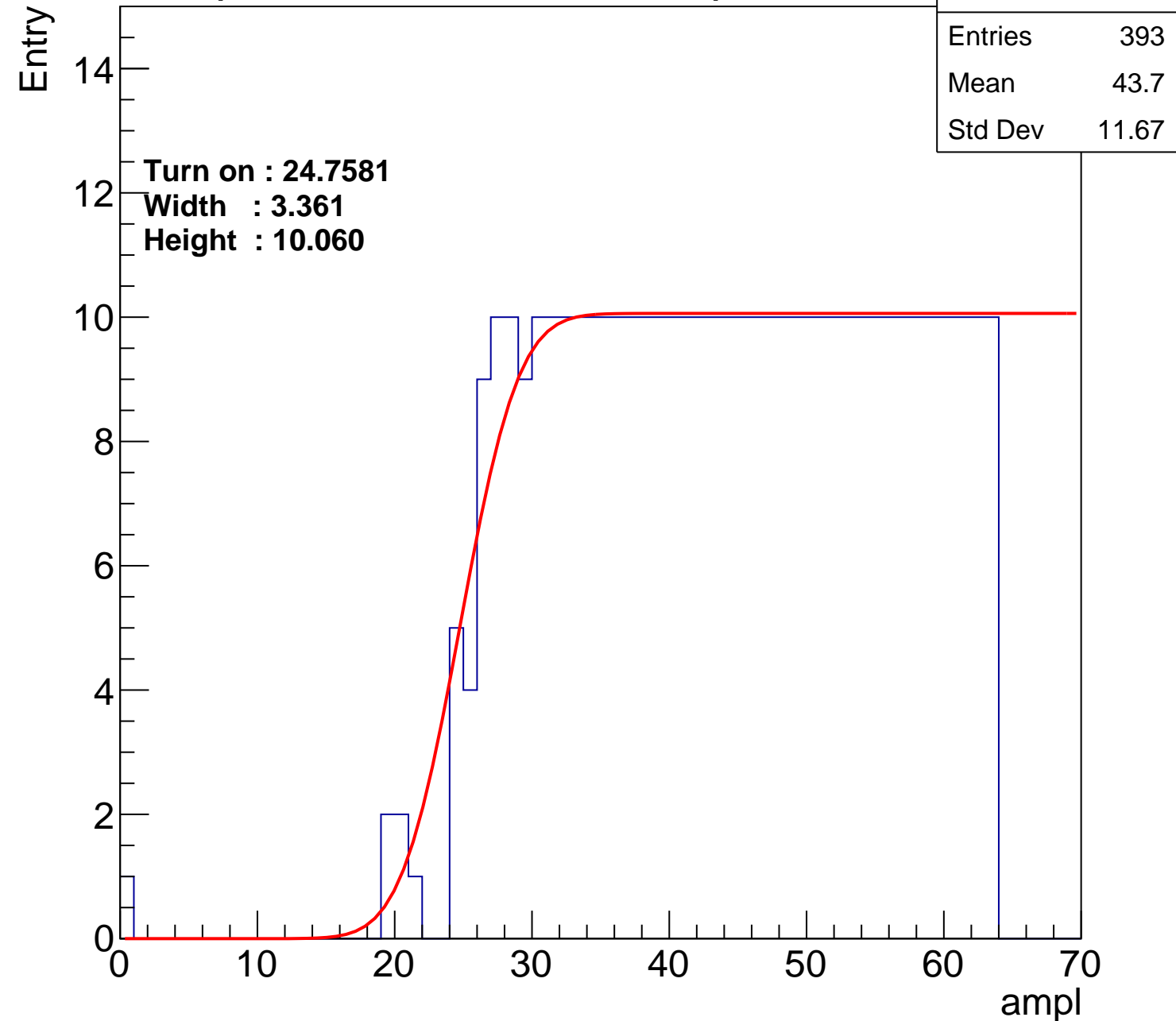
Width : 3.361

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch63

calib_packv5_042523_0143.root, FC#10, port B3

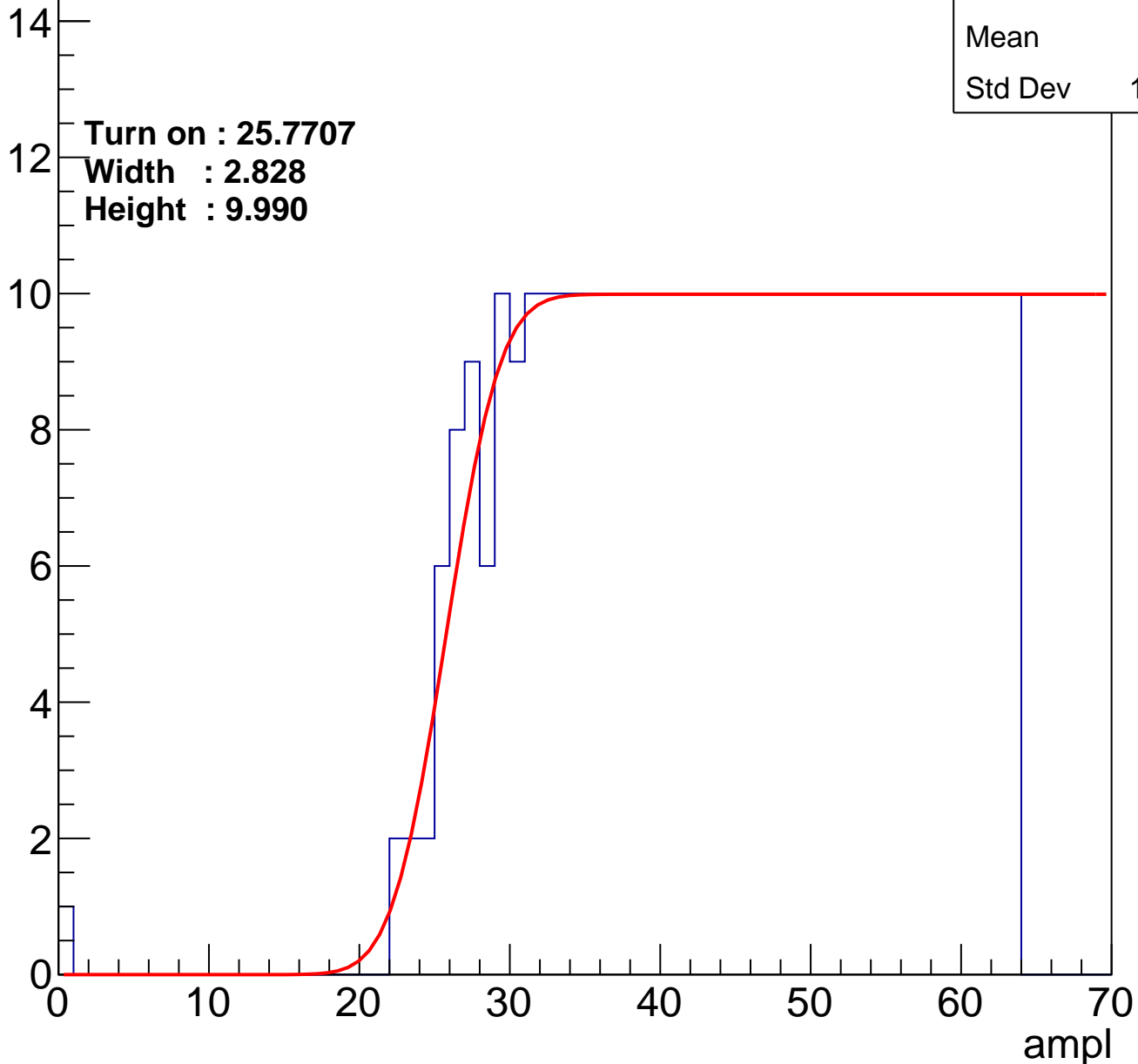
Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.7707

Width : 2.828

Height : 9.990

Entry



B1L002S, U22-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.25
Std Dev	12.24

Turn on : 24.6230

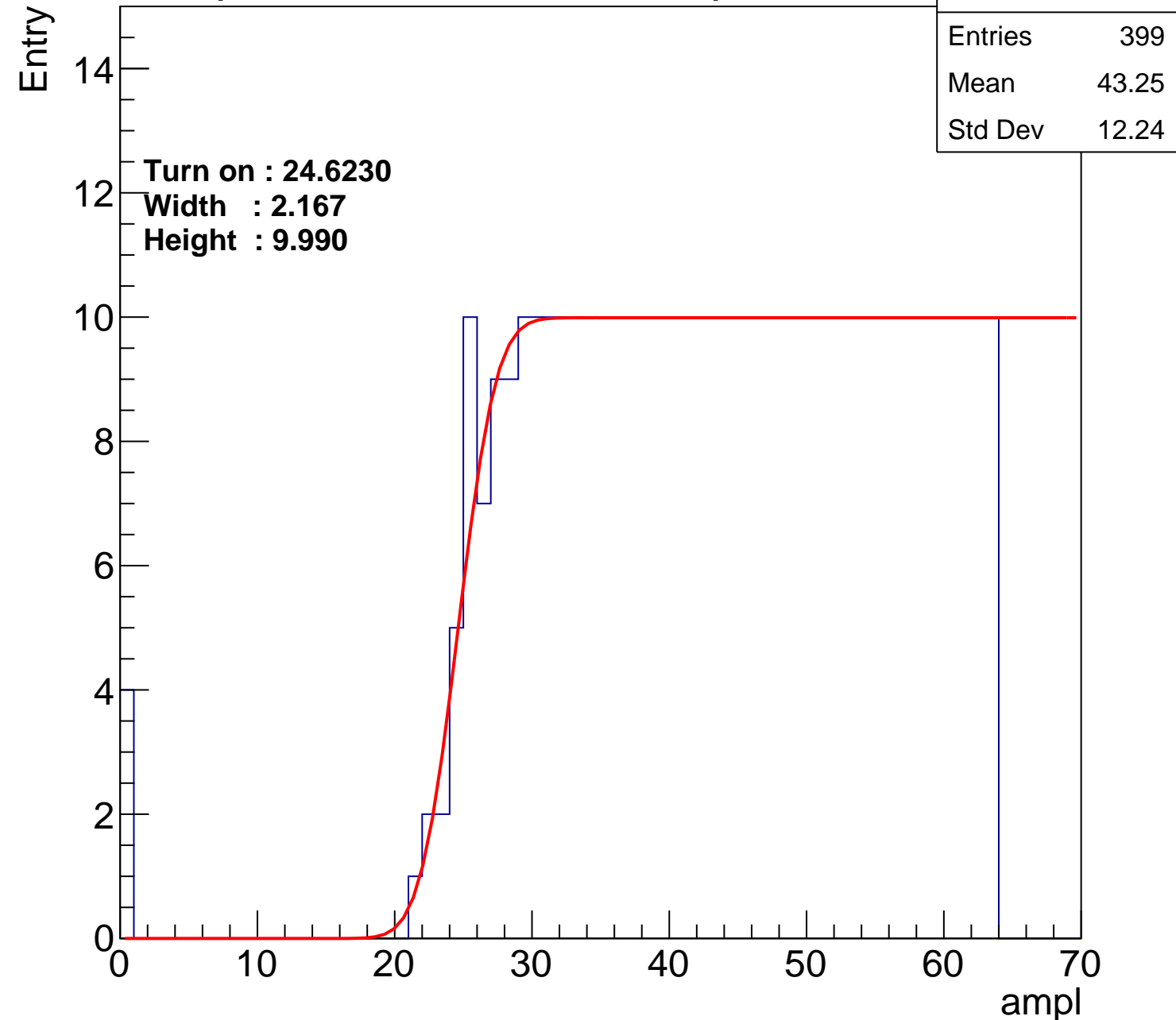
Width : 2.167

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch65

calib_packv5_042523_0143.root, FC#10, port B3

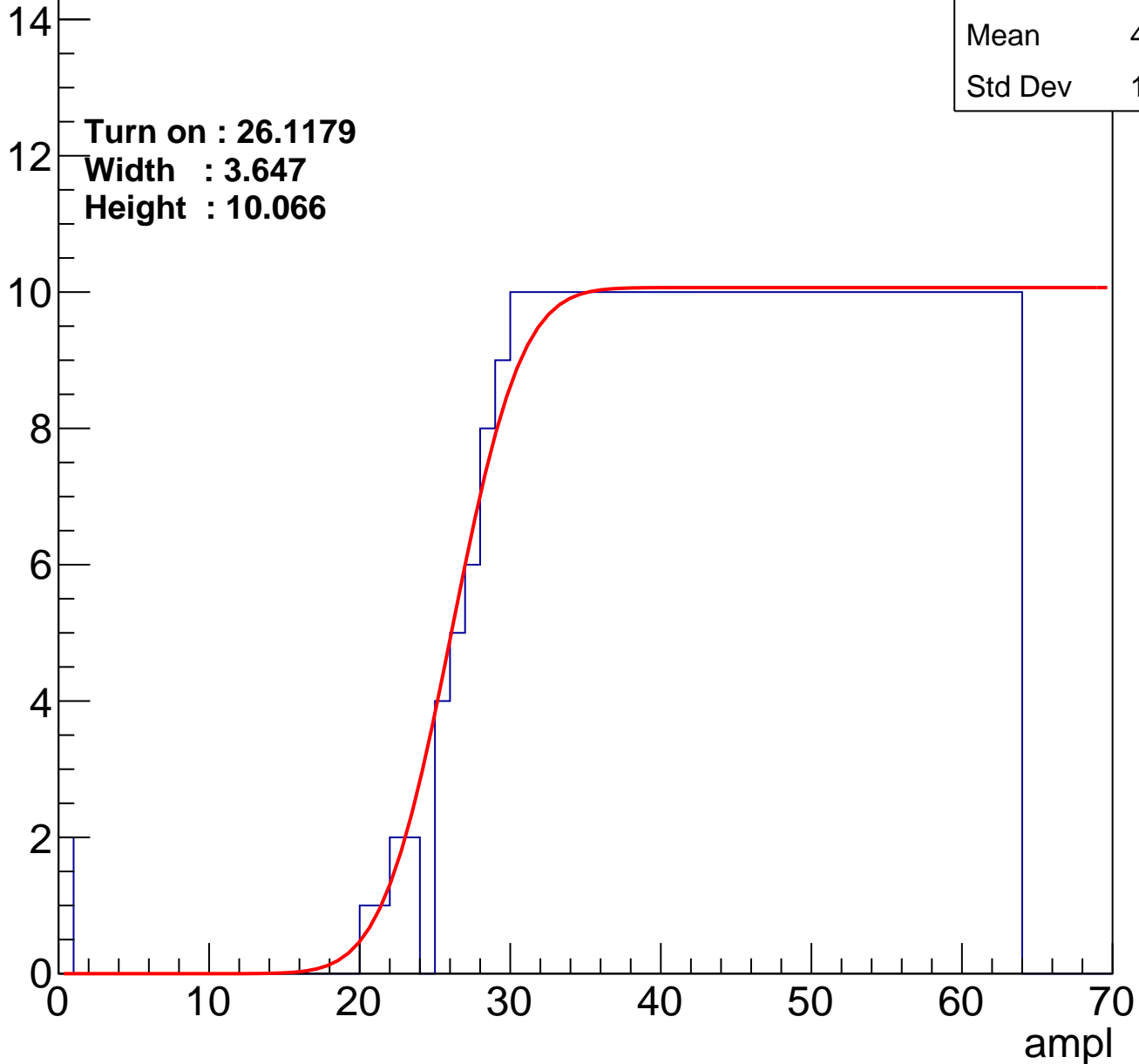
Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.1179

Width : 3.647

Height : 10.066

Entry



B1L002S, U22-ch66

calib_packv5_042523_0143.root, FC#10, port B3

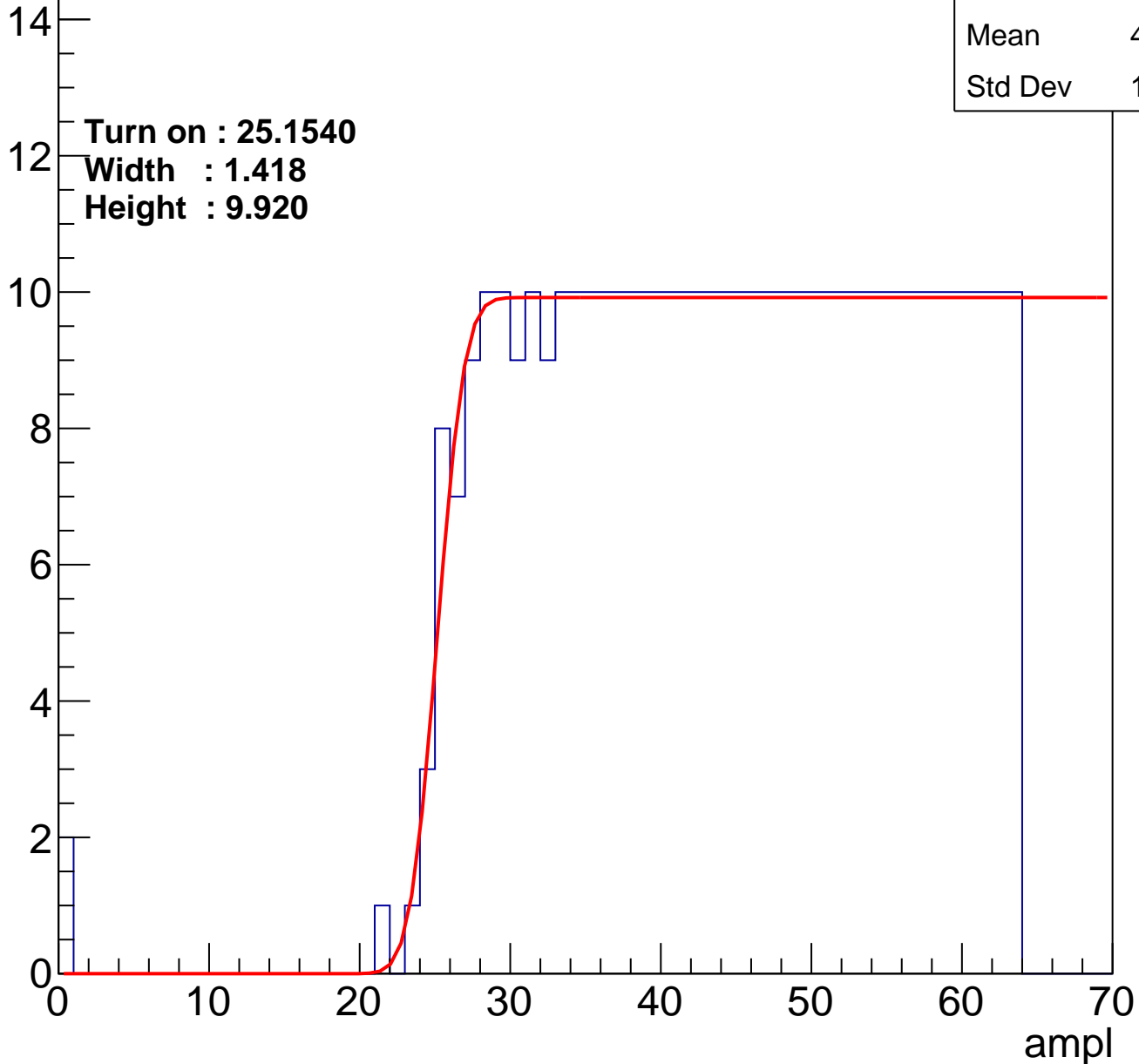
Entries	389
Mean	43.85
Std Dev	11.68

Turn on : 25.1540

Width : 1.418

Height : 9.920

Entry



B1L002S, U22-ch67

calib_packv5_042523_0143.root, FC#10, port B3

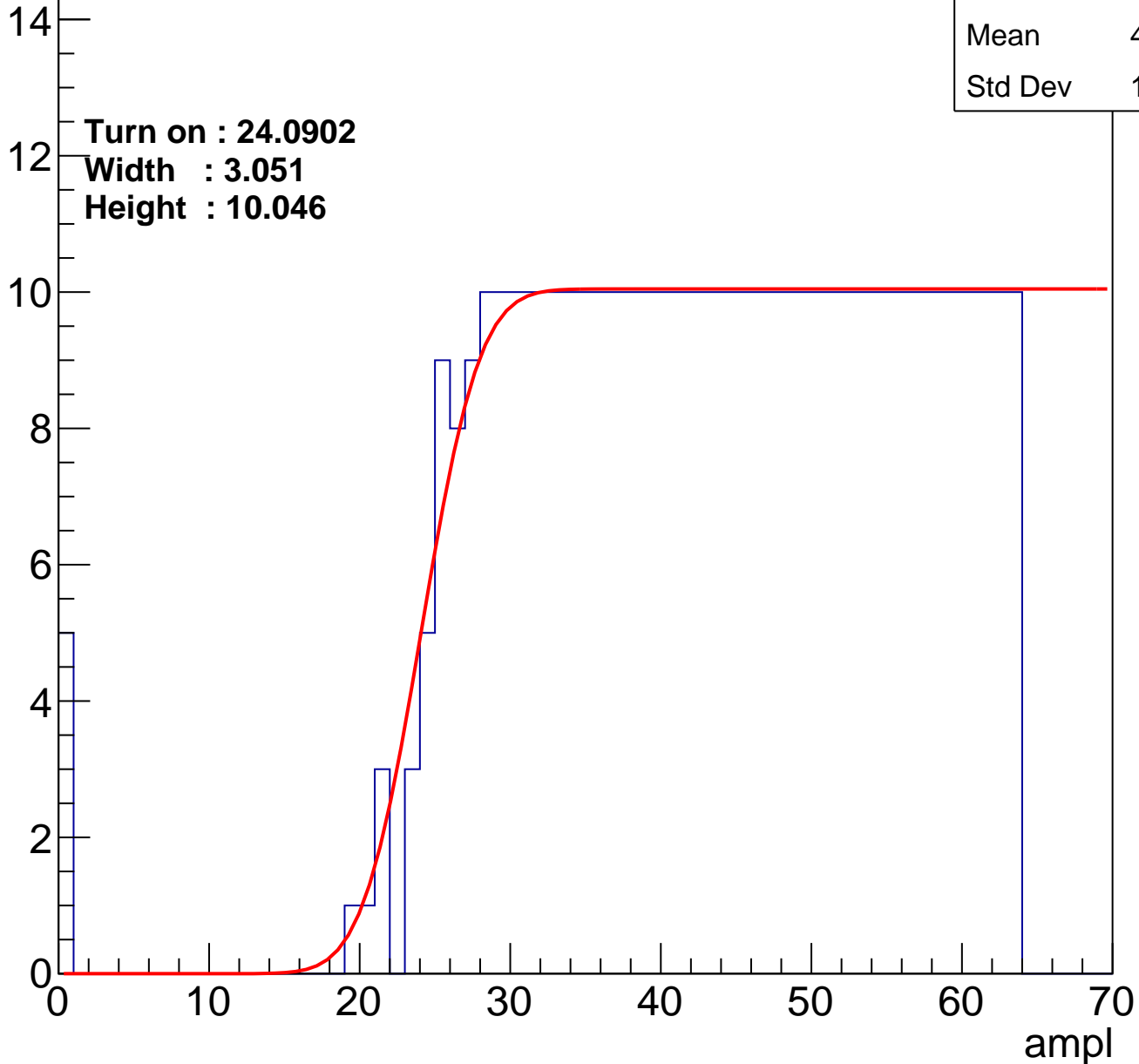
Entries	404
Mean	42.94
Std Dev	12.53

Turn on : 24.0902

Width : 3.051

Height : 10.046

Entry



B1L002S, U22-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.77
Std Dev	11.66

Turn on : 26.1912

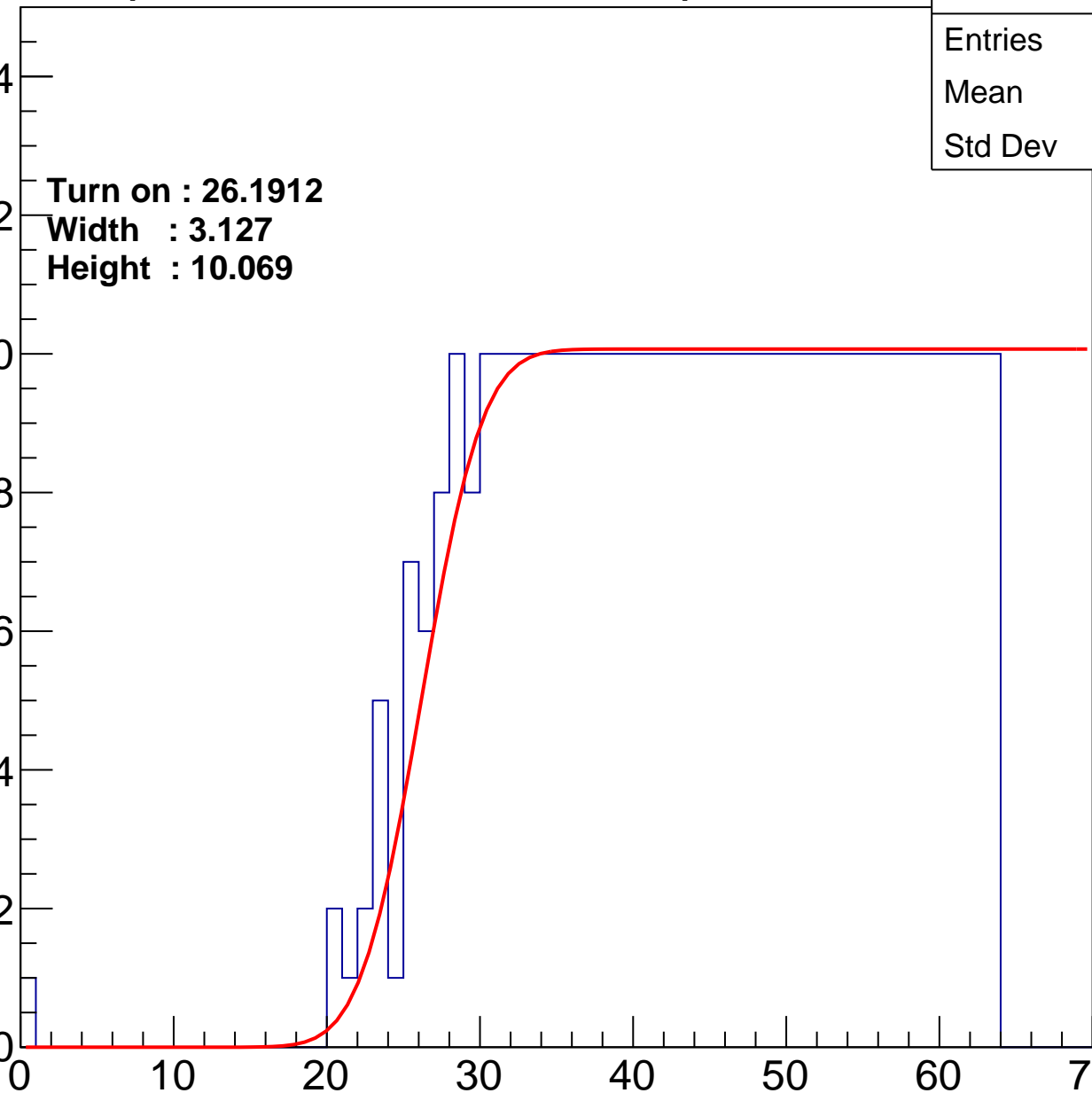
Width : 3.127

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch69

calib_packv5_042523_0143.root, FC#10, port B3

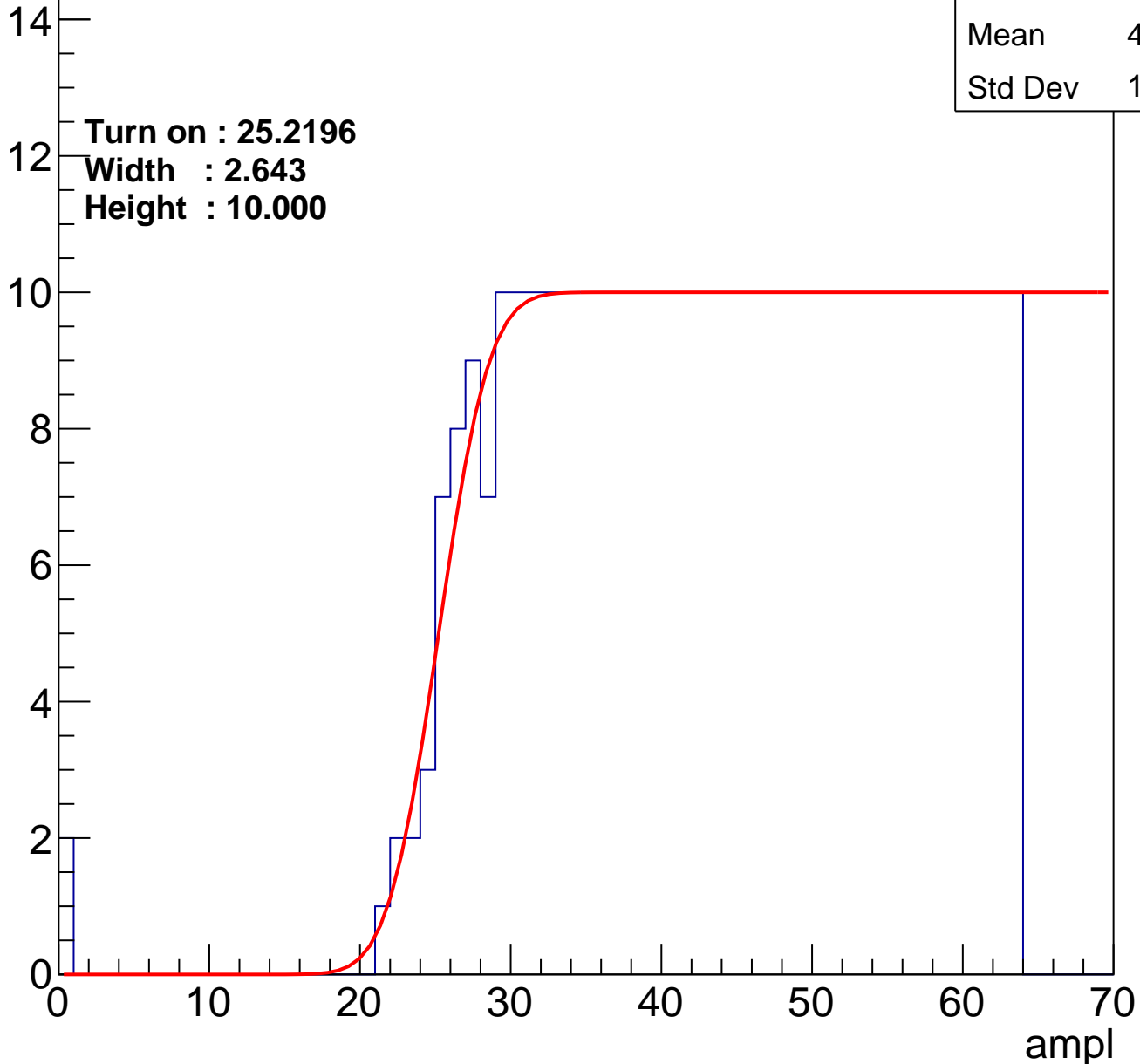
Entries	391
Mean	43.75
Std Dev	11.76

Turn on : 25.2196

Width : 2.643

Height : 10.000

Entry



B1L002S, U22-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.32
Std Dev	11.49

Turn on : 26.7389

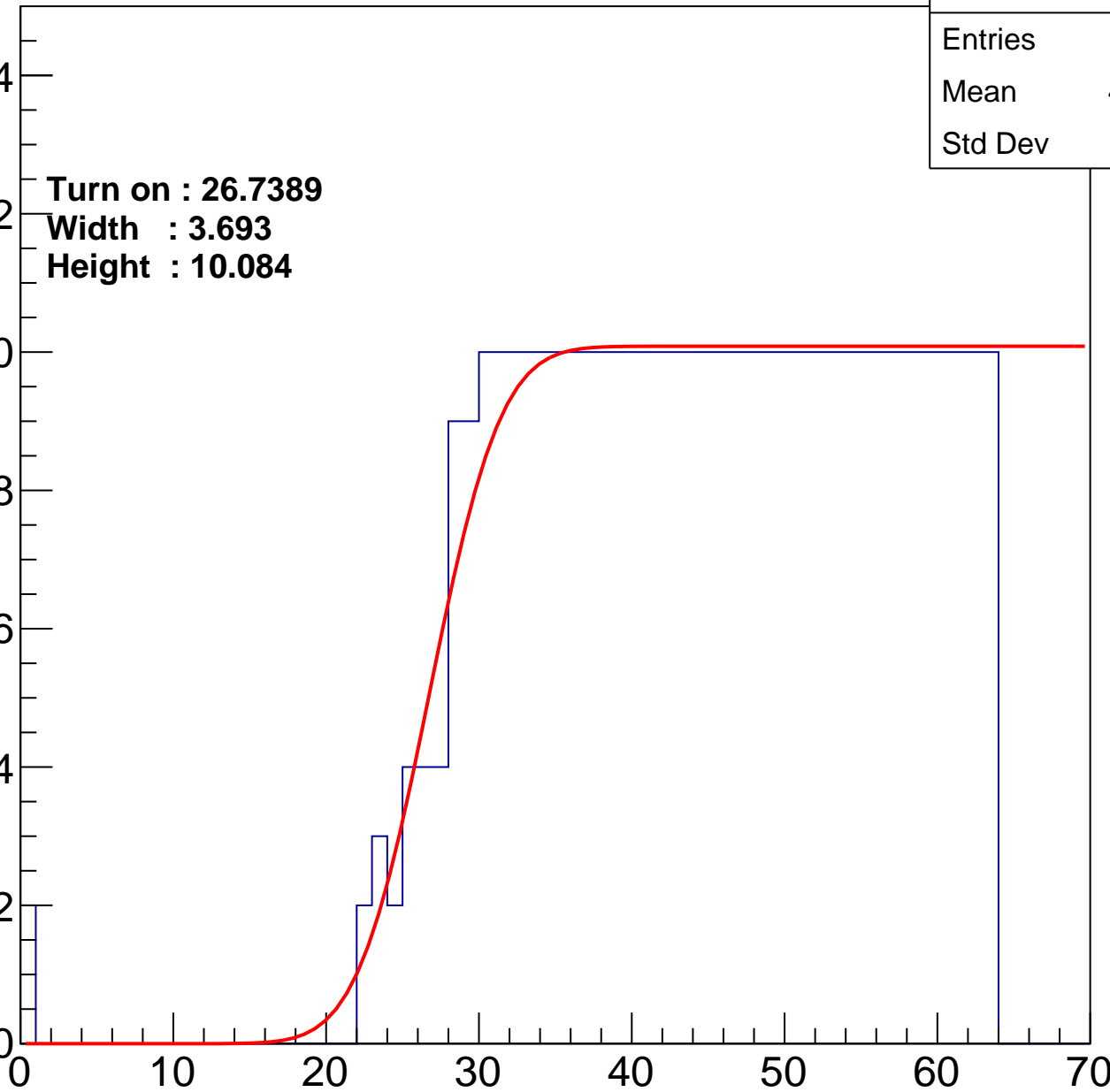
Width : 3.693

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.66
Std Dev	11.44

Turn on : 27.6567

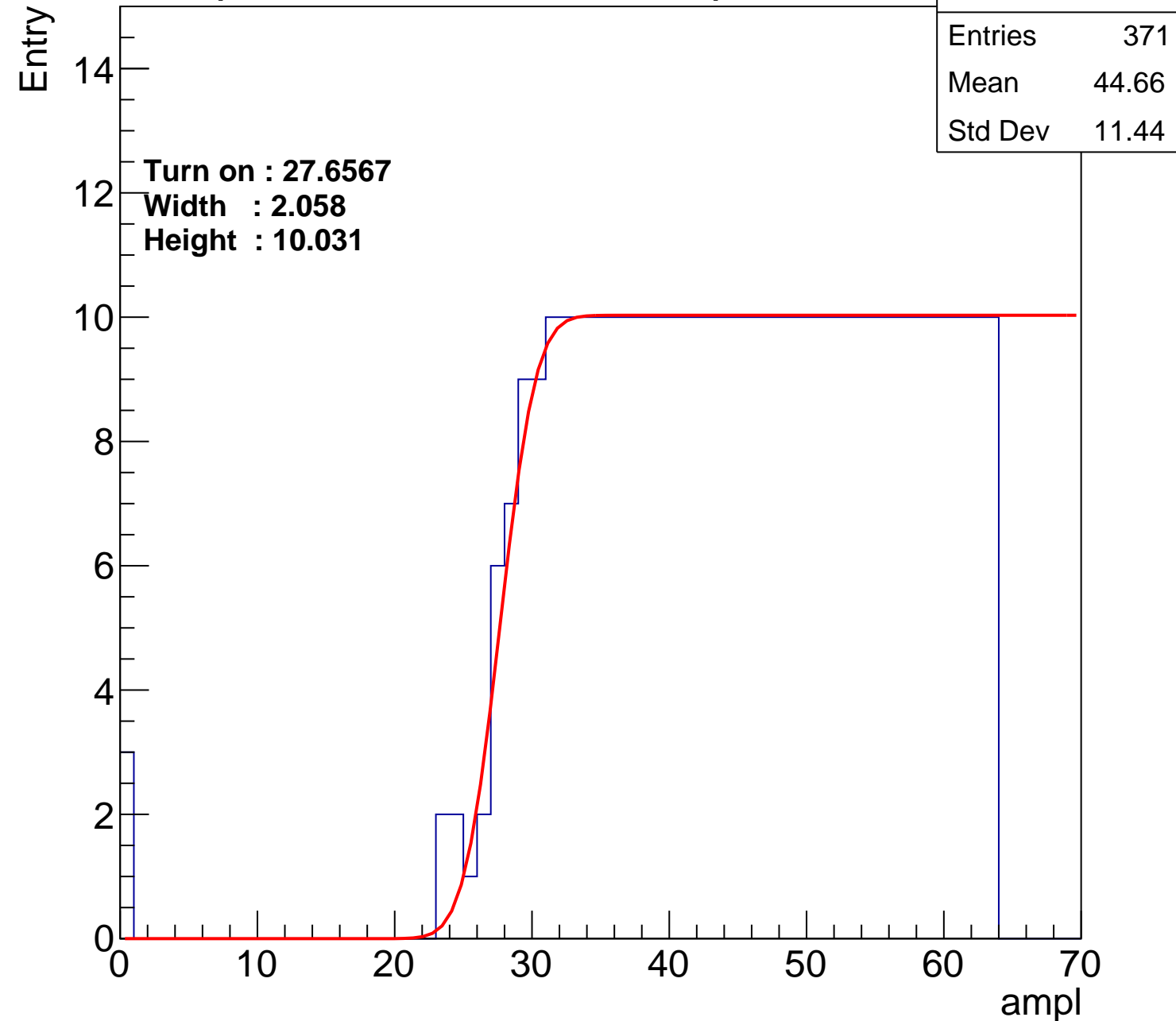
Width : 2.058

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.52
Std Dev	11.8

Turn on : 25.0905

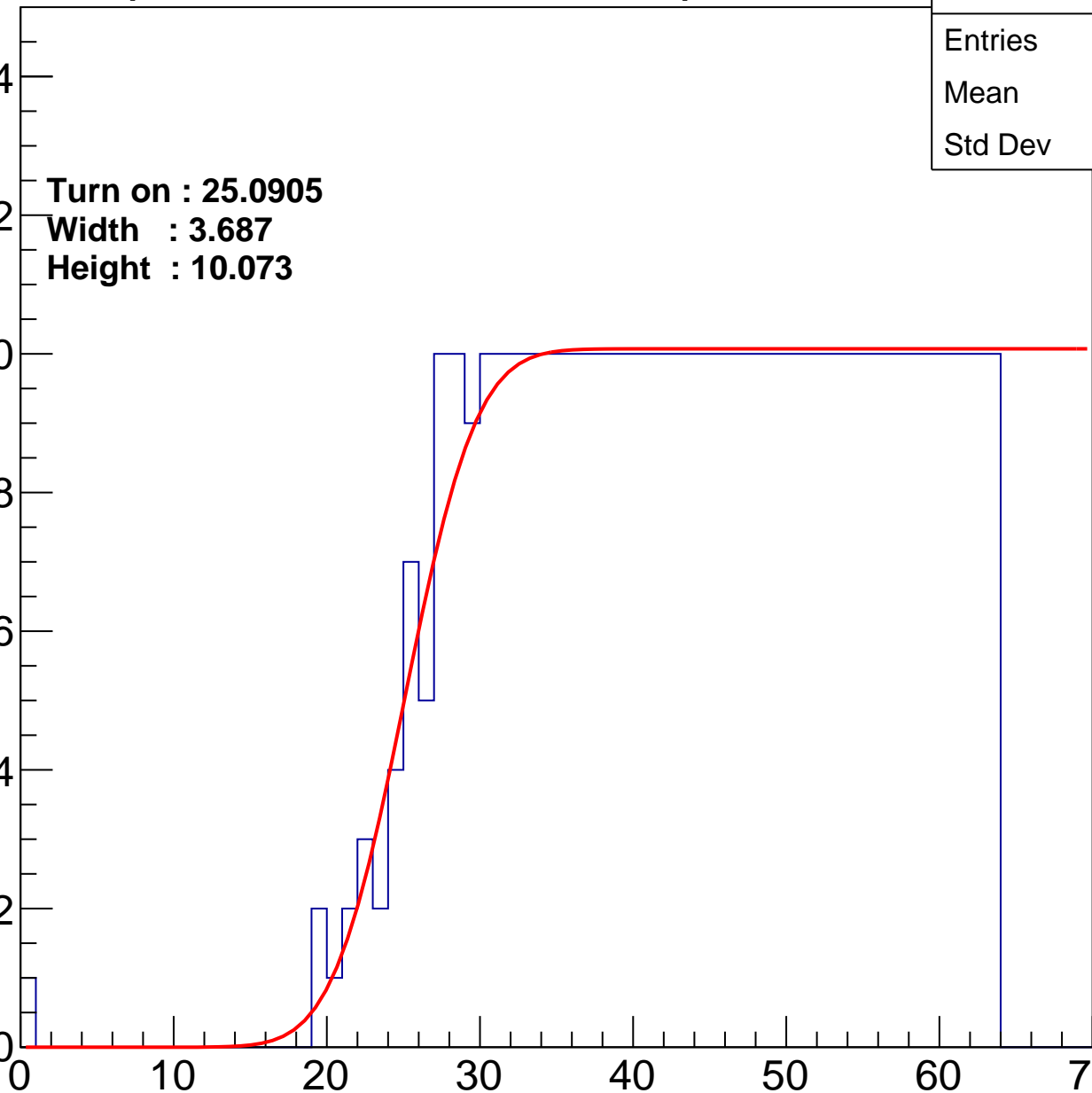
Width : 3.687

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch73

calib_packv5_042523_0143.root, FC#10, port B3

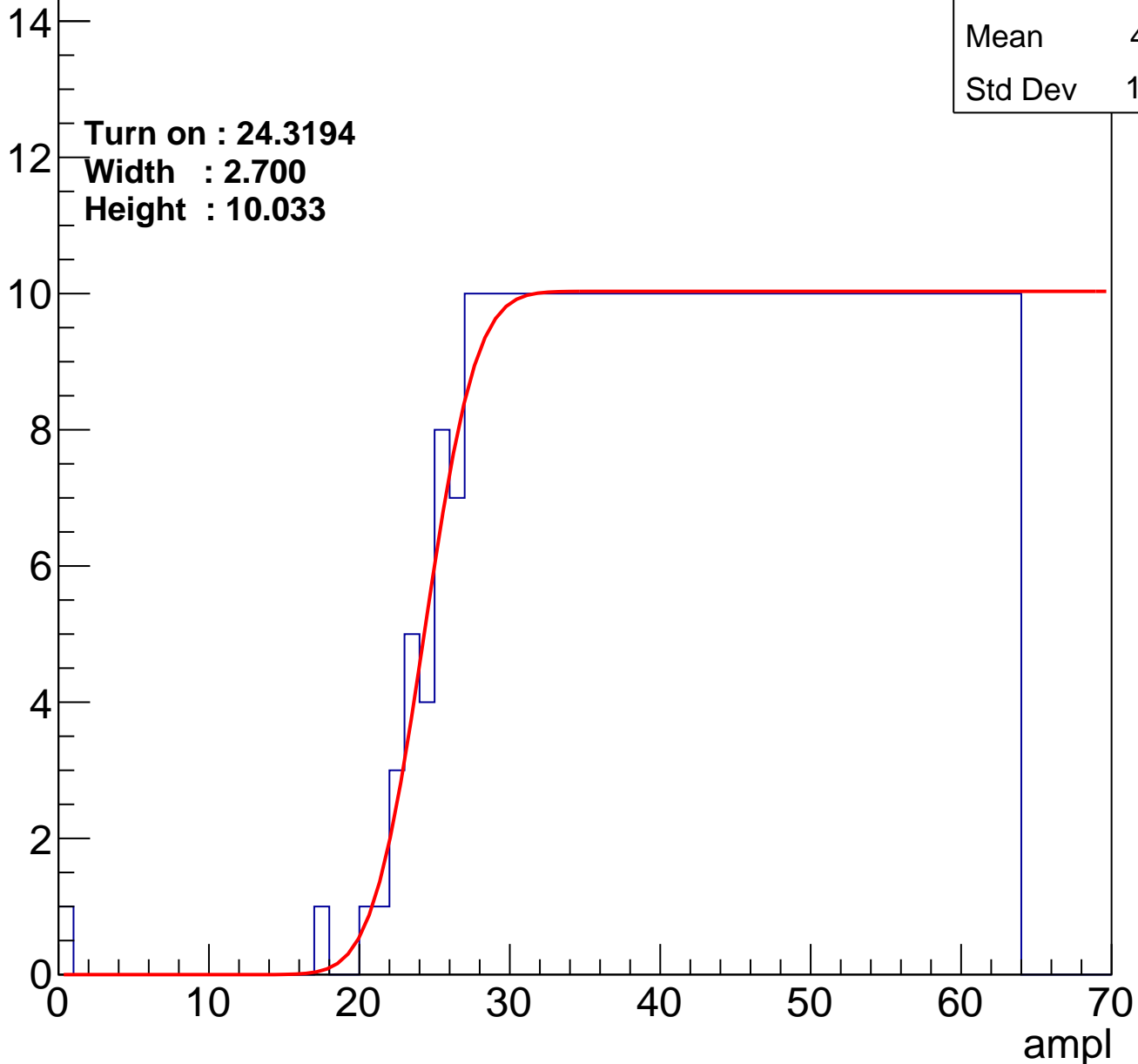
Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 24.3194

Width : 2.700

Height : 10.033

Entry



B1L002S, U22-ch74

calib_packv5_042523_0143.root, FC#10, port B3

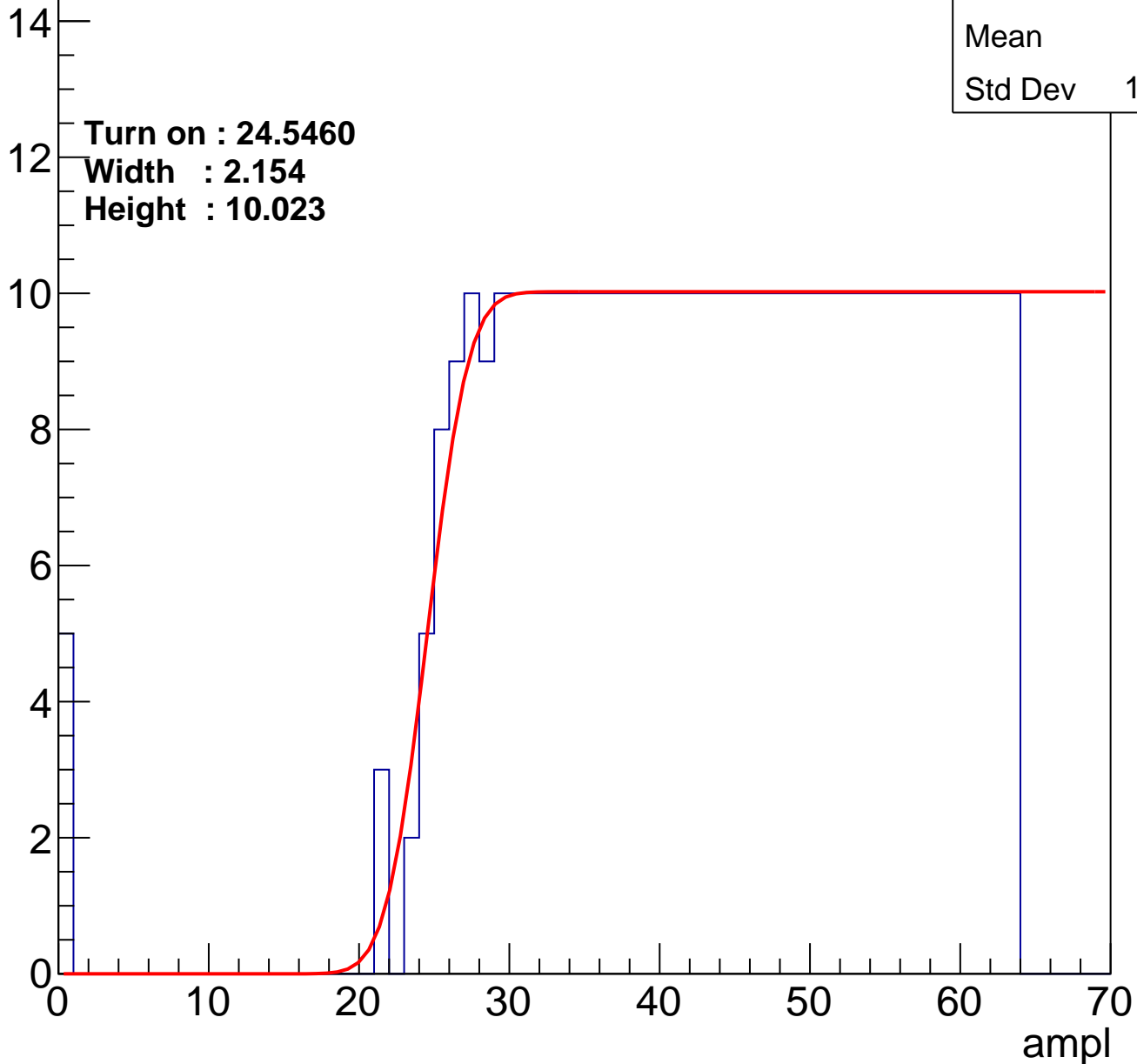
Entries	401
Mean	43.1
Std Dev	12.43

Turn on : 24.5460

Width : 2.154

Height : 10.023

Entry



B1L002S, U22-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.33

Turn on : 26.1033

Width : 3.167

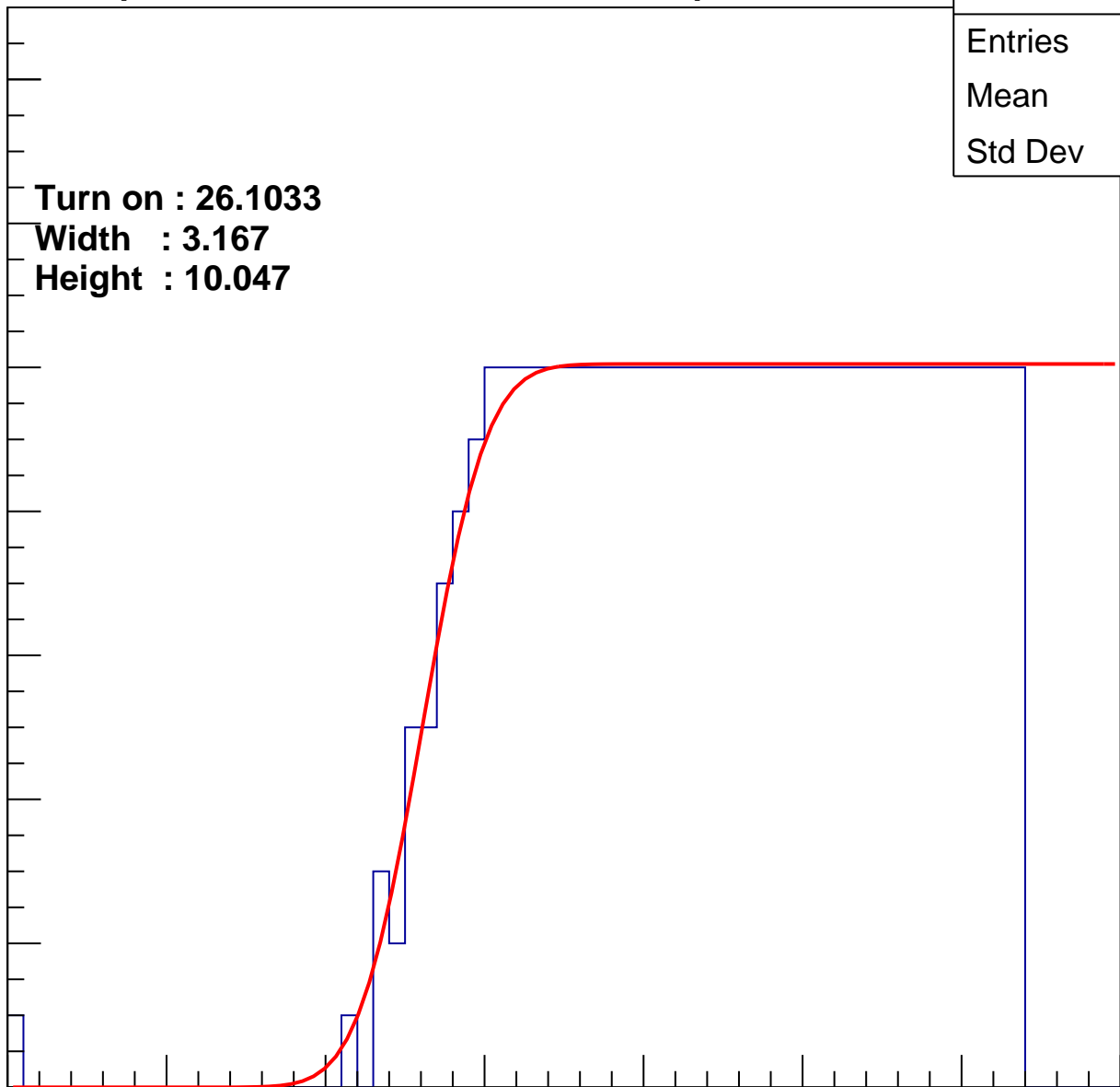
Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U22-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.6467

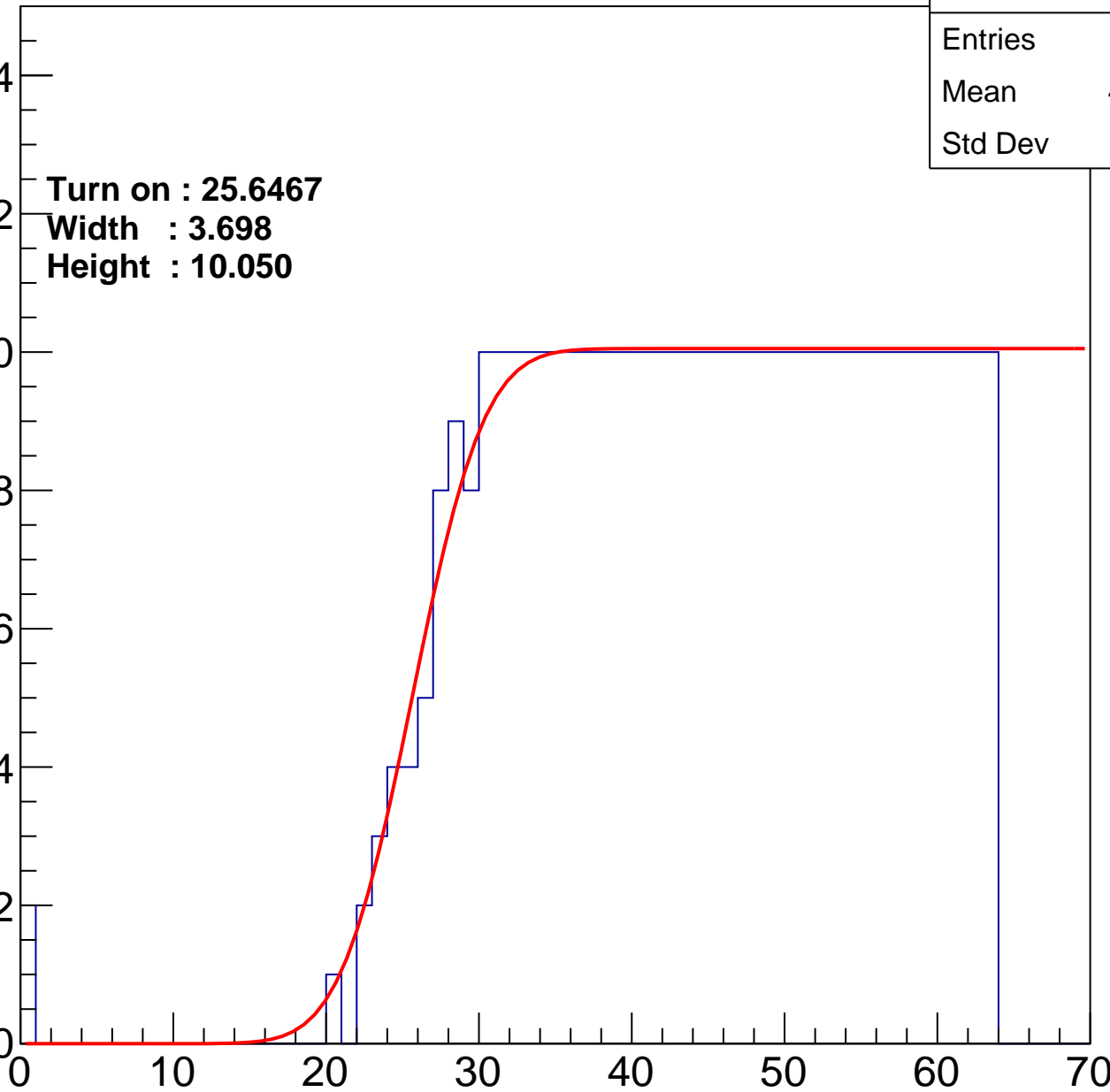
Width : 3.698

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 24.9076

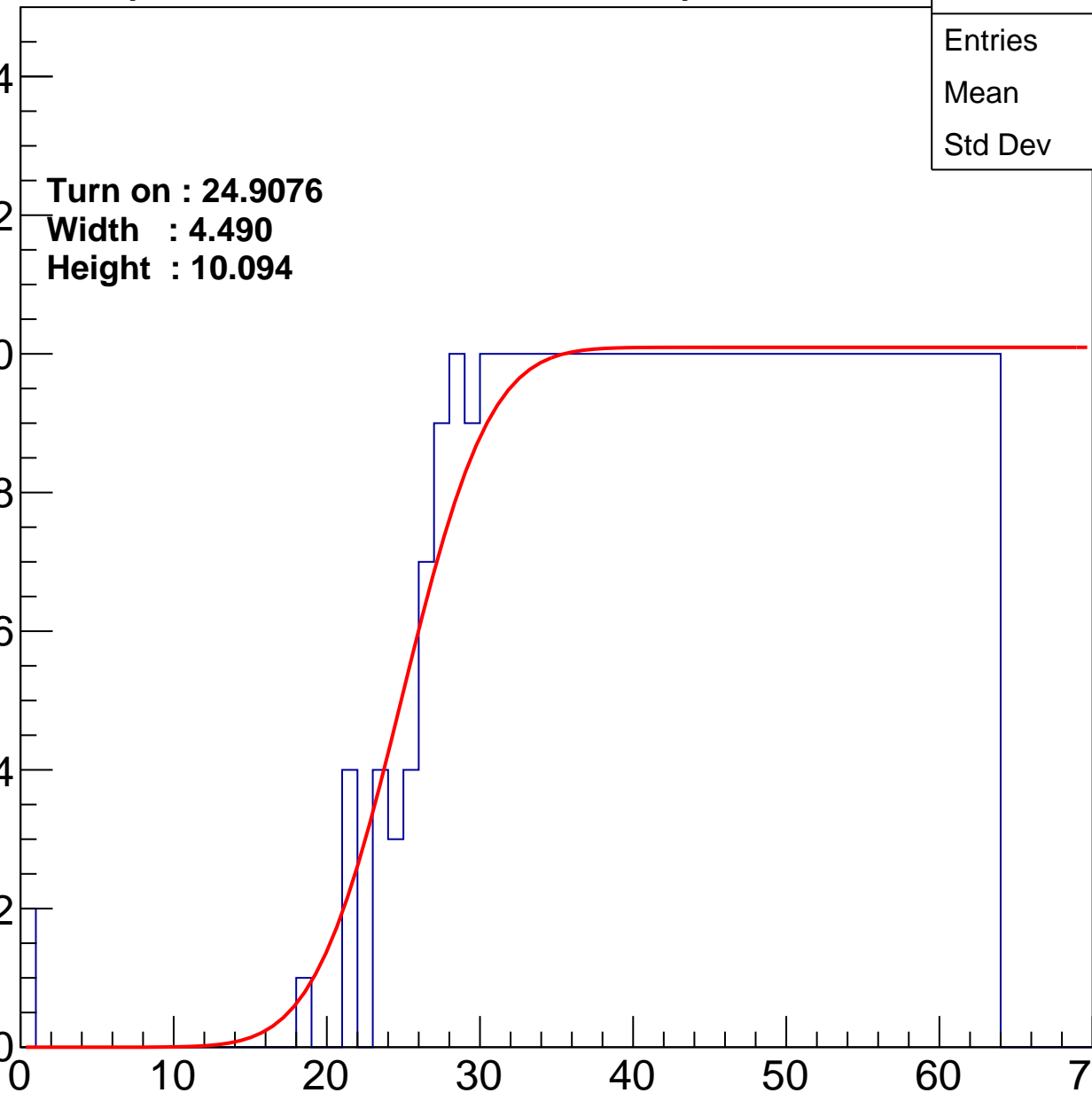
Width : 4.490

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.48
Std Dev	11.44

Turn on : 27.2889

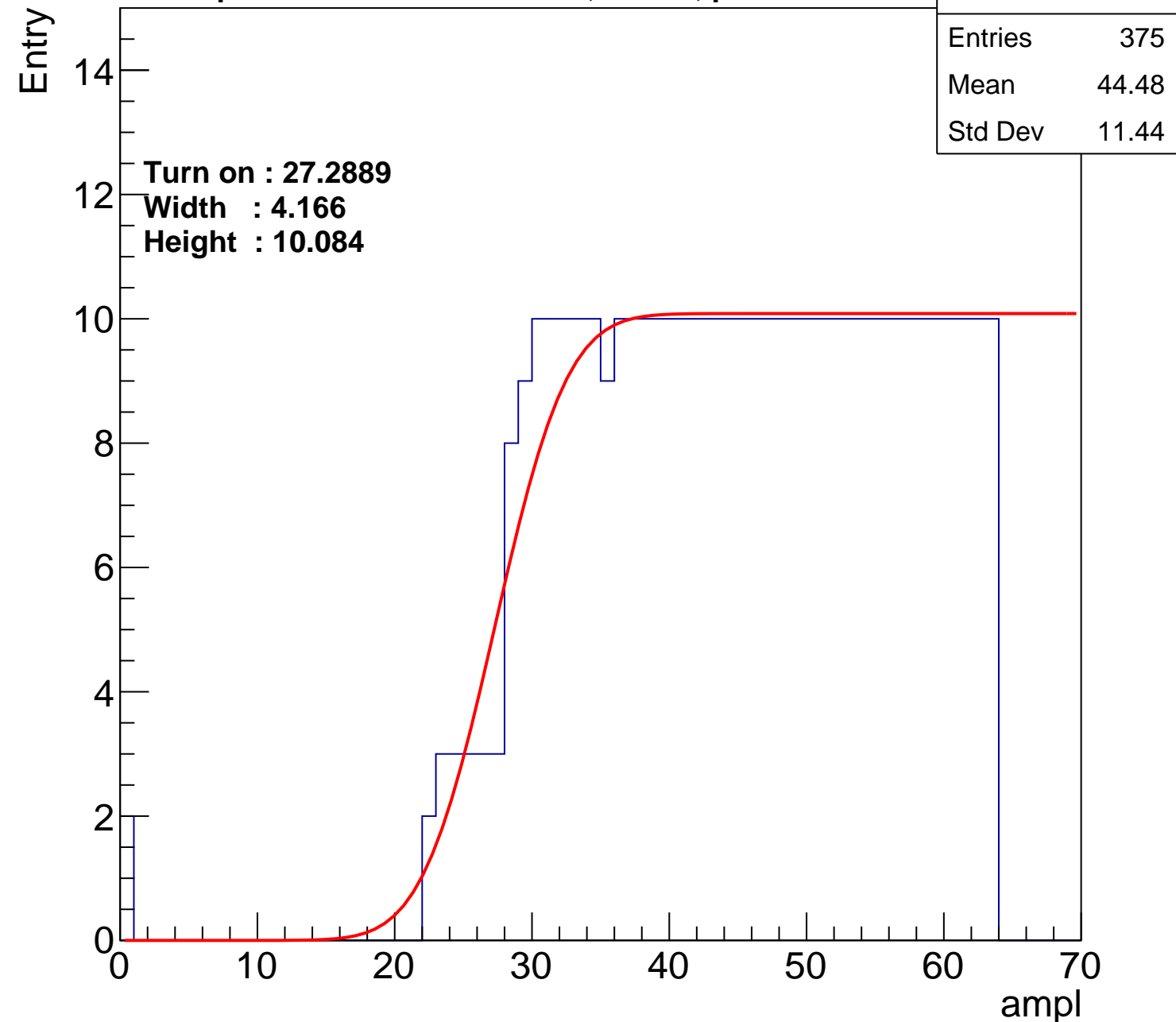
Width : 4.166

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.31
Std Dev	12.53

Turn on : 25.1162

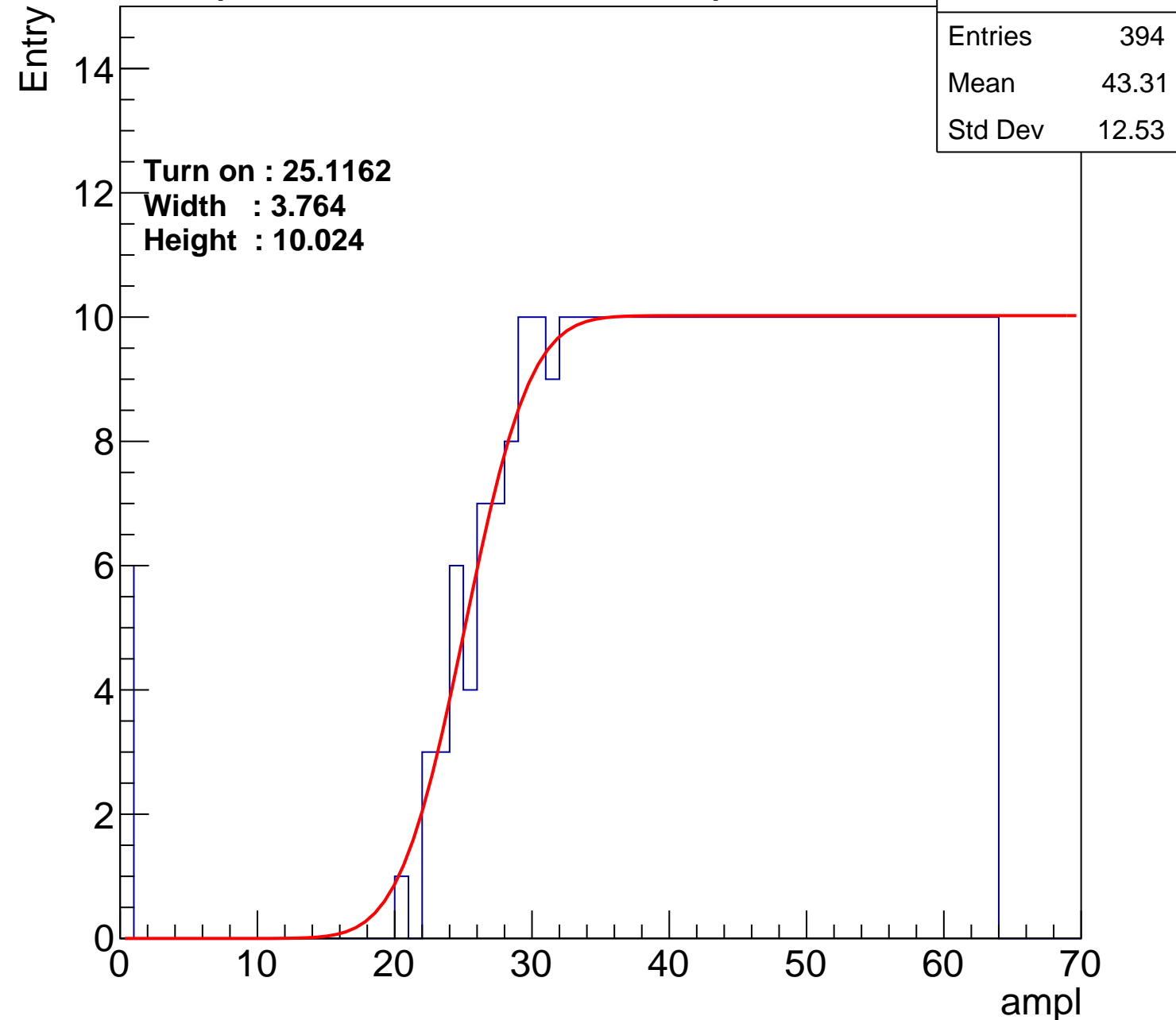
Width : 3.764

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch80

calib_packv5_042523_0143.root, FC#10, port B3

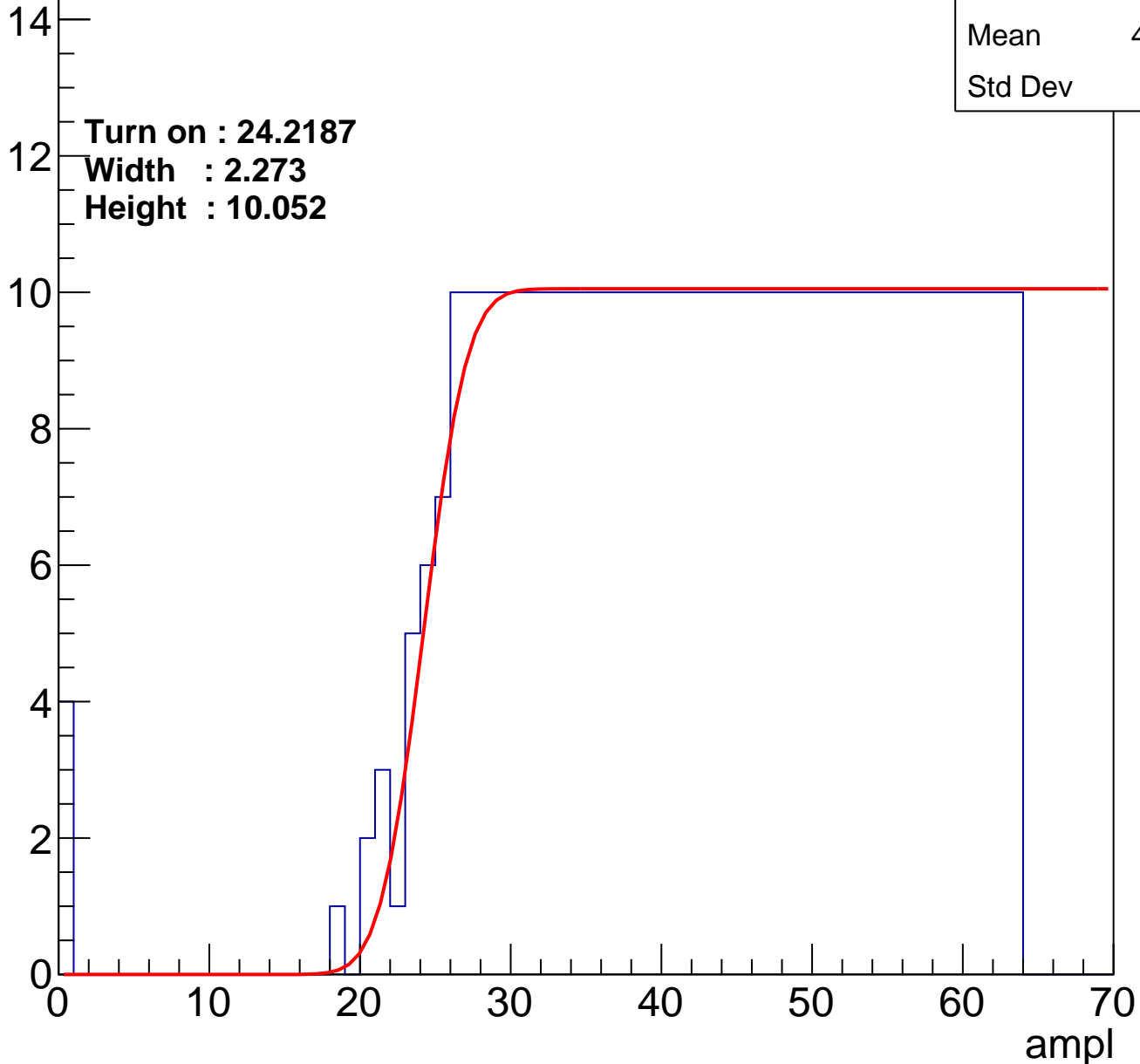
Entries	409
Mean	42.76
Std Dev	12.5

Turn on : 24.2187

Width : 2.273

Height : 10.052

Entry



B1L002S, U22-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 25.0833

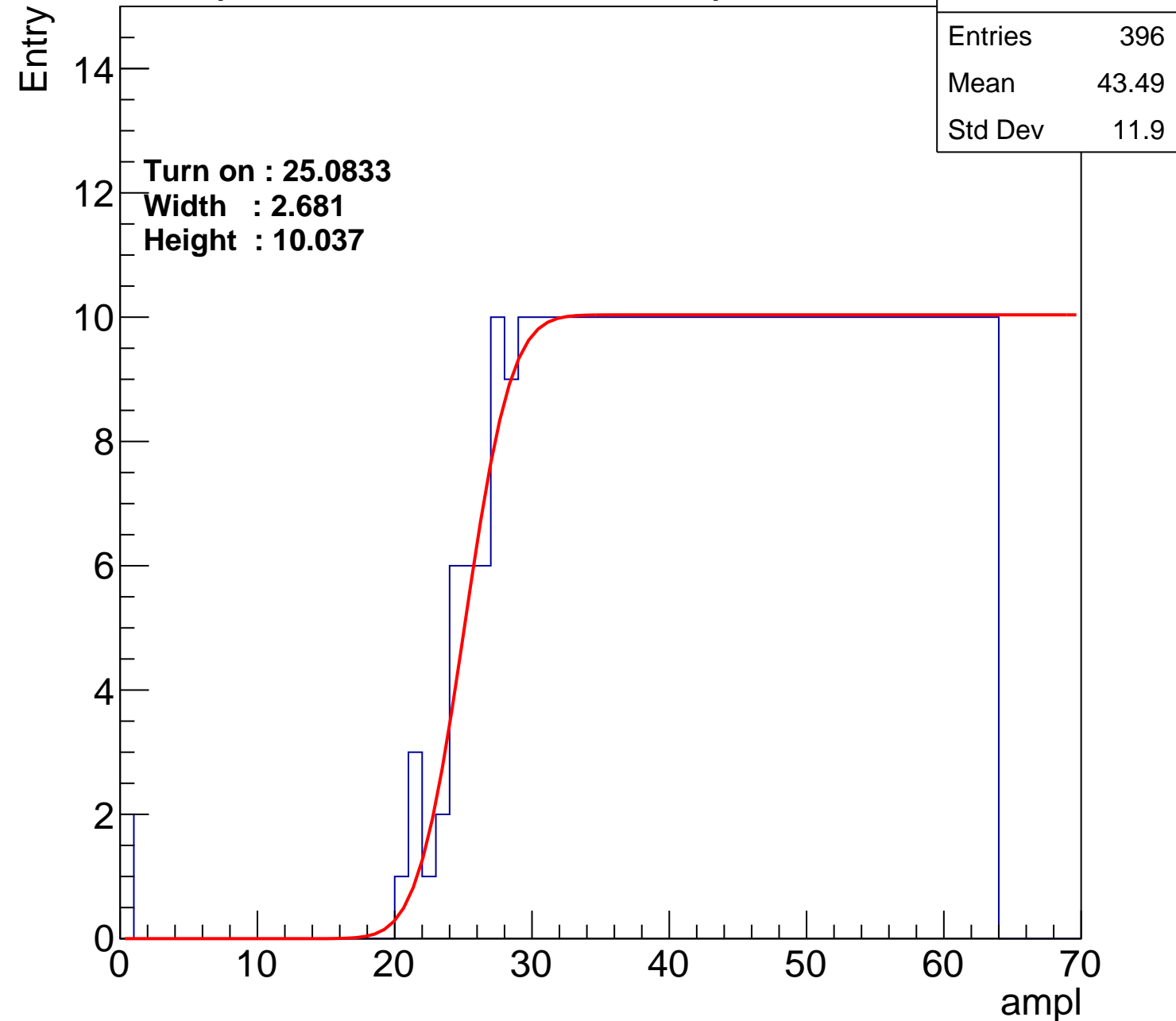
Width : 2.681

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.9523

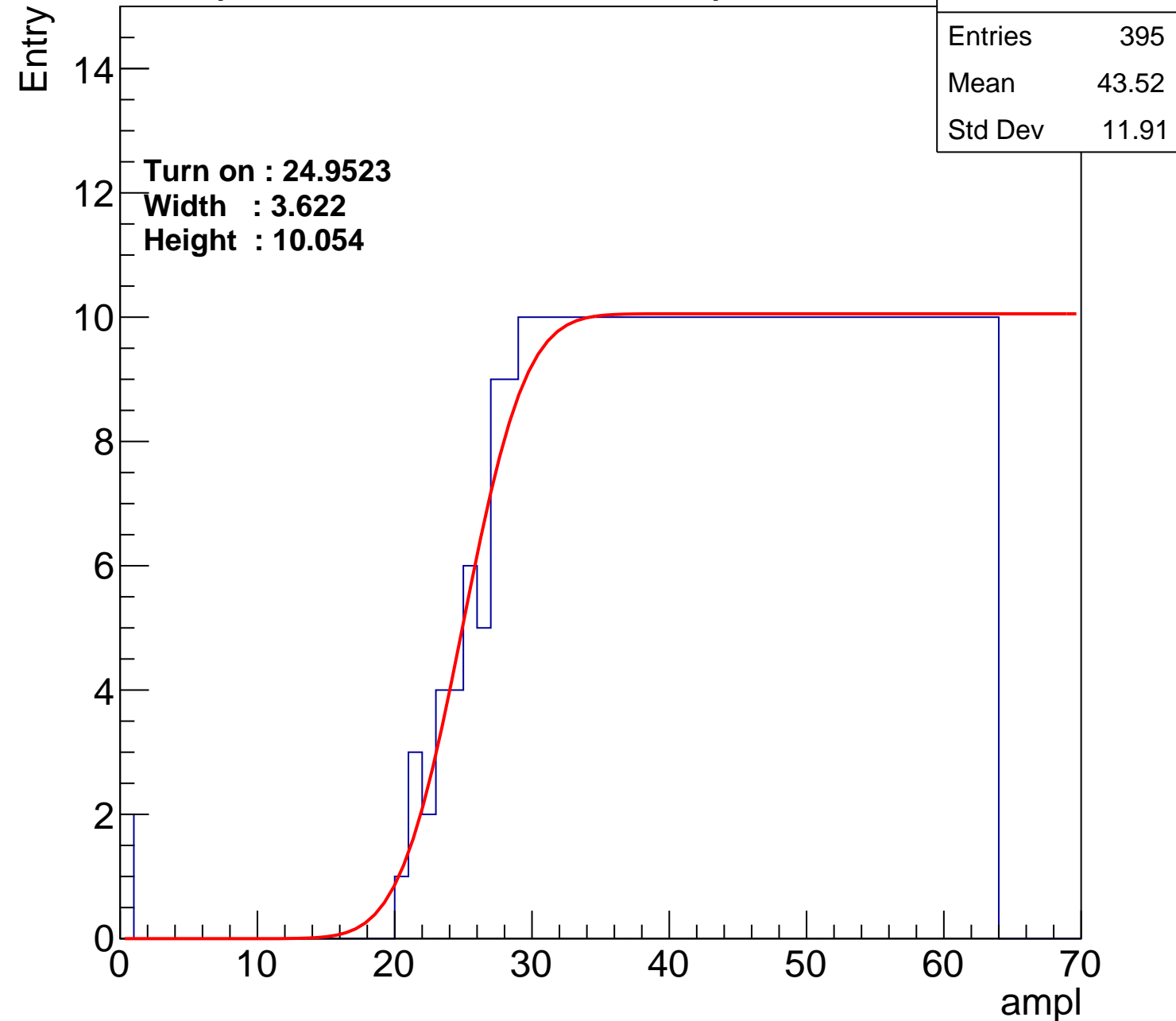
Width : 3.622

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.61
Std Dev	11.33

Turn on : 27.3095

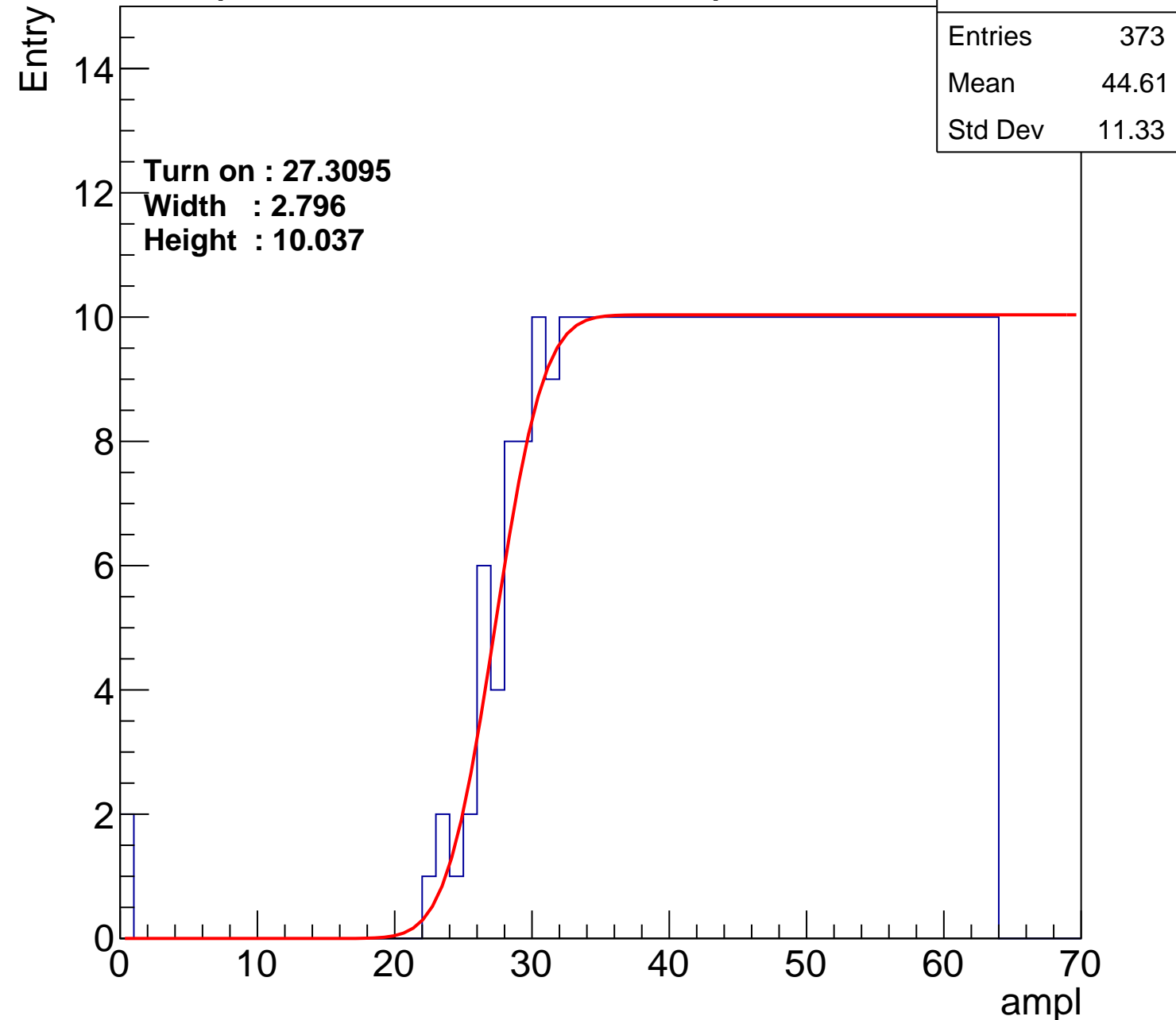
Width : 2.796

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.2038

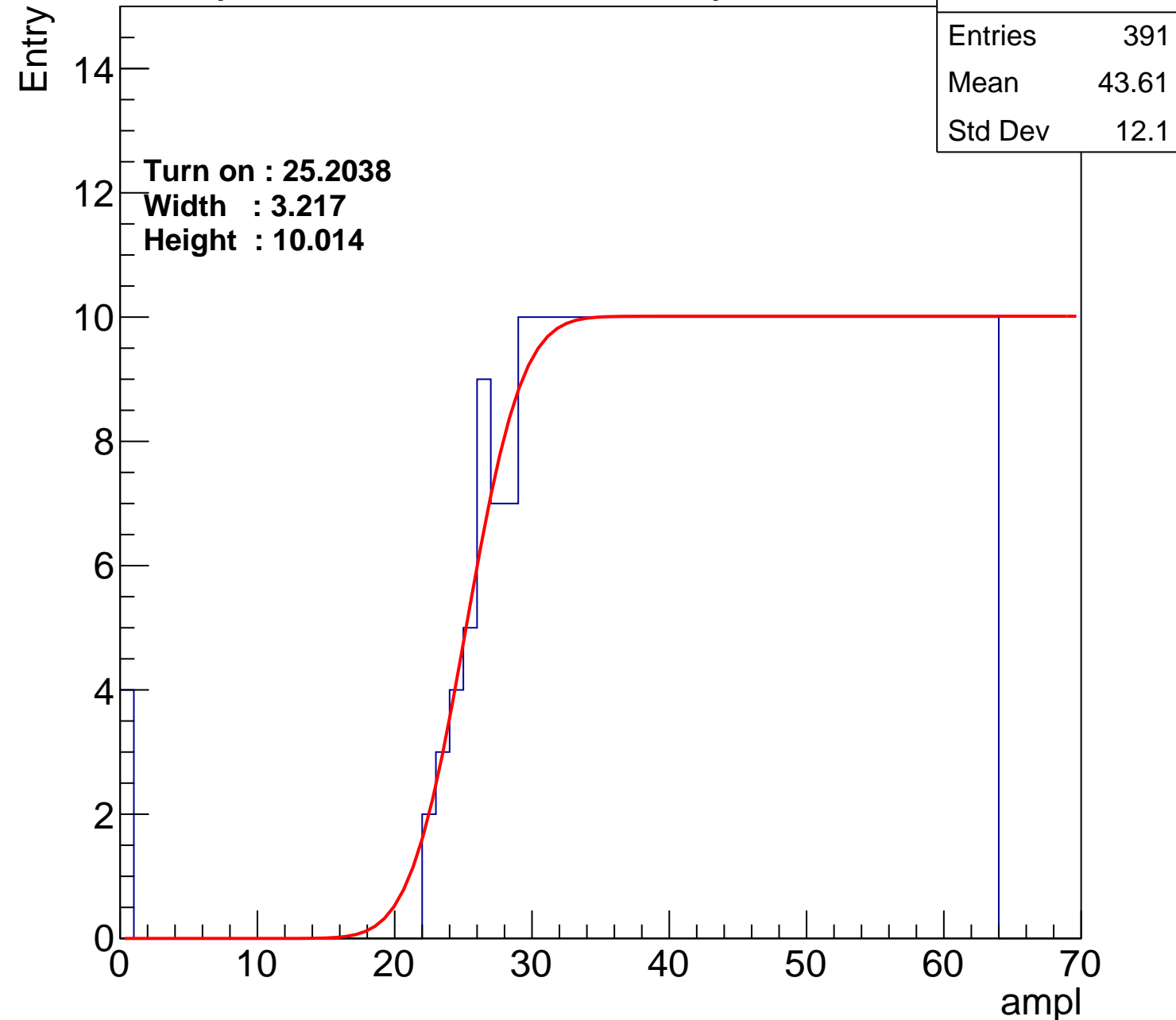
Width : 3.217

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.3309

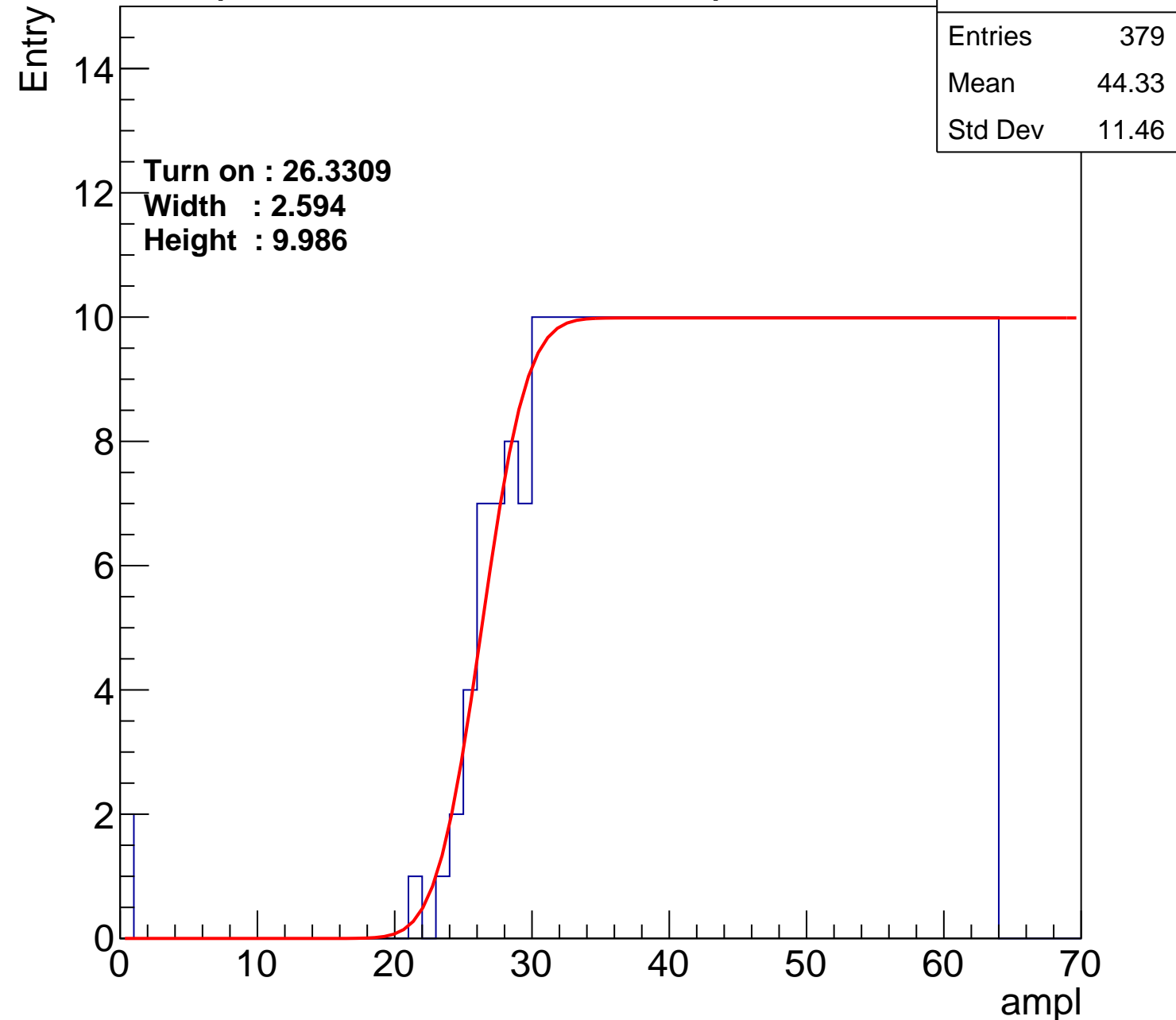
Width : 2.594

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.89
Std Dev	11.74

Turn on : 26.0161

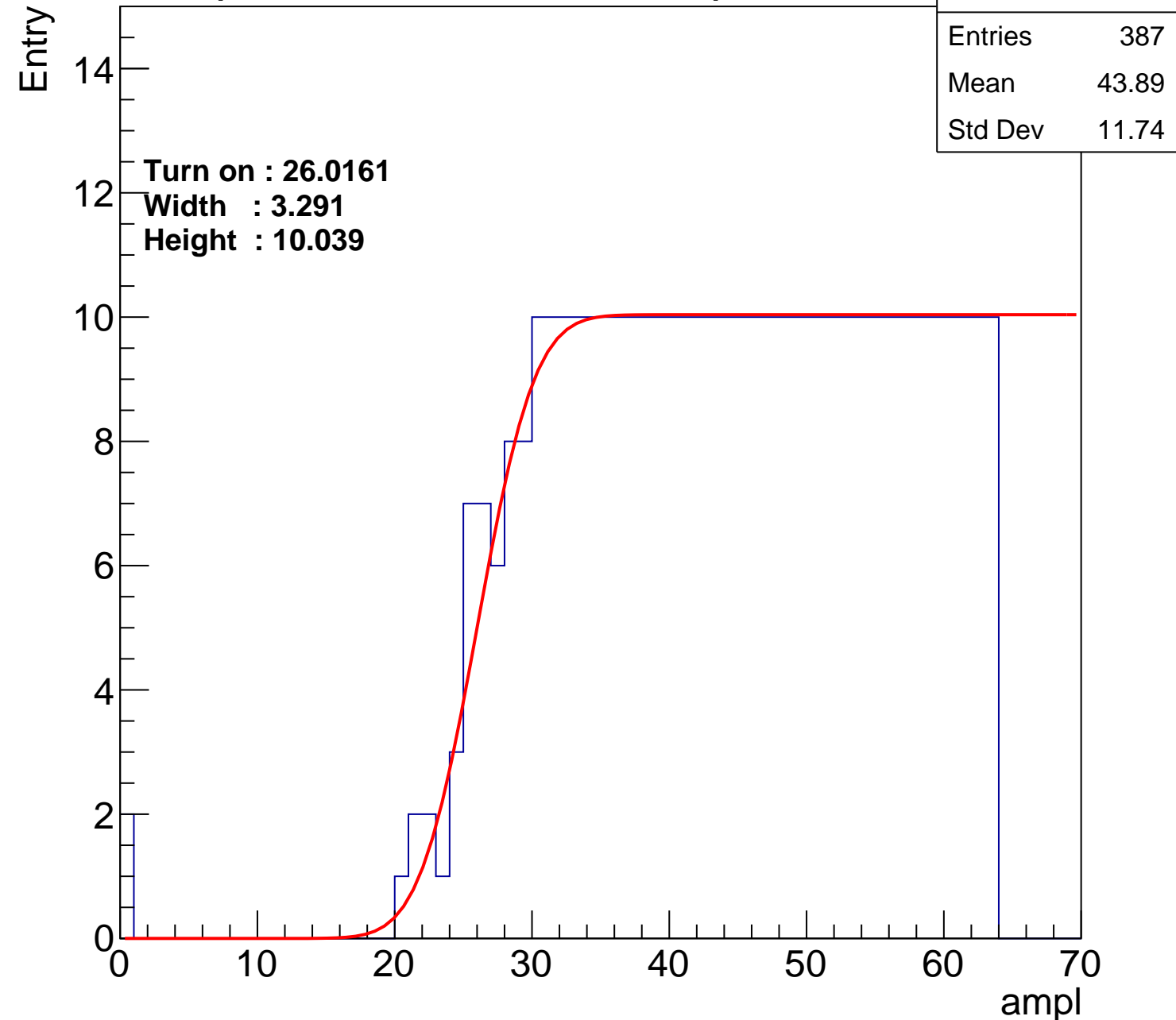
Width : 3.291

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.35
Std Dev	12.36

Turn on : 25.3258

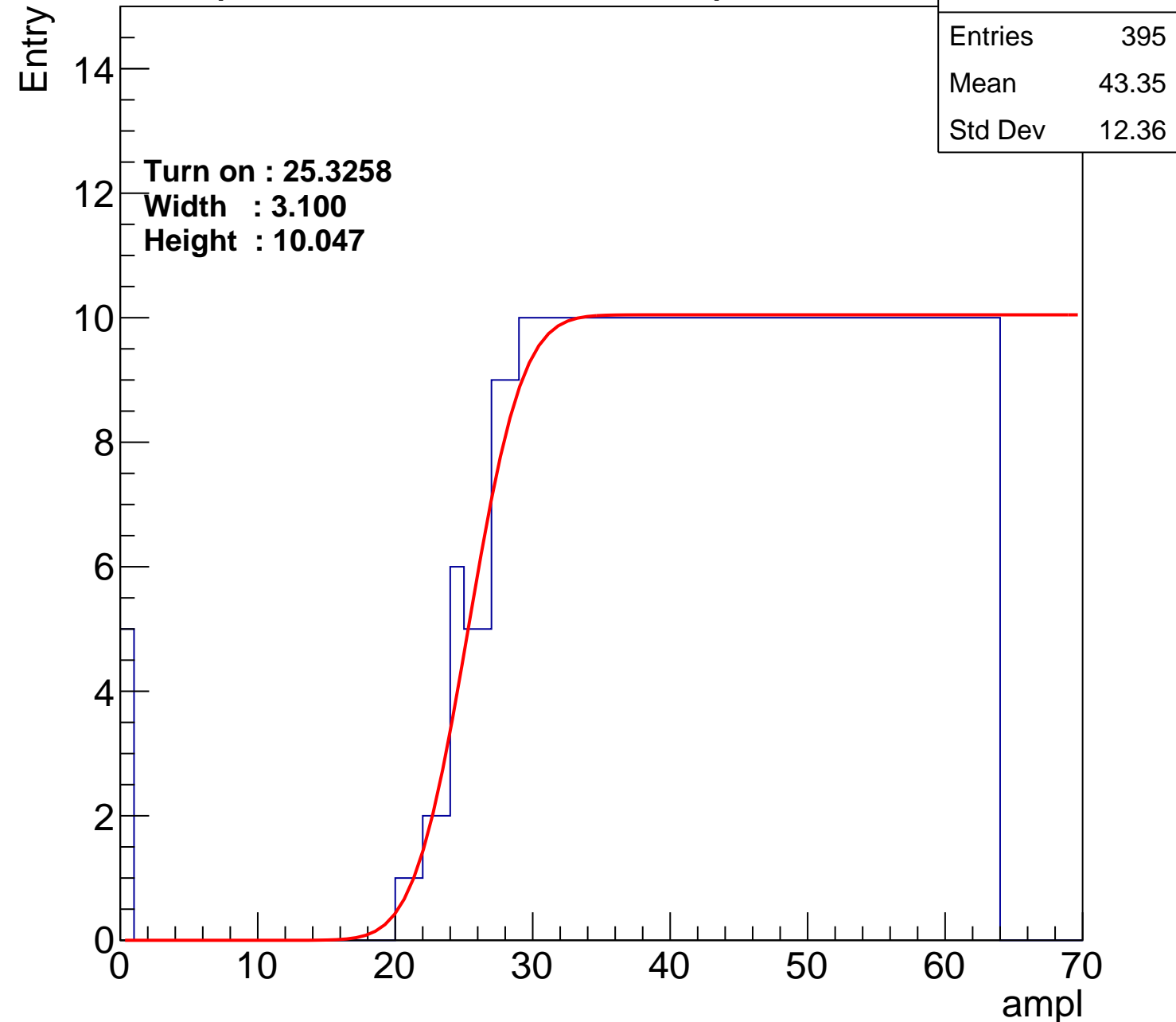
Width : 3.100

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.66
Std Dev	12.76

Turn on : 23.9117

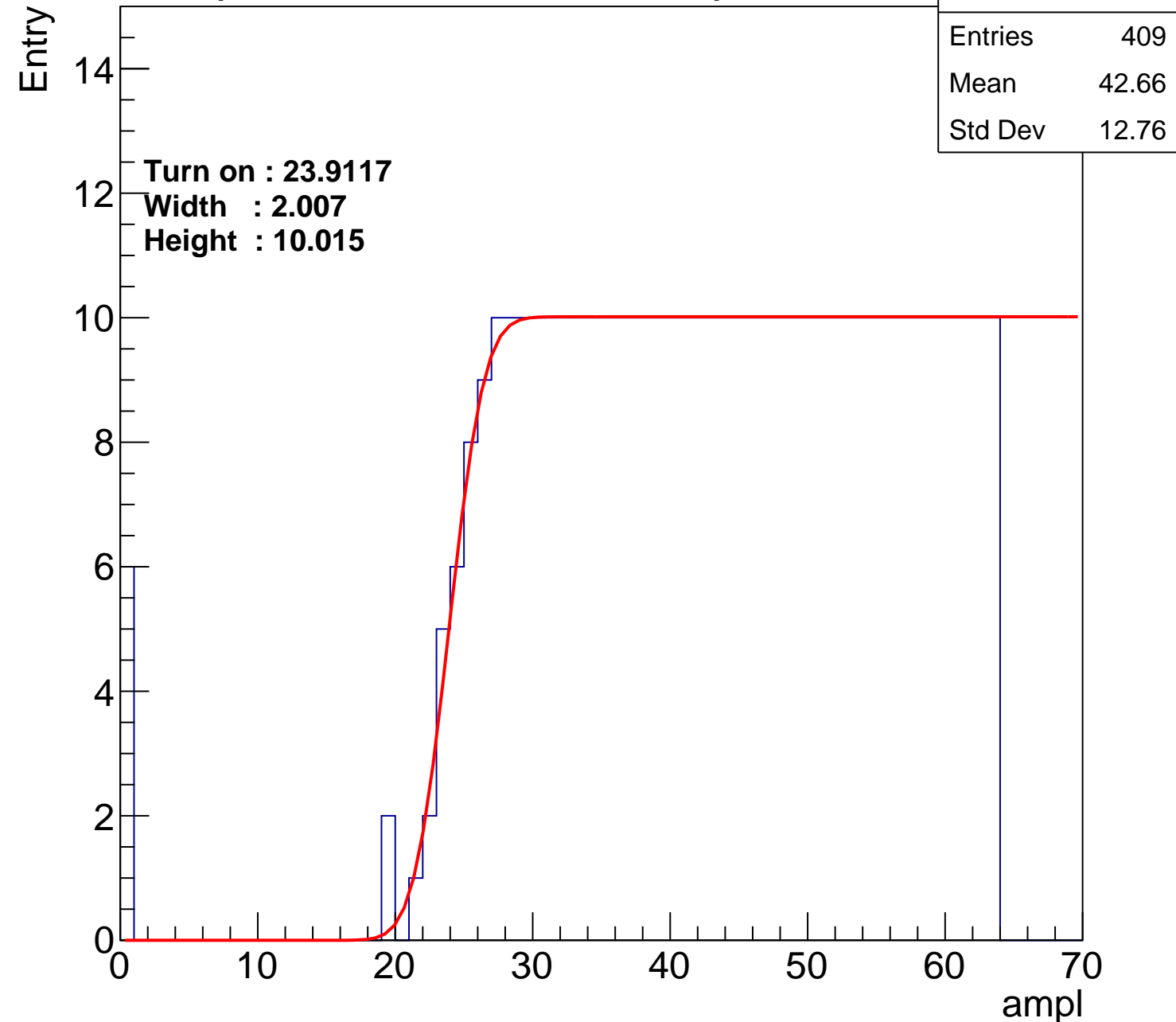
Width : 2.007

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.13
Std Dev	12.34

Turn on : 24.5751

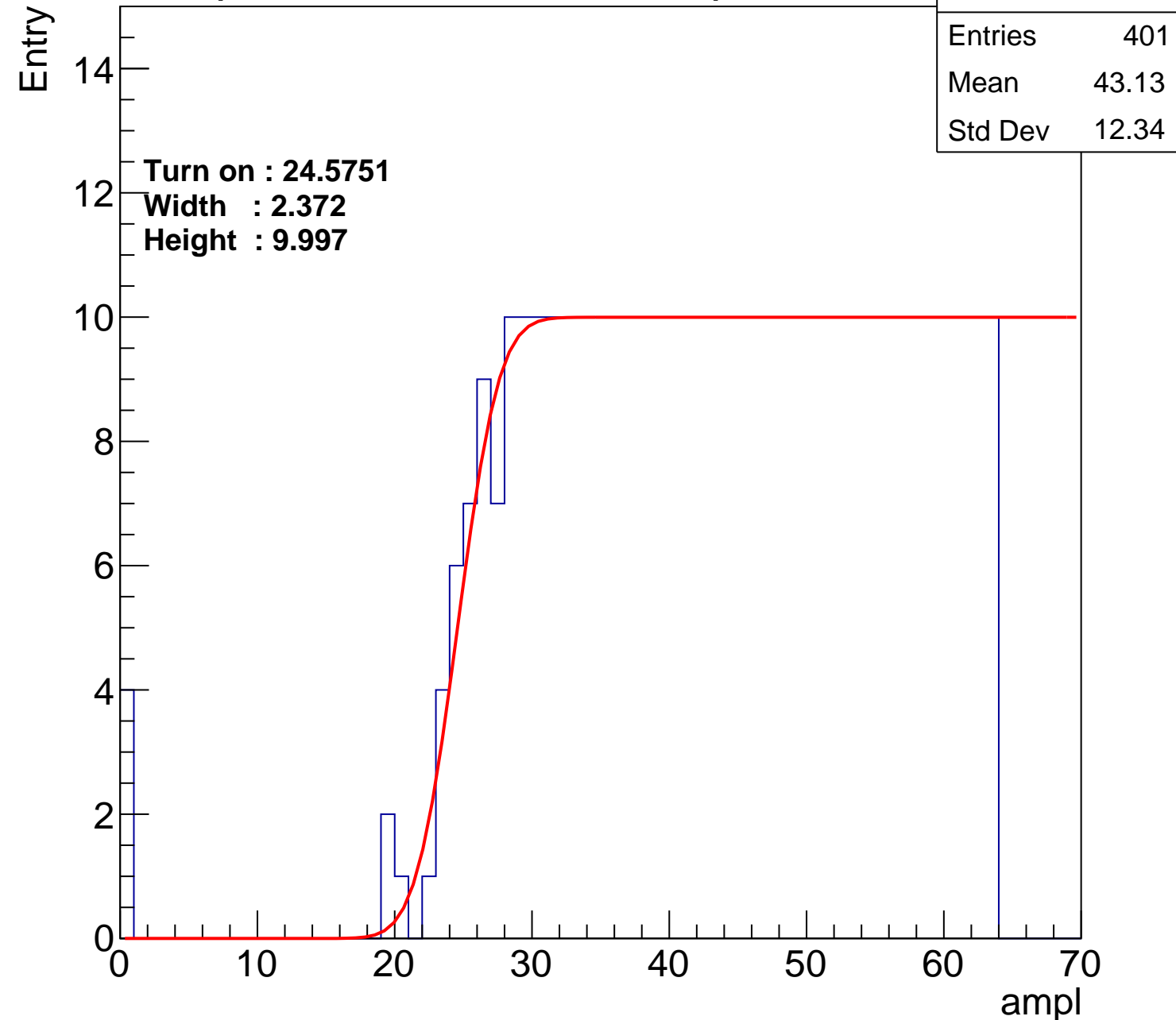
Width : 2.372

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch90

calib_packv5_042523_0143.root, FC#10, port B3

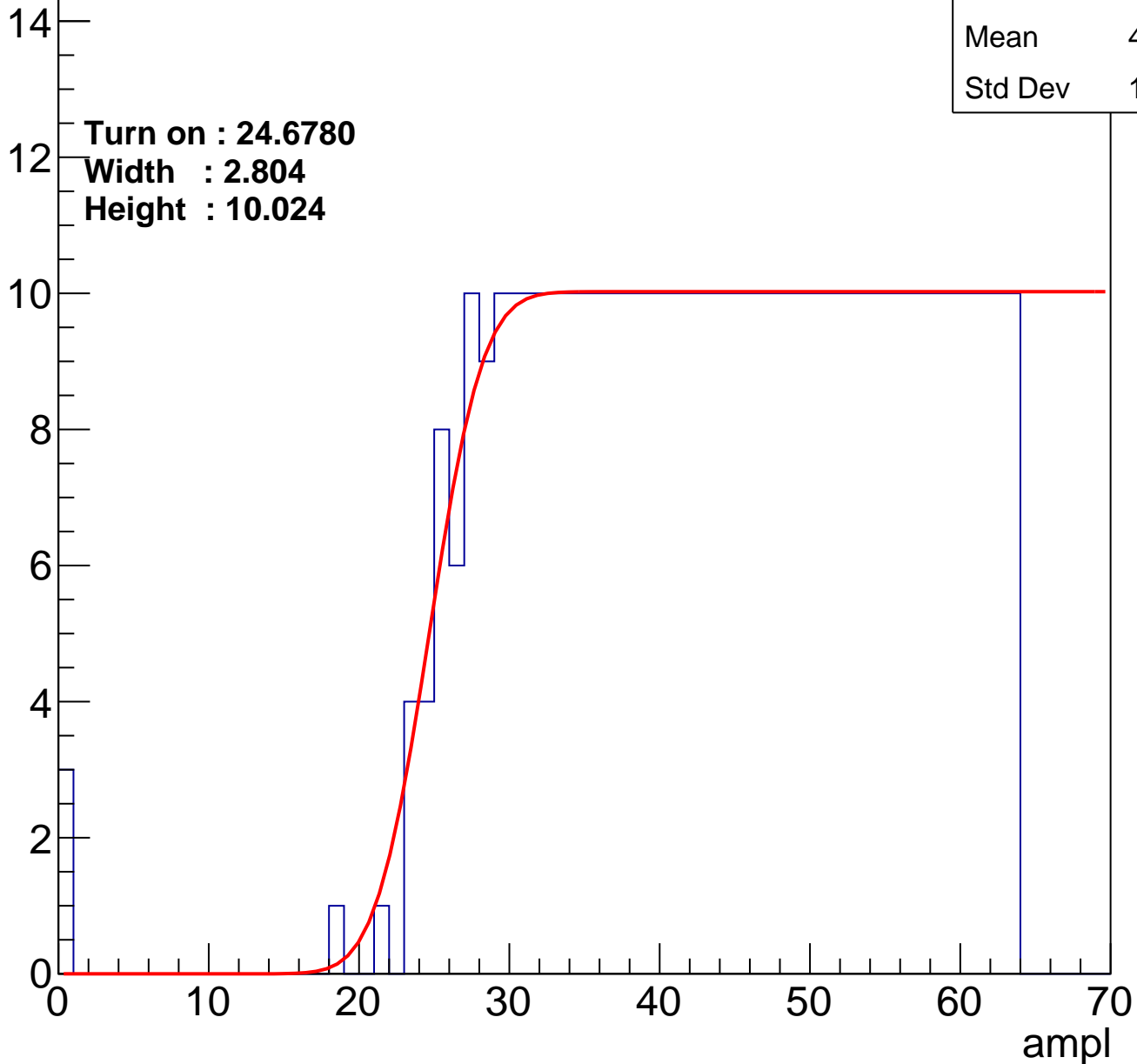
Entries	396
Mean	43.45
Std Dev	12.04

Turn on : 24.6780

Width : 2.804

Height : 10.024

Entry



B1L002S, U22-ch91

calib_packv5_042523_0143.root, FC#10, port B3

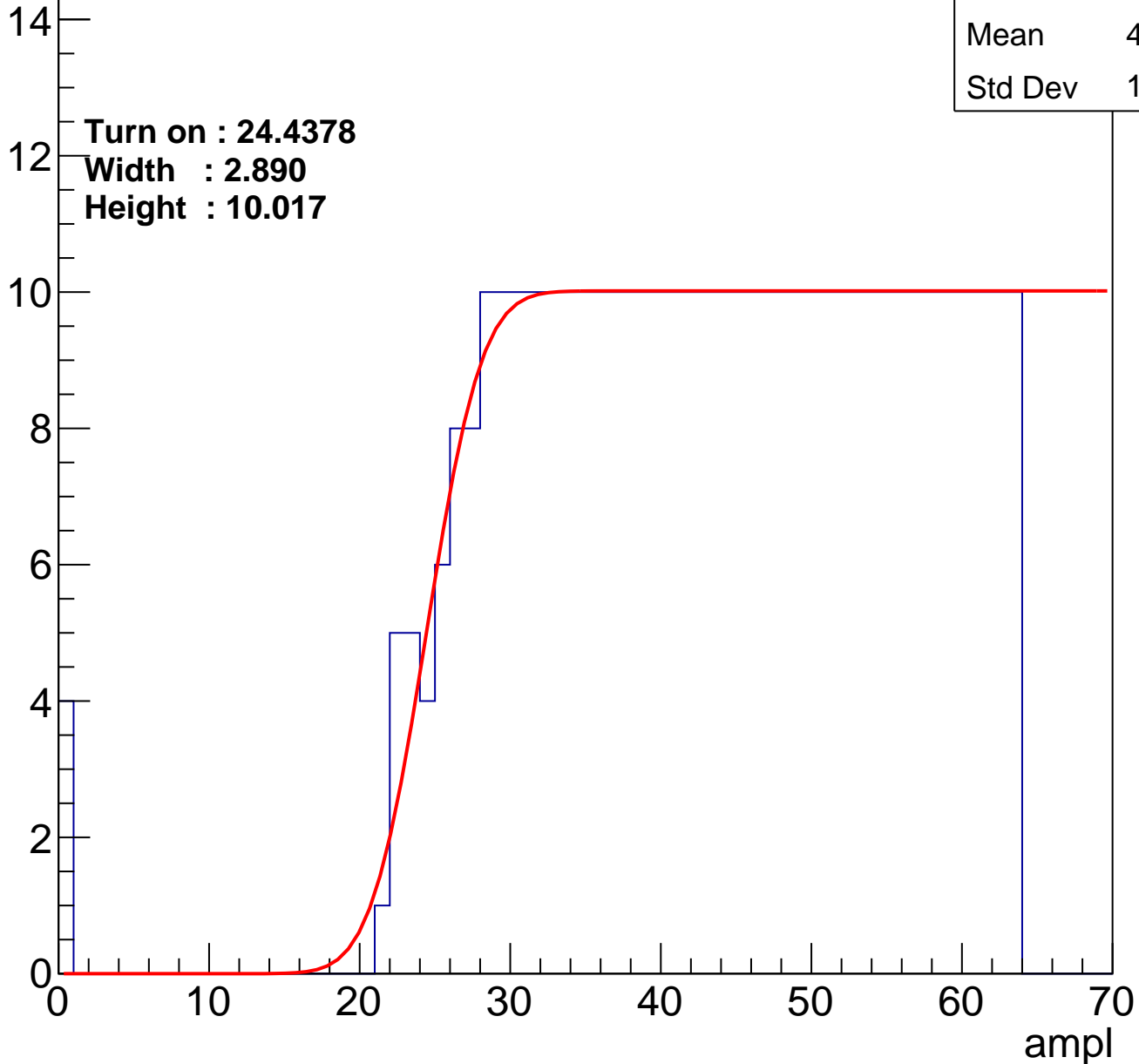
Entries	401
Mean	43.13
Std Dev	12.33

Turn on : 24.4378

Width : 2.890

Height : 10.017

Entry



B1L002S, U22-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.8

Turn on : 25.6702

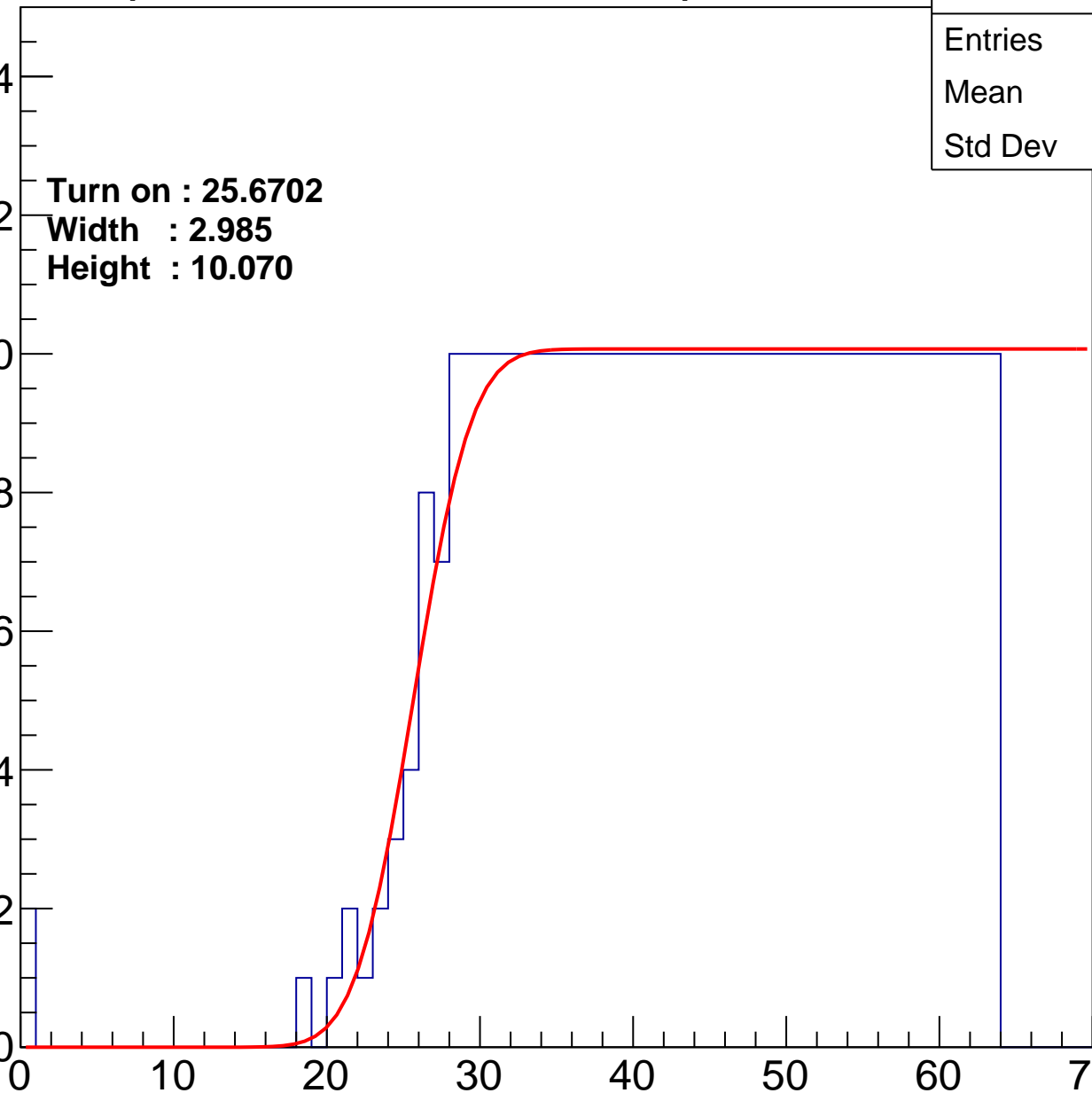
Width : 2.985

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	413
Mean	42.56
Std Dev	12.55

Turn on : 23.3382

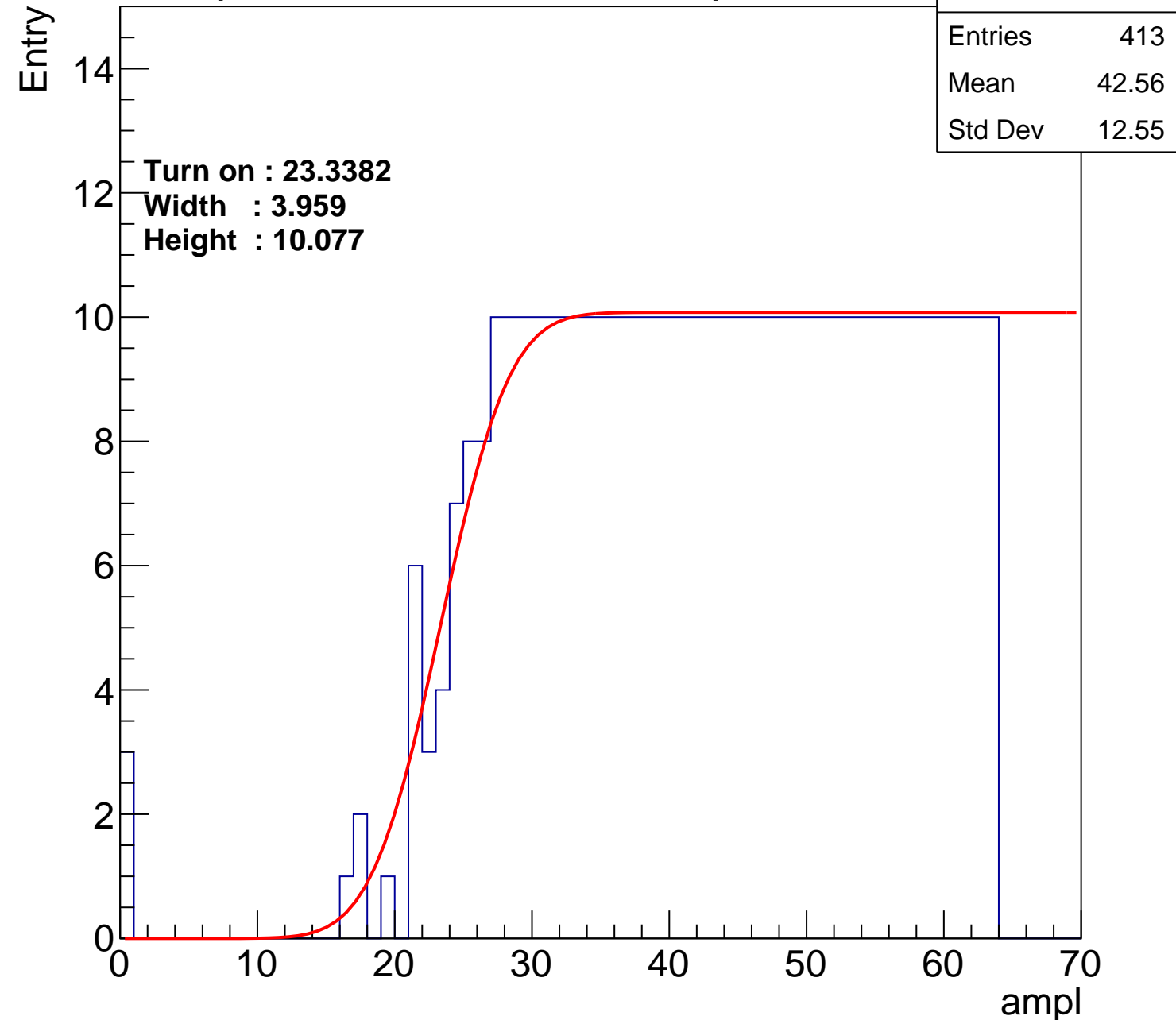
Width : 3.959

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch94

calib_packv5_042523_0143.root, FC#10, port B3

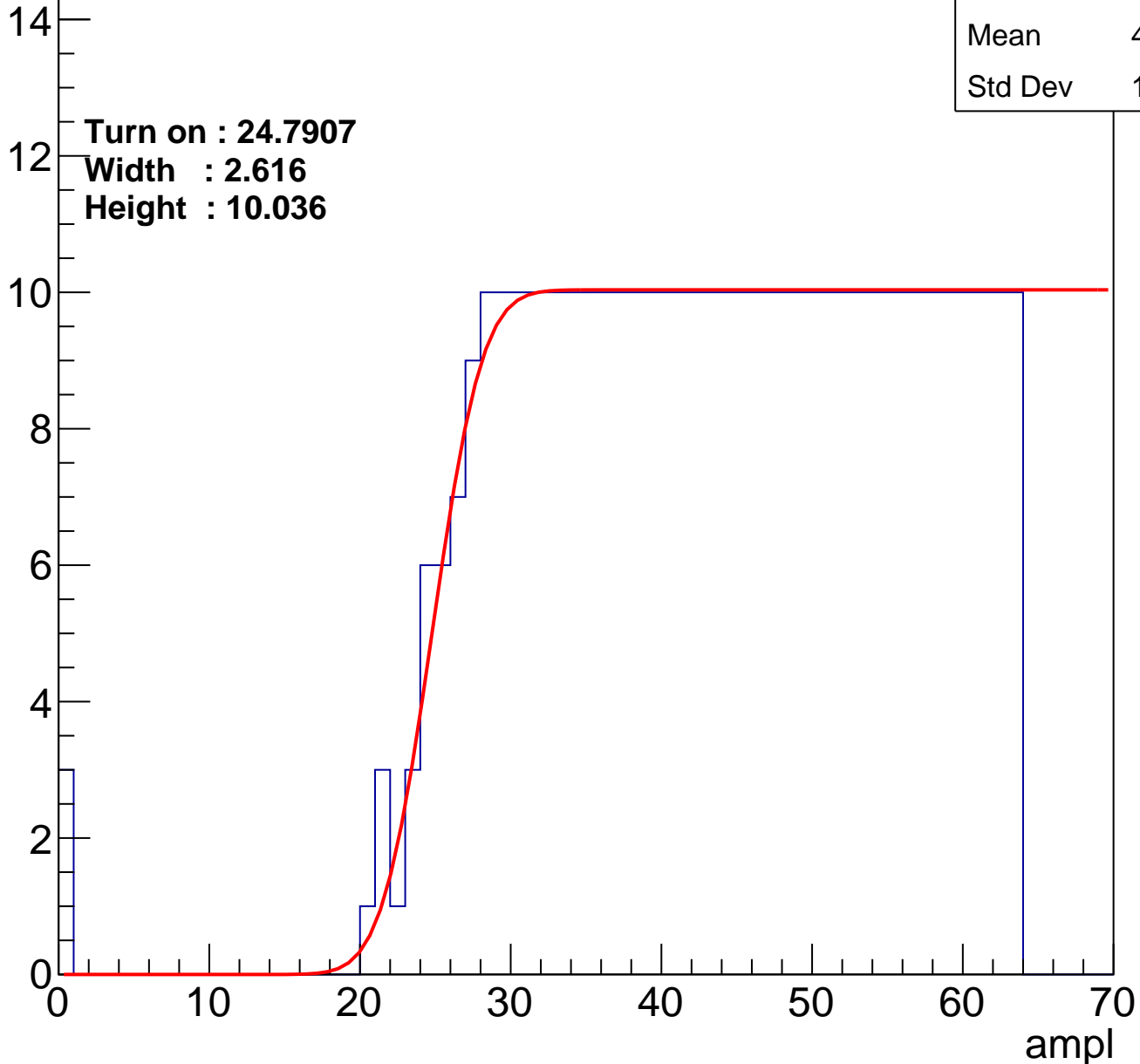
Entries	399
Mean	43.29
Std Dev	12.13

Turn on : 24.7907

Width : 2.616

Height : 10.036

Entry



B1L002S, U22-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.35
Std Dev	12.25

Turn on : 24.8260

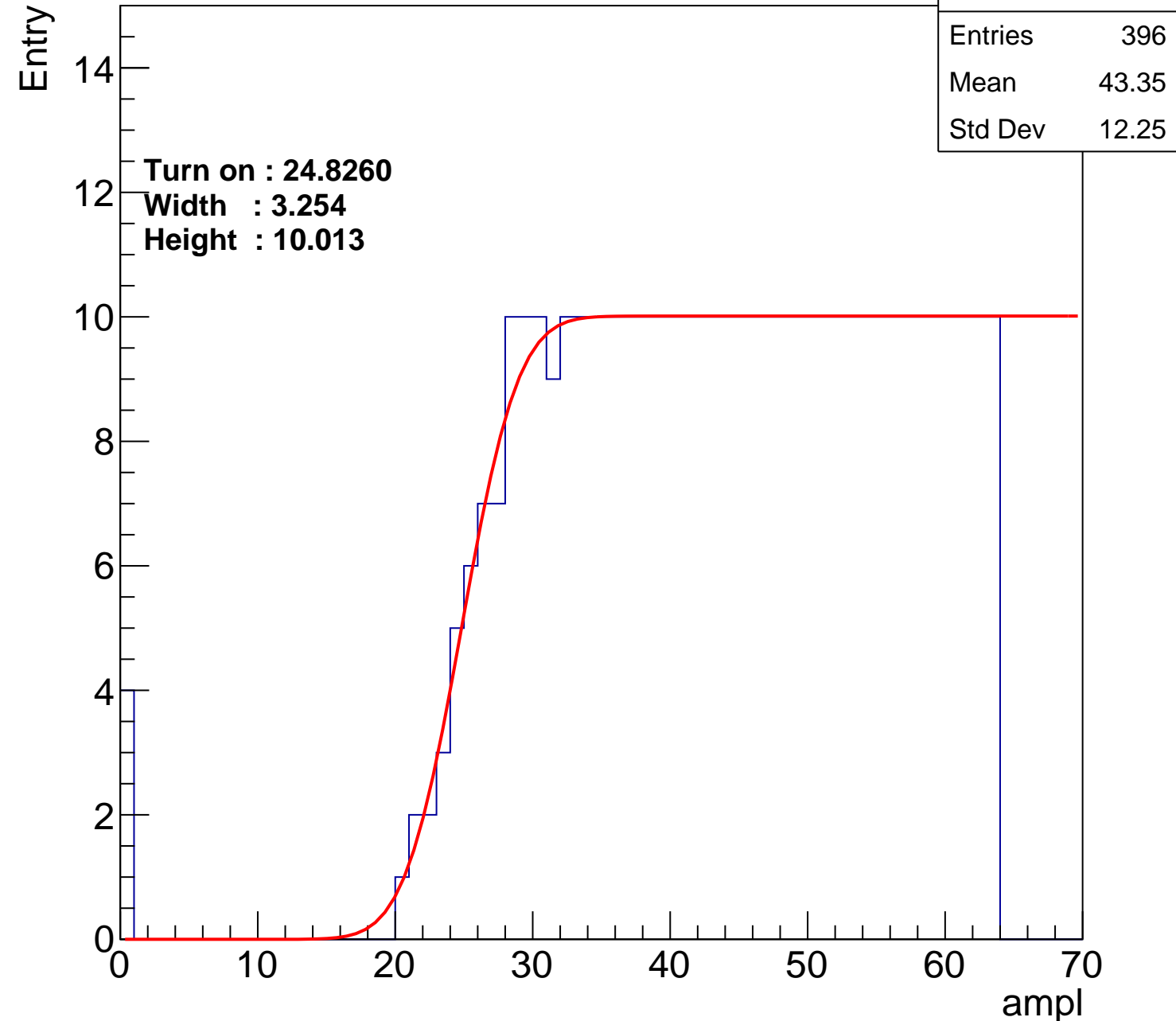
Width : 3.254

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.25
Std Dev	12.58

Turn on : 25.3700

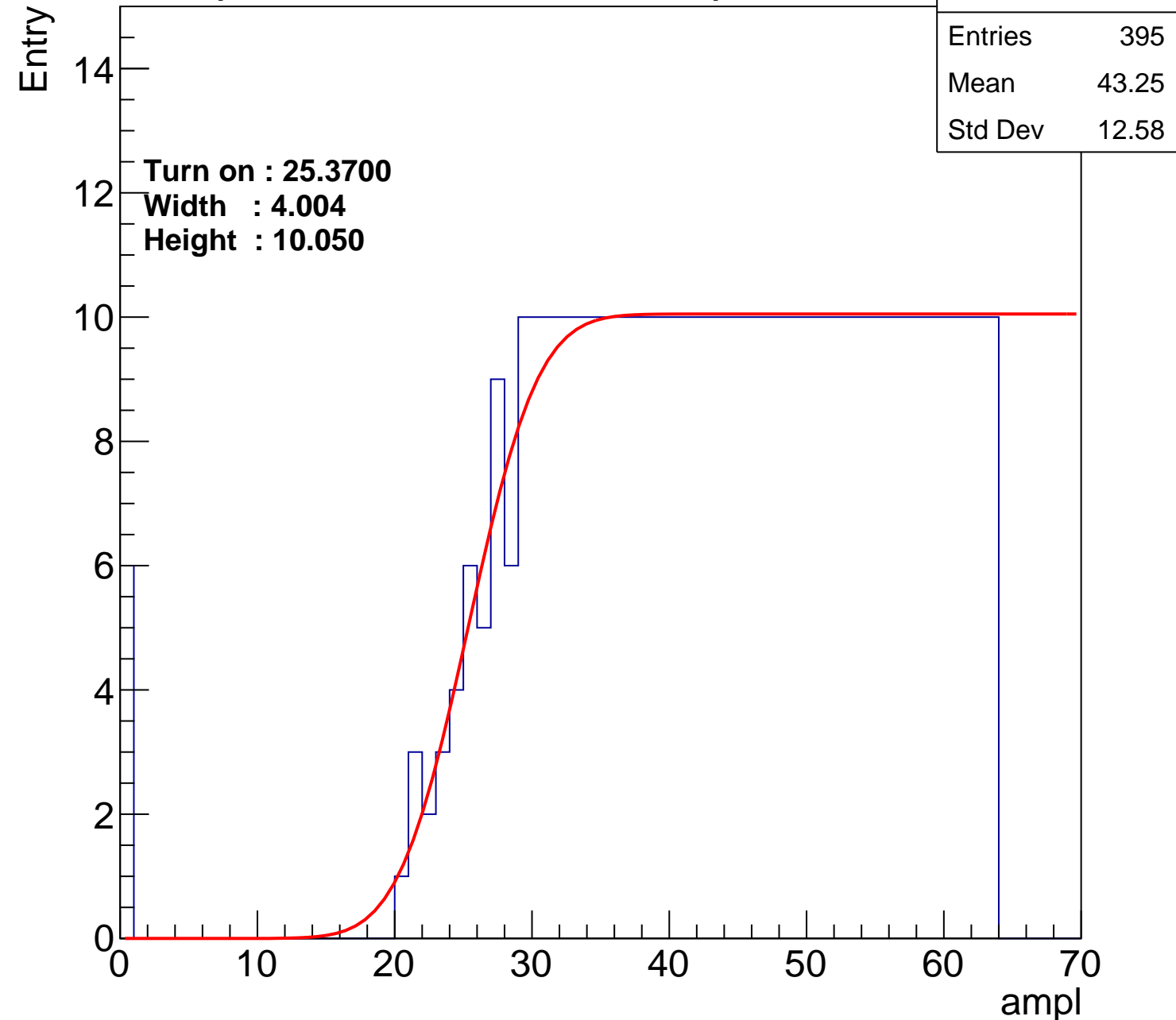
Width : 4.004

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.39
Std Dev	12.24

Turn on : 25.0450

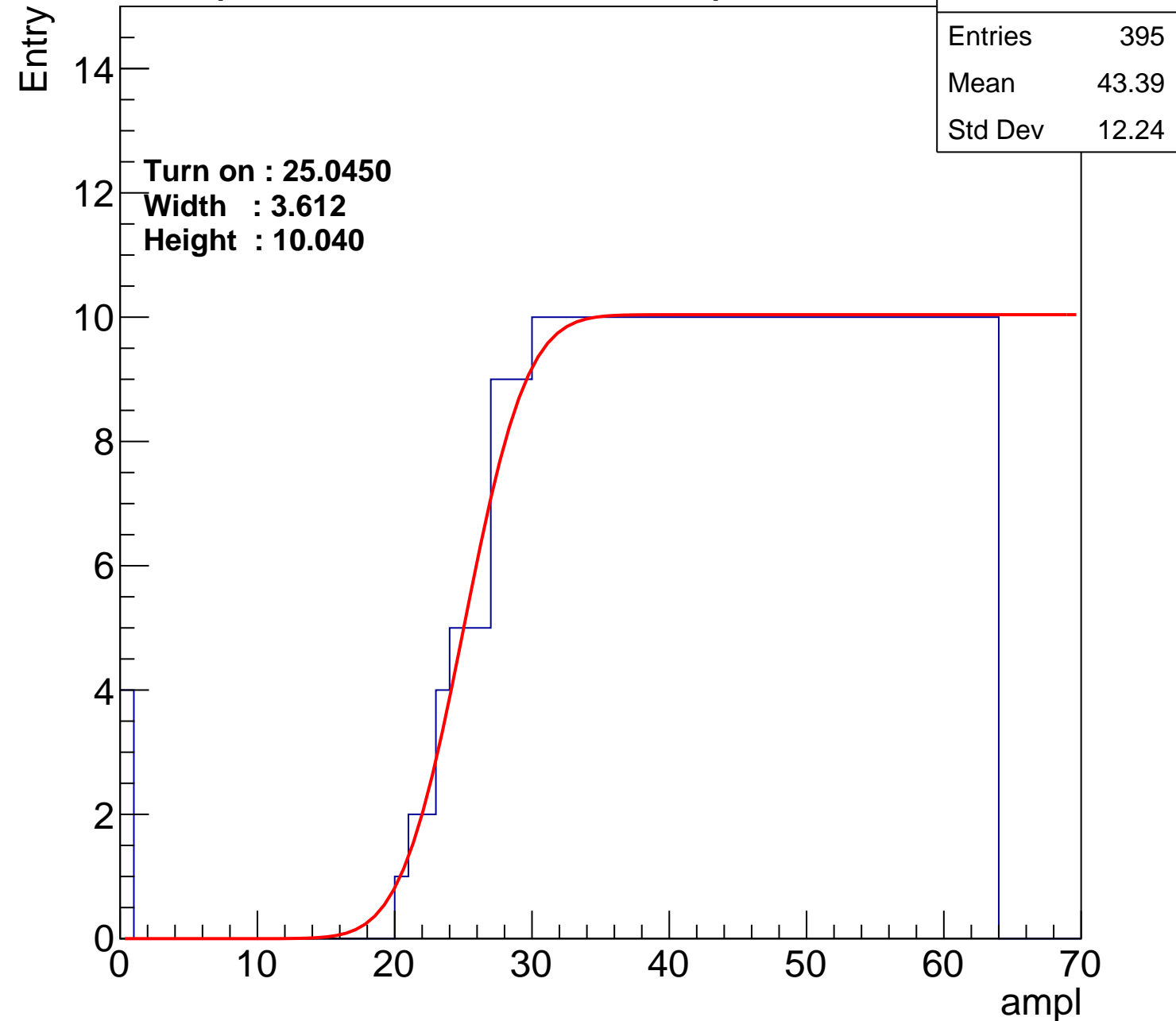
Width : 3.612

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch98

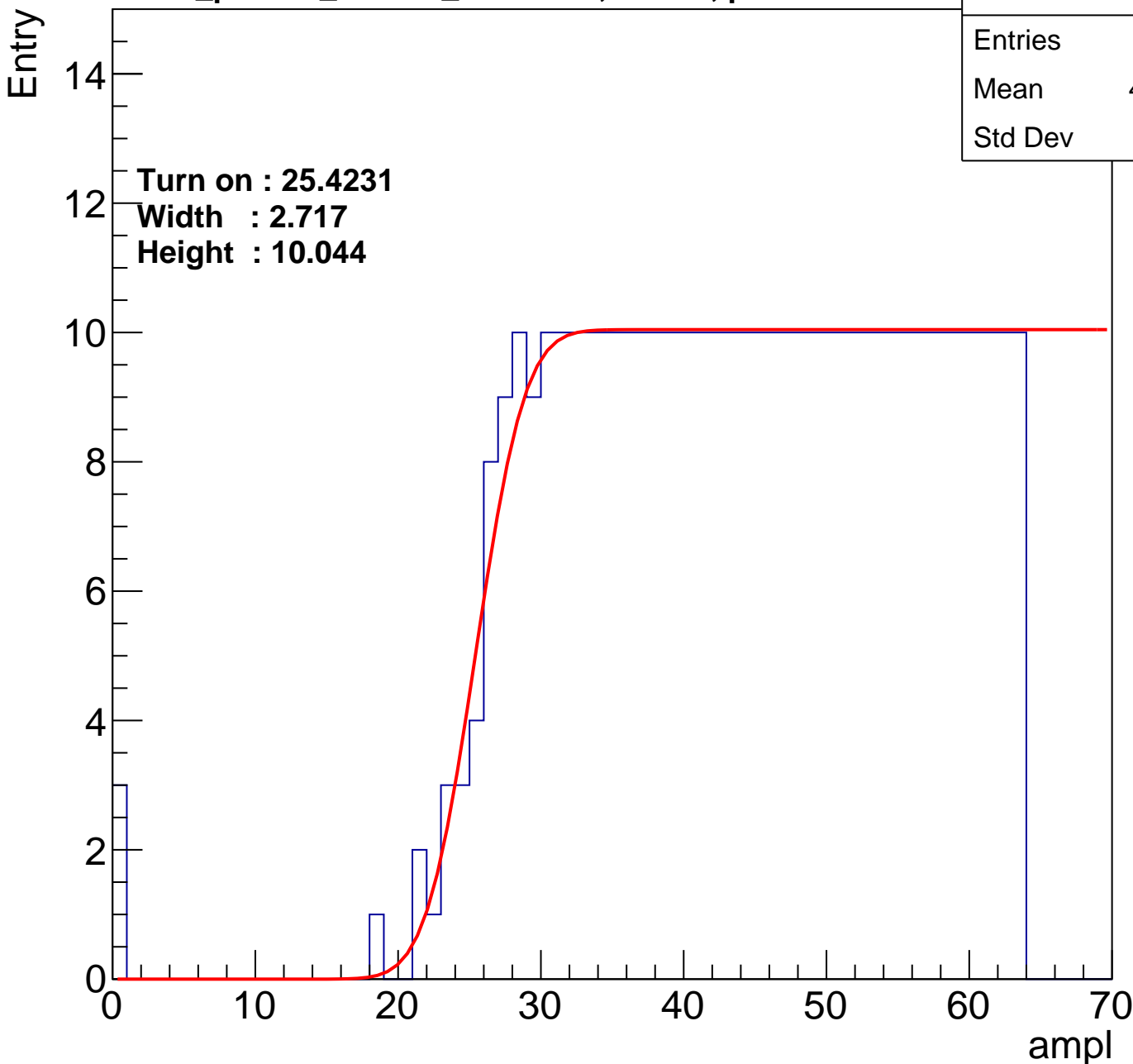
calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.58
Std Dev	12

Turn on : 25.4231

Width : 2.717

Height : 10.044



B1L002S, U22-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.27
Std Dev	12.27

Turn on : 24.7816

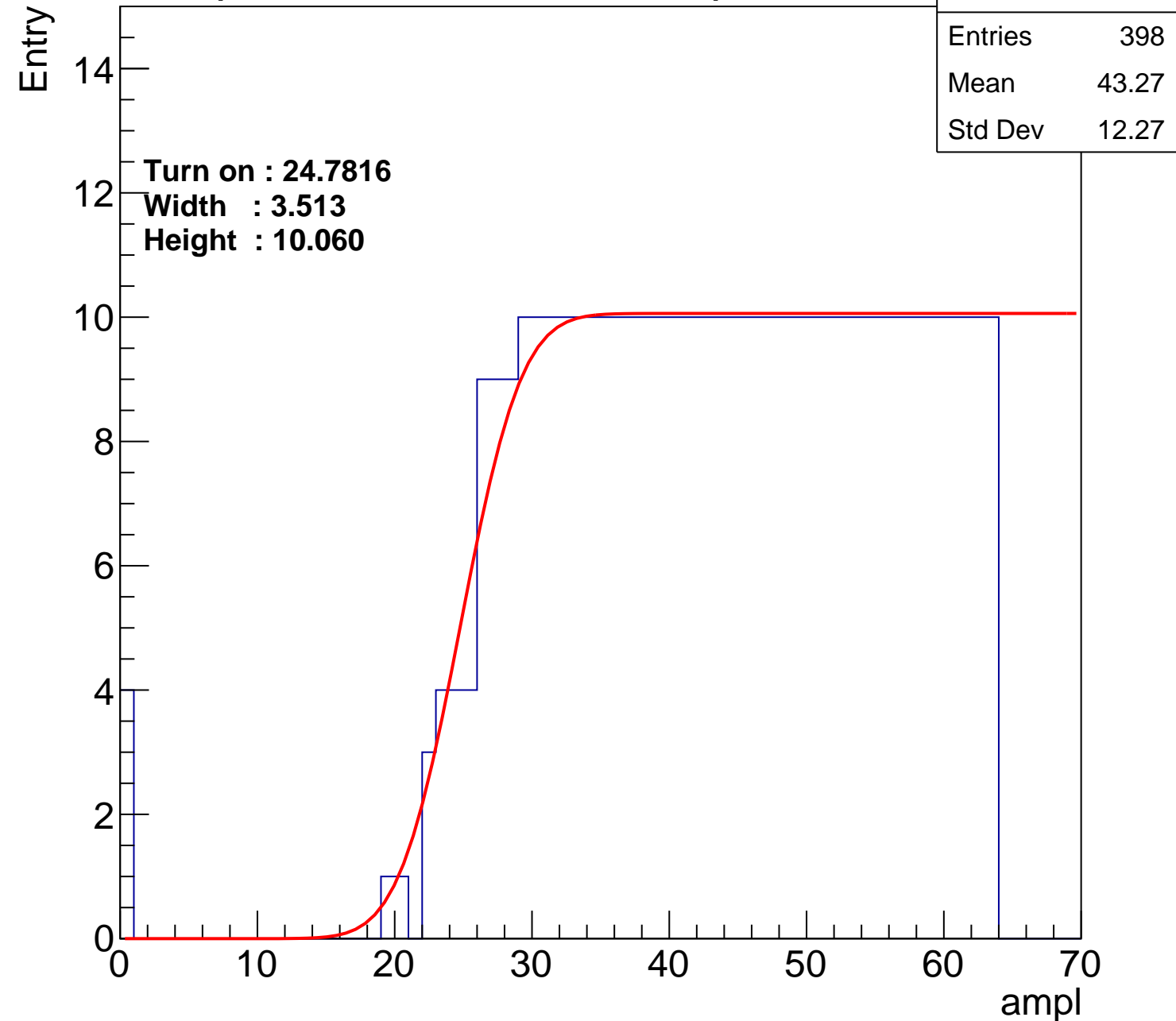
Width : 3.513

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch100

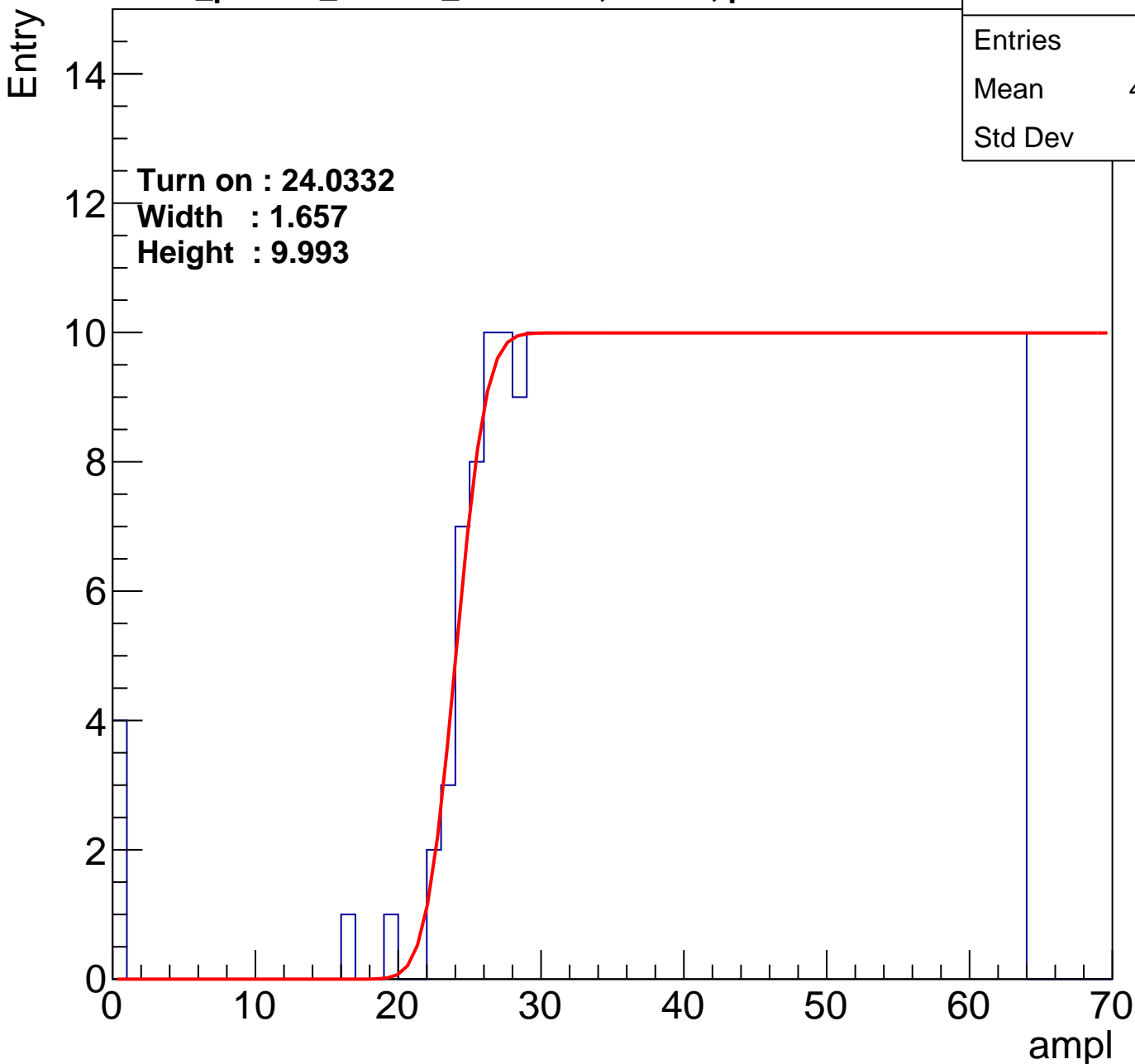
calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.96
Std Dev	12.4

Turn on : 24.0332

Width : 1.657

Height : 9.993



B1L002S, U22-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.25
Std Dev	12.09

Turn on : 27.2732

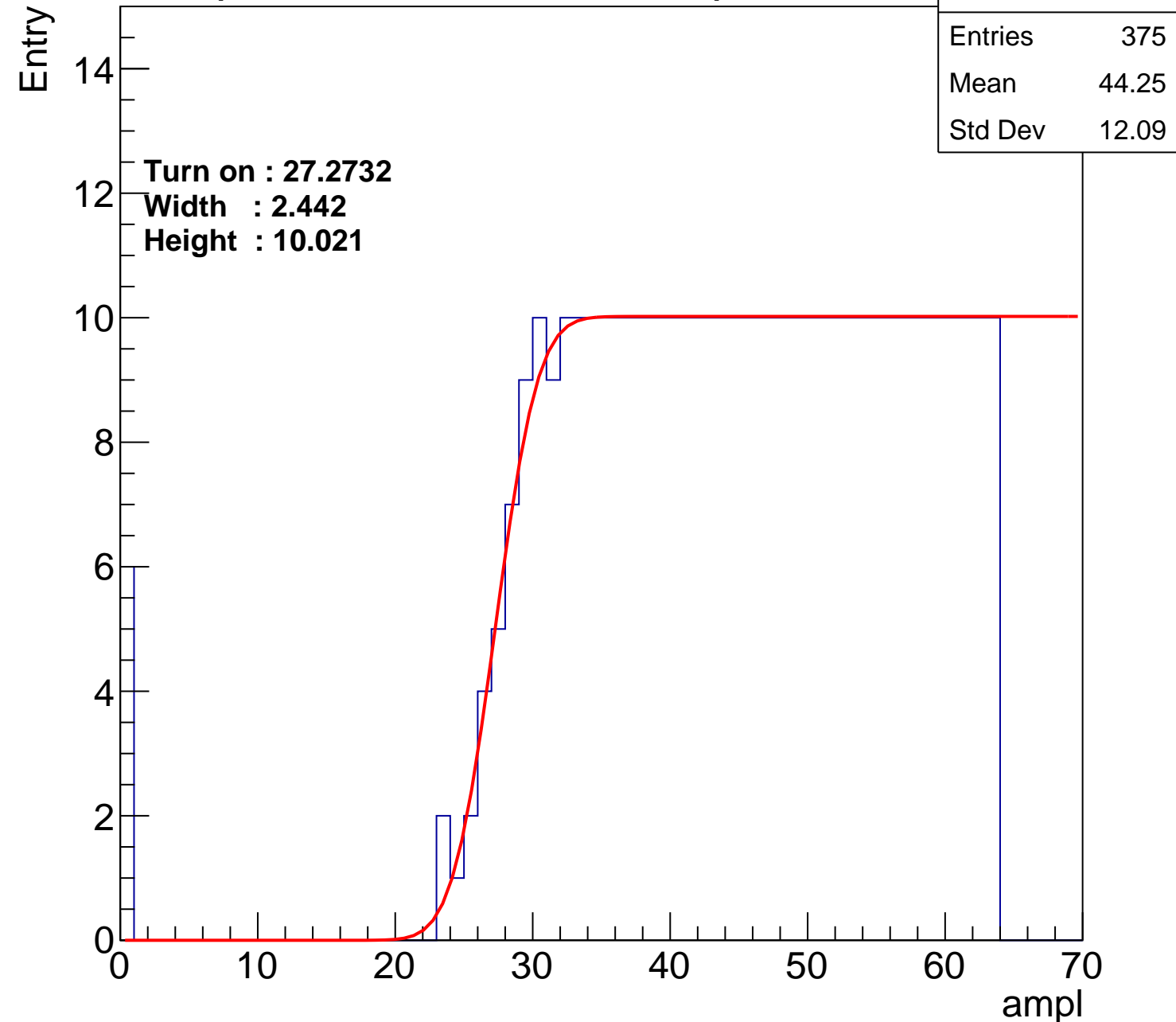
Width : 2.442

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch102

calib_packv5_042523_0143.root, FC#10, port B3

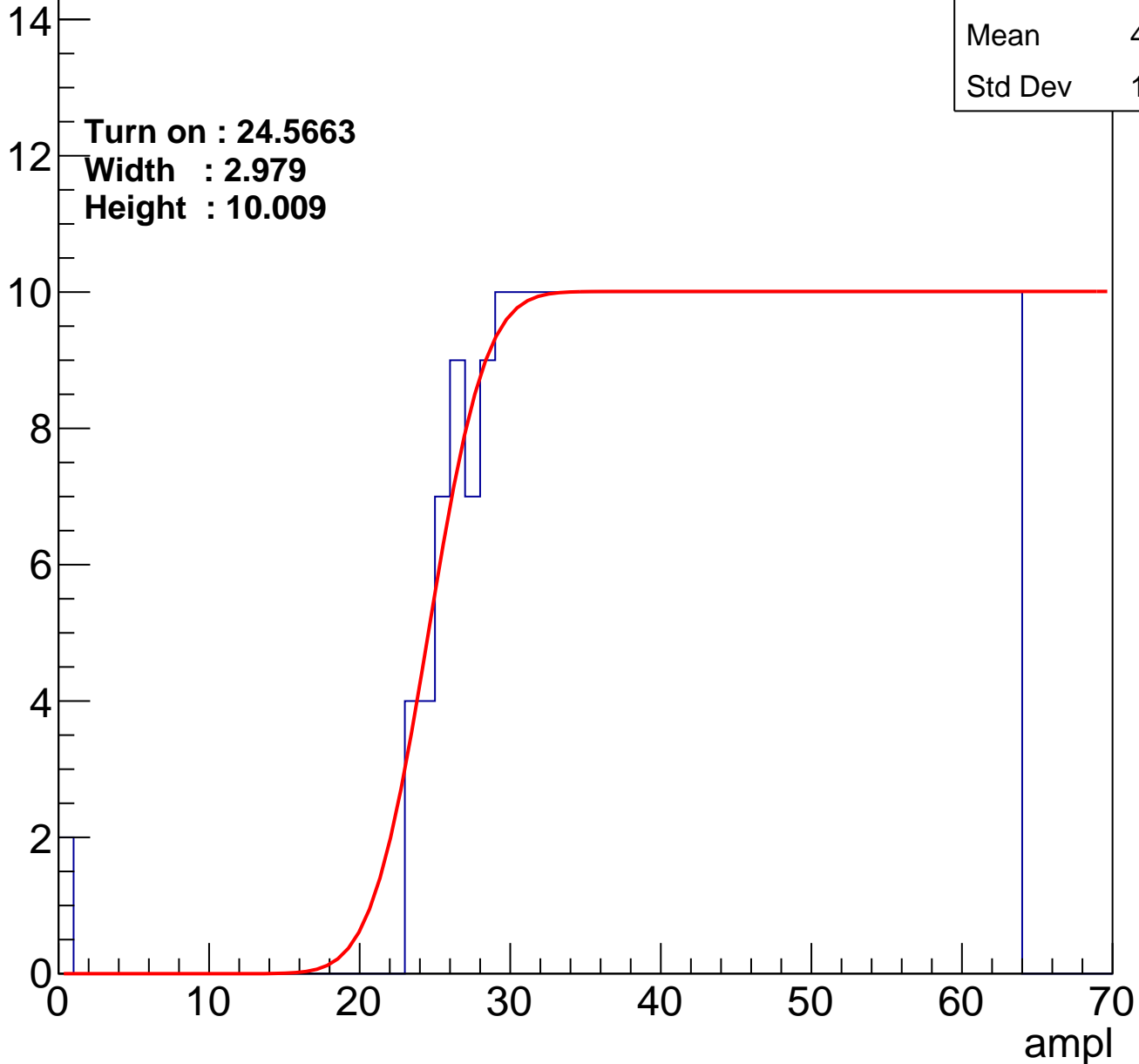
Entries	392
Mean	43.72
Std Dev	11.75

Turn on : 24.5663

Width : 2.979

Height : 10.009

Entry



B1L002S, U22-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.78

Turn on : 25.6852

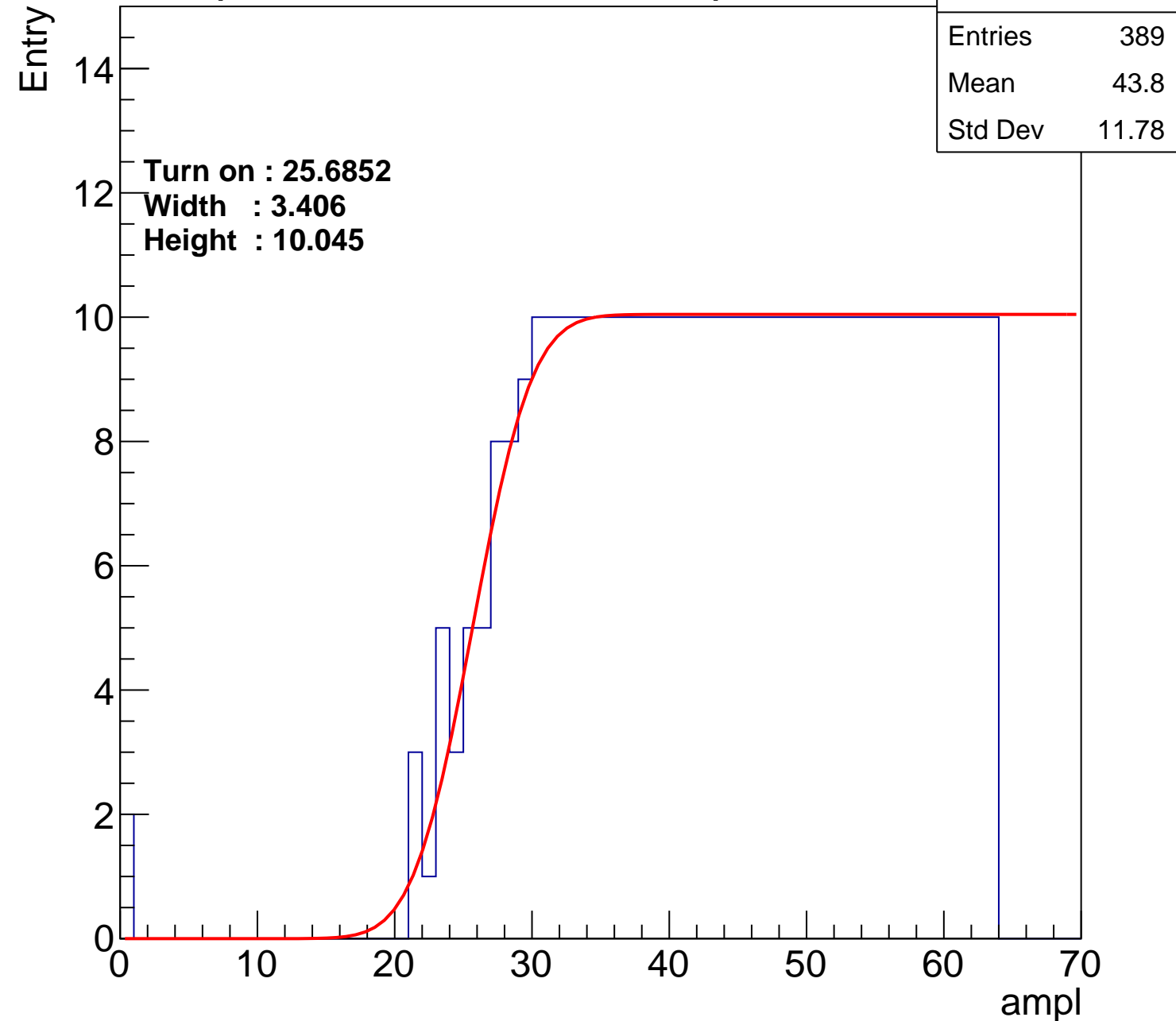
Width : 3.406

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch104

calib_packv5_042523_0143.root, FC#10, port B3

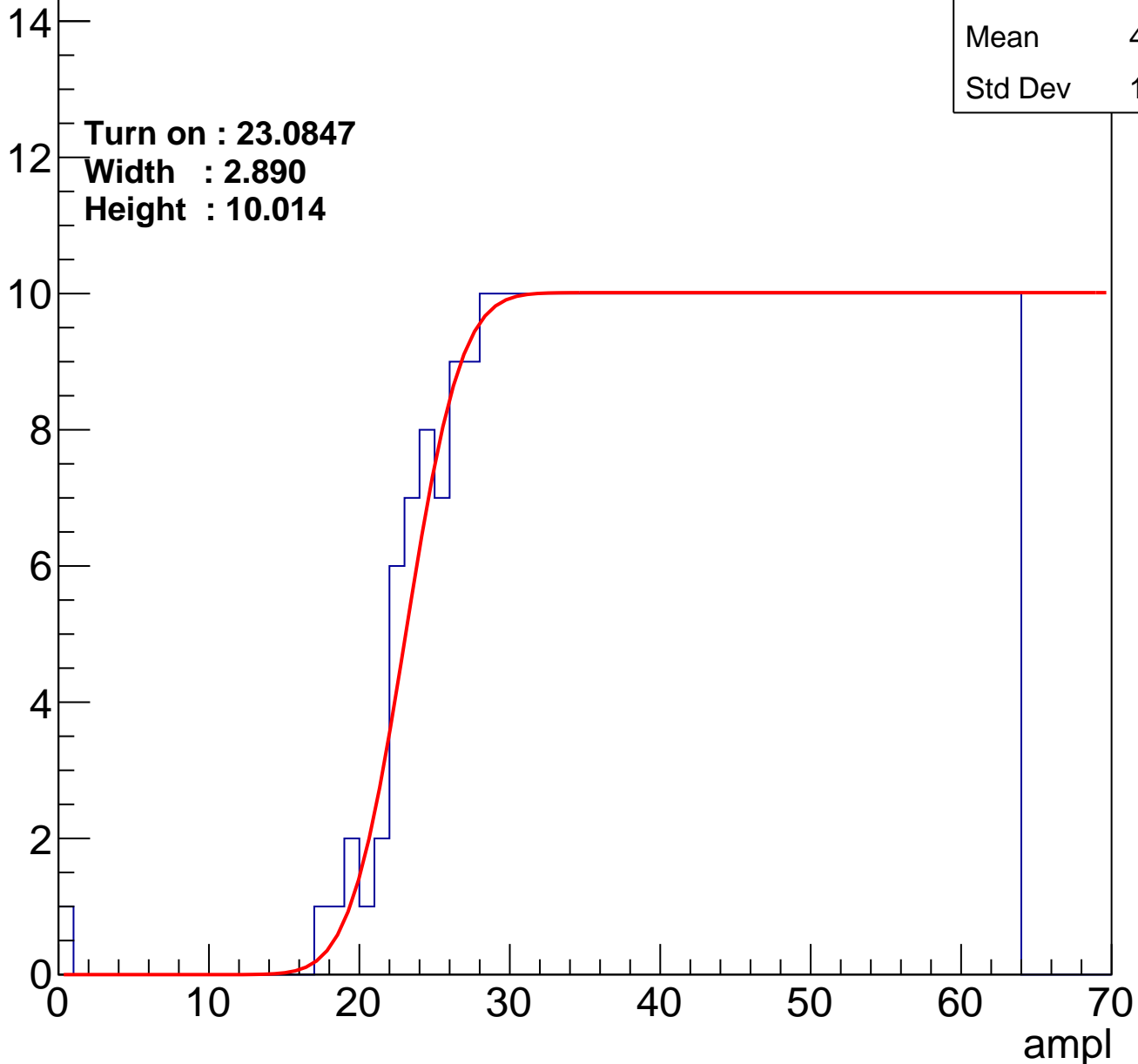
Entries	414
Mean	42.64
Std Dev	12.27

Turn on : 23.0847

Width : 2.890

Height : 10.014

Entry



B1L002S, U22-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.4
Std Dev	11.45

Turn on : 26.8260

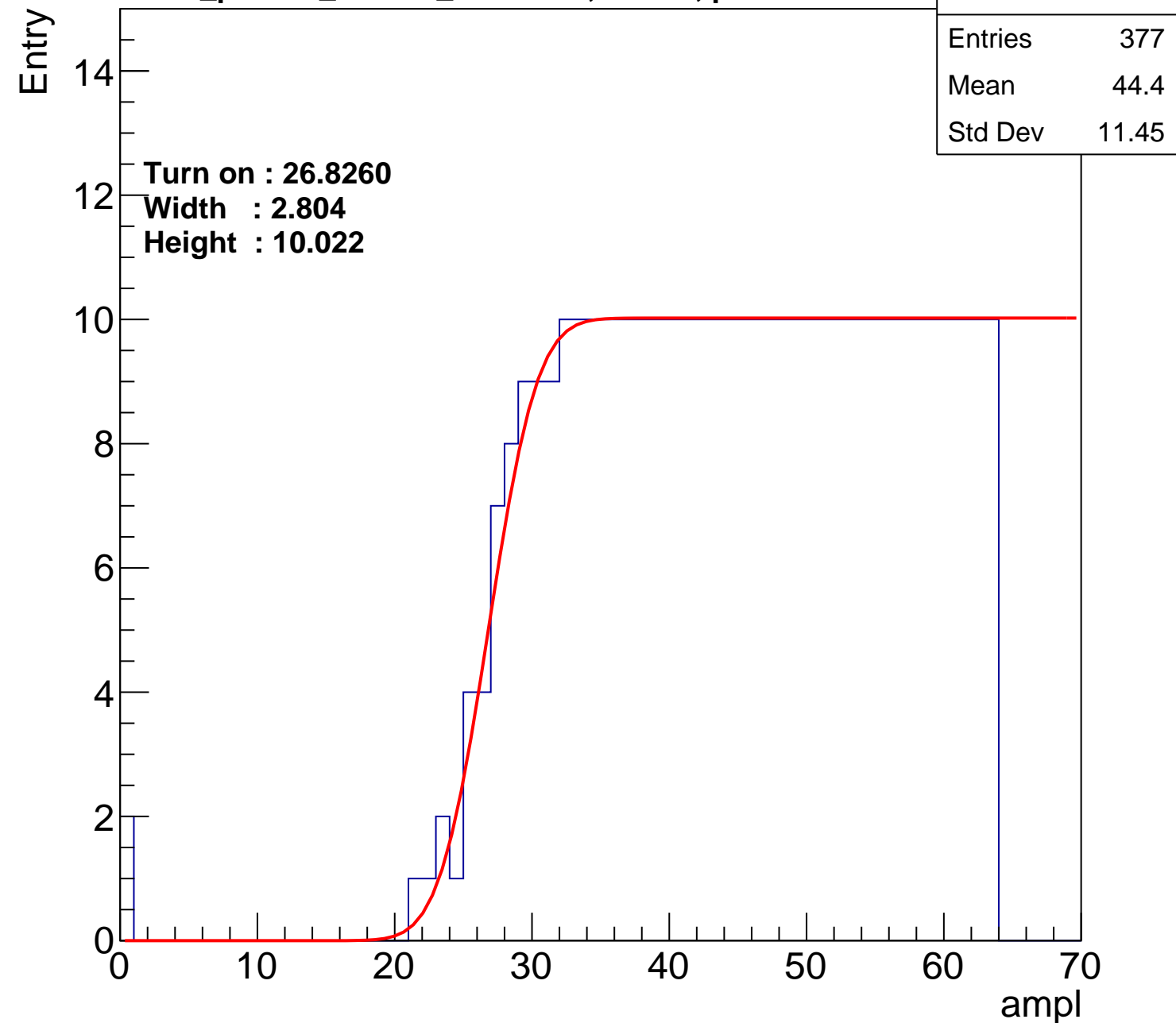
Width : 2.804

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch106

calib_packv5_042523_0143.root, FC#10, port B3

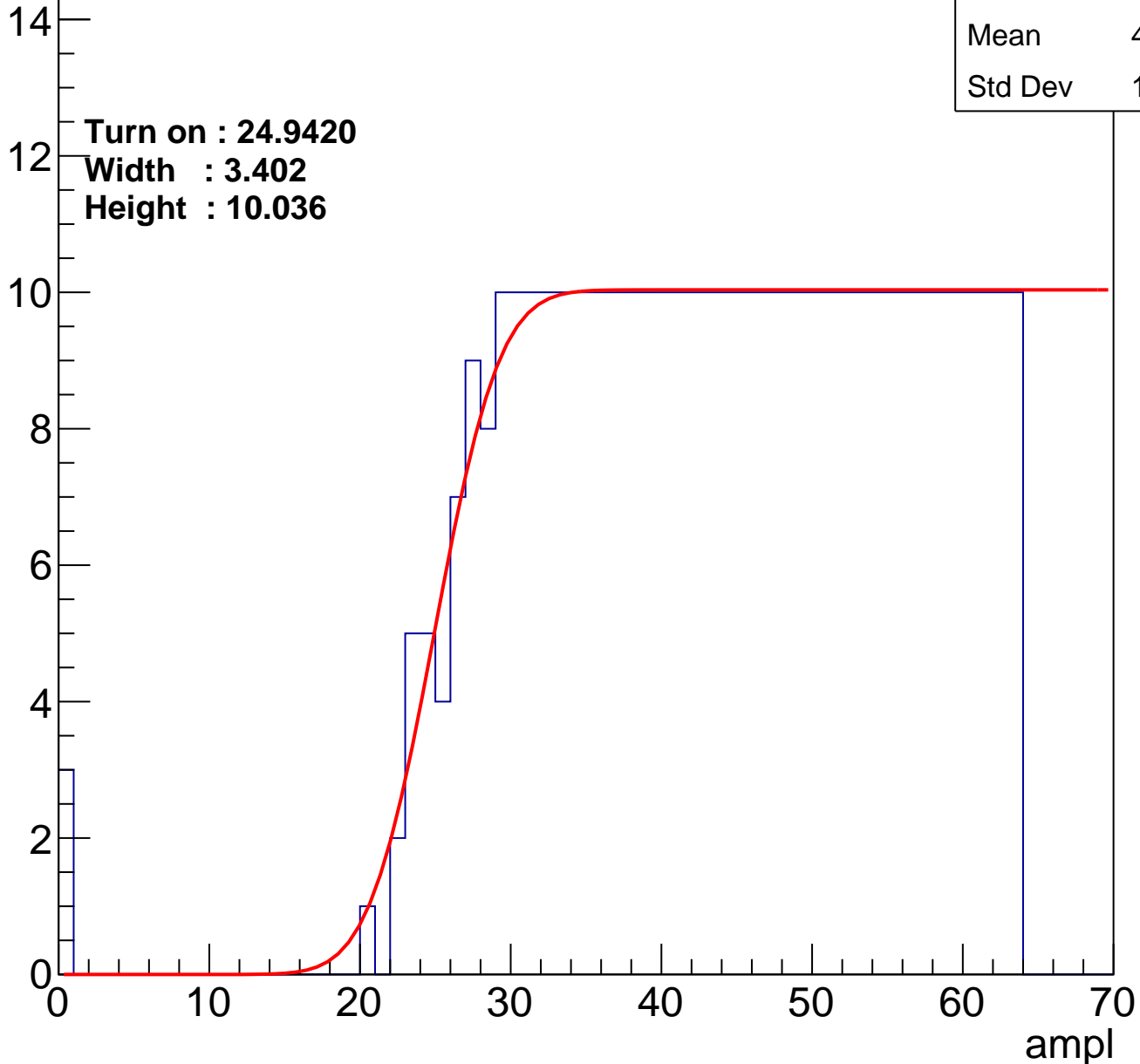
Entries	394
Mean	43.52
Std Dev	12.02

Turn on : 24.9420

Width : 3.402

Height : 10.036

Entry



B1L002S, U22-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.04
Std Dev	12.2

Turn on : 27.4208

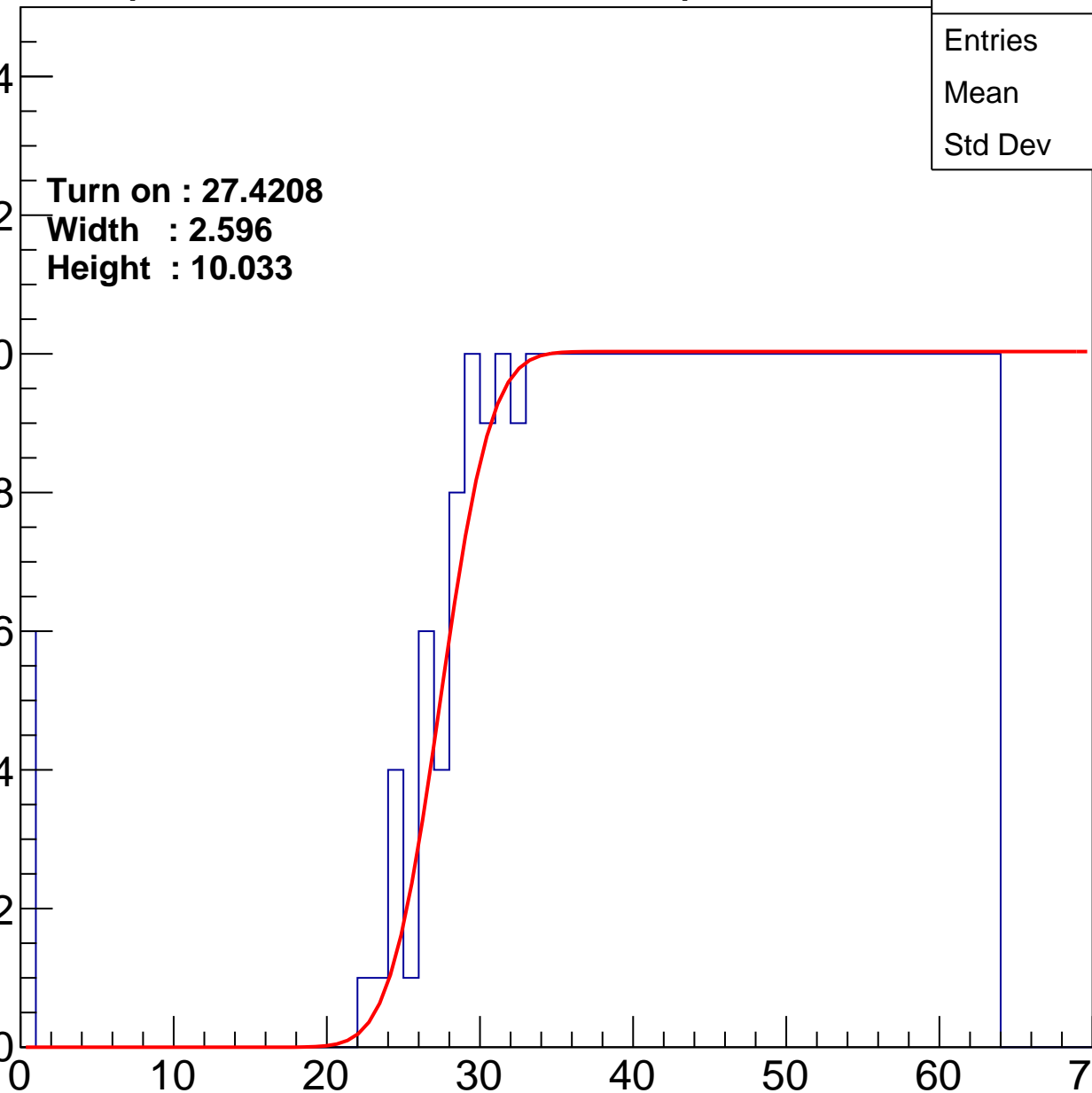
Width : 2.596

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch108

calib_packv5_042523_0143.root, FC#10, port B3

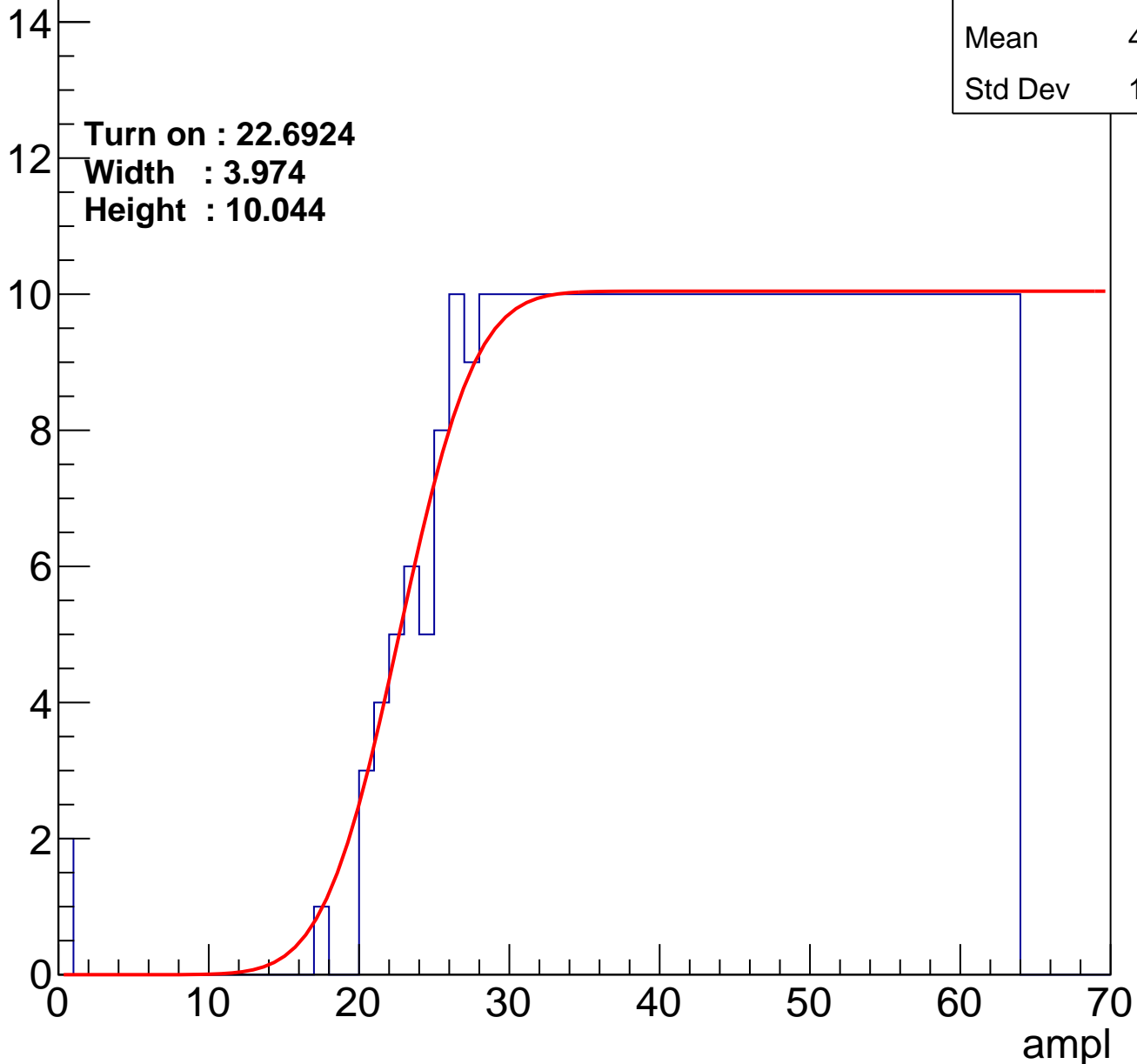
Entries	413
Mean	42.64
Std Dev	12.36

Turn on : 22.6924

Width : 3.974

Height : 10.044

Entry



B1L002S, U22-ch109

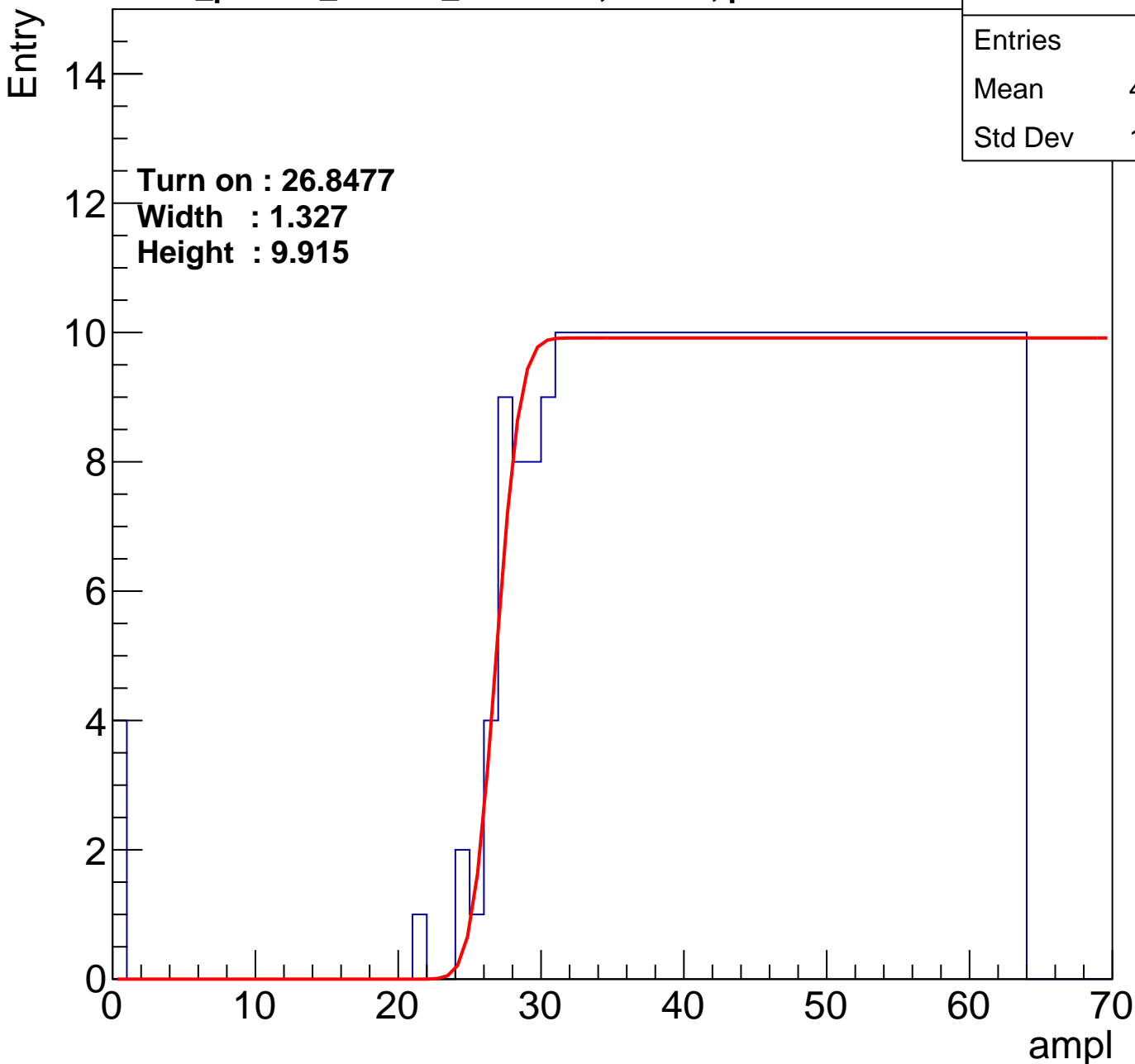
calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 26.8477

Width : 1.327

Height : 9.915



B1L002S, U22-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.5513

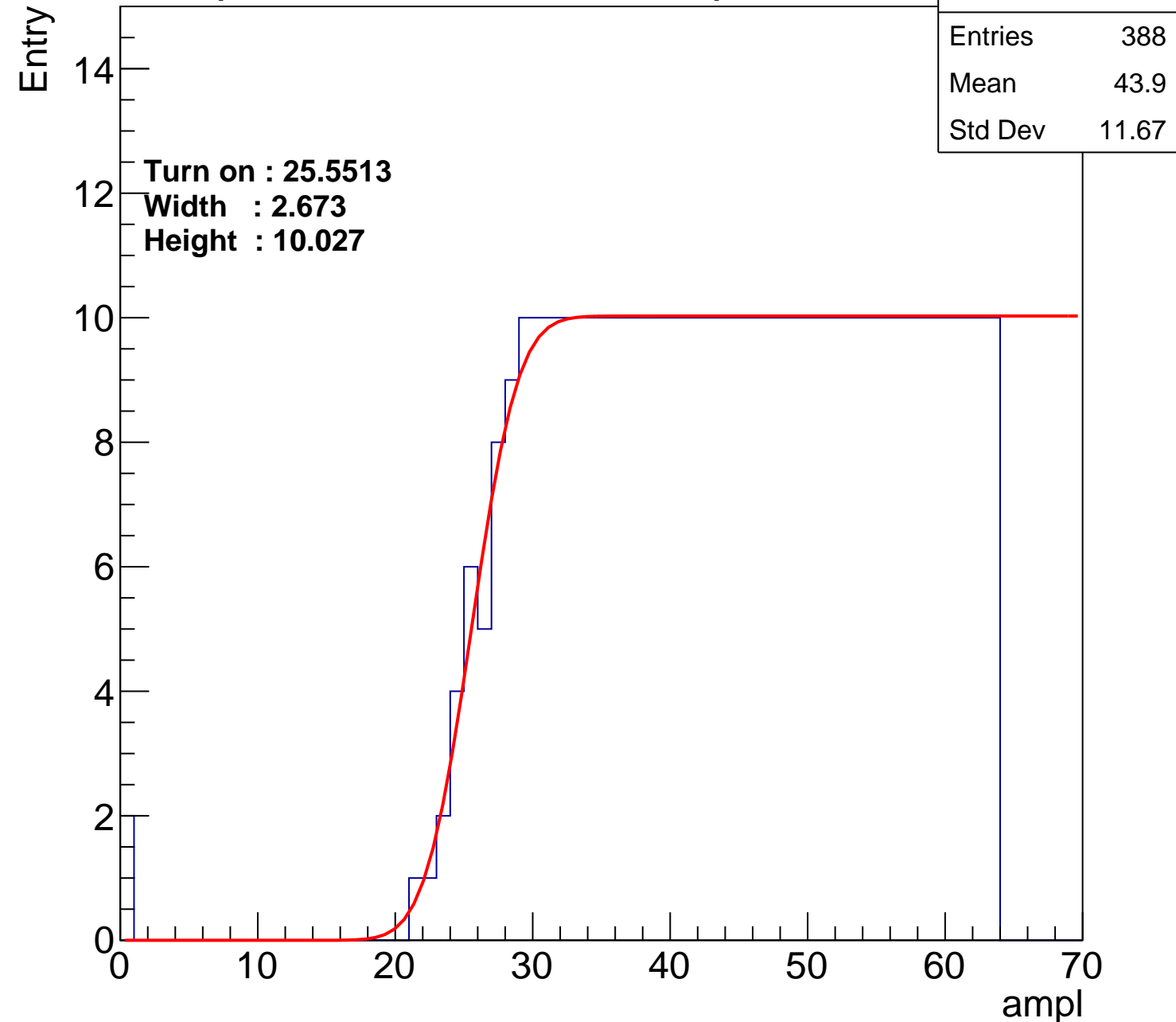
Width : 2.673

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.28
Std Dev	12.29

Turn on : 24.7343

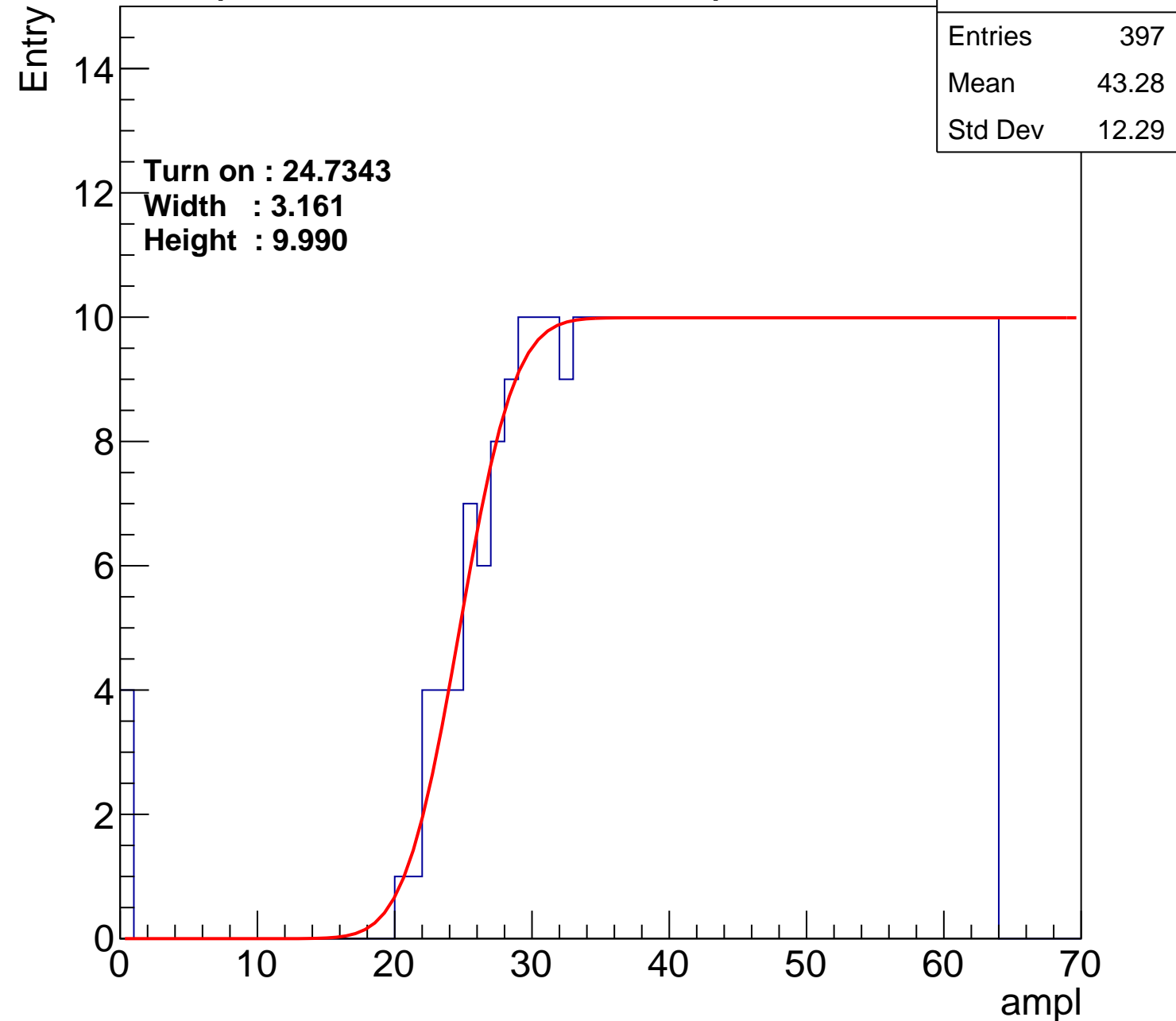
Width : 3.161

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	43.87
Std Dev	12.43

Turn on : 26.6381

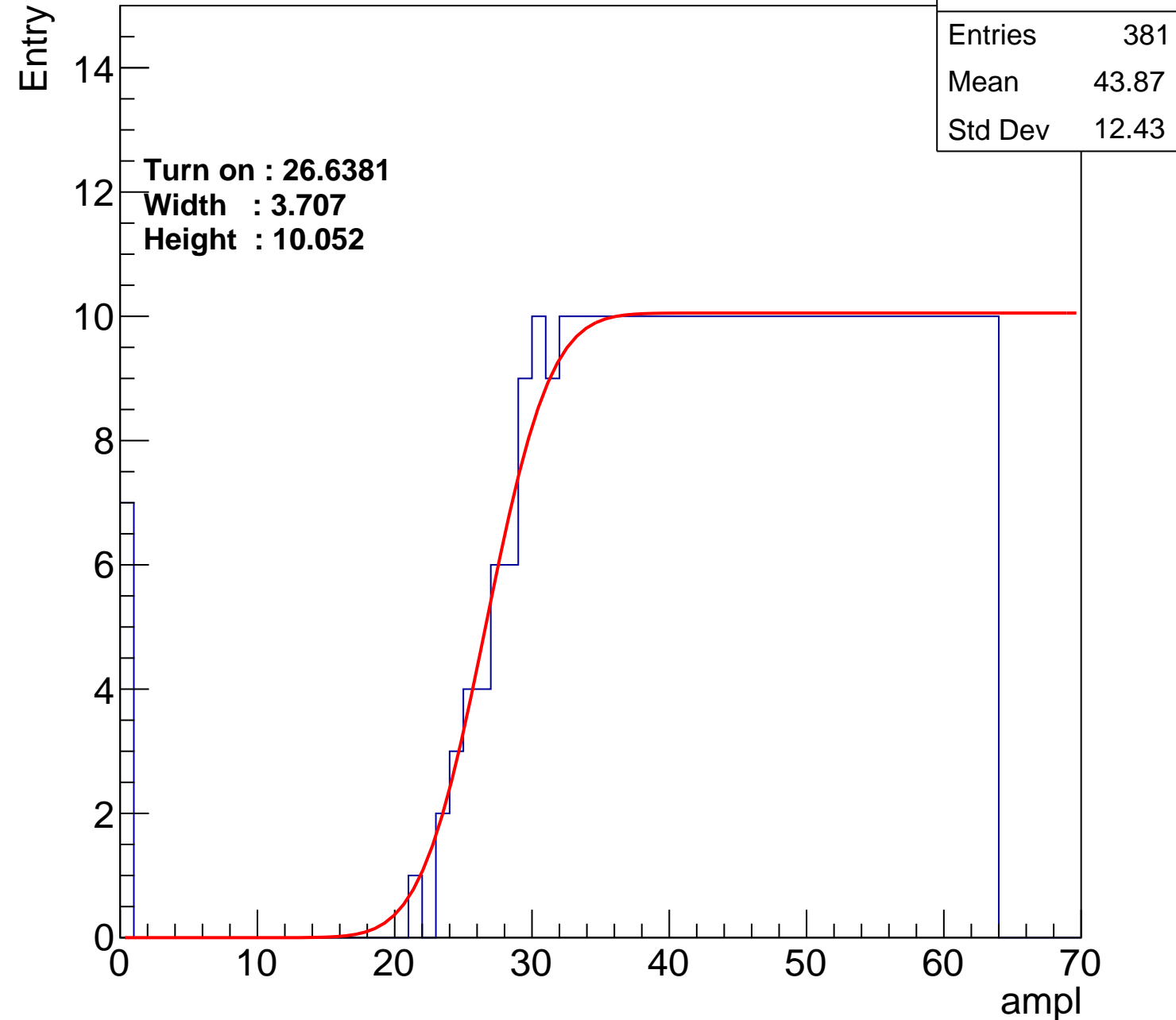
Width : 3.707

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.54
Std Dev	11.79

Turn on : 27.5583

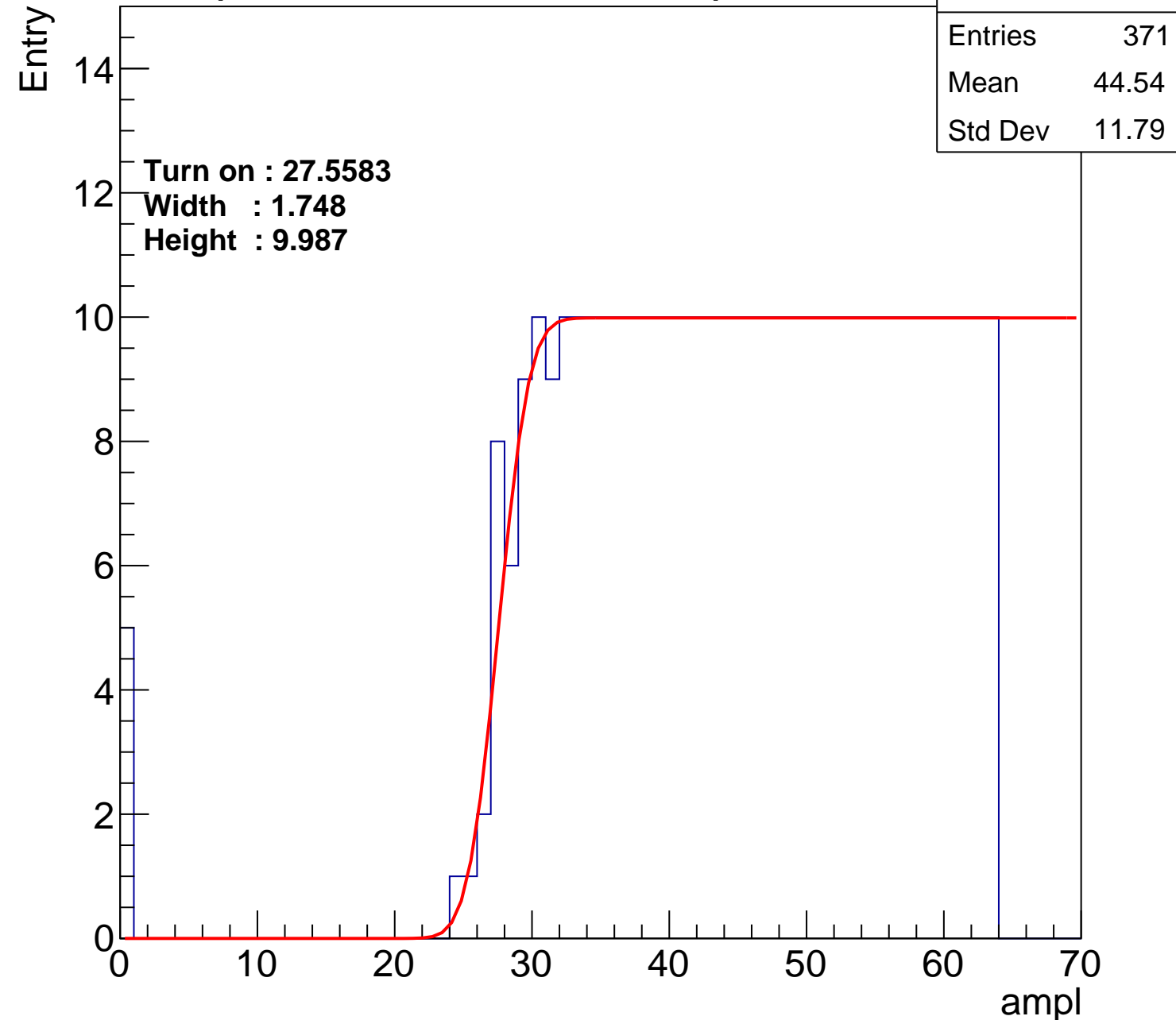
Width : 1.748

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.0740

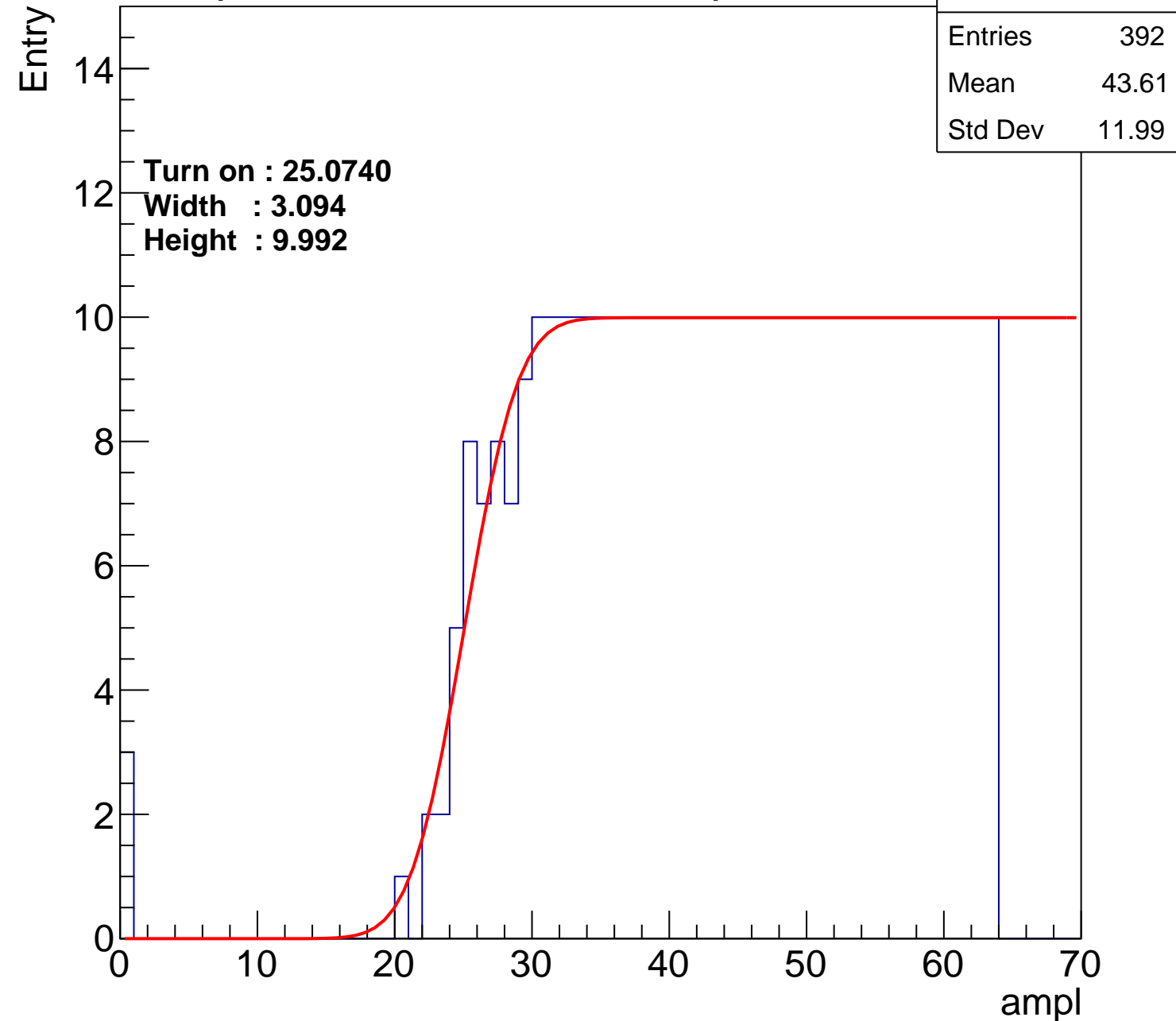
Width : 3.094

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch115

calib_packv5_042523_0143.root, FC#10, port B3

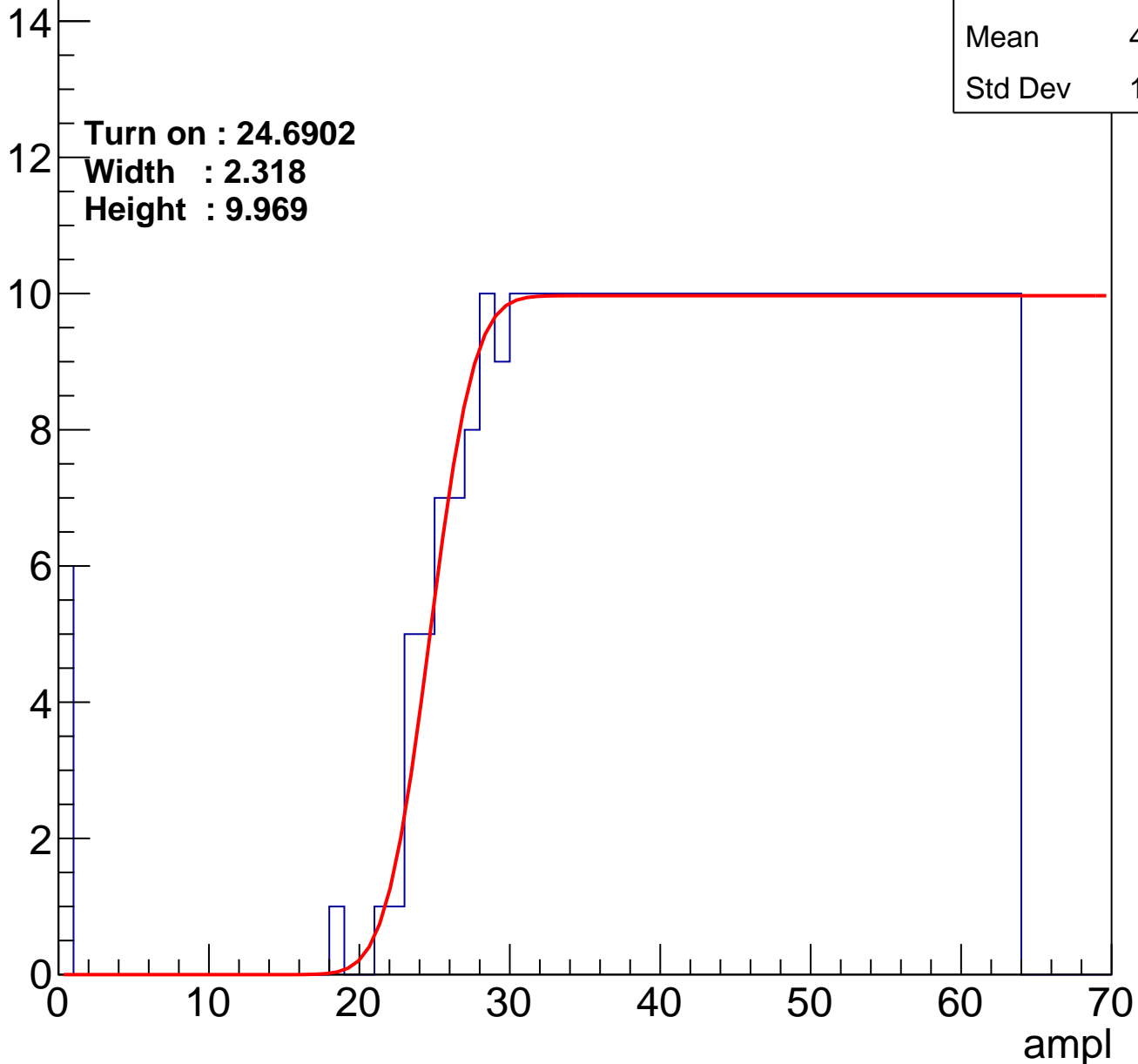
Entries	400
Mean	43.05
Std Dev	12.62

Turn on : 24.6902

Width : 2.318

Height : 9.969

Entry



B1L002S, U22-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.14
Std Dev	12.1

Turn on : 24.3208

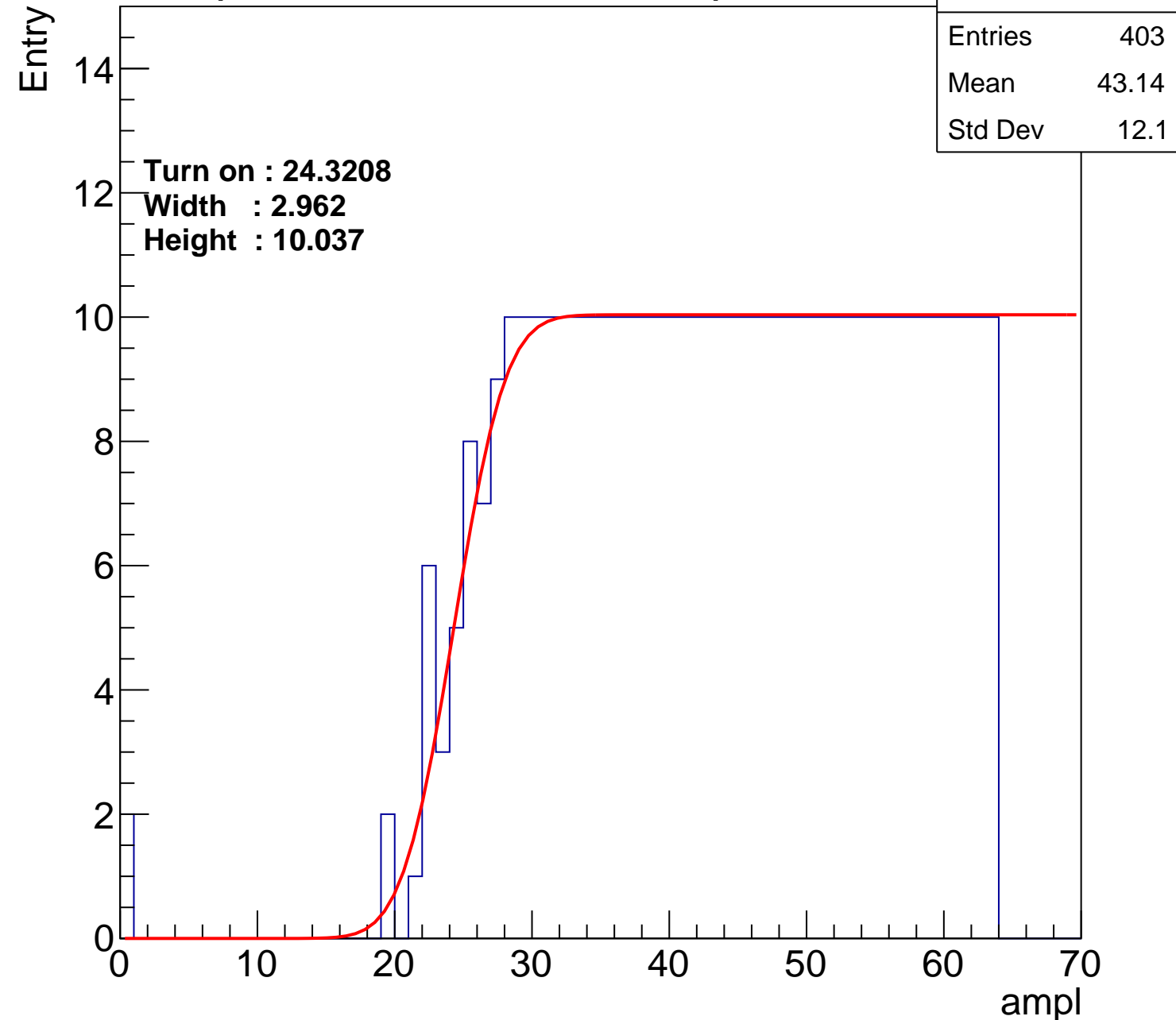
Width : 2.962

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.87
Std Dev	11.9

Turn on : 25.9762

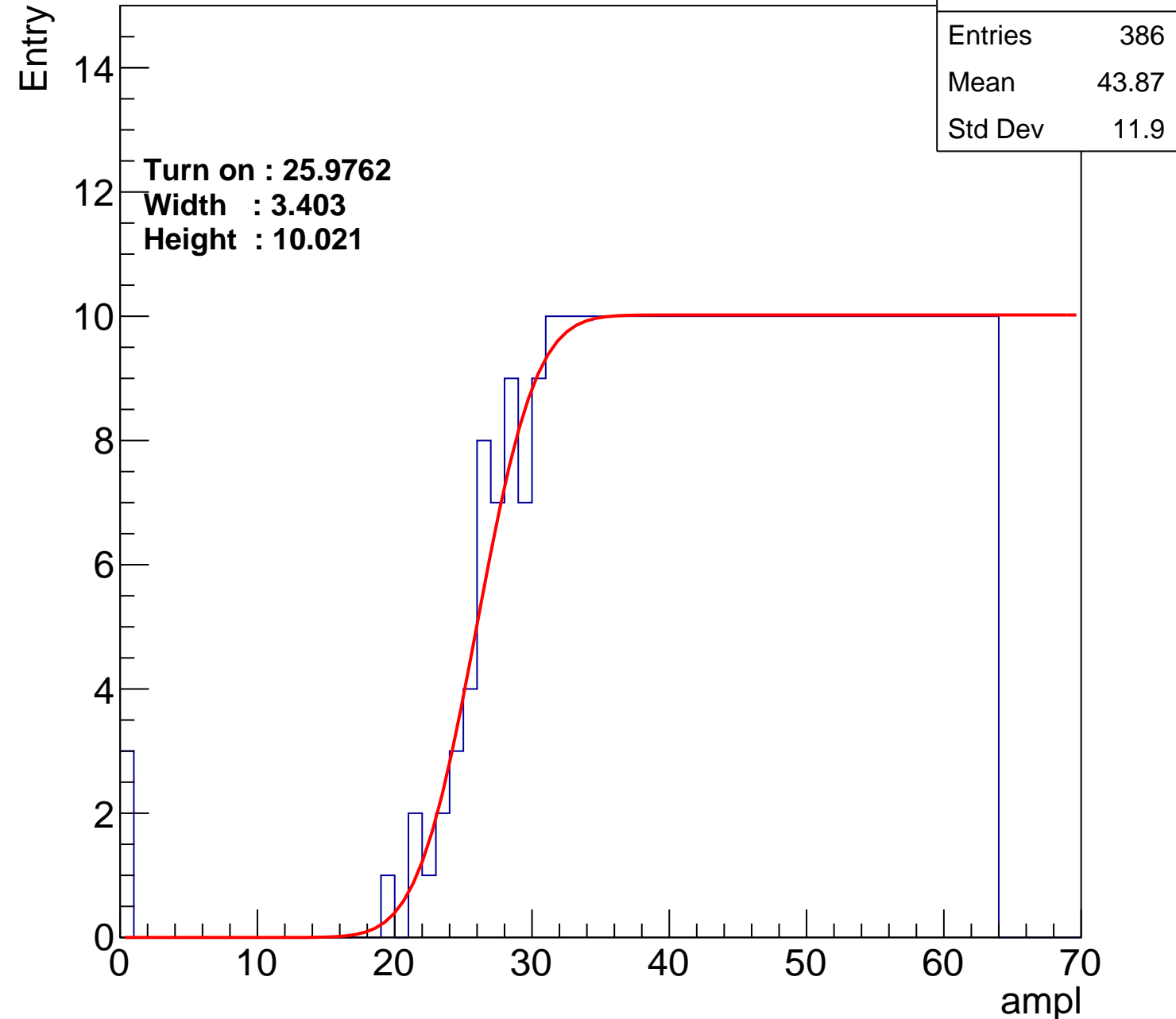
Width : 3.403

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.3
Std Dev	11.99

Turn on : 23.7642

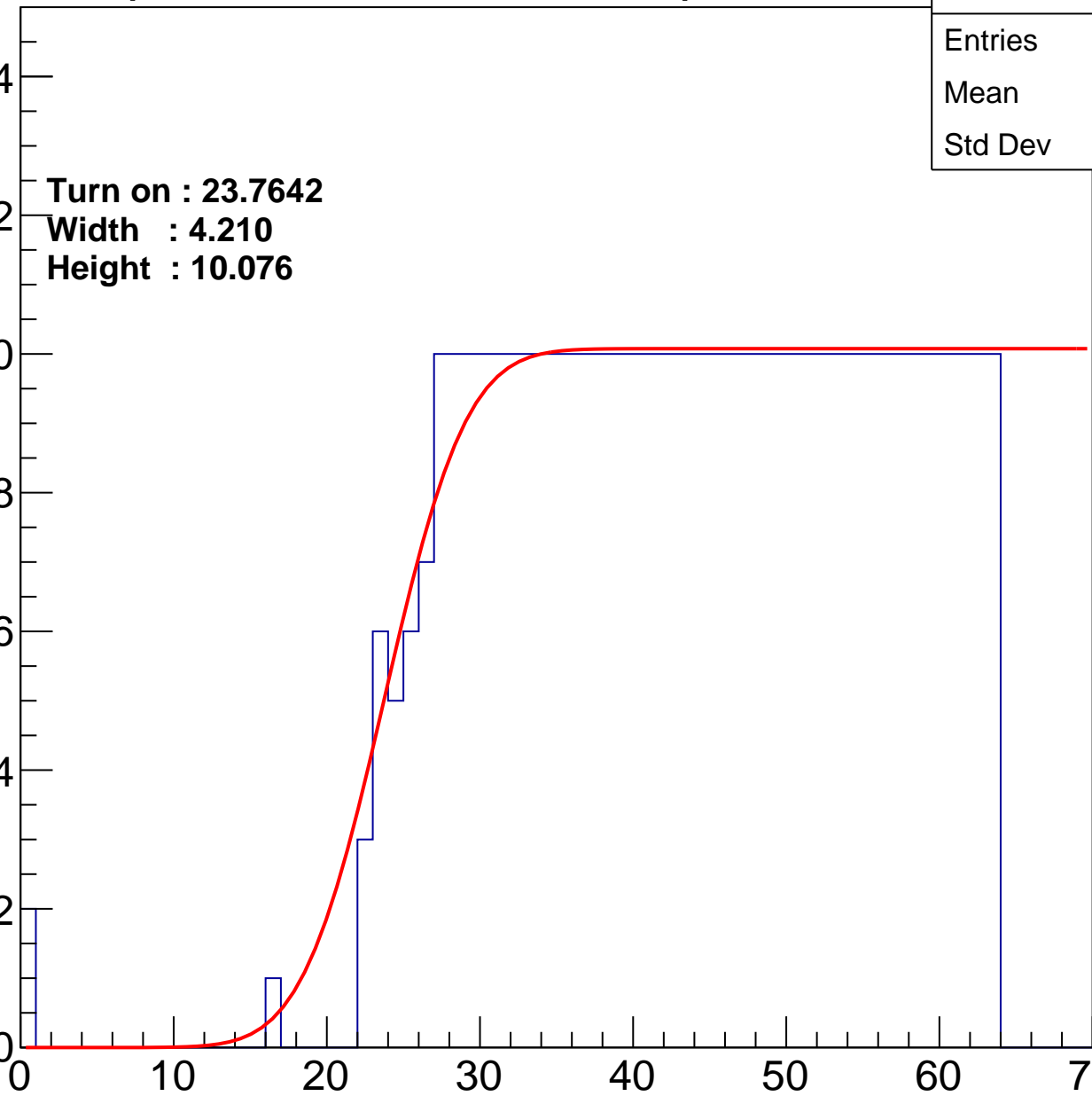
Width : 4.210

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch119

calib_packv5_042523_0143.root, FC#10, port B3

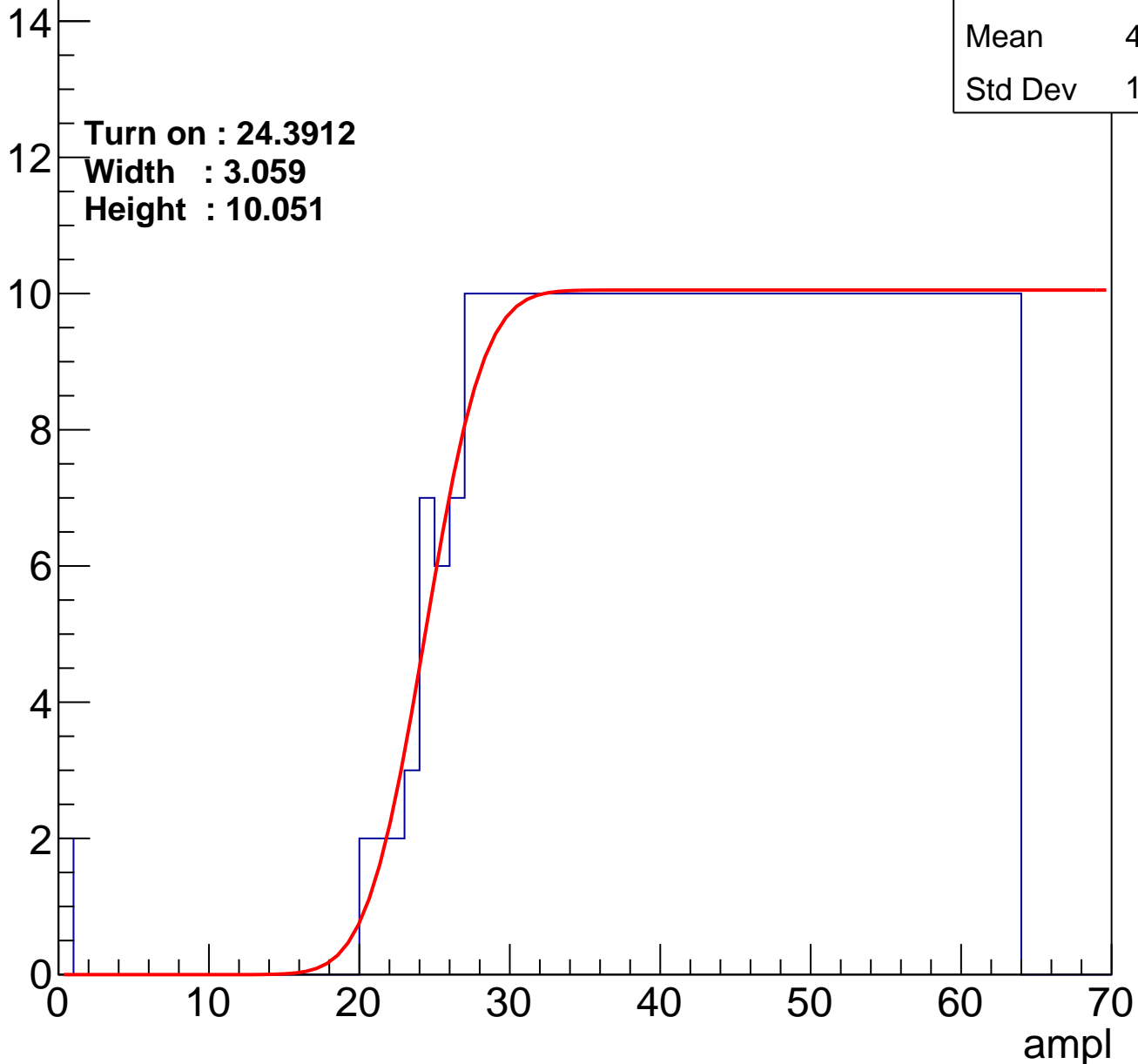
Entries	401
Mean	43.25
Std Dev	12.02

Turn on : 24.3912

Width : 3.059

Height : 10.051

Entry



B1L002S, U22-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.34
Std Dev	11.99

Turn on : 24.3153

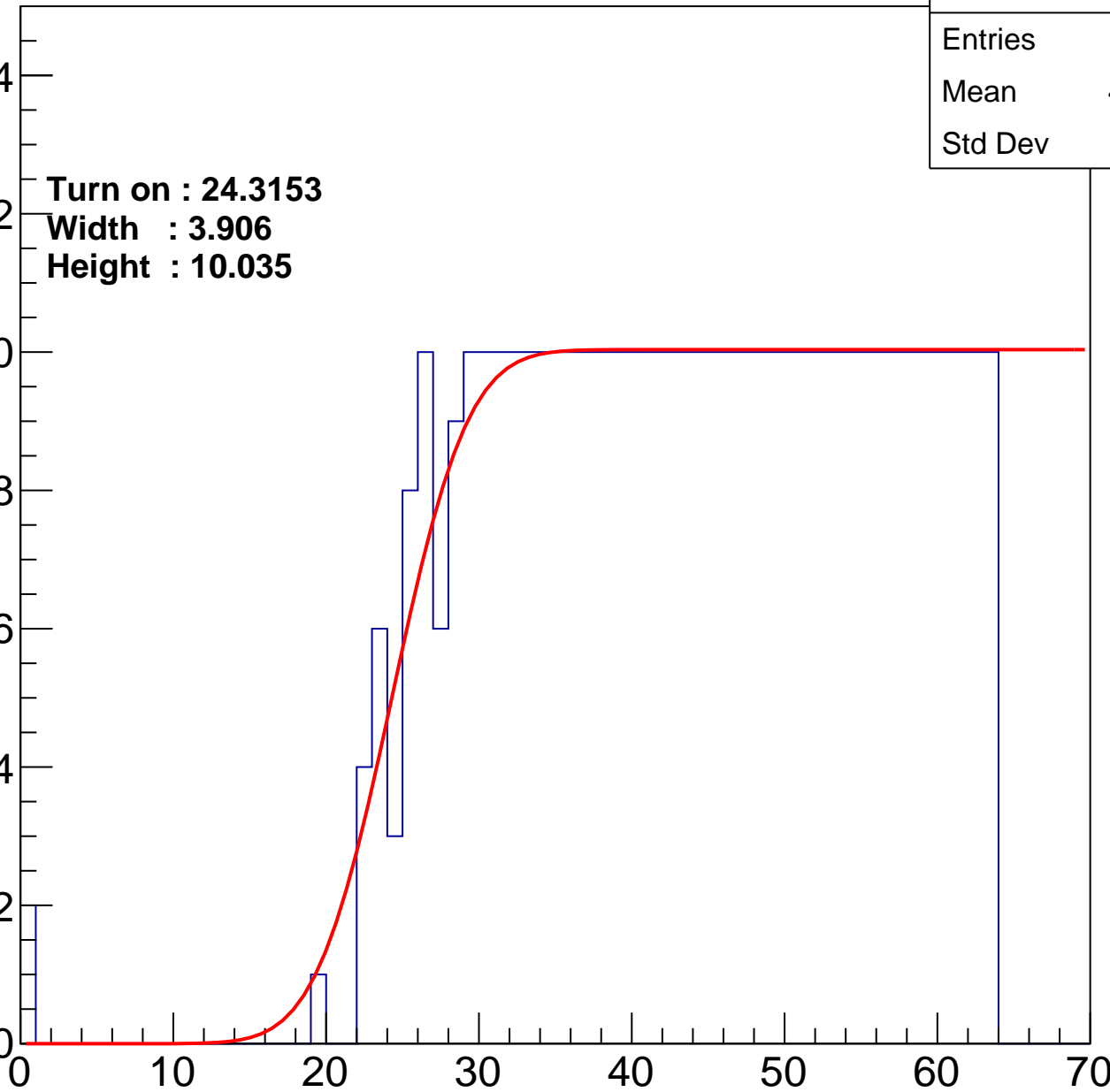
Width : 3.906

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.03
Std Dev	11.45

Turn on : 25.4877

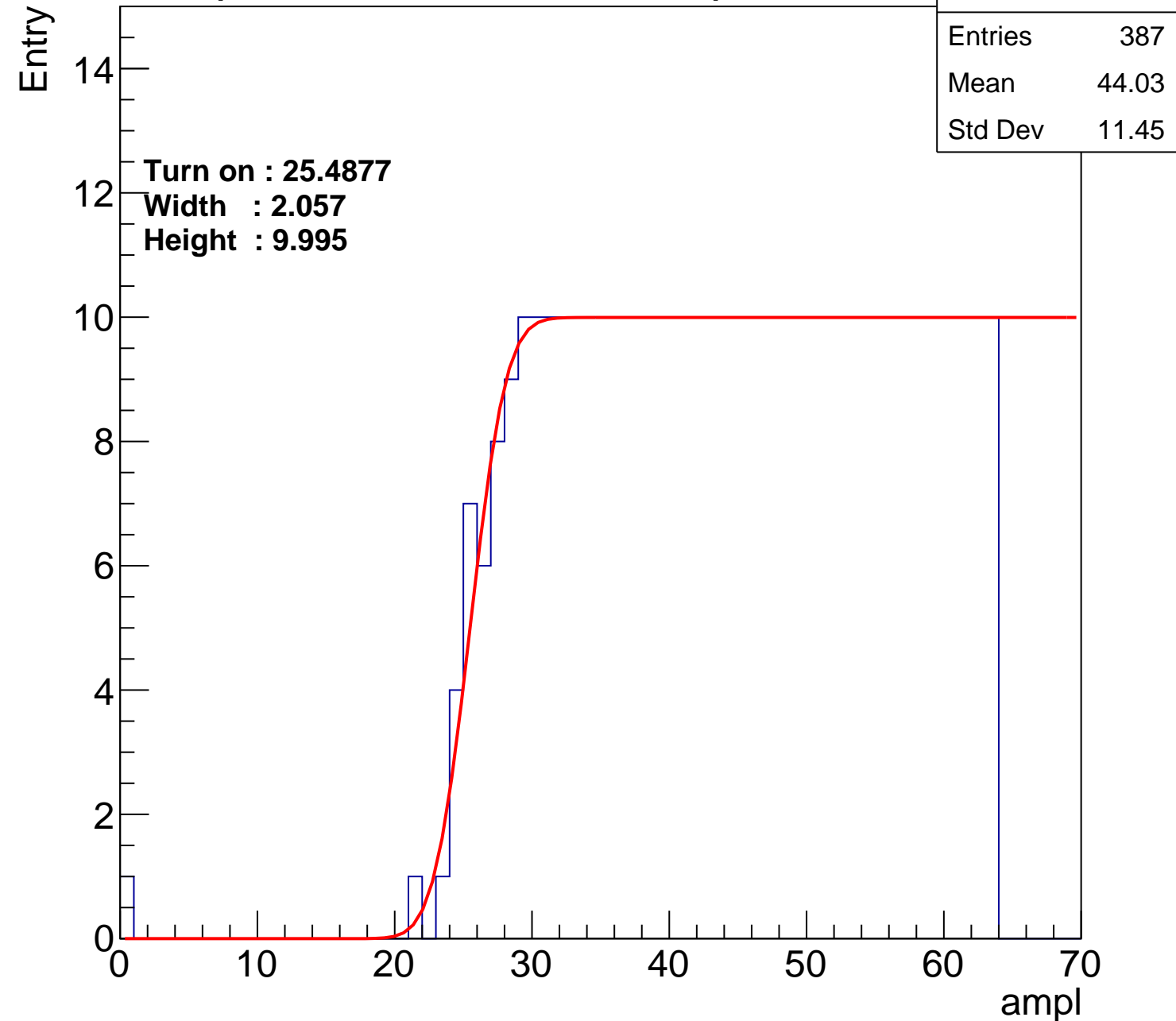
Width : 2.057

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 25.9728

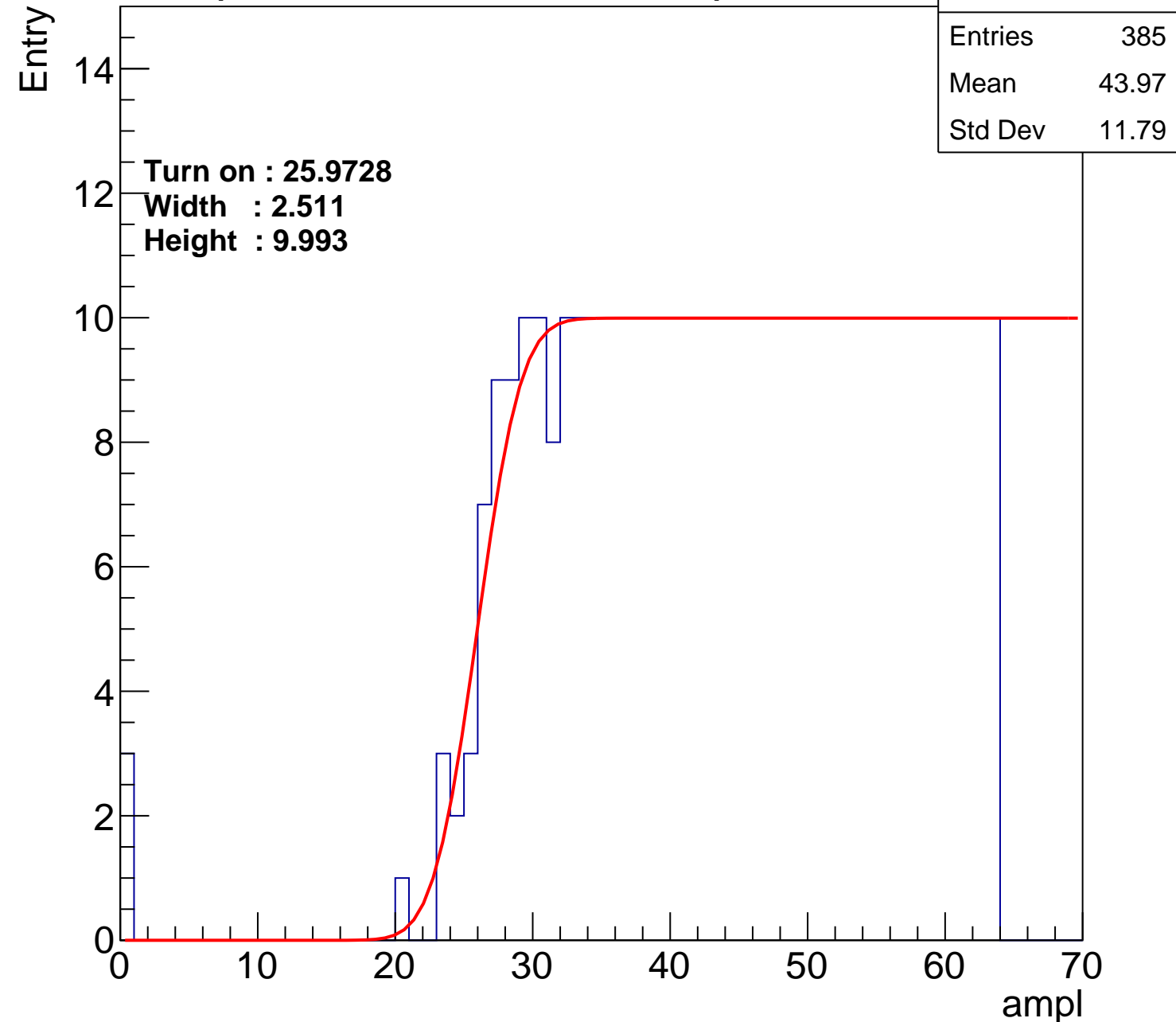
Width : 2.511

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 25.1495

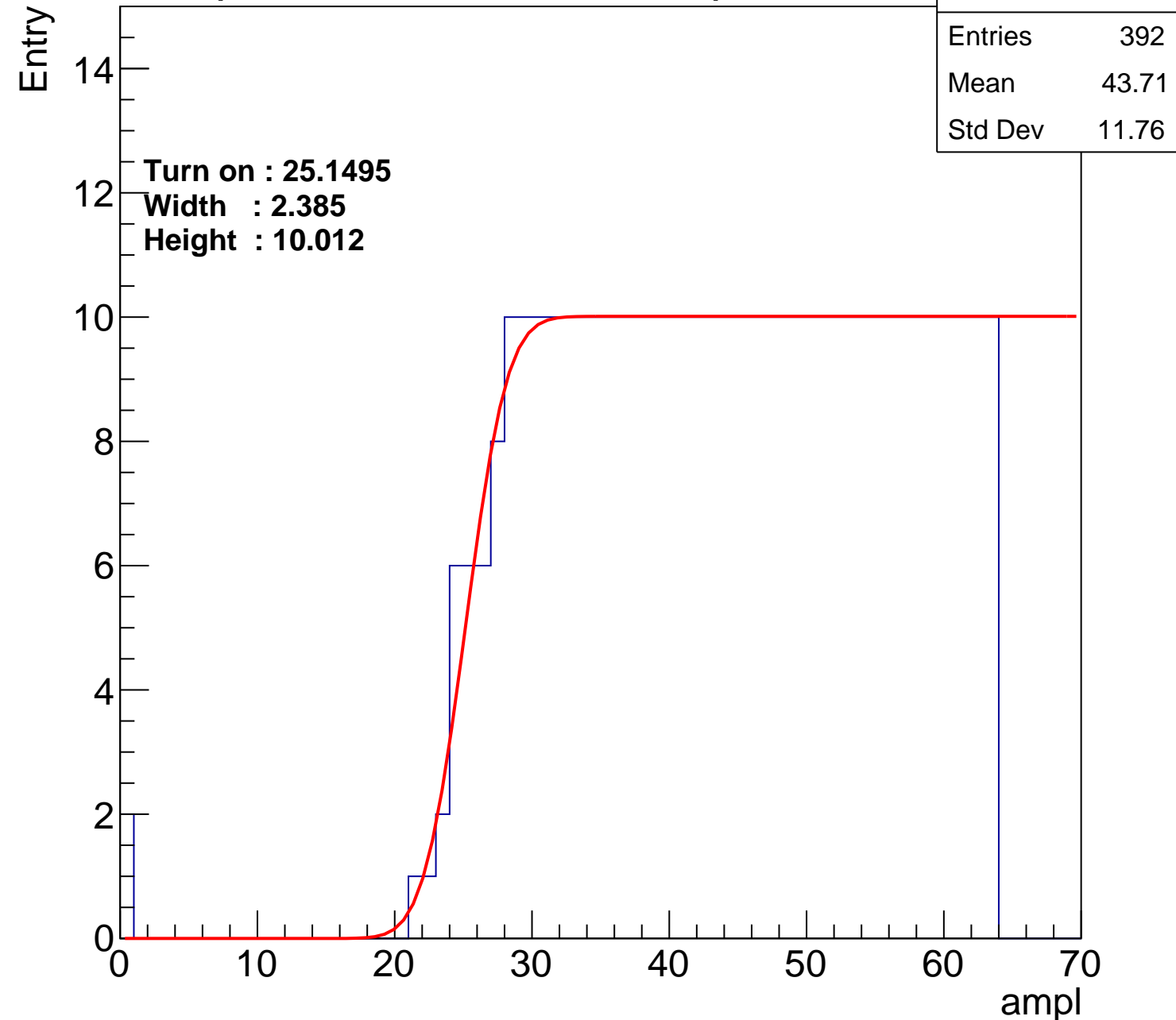
Width : 2.385

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.82
Std Dev	12.13

Turn on : 23.0363

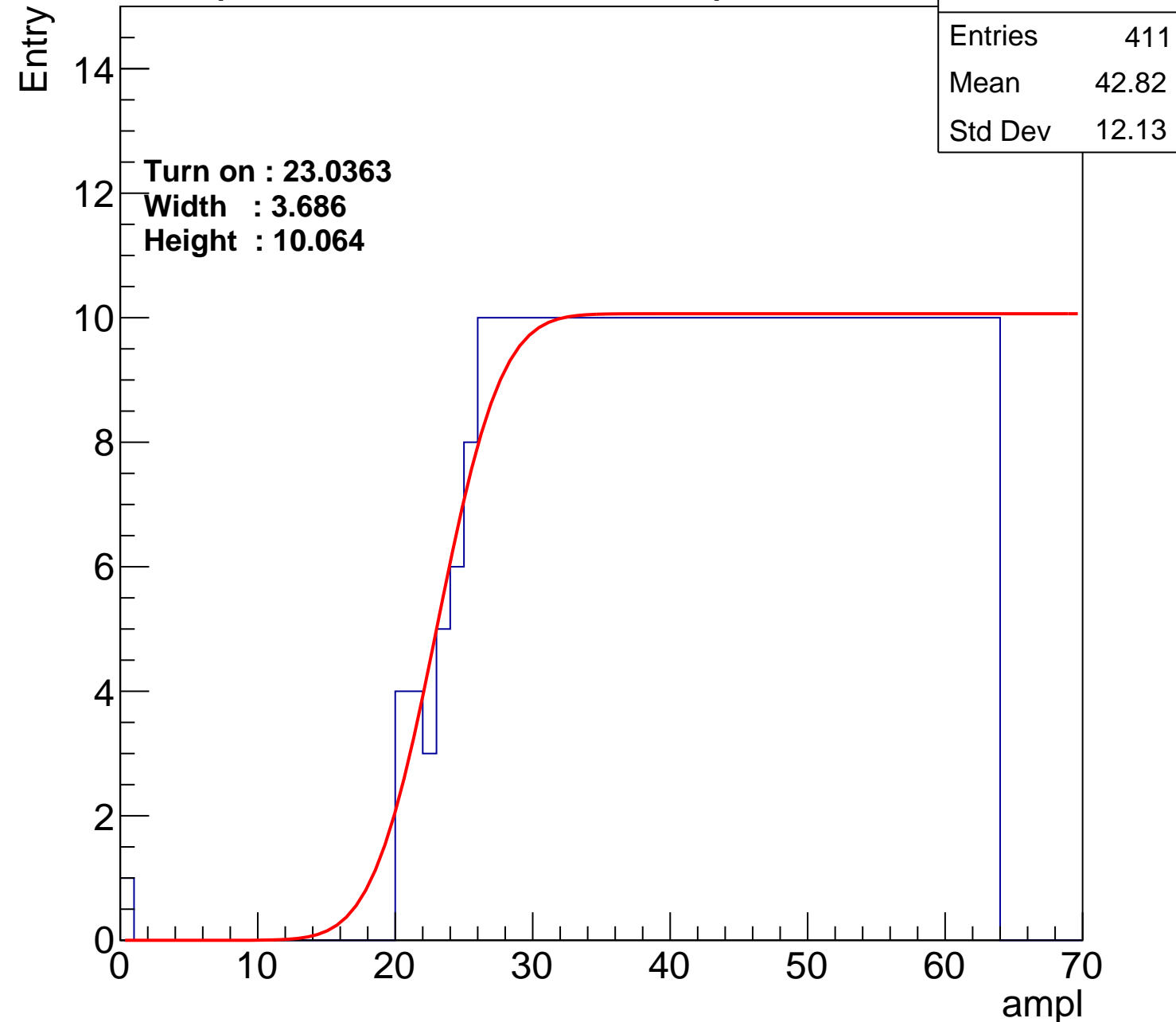
Width : 3.686

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.49
Std Dev	11.91

Turn on : 24.7881

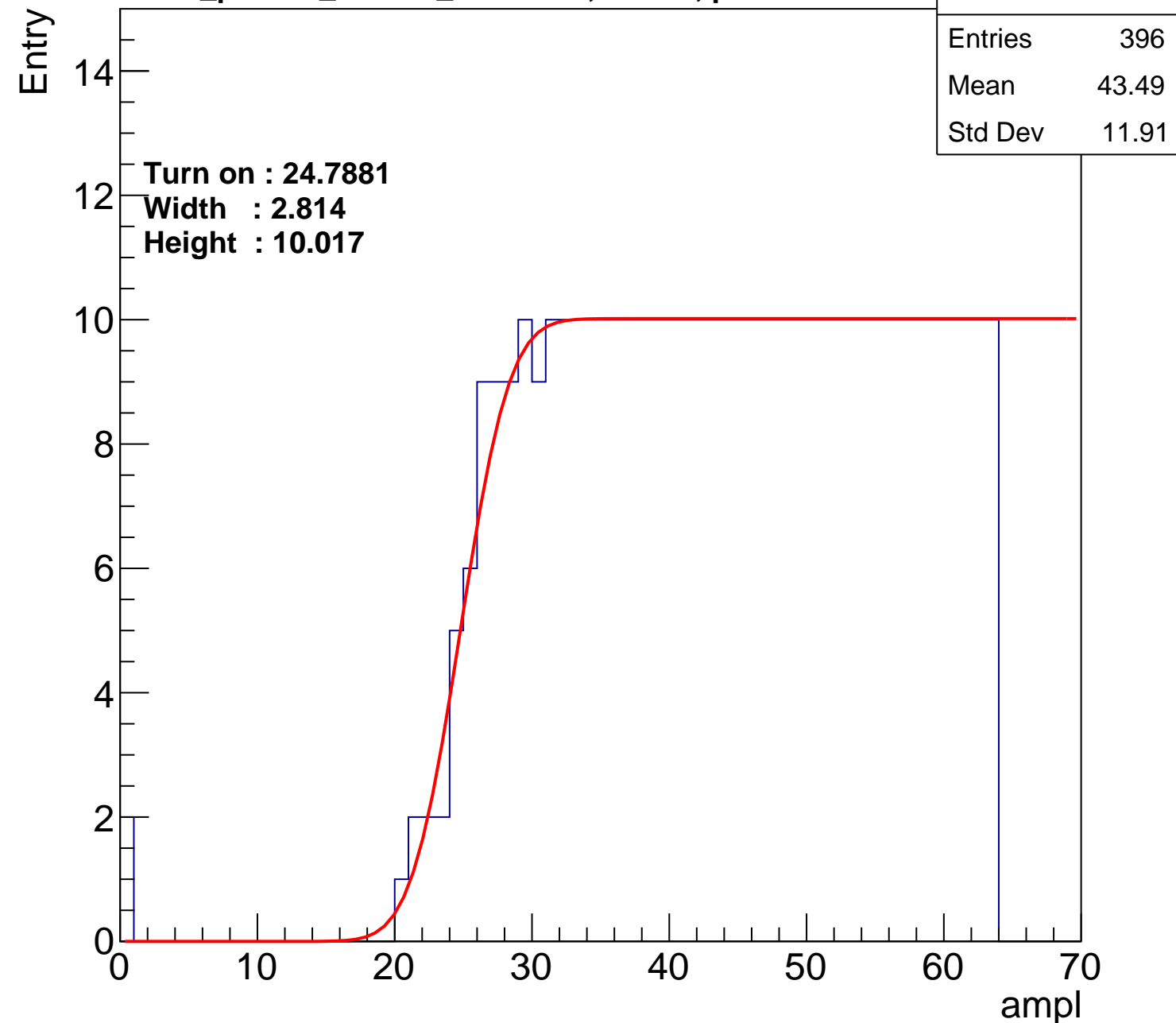
Width : 2.814

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch126

calib_packv5_042523_0143.root, FC#10, port B3

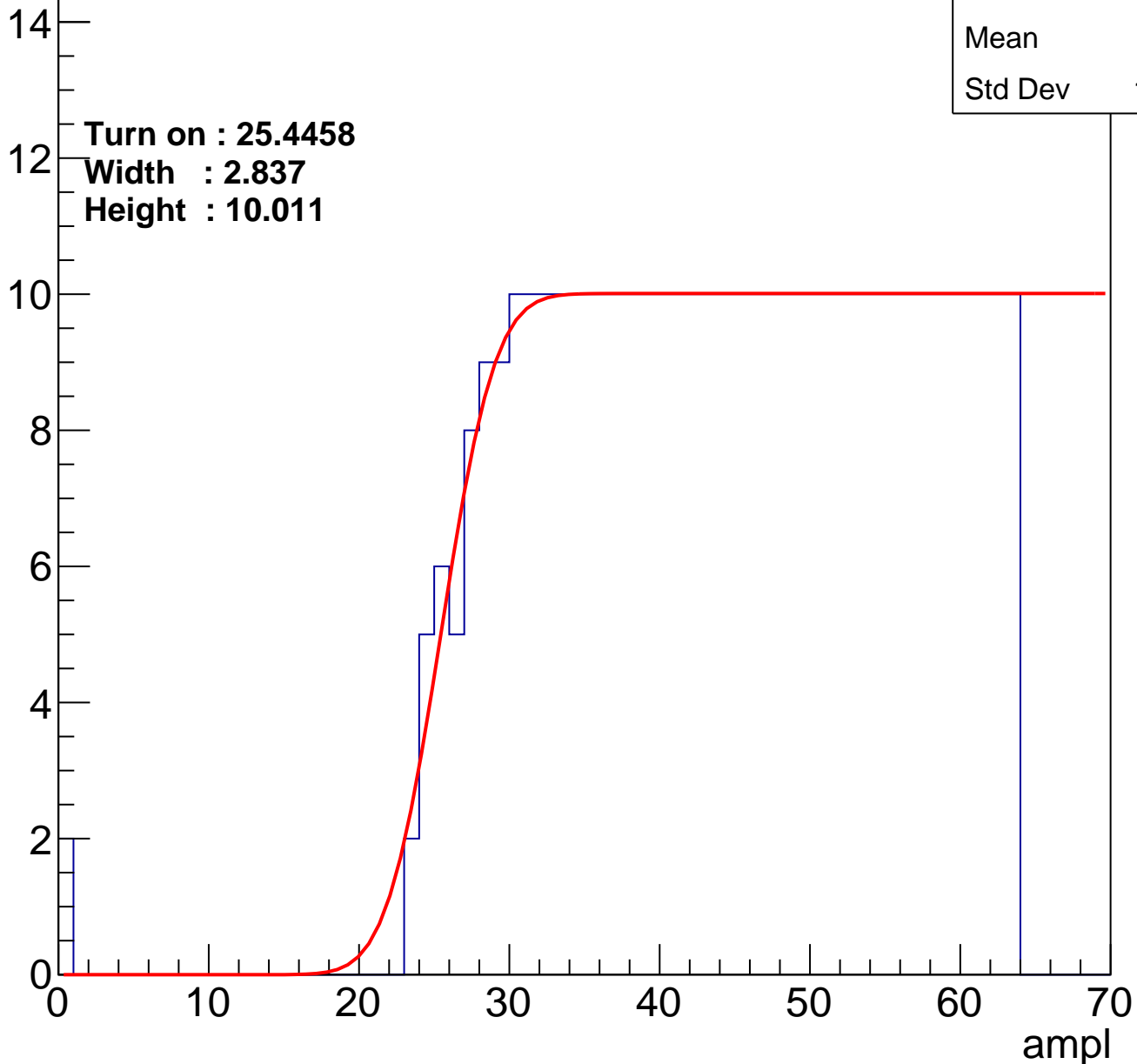
Entries	386
Mean	44
Std Dev	11.61

Turn on : 25.4458

Width : 2.837

Height : 10.011

Entry



B1L002S, U22-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.27
Std Dev	11.81

Turn on : 27.2765

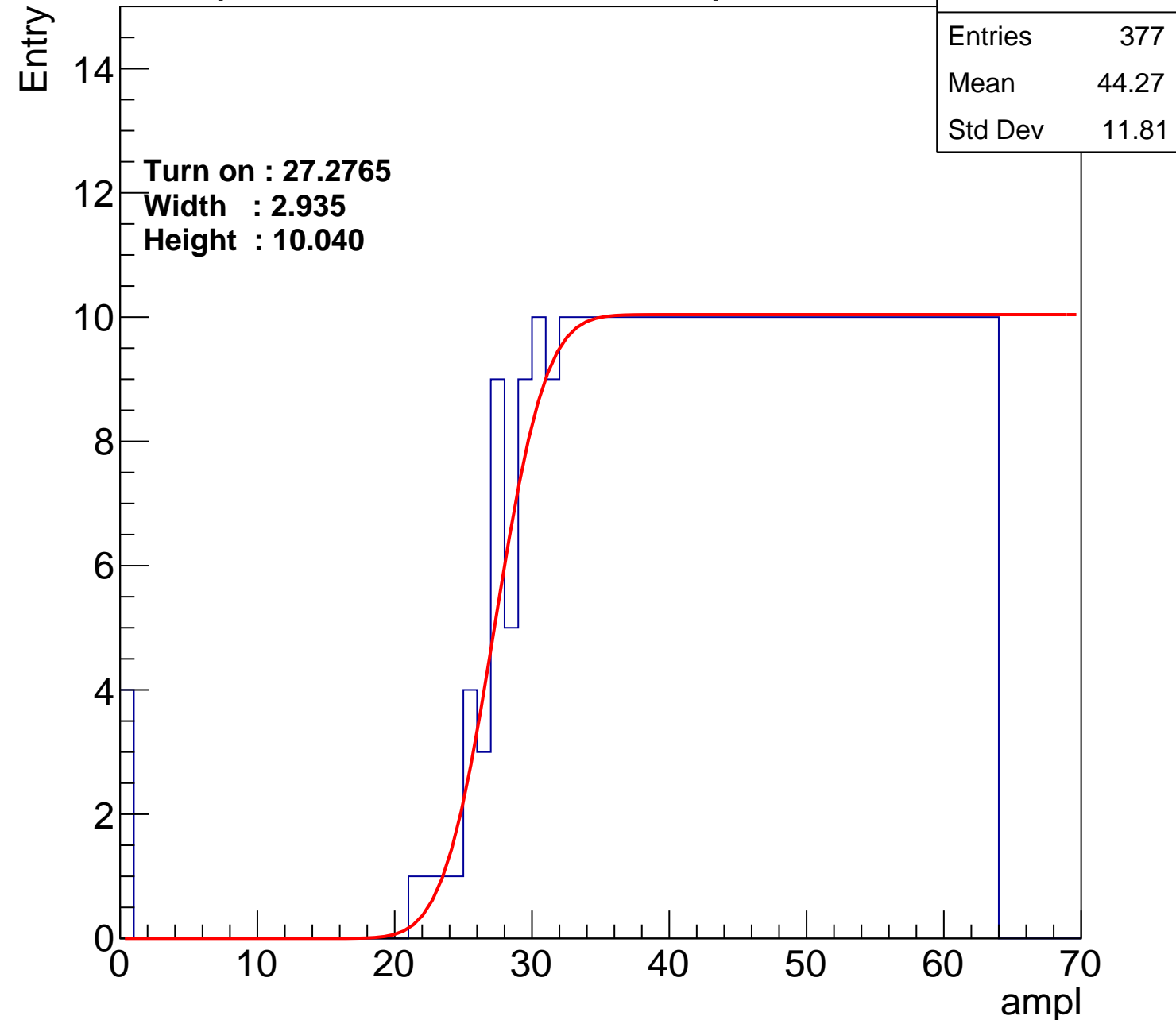
Width : 2.935

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U22-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.27
Std Dev	11.81

Turn on : 27.2765

Width : 2.935

Height : 10.040

Entry

