

B1L102S, U18-ch0, adc0

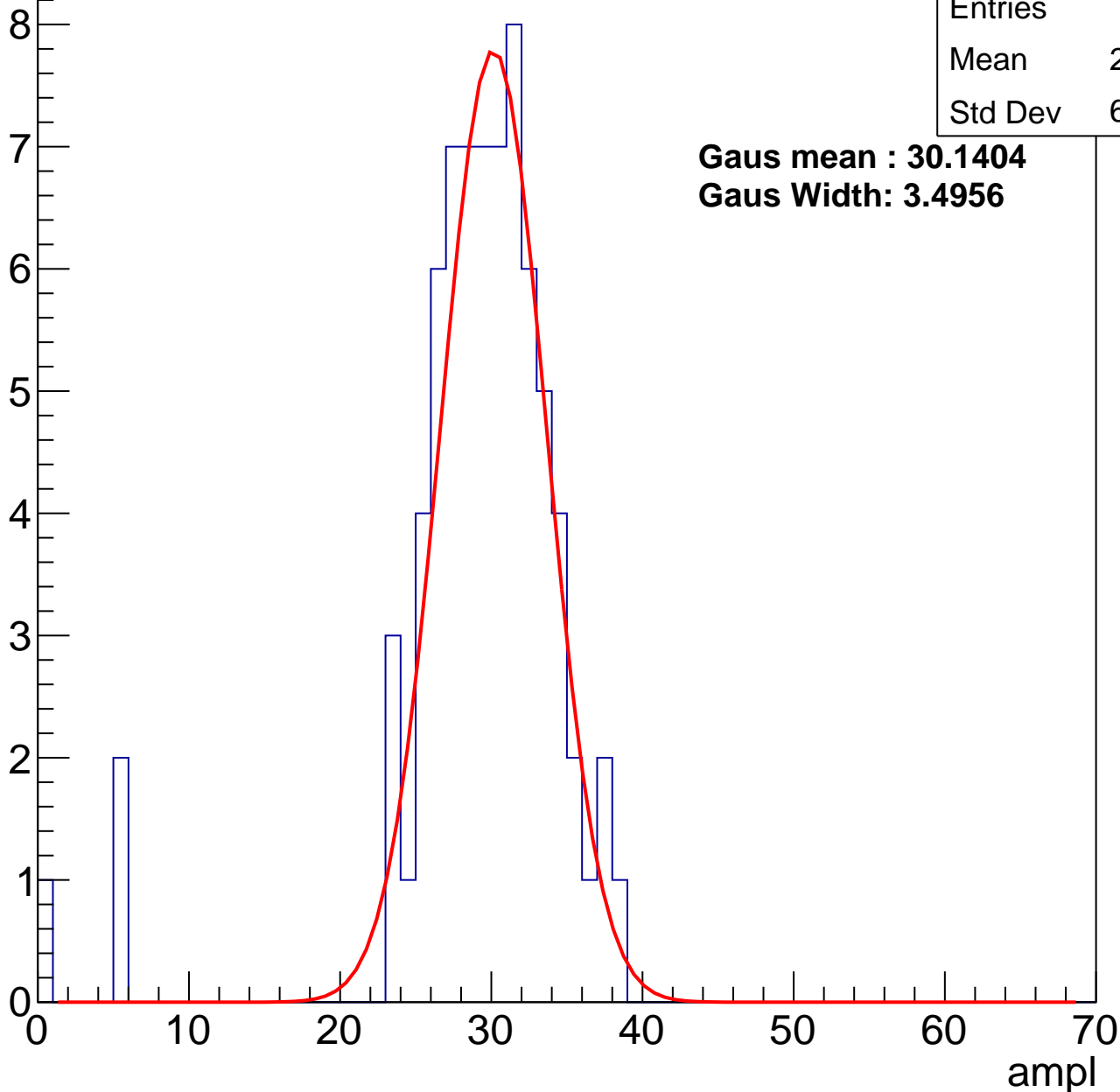
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28.59
Std Dev	6.227

Gaus mean : 30.1404

Gaus Width: 3.4956



B1L102S, U18-ch0, adc1

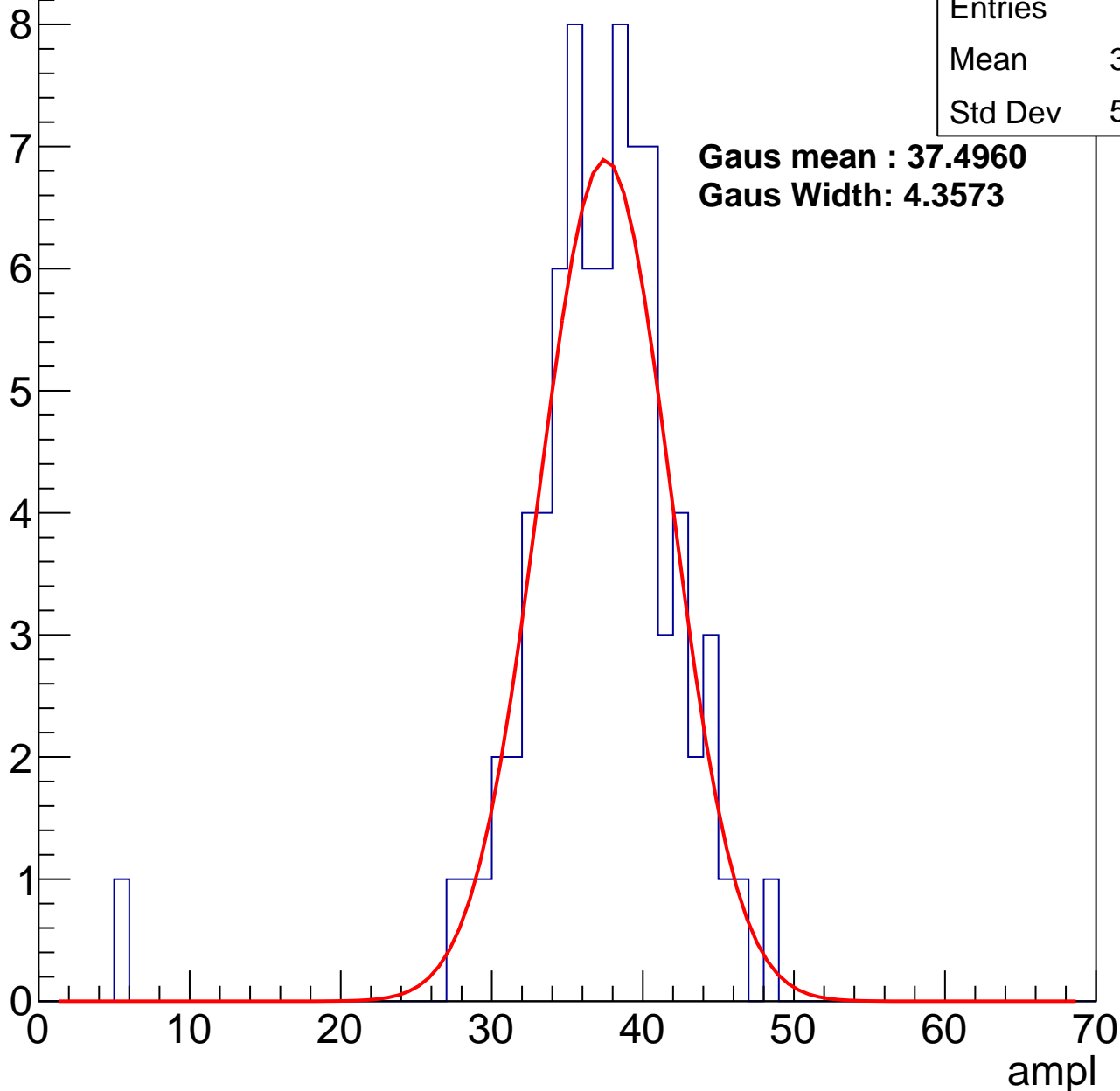
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	36.68
Std Dev	5.534

Gaus mean : 37.4960

Gaus Width: 4.3573



B1L102S, U18-ch0, adc2

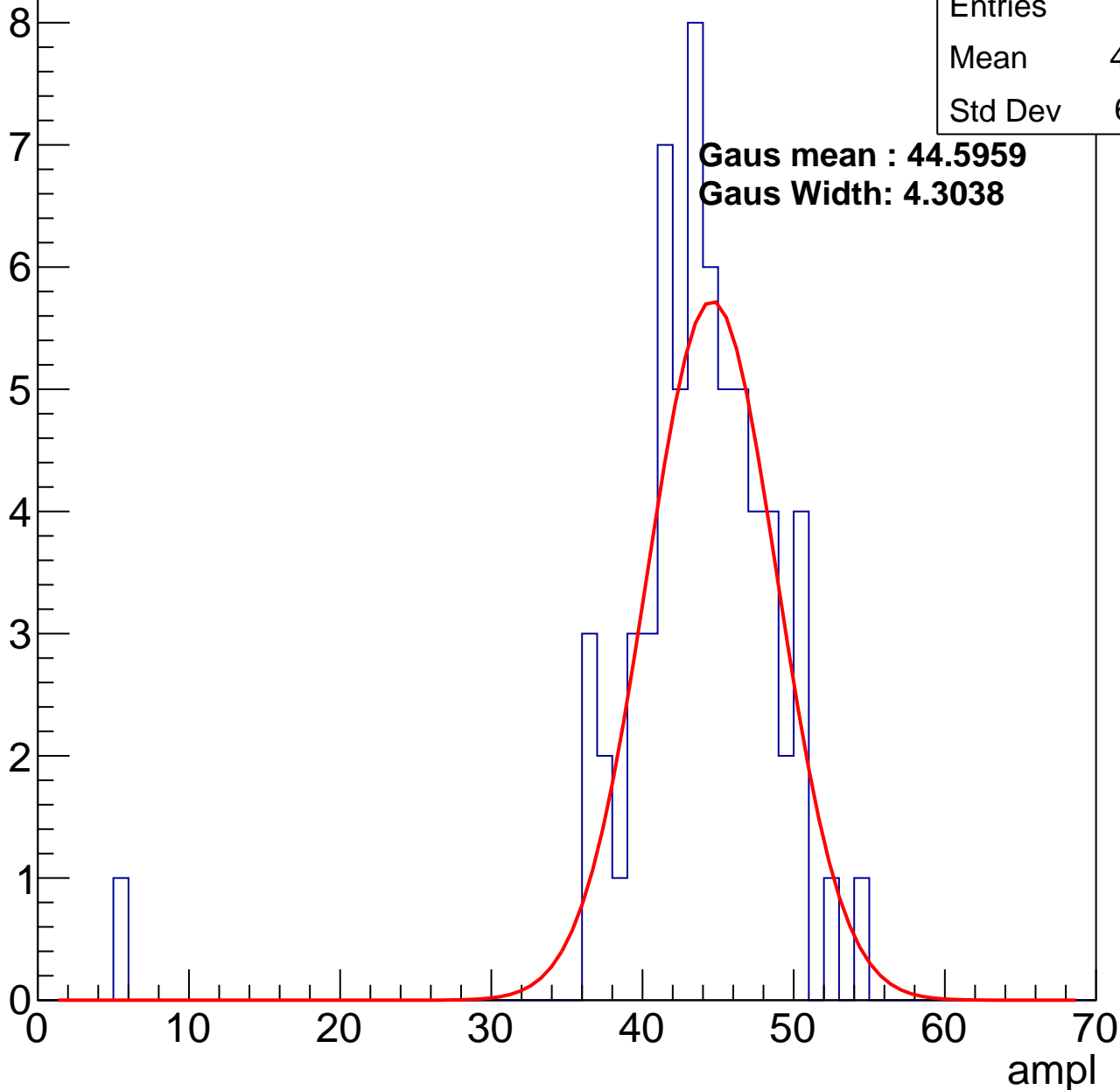
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.17
Std Dev	6.201

Gaus mean : 44.5959

Gaus Width: 4.3038

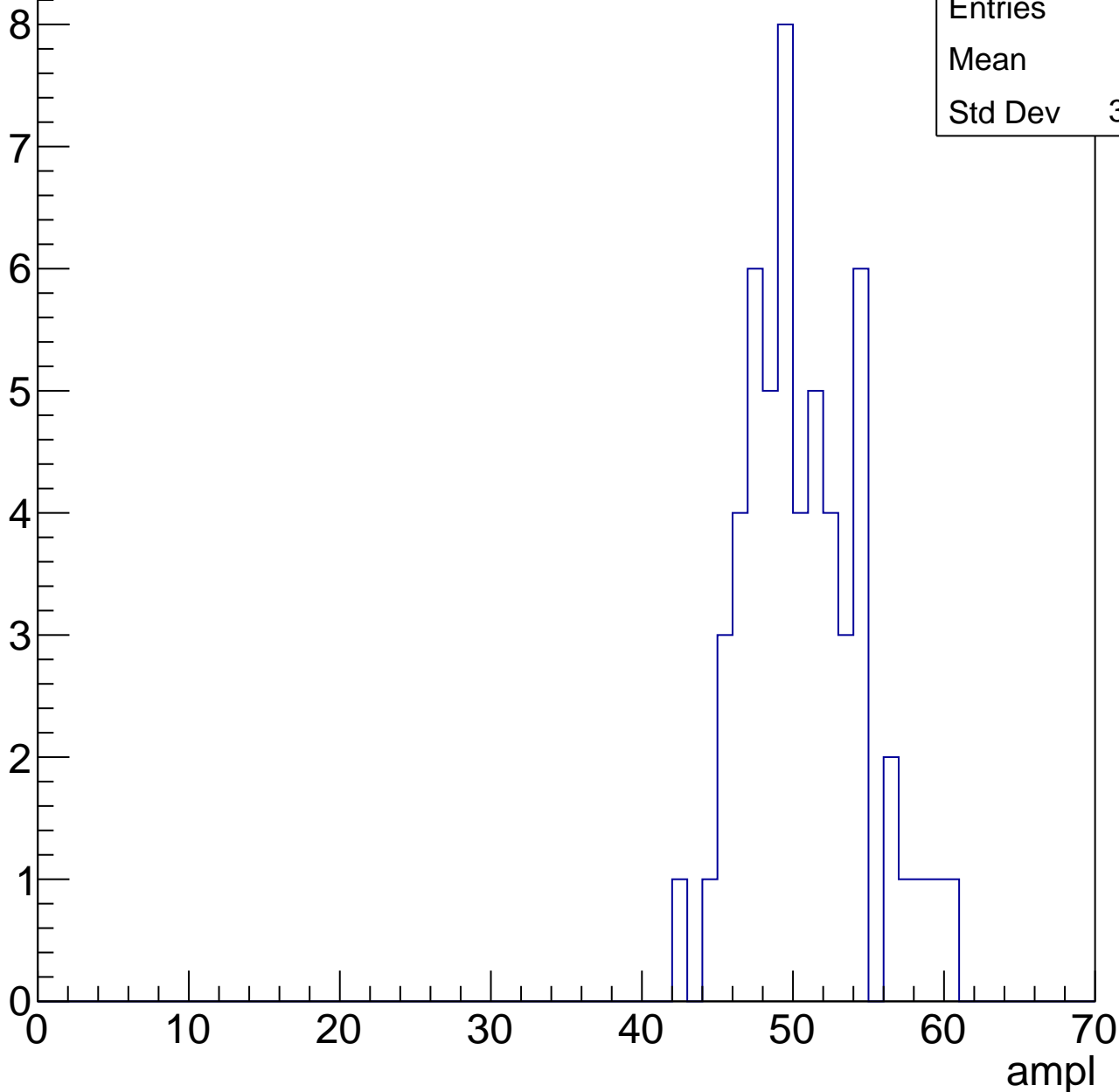


B1L102S, U18-ch0, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	50.2
Std Dev	3.847

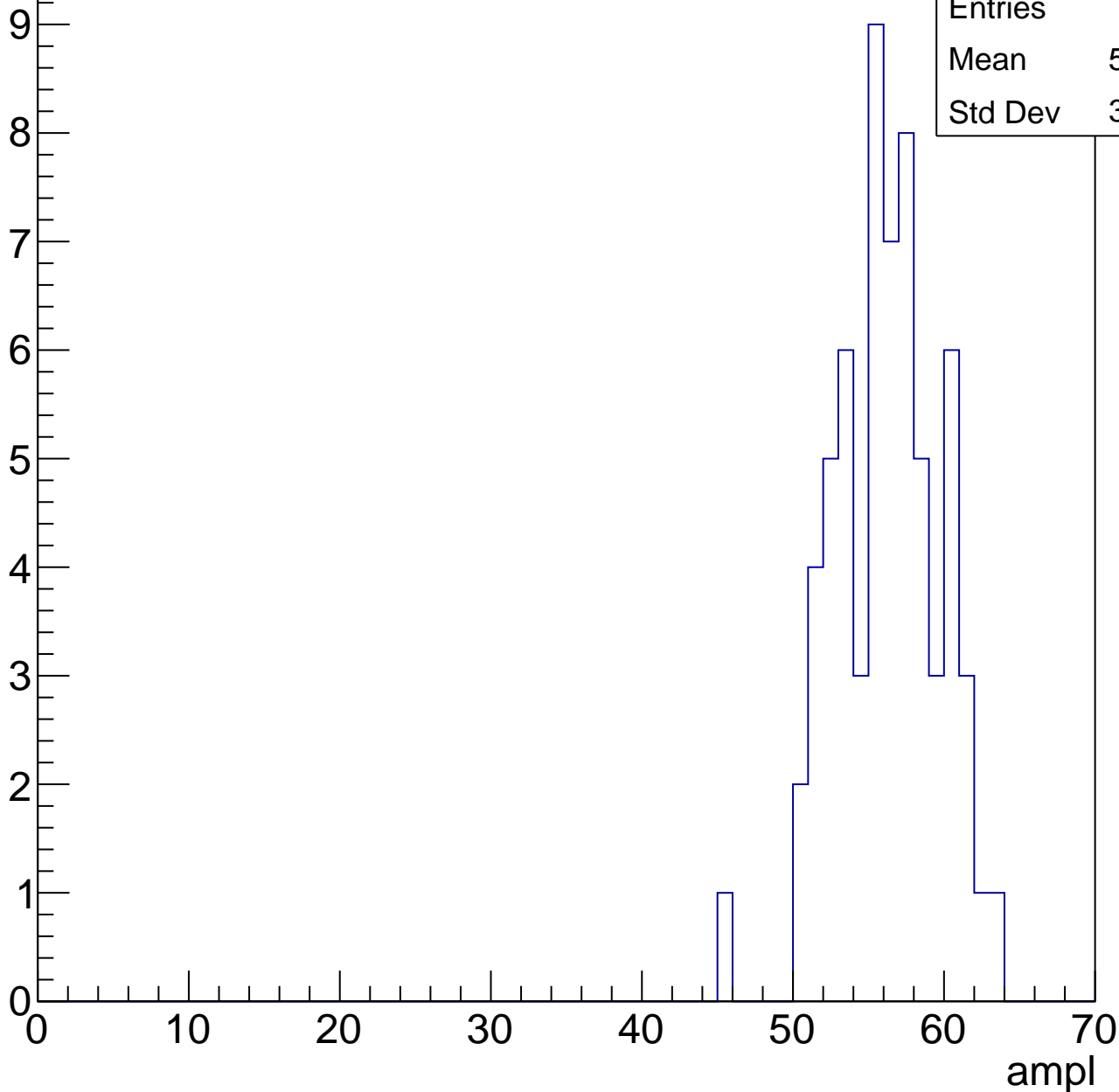


B1L102S, U18-ch0, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	55.73
Std Dev	3.433

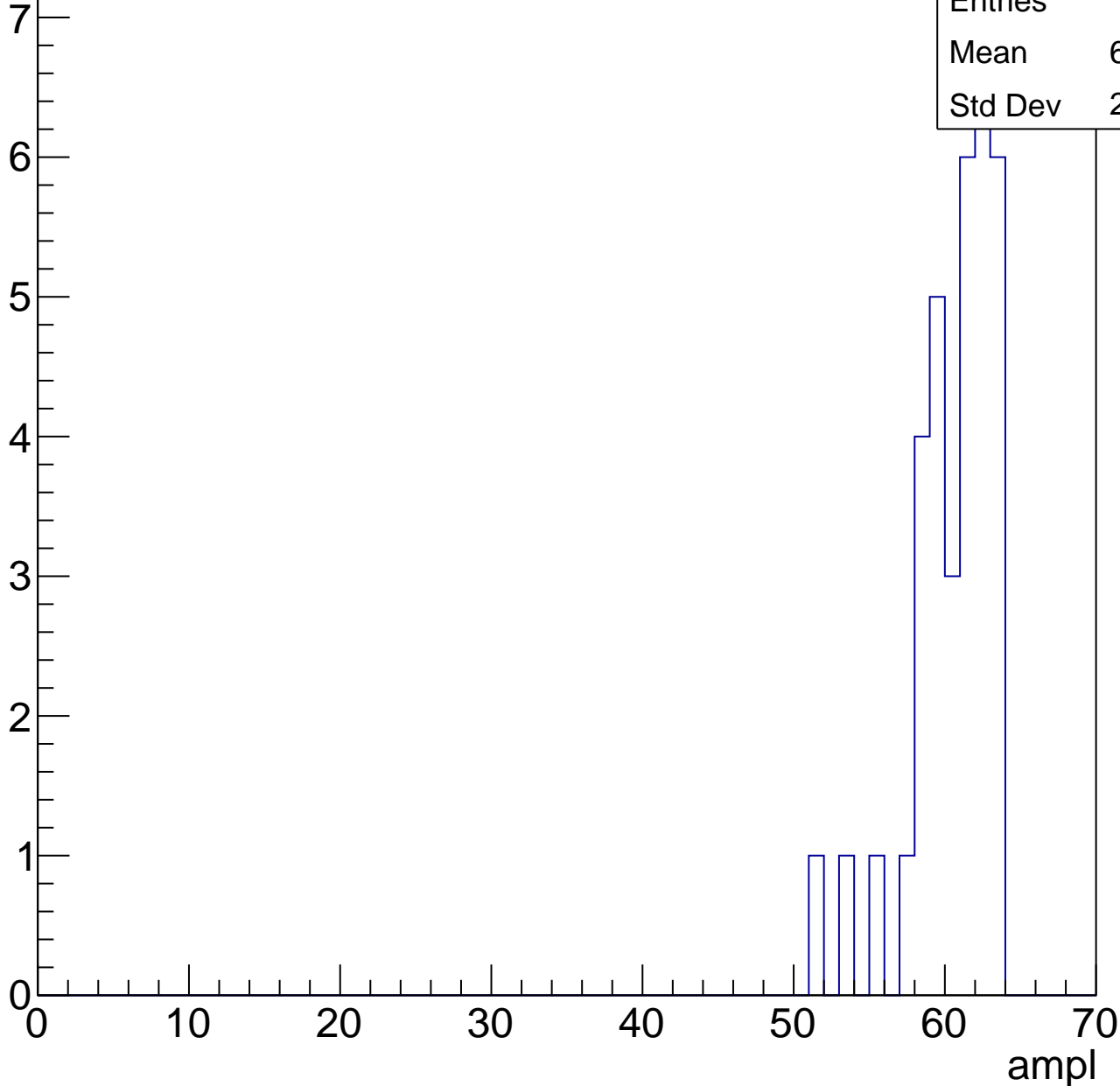


B1L102S, U18-ch0, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

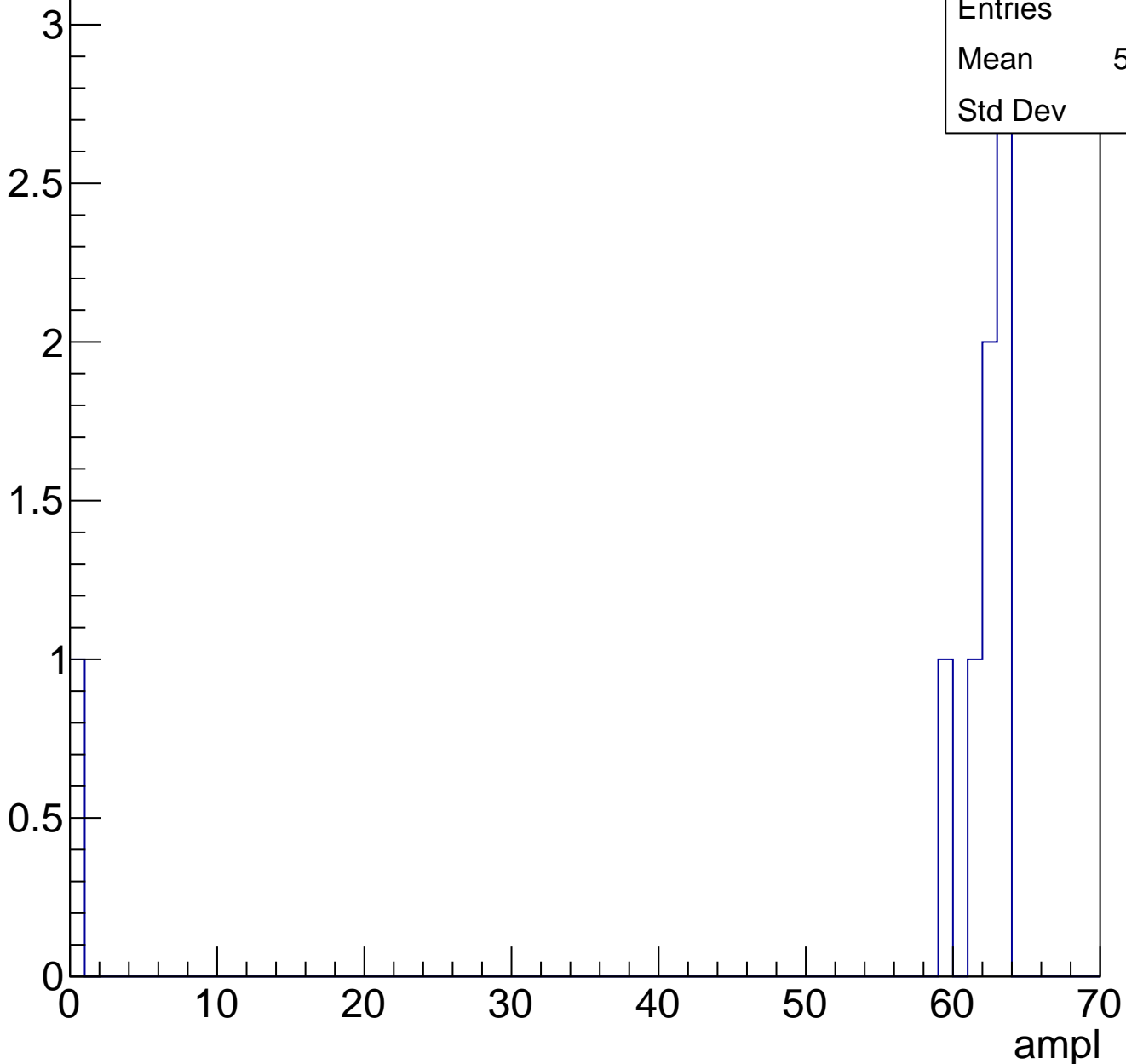
Entries	35
Mean	60.03
Std Dev	2.793



B1L102S, U18-ch0, adc6

calib_packv5_042523_0143.root, FC#11, port A2

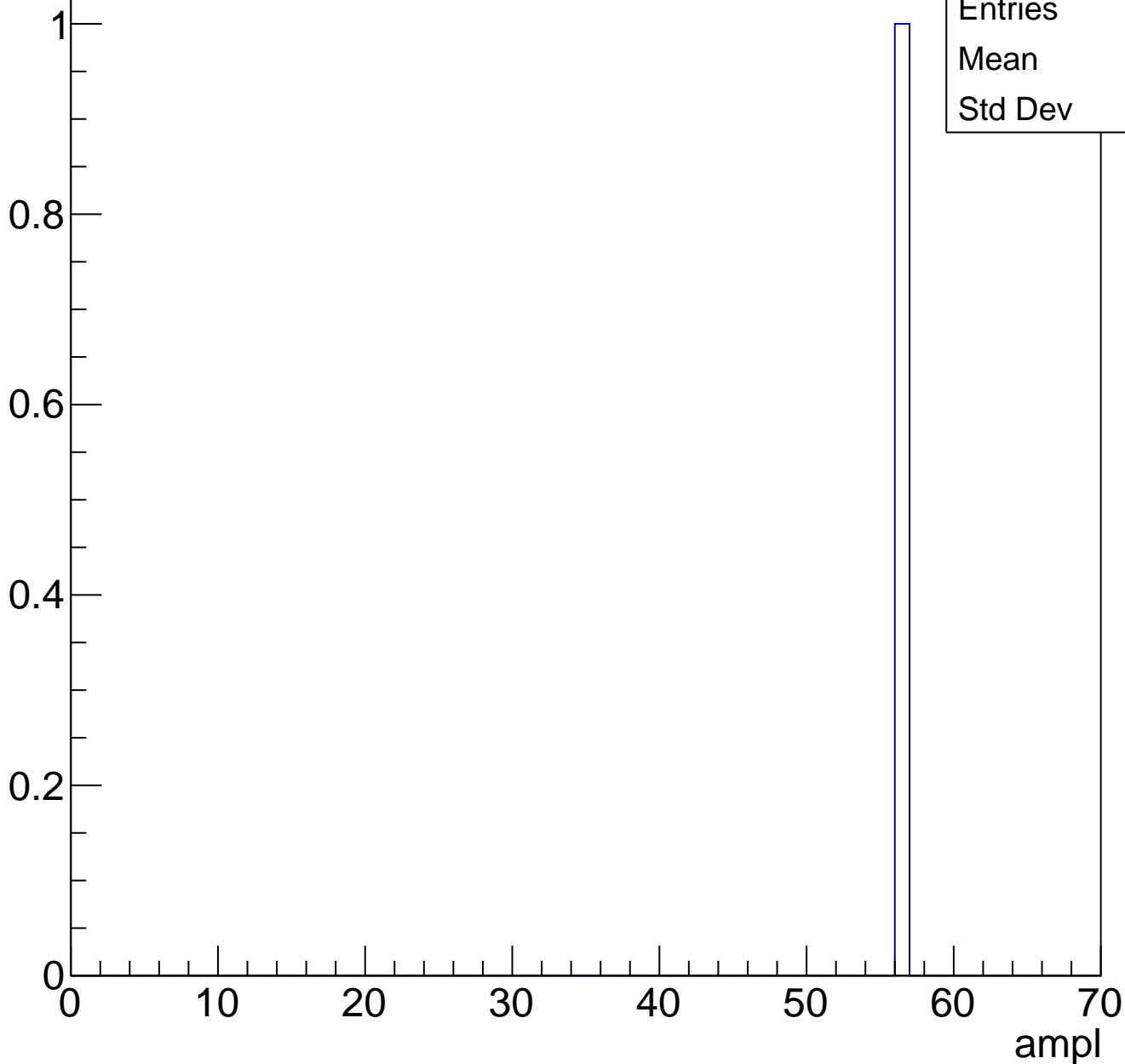
Entry



B1L102S, U18-ch0, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch1, adc0

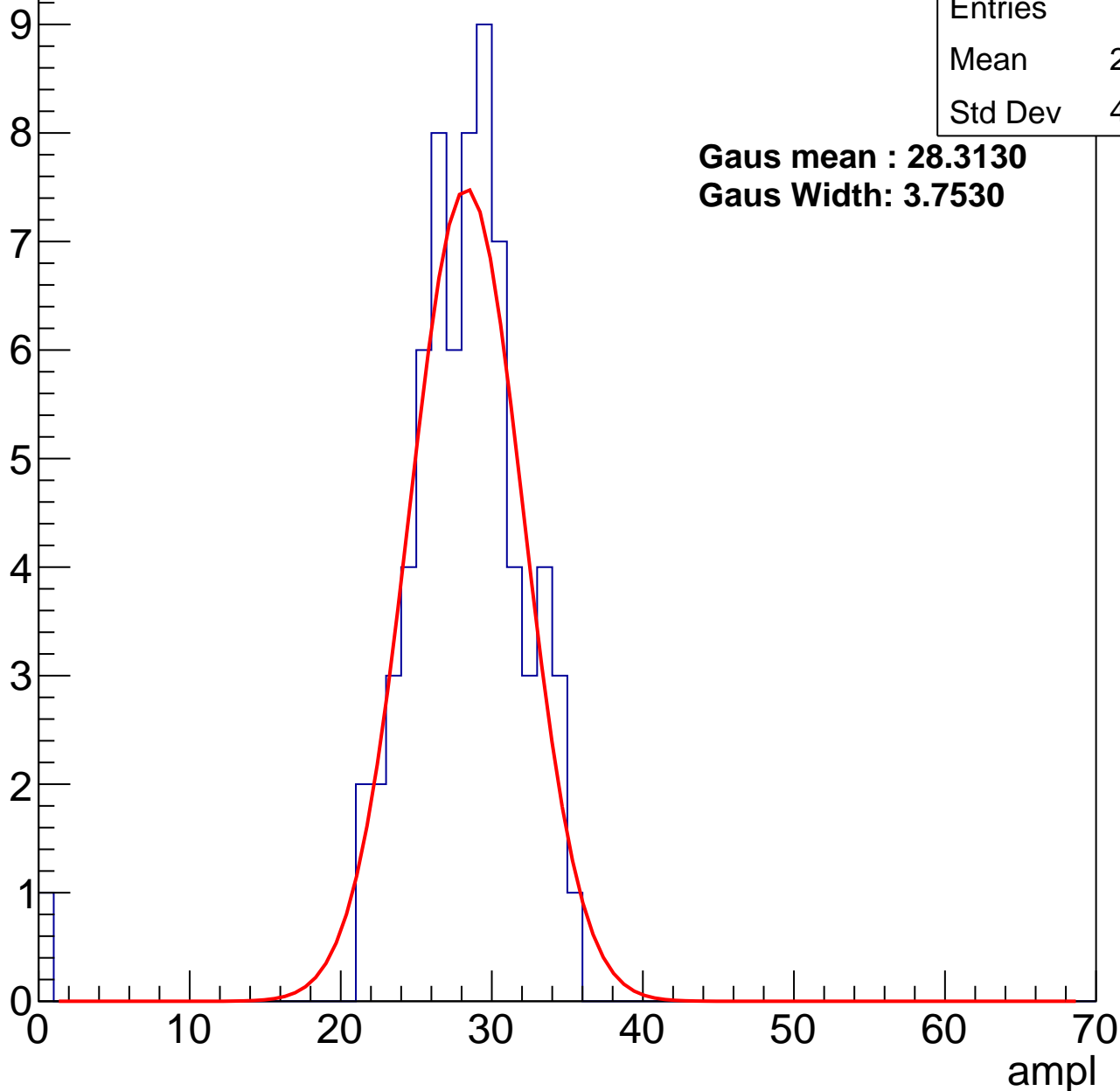
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	27.54
Std Dev	4.669

Gaus mean : 28.3130

Gaus Width: 3.7530



B1L102S, U18-ch1, adc1

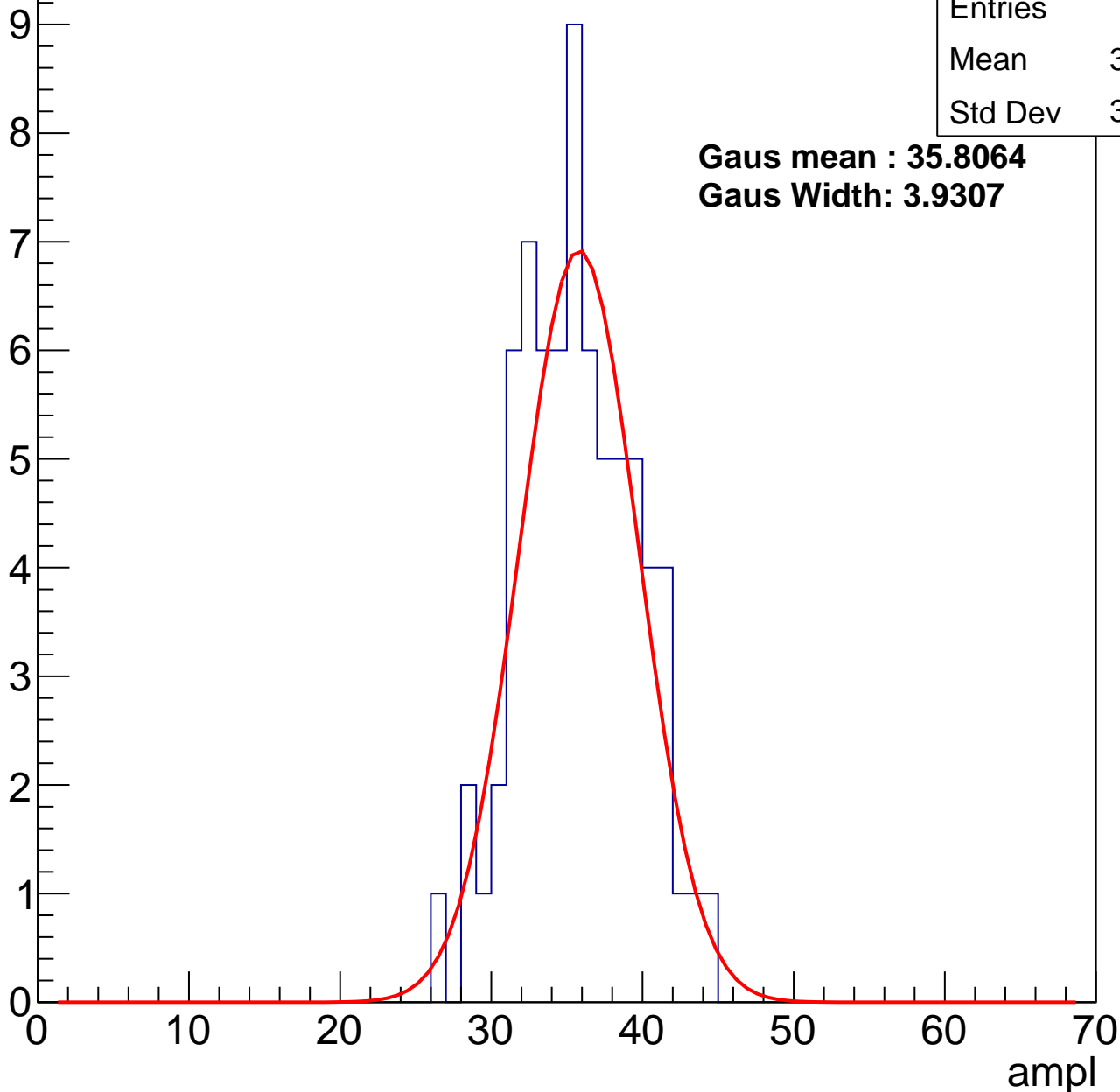
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	35.24
Std Dev	3.799

Gaus mean : 35.8064

Gaus Width: 3.9307



B1L102S, U18-ch1, adc2

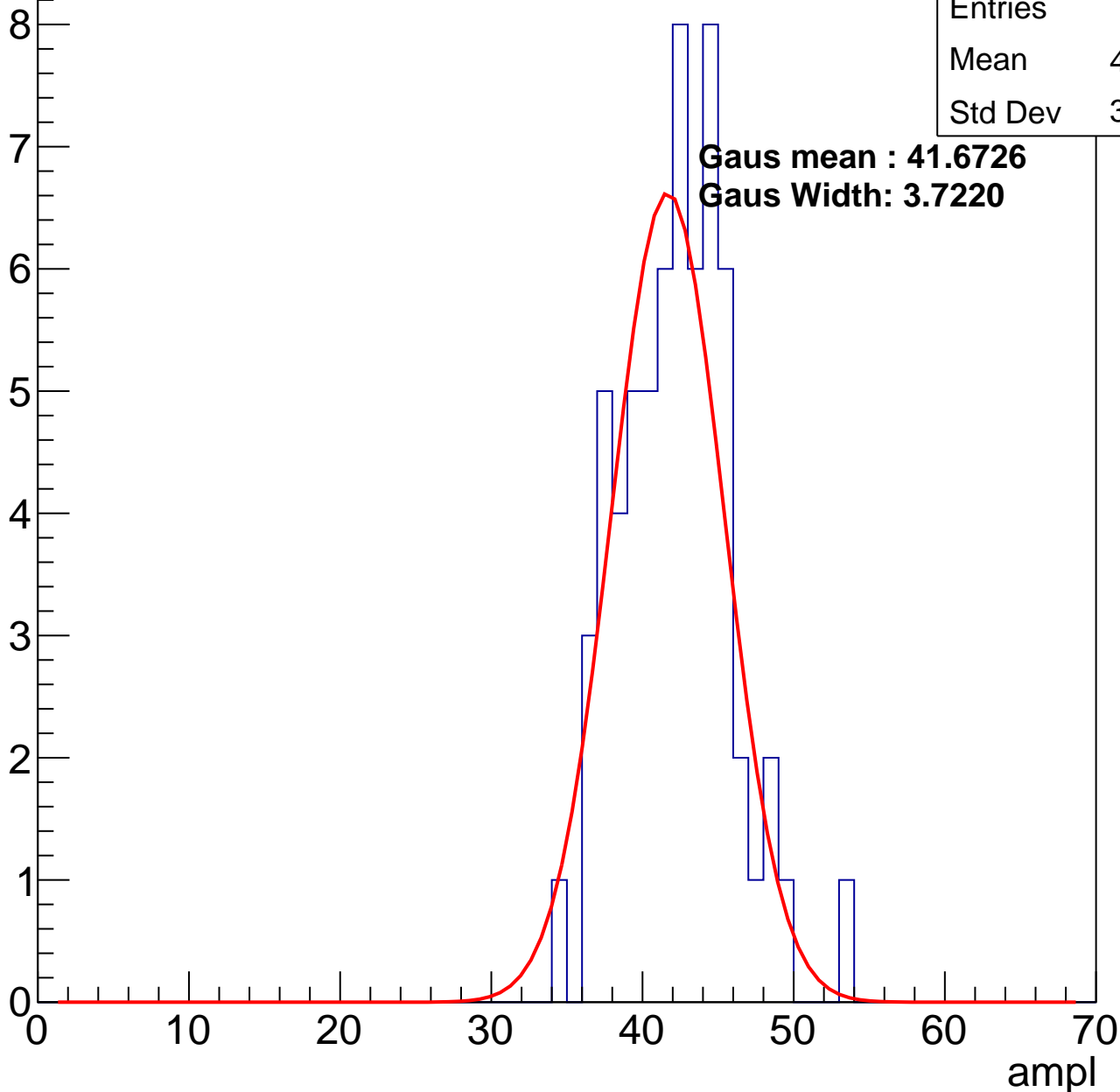
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.77
Std Dev	3.578

Gaus mean : 41.6726

Gaus Width: 3.7220

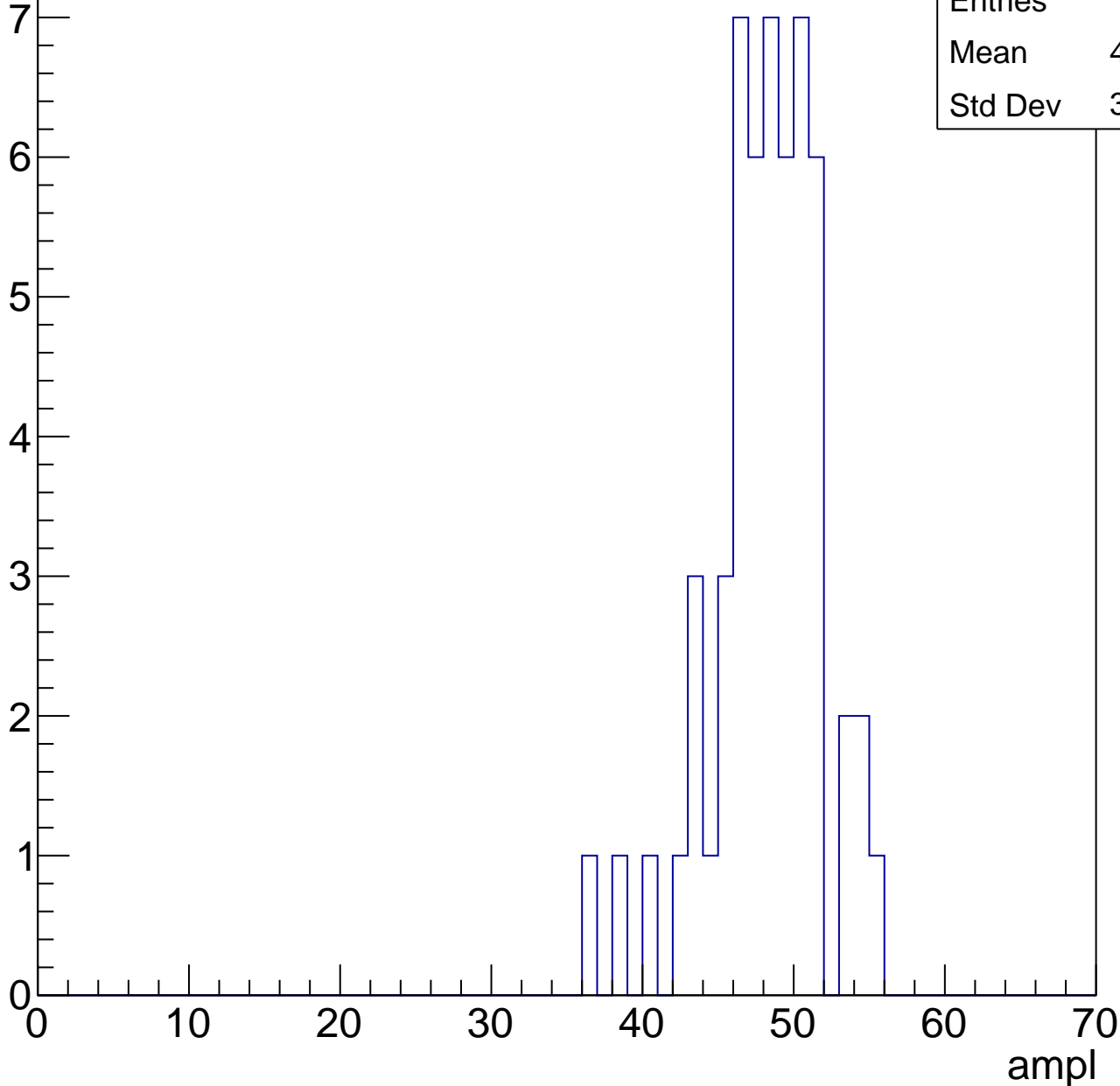


B1L102S, U18-ch1, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	47.69
Std Dev	3.692

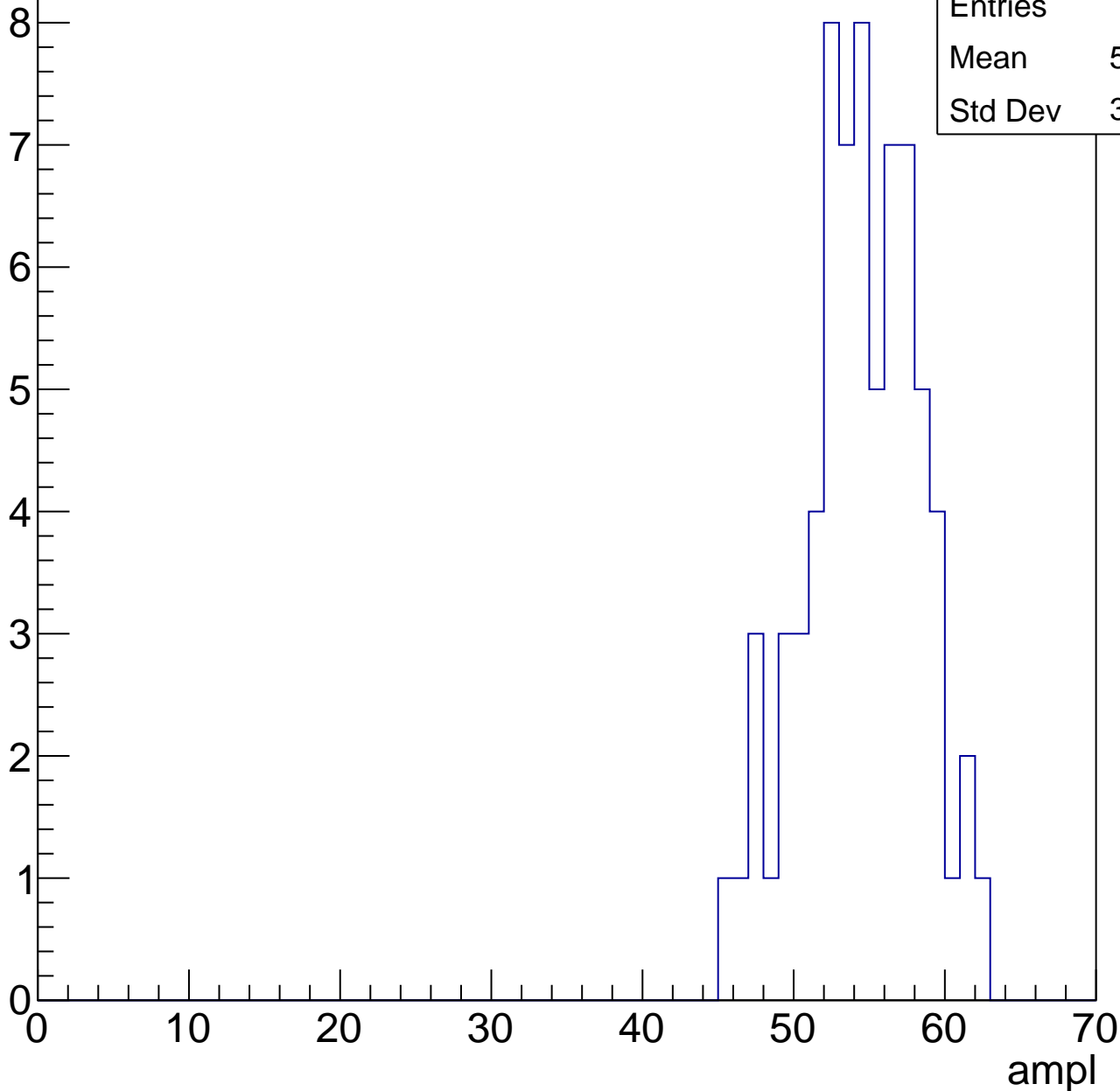


B1L102S, U18-ch1, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.03
Std Dev	3.753

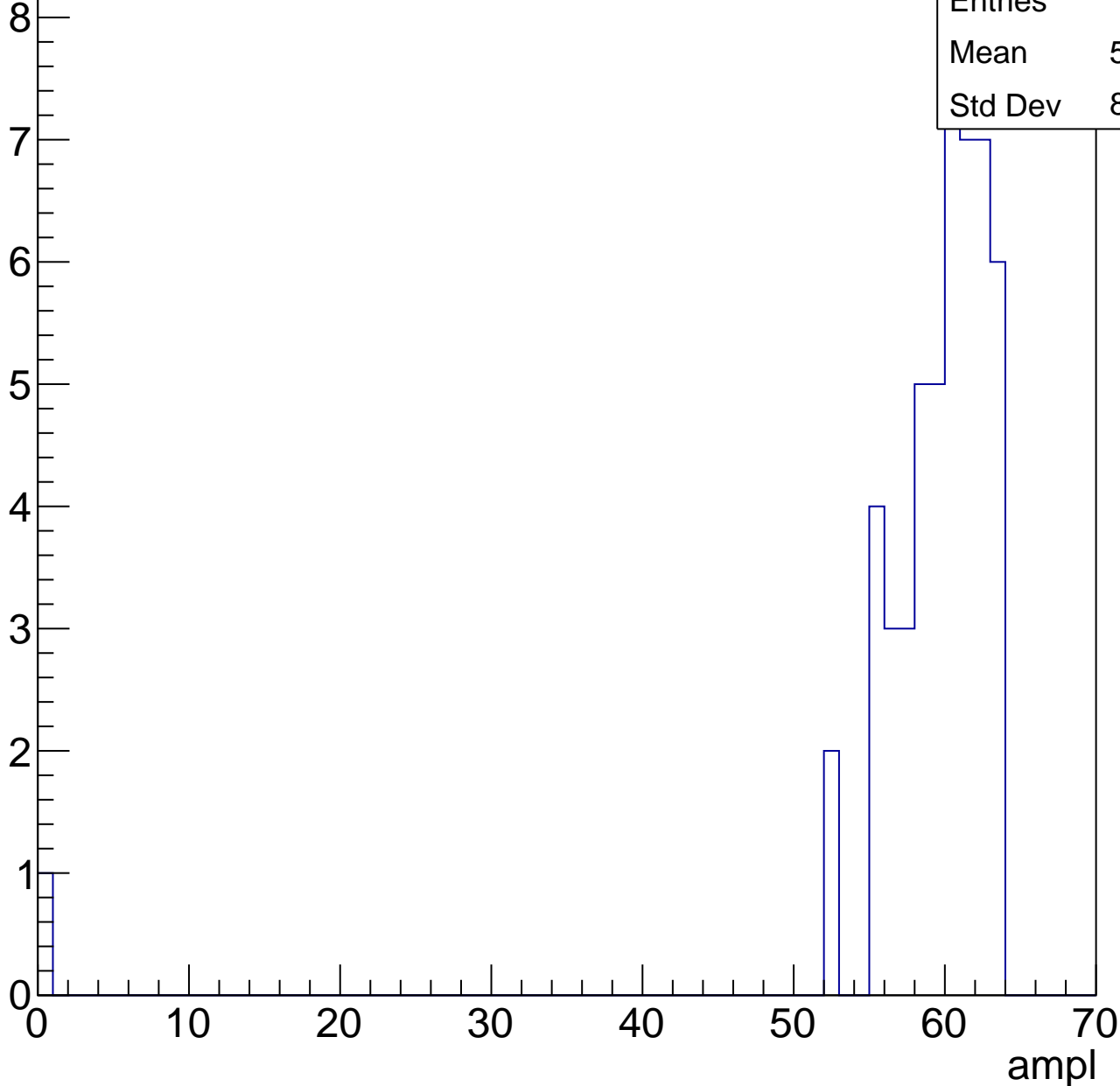


B1L102S, U18-ch1, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

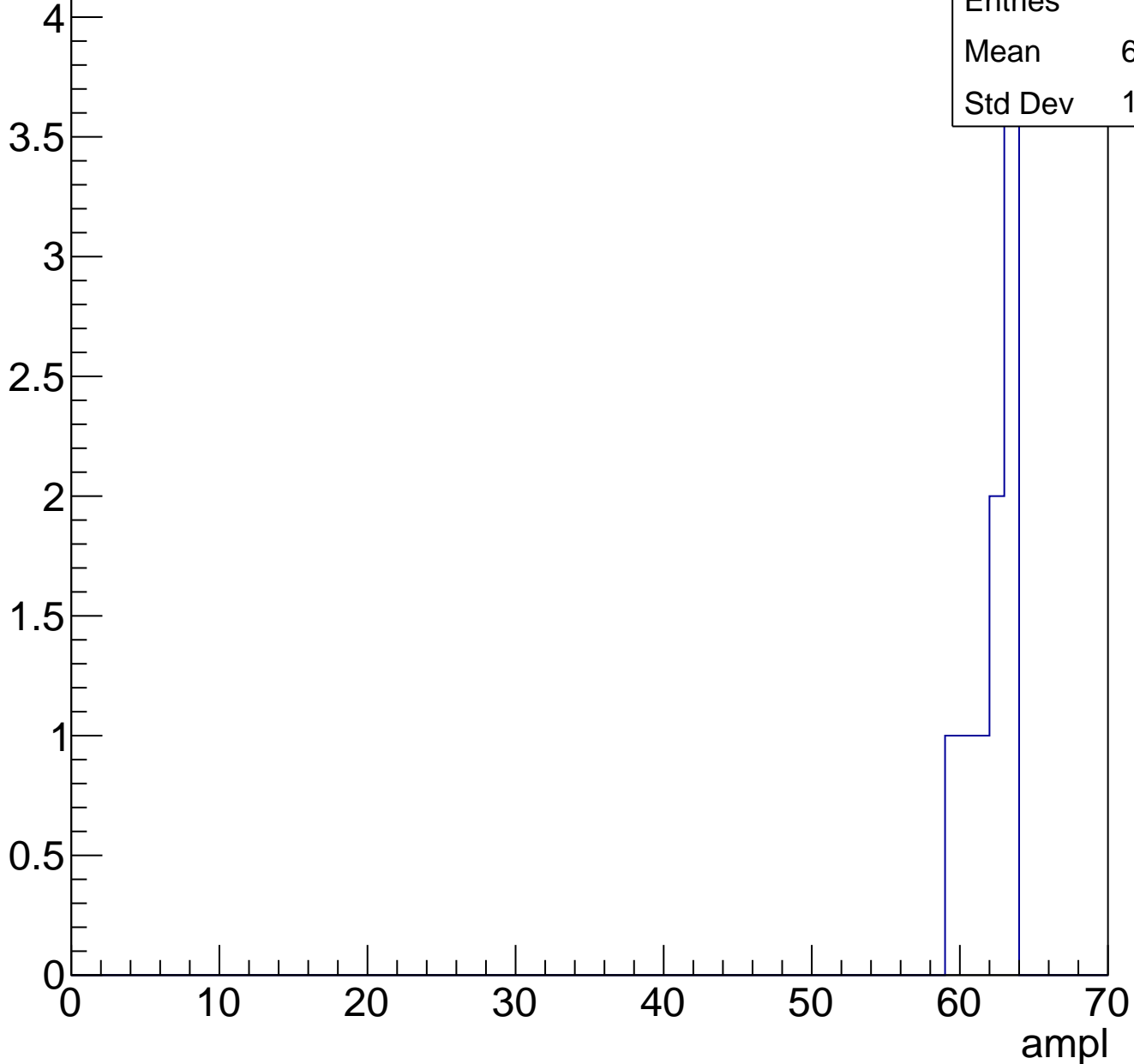
Entries	51
Mean	58.18
Std Dev	8.686



B1L102S, U18-ch1, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch1, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch2, adc0

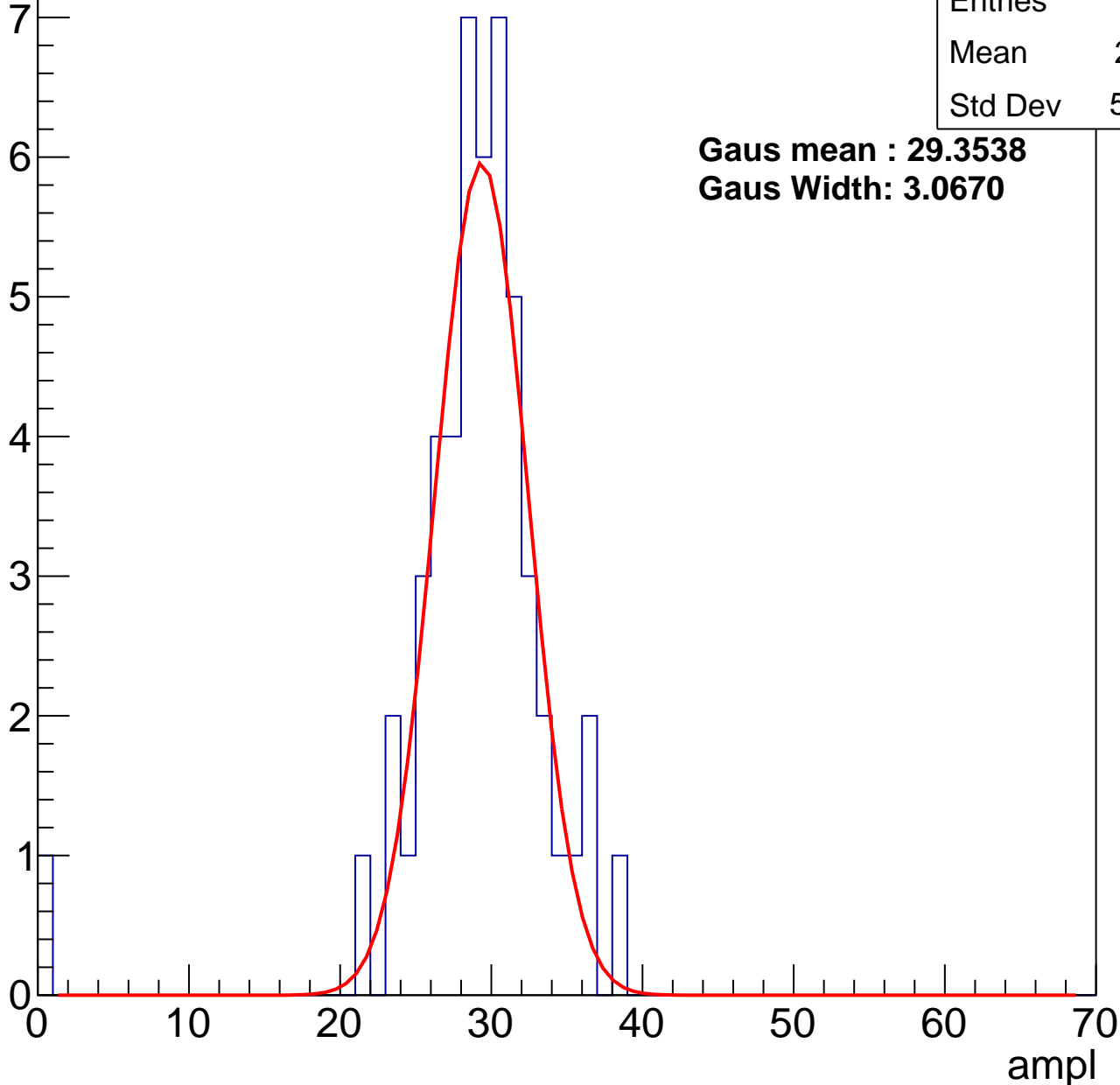
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	28.51
Std Dev	5.282

Gaus mean : 29.3538

Gaus Width: 3.0670



B1L102S, U18-ch2, adc1

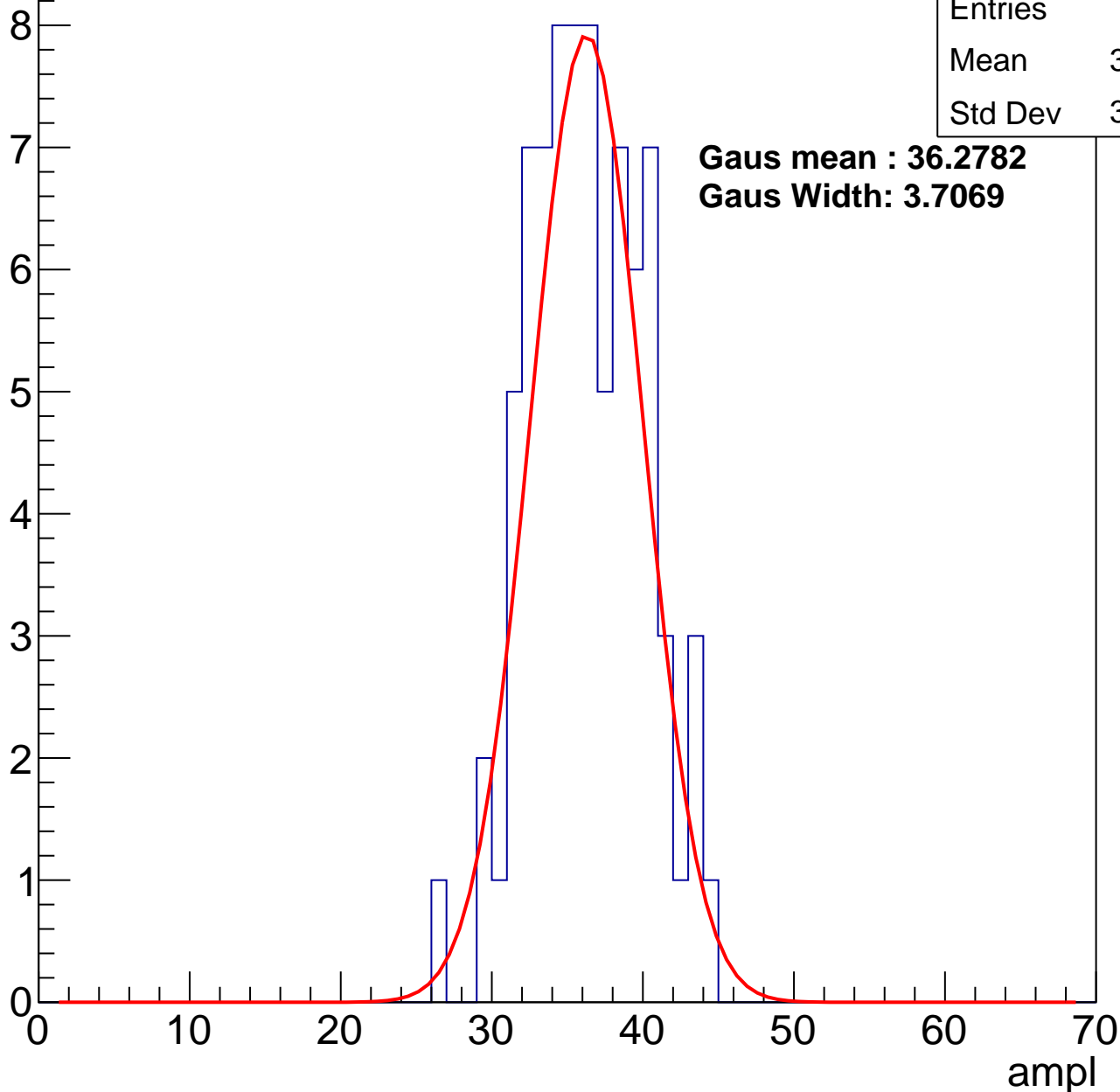
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	35.84
Std Dev	3.696

Gaus mean : 36.2782

Gaus Width: 3.7069



B1L102S, U18-ch2, adc2

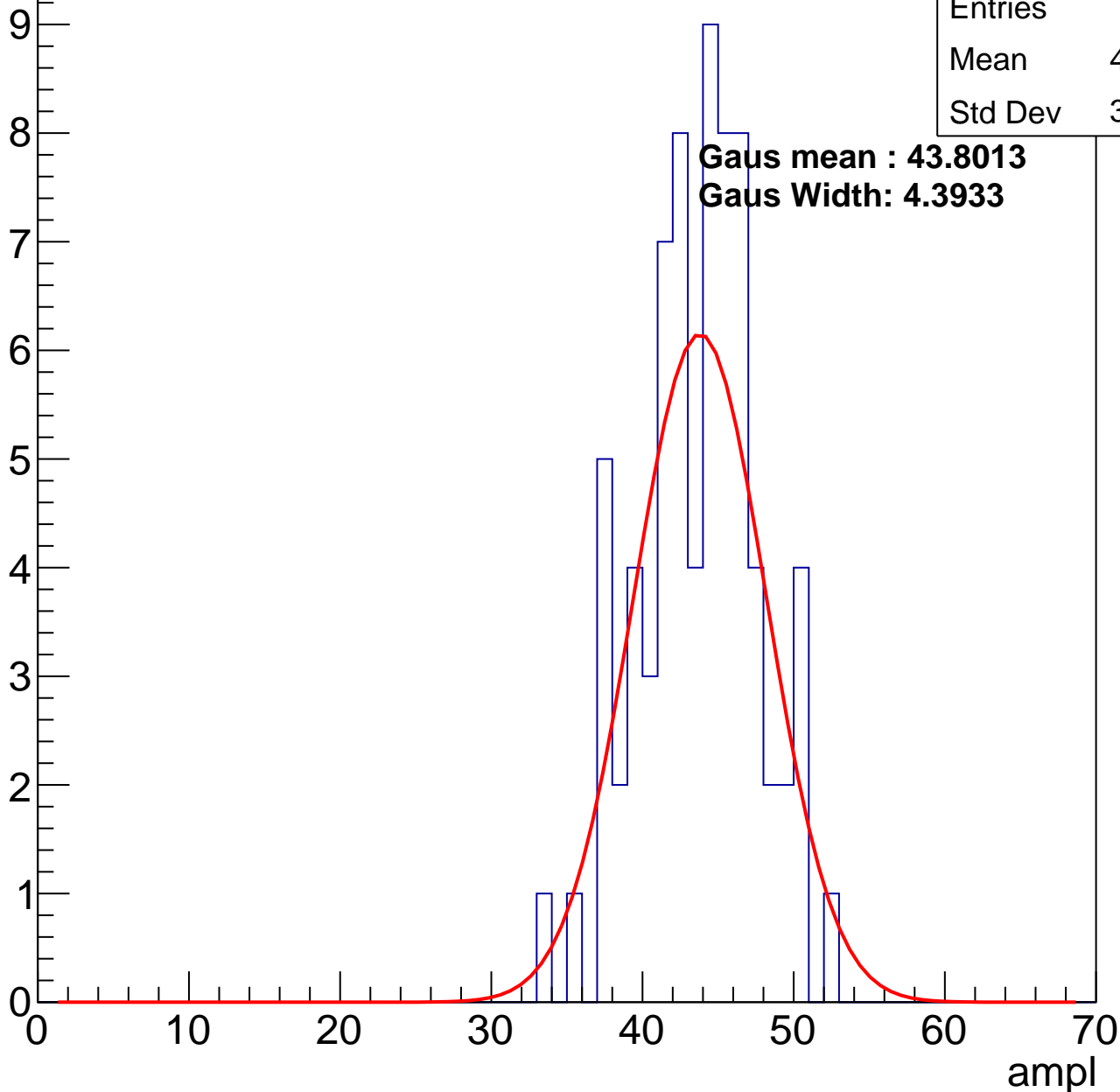
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.26
Std Dev	3.868

Gaus mean : 43.8013

Gaus Width: 4.3933

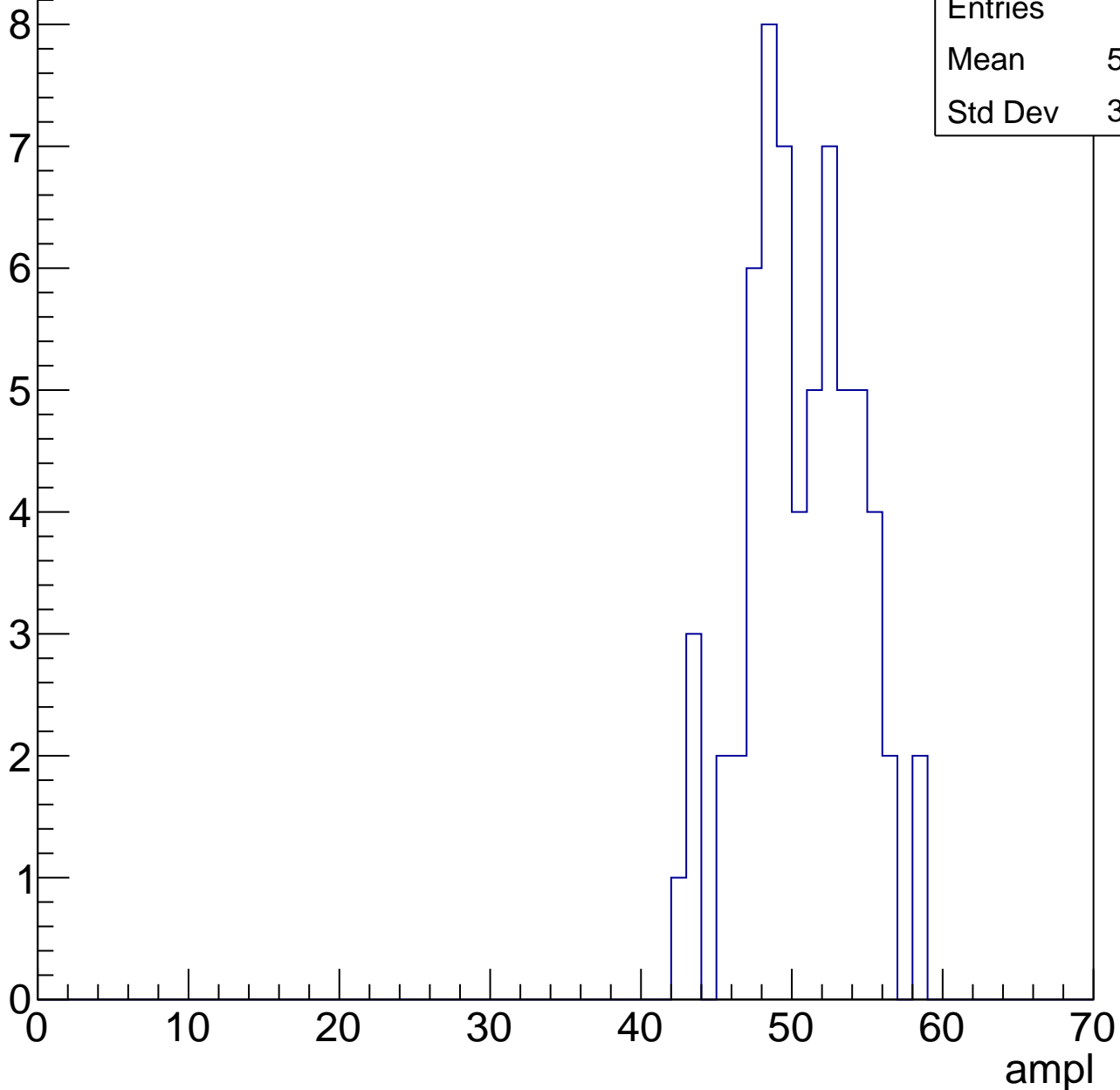


B1L102S, U18-ch2, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	50.22
Std Dev	3.653

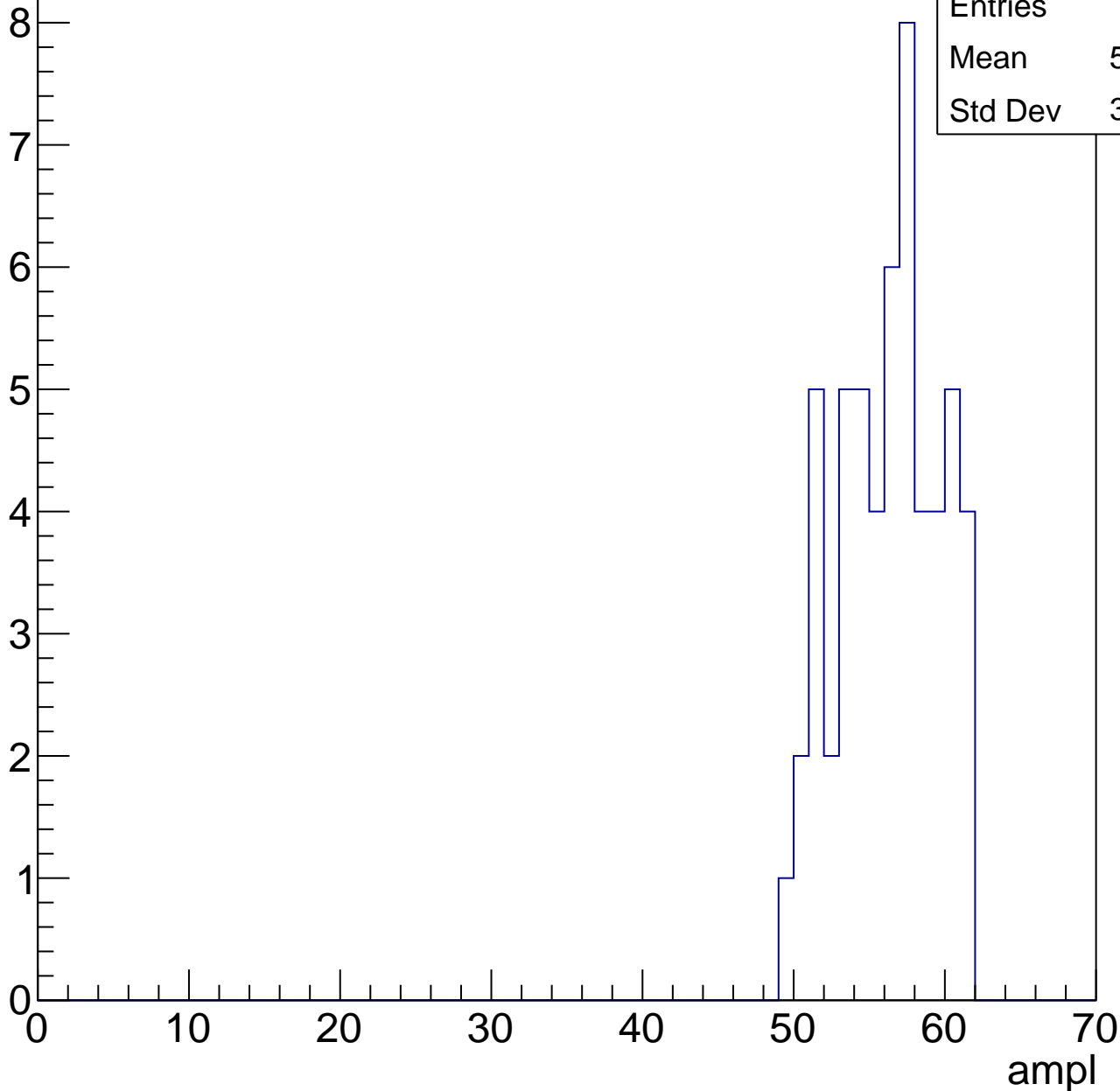


B1L102S, U18-ch2, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	55.76
Std Dev	3.258

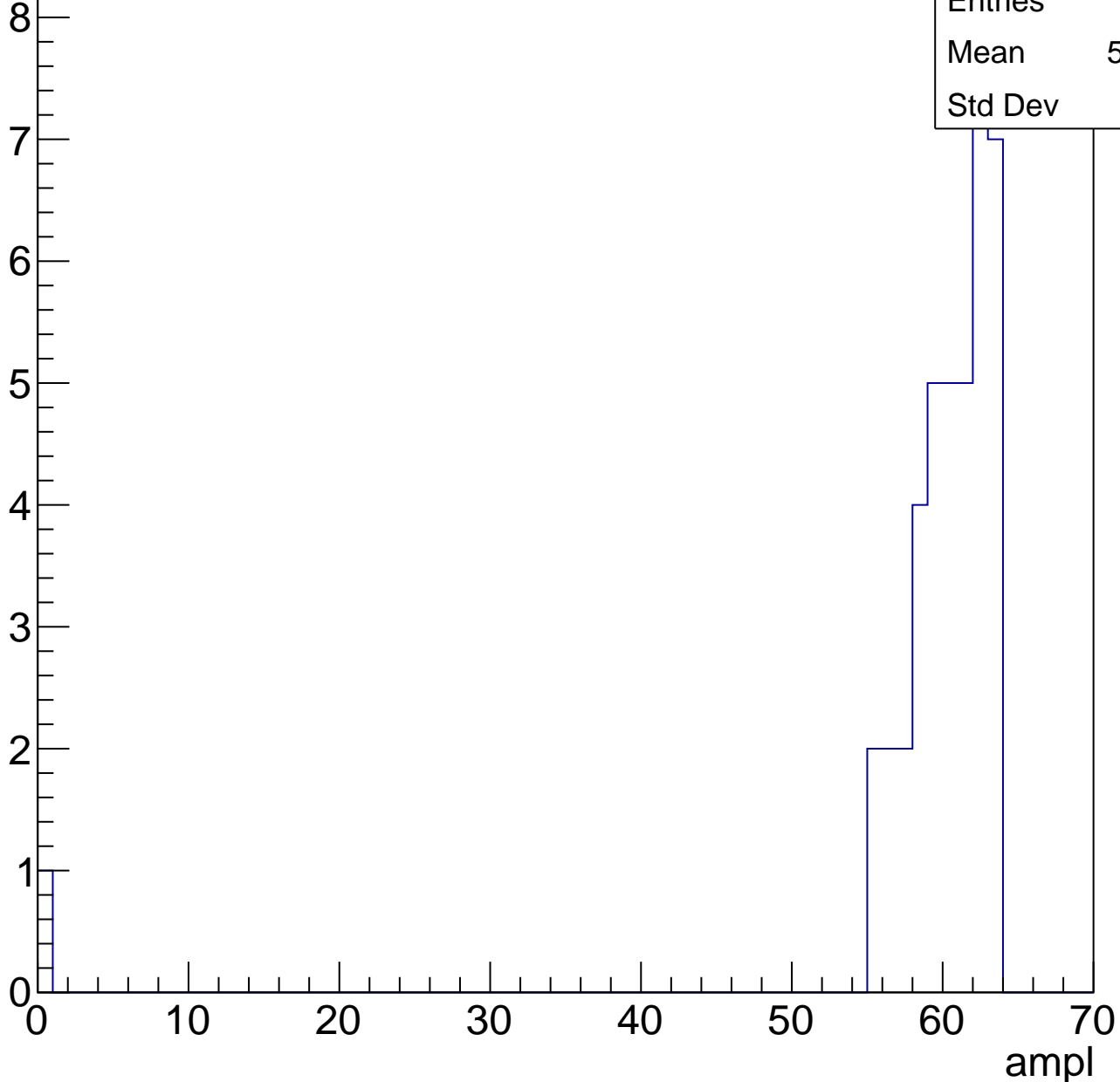


B1L102S, U18-ch2, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	58.66
Std Dev	9.56



B1L102S, U18-ch2, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch2, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch3, adc0

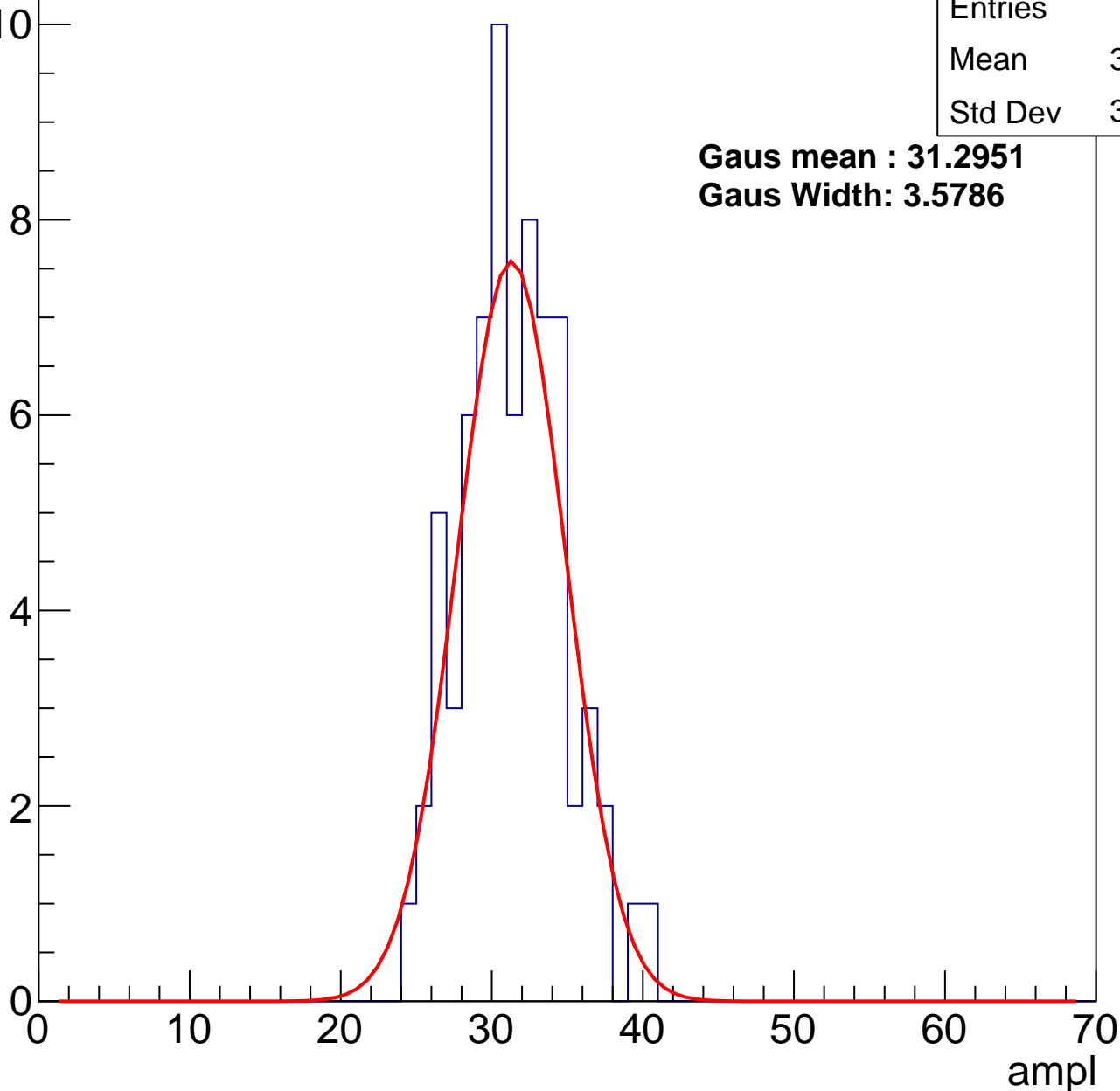
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	30.96
Std Dev	3.375

Gaus mean : 31.2951

Gaus Width: 3.5786



B1L102S, U18-ch3, adc1

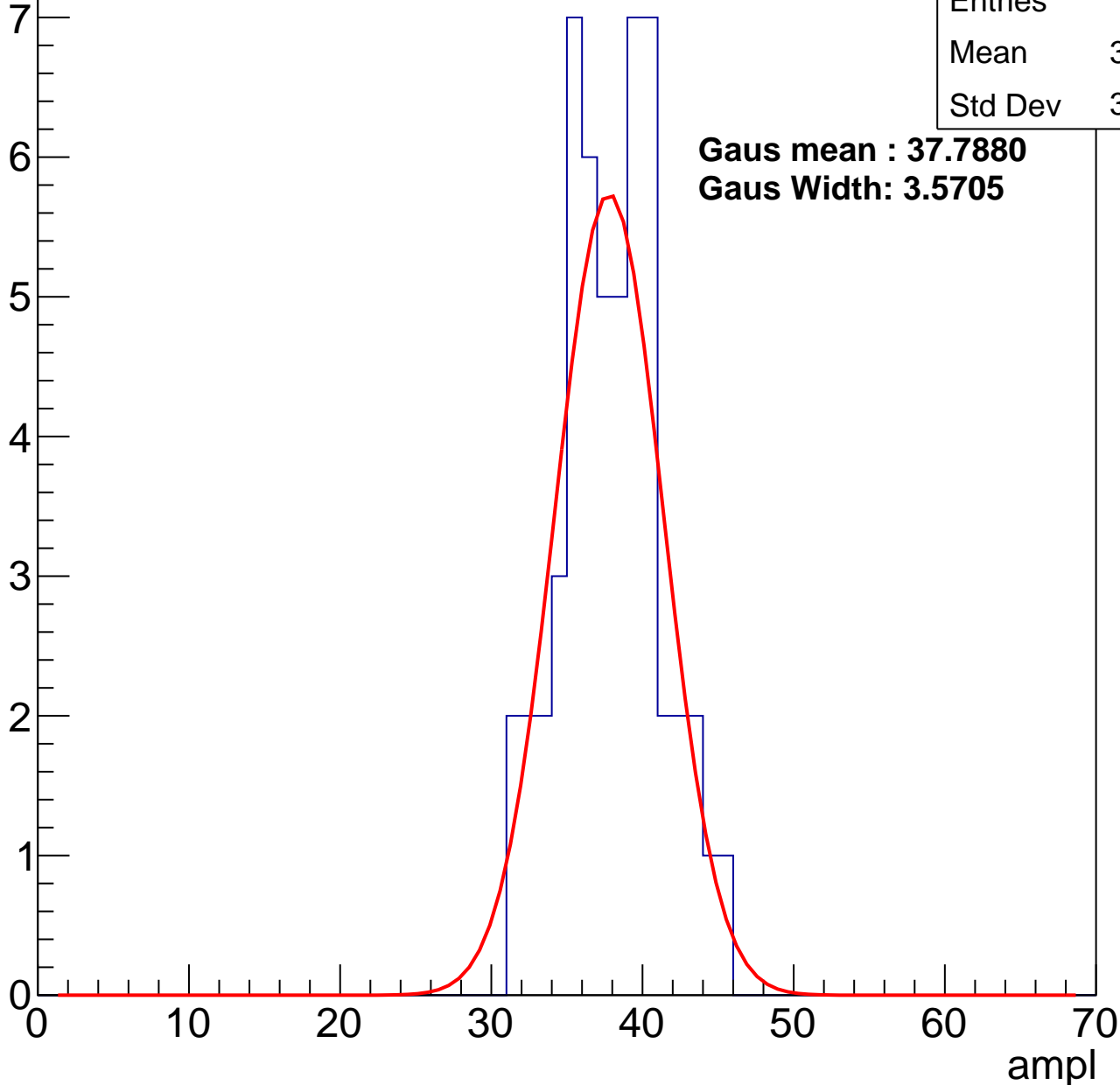
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	37.48
Std Dev	3.236

Gaus mean : 37.7880

Gaus Width: 3.5705



B1L102S, U18-ch3, adc2

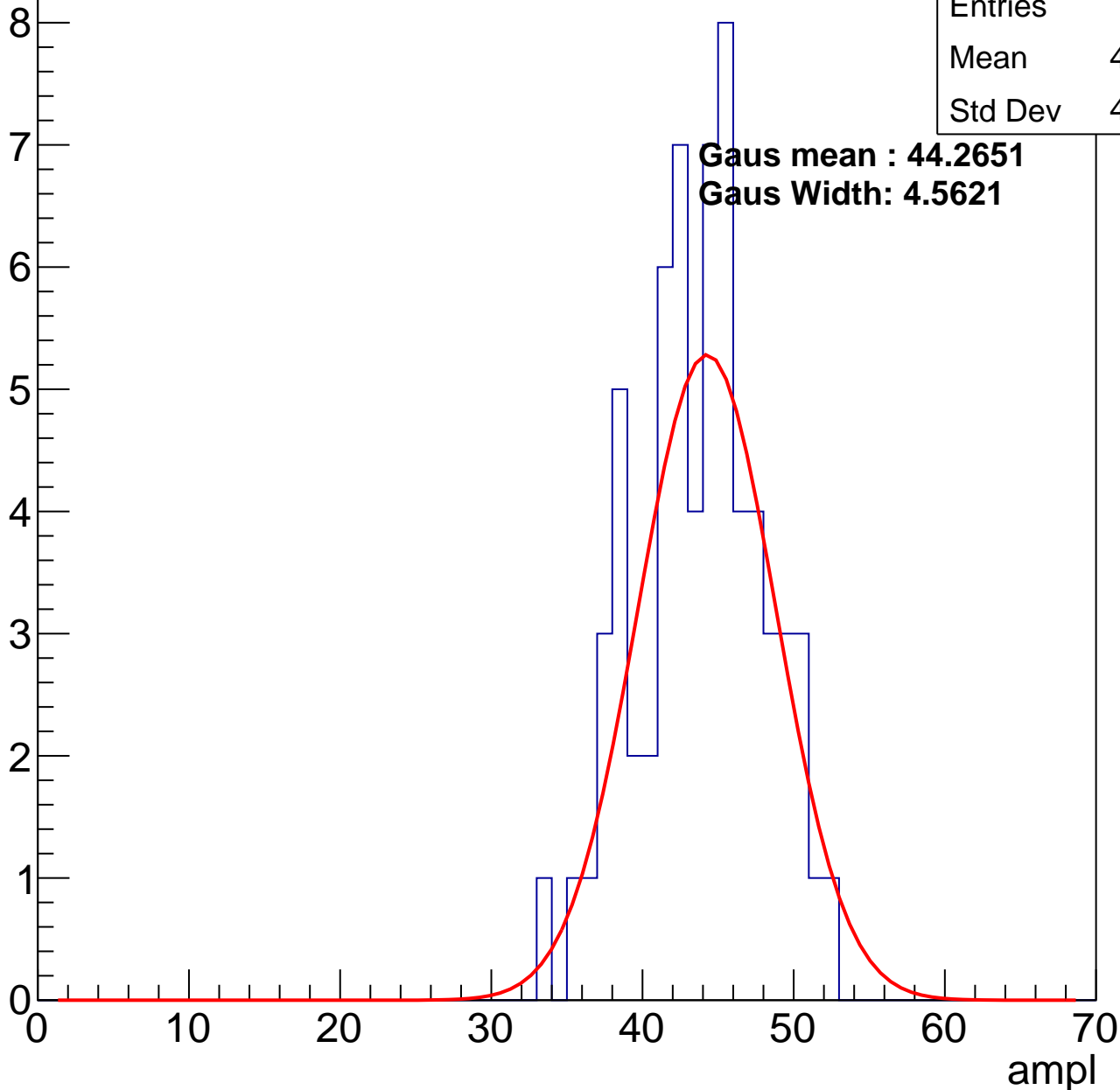
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.32
Std Dev	4.146

Gaus mean : 44.2651

Gaus Width: 4.5621

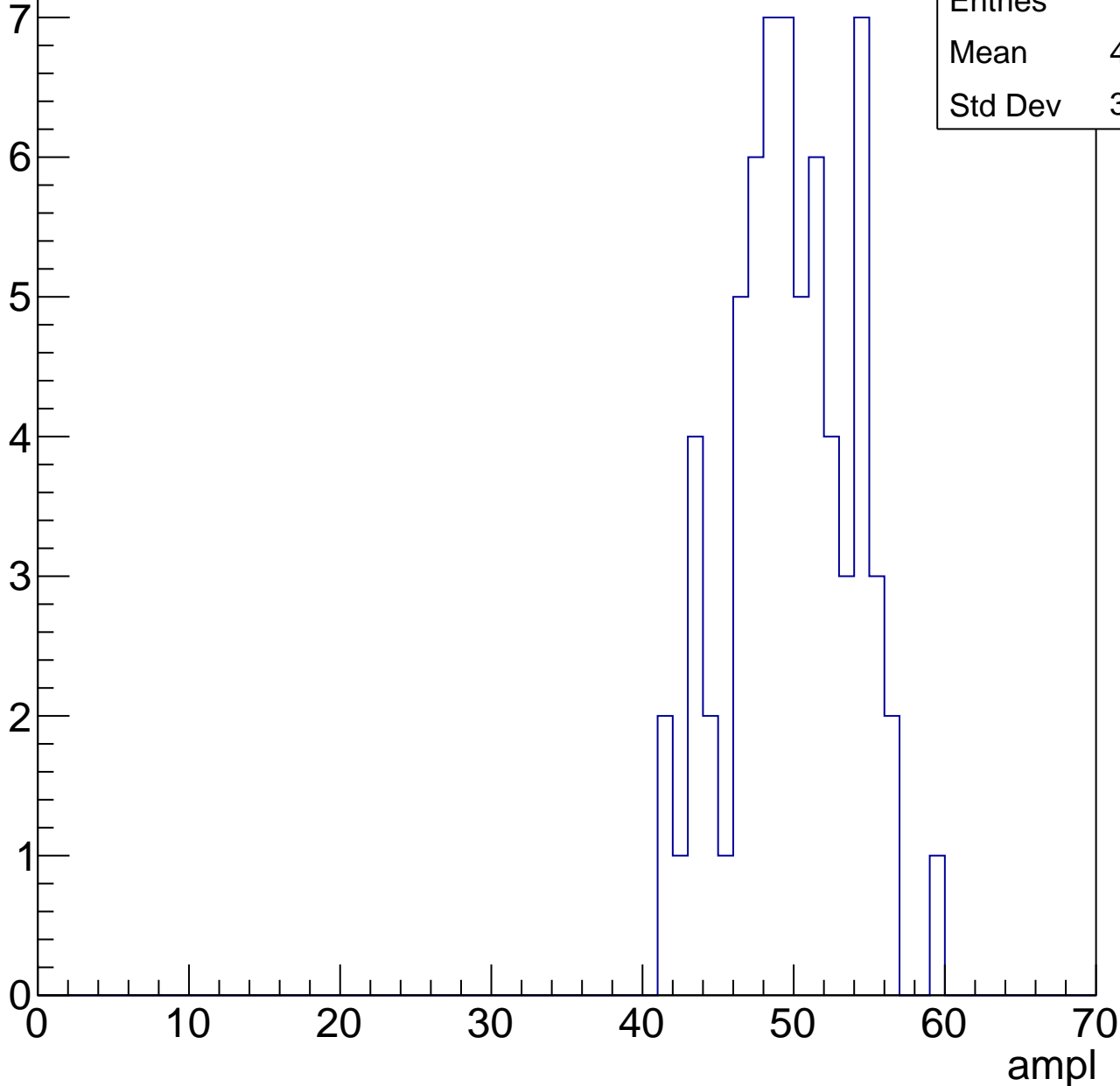


B1L102S, U18-ch3, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.35
Std Dev	3.987

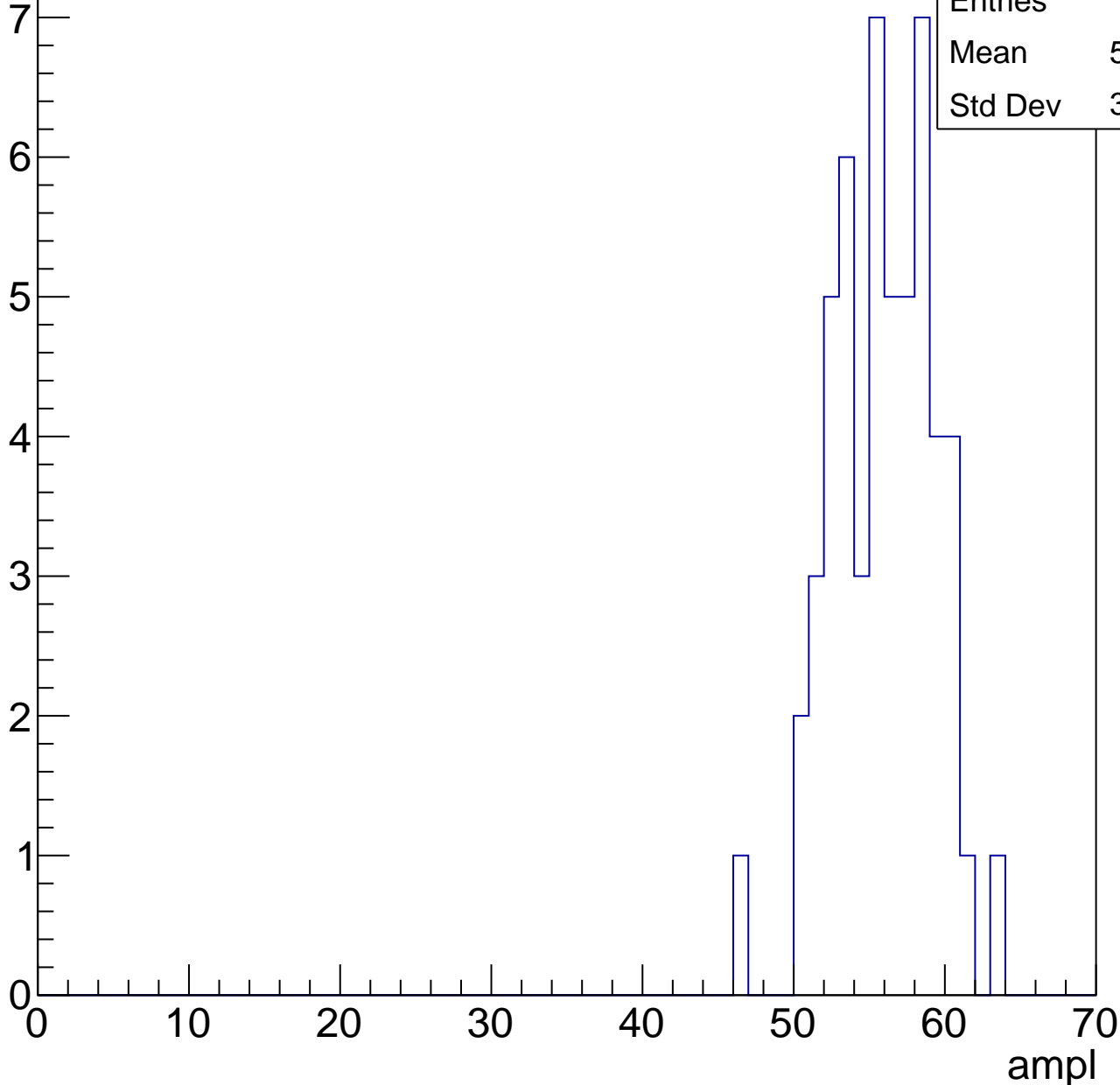


B1L102S, U18-ch3, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	55.46
Std Dev	3.309

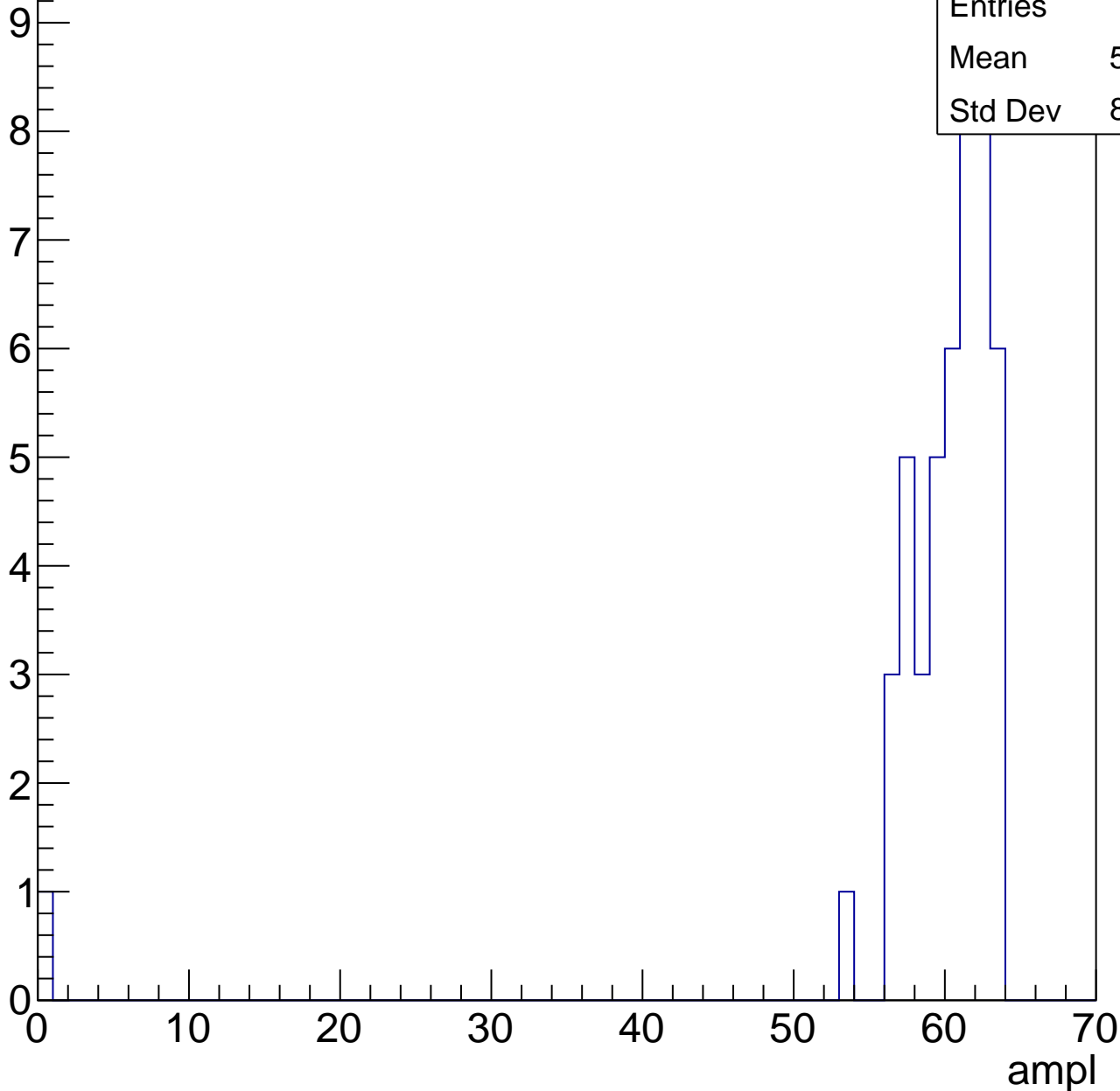


B1L102S, U18-ch3, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

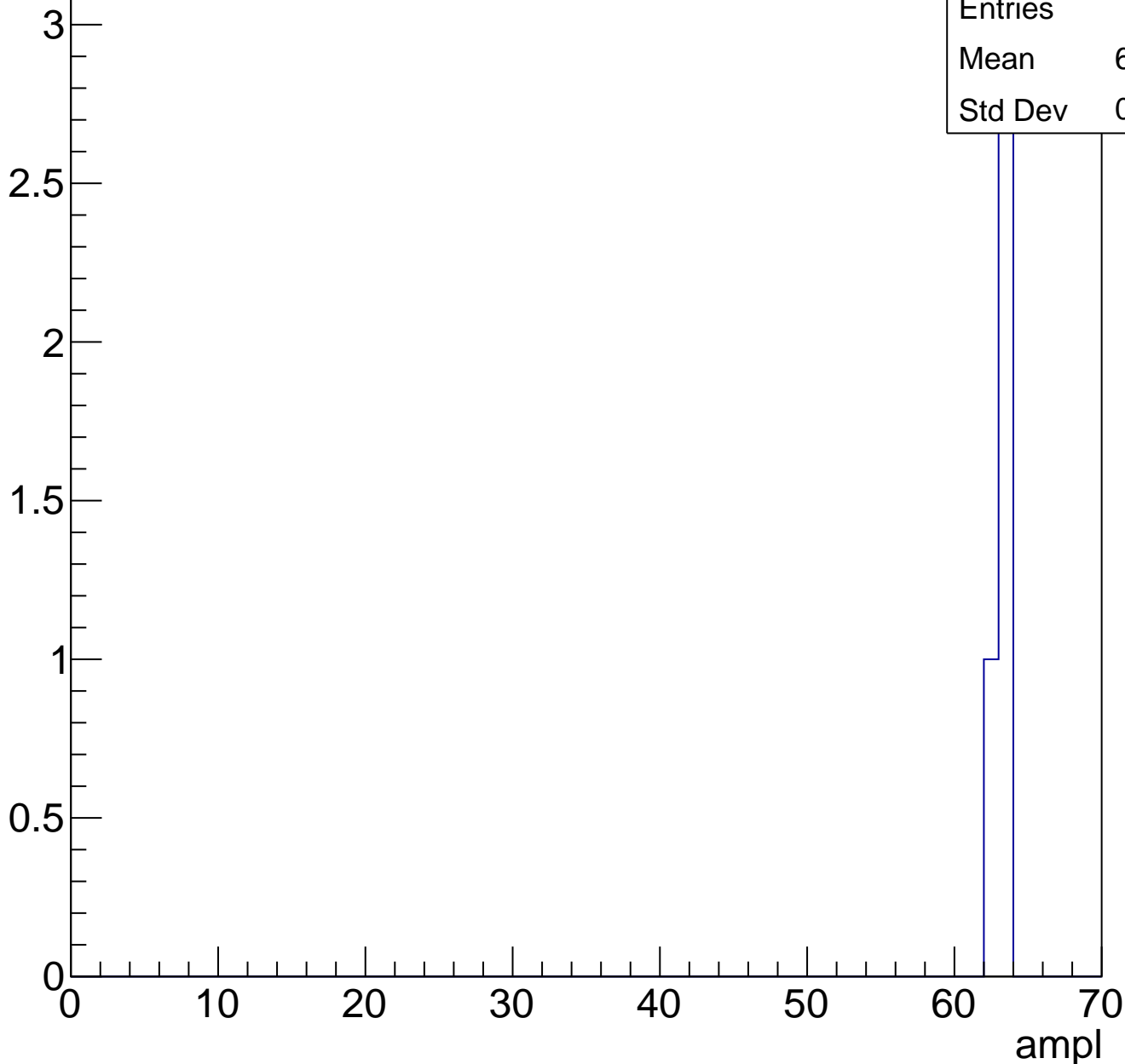
Entries	48
Mean	58.75
Std Dev	8.878



B1L102S, U18-ch3, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch3, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch4, adc0

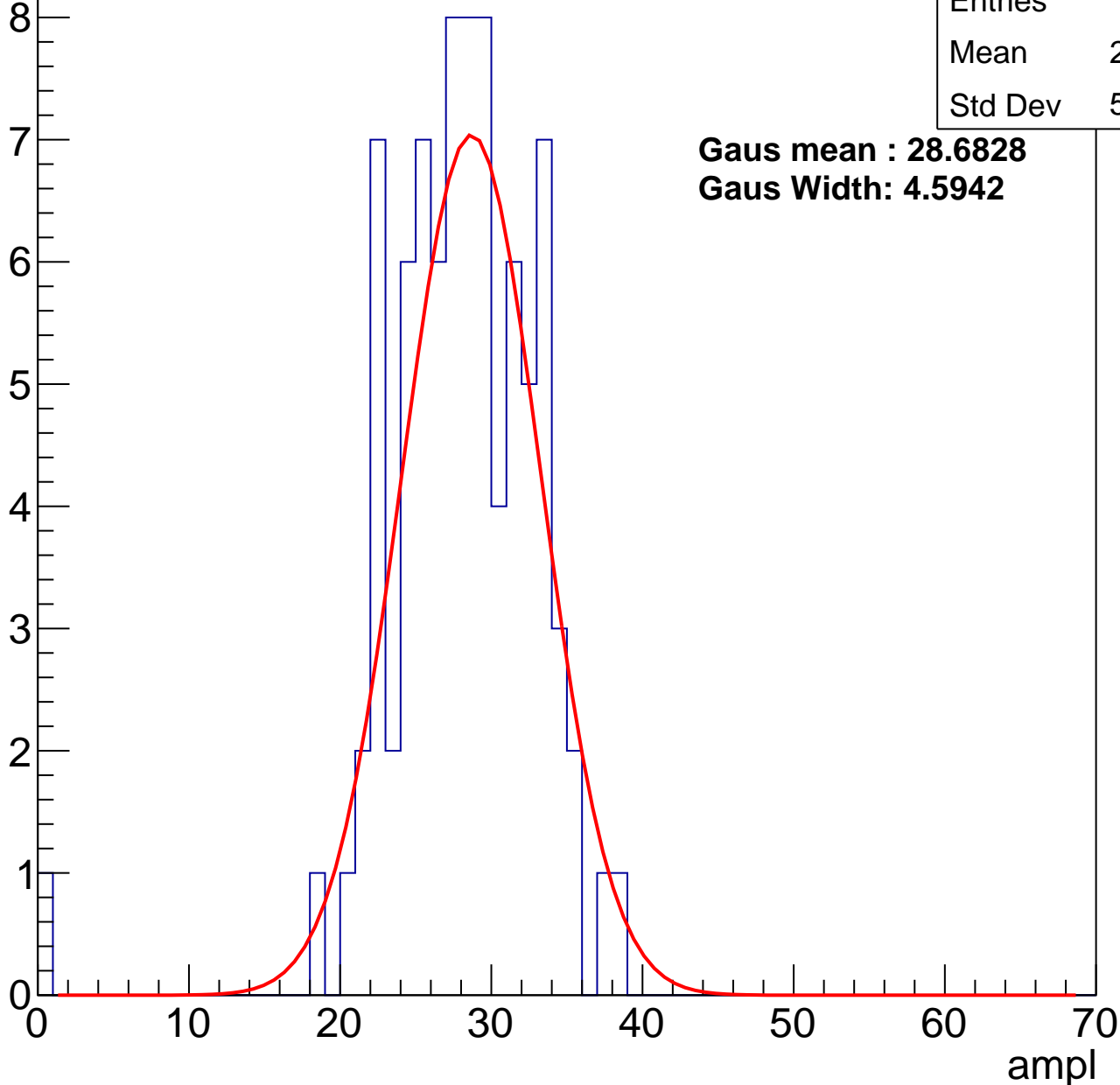
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	27.57
Std Dev	5.107

Gaus mean : 28.6828

Gaus Width: 4.5942



B1L102S, U18-ch4, adc1

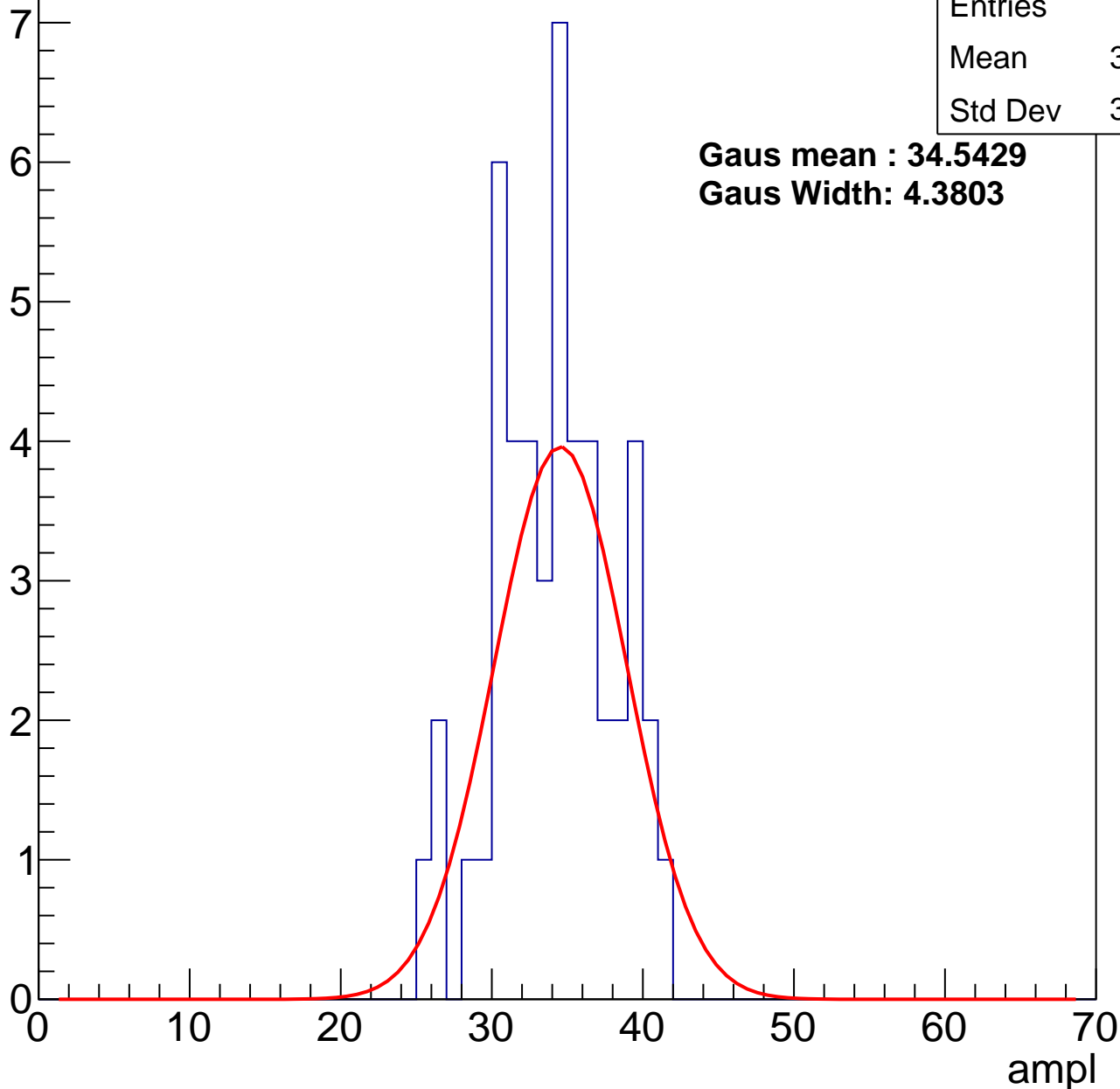
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	33.62
Std Dev	3.833

Gaus mean : 34.5429

Gaus Width: 4.3803



B1L102S, U18-ch4, adc2

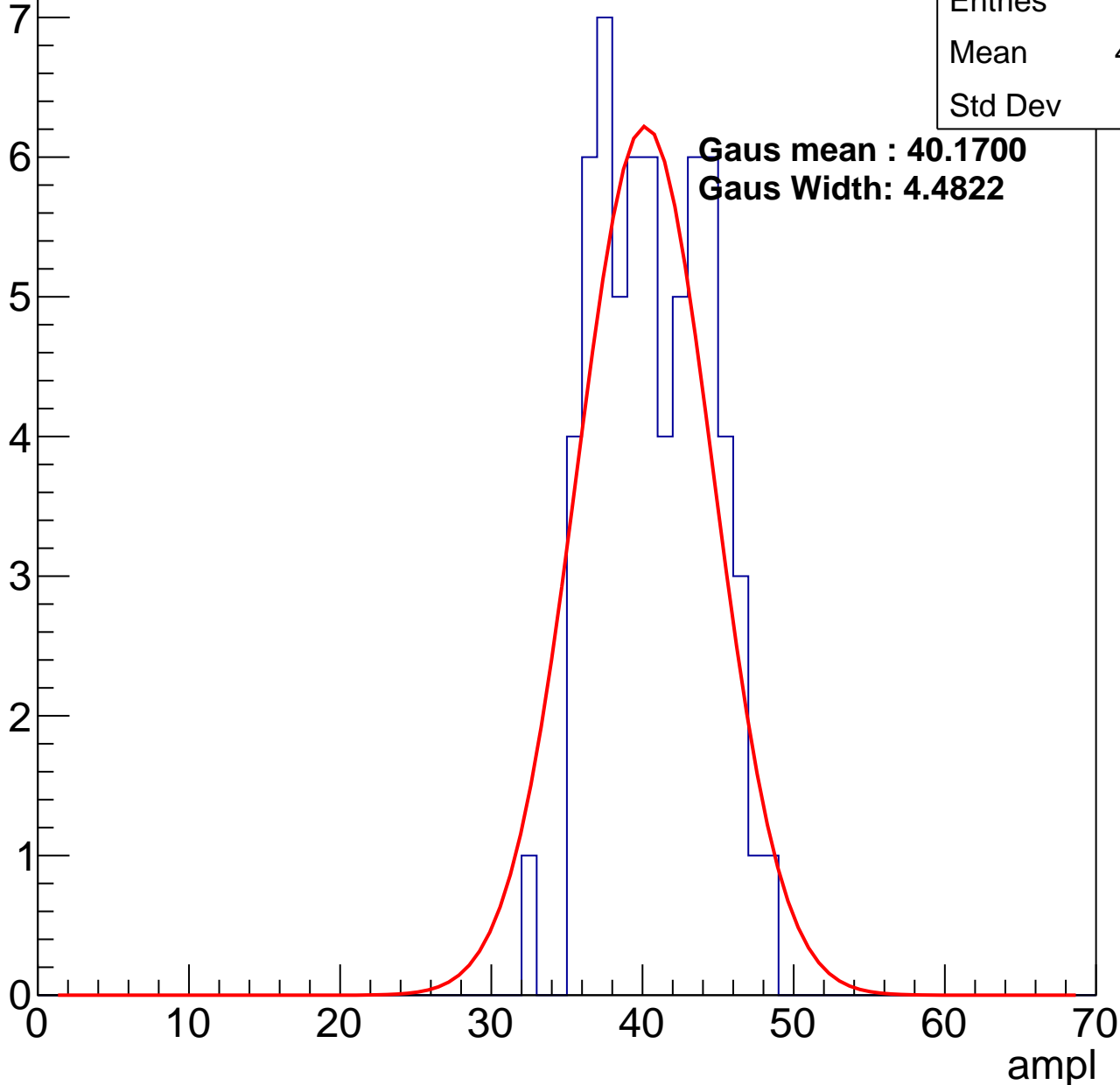
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.31
Std Dev	3.59

Gaus mean : 40.1700

Gaus Width: 4.4822

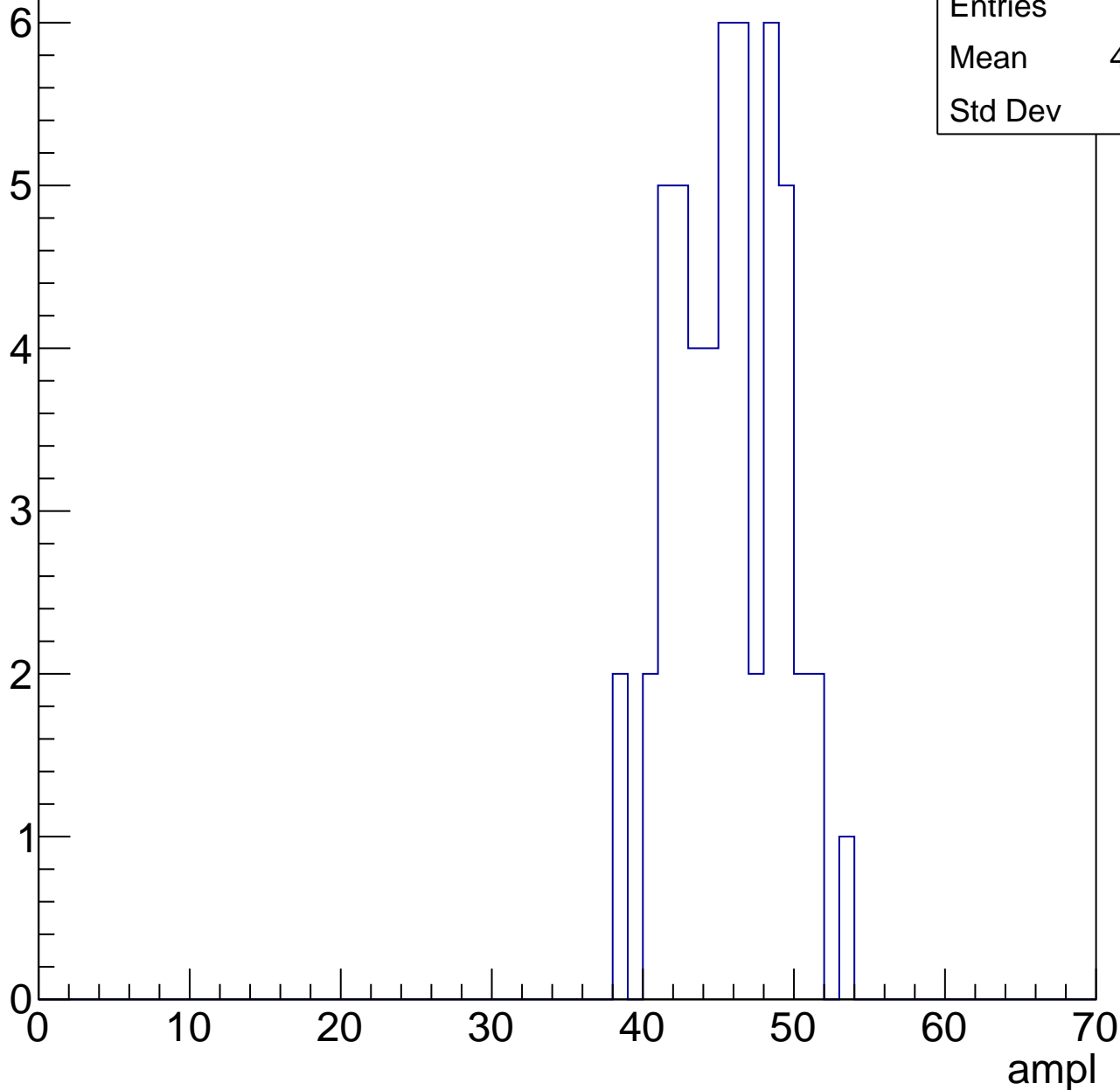


B1L102S, U18-ch4, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	45.13
Std Dev	3.47

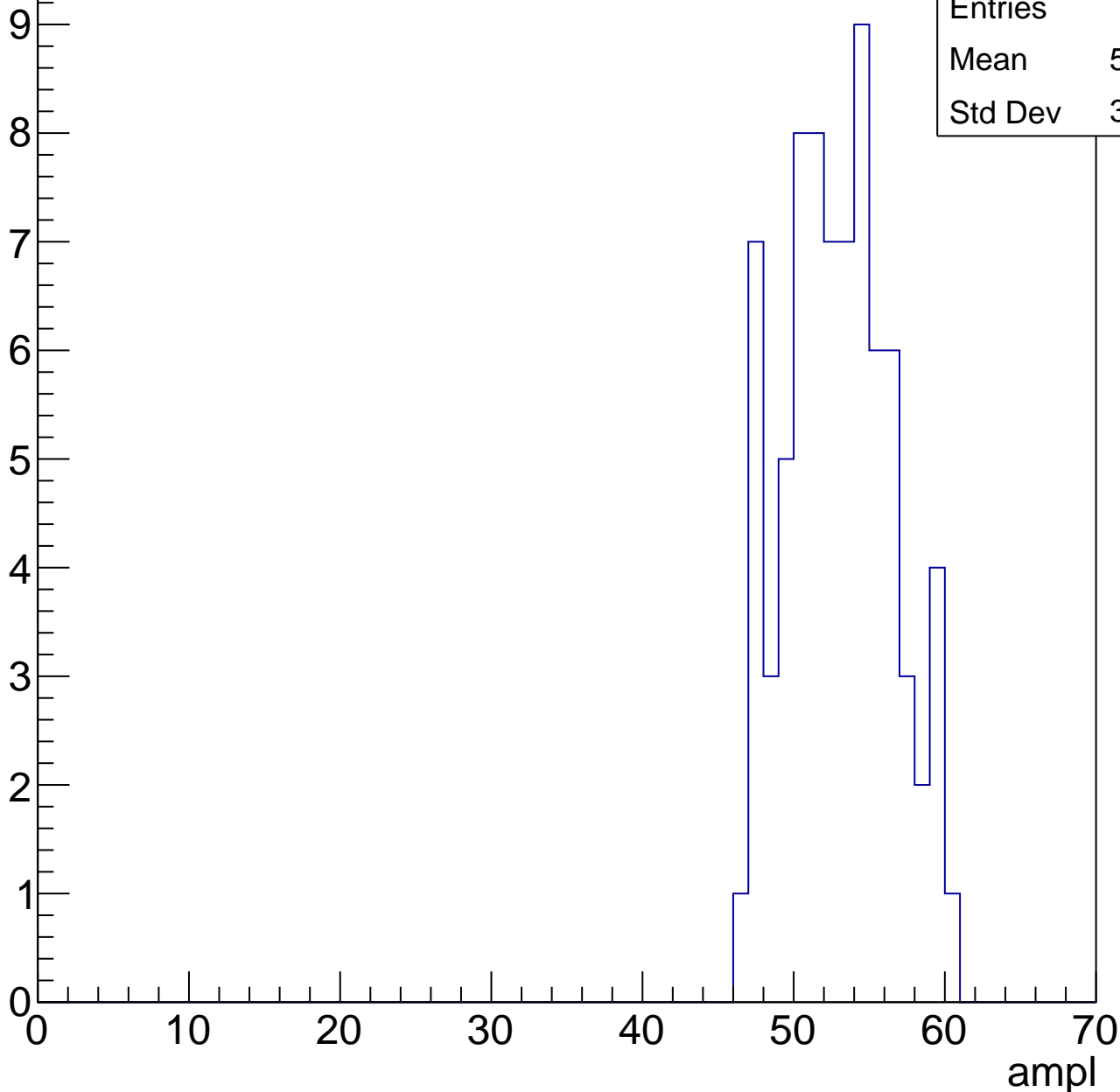


B1L102S, U18-ch4, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	52.49
Std Dev	3.466



B1L102S, U18-ch4, adc5

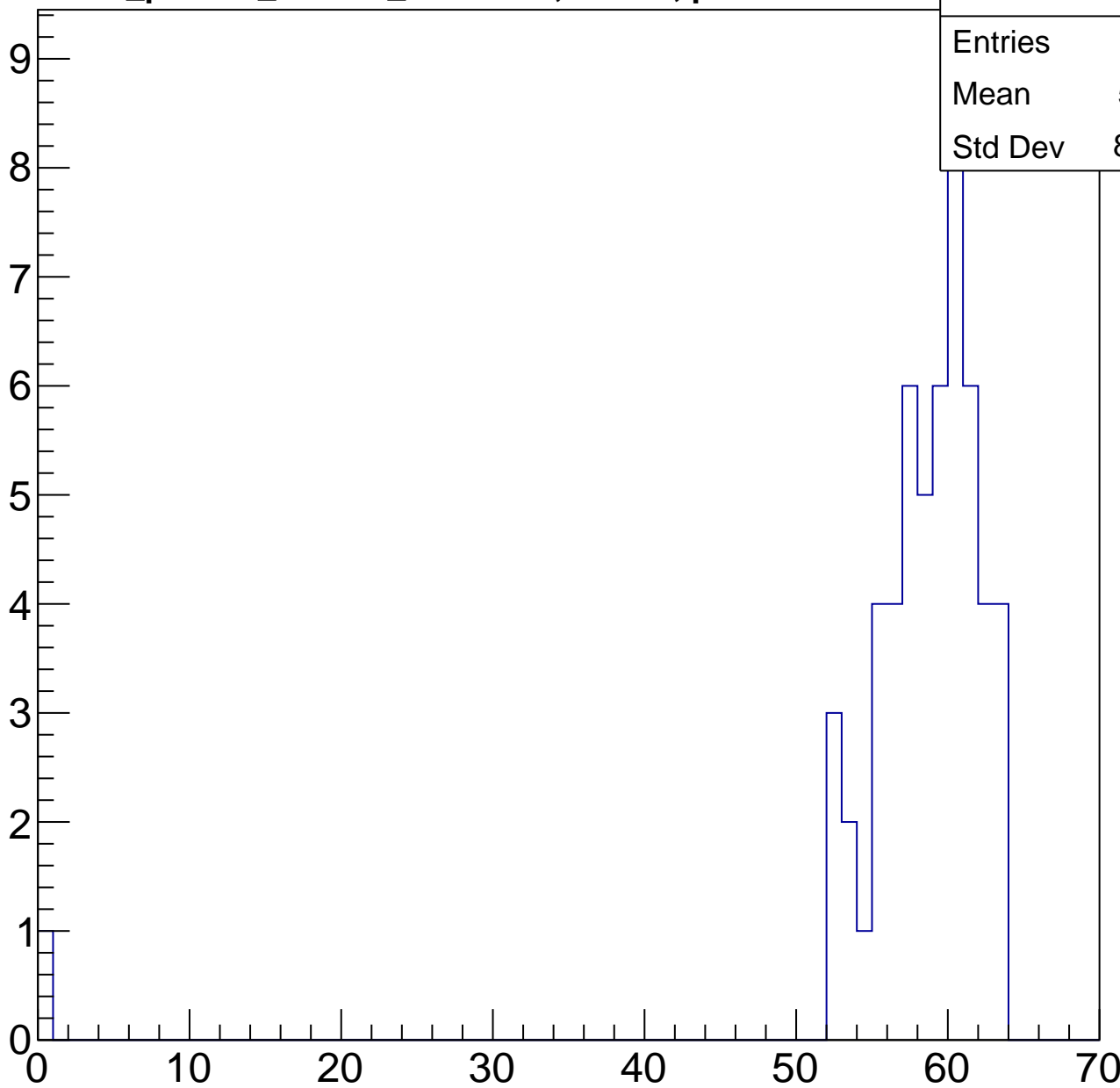
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	55
Mean	57.31
Std Dev	8.345

ampl

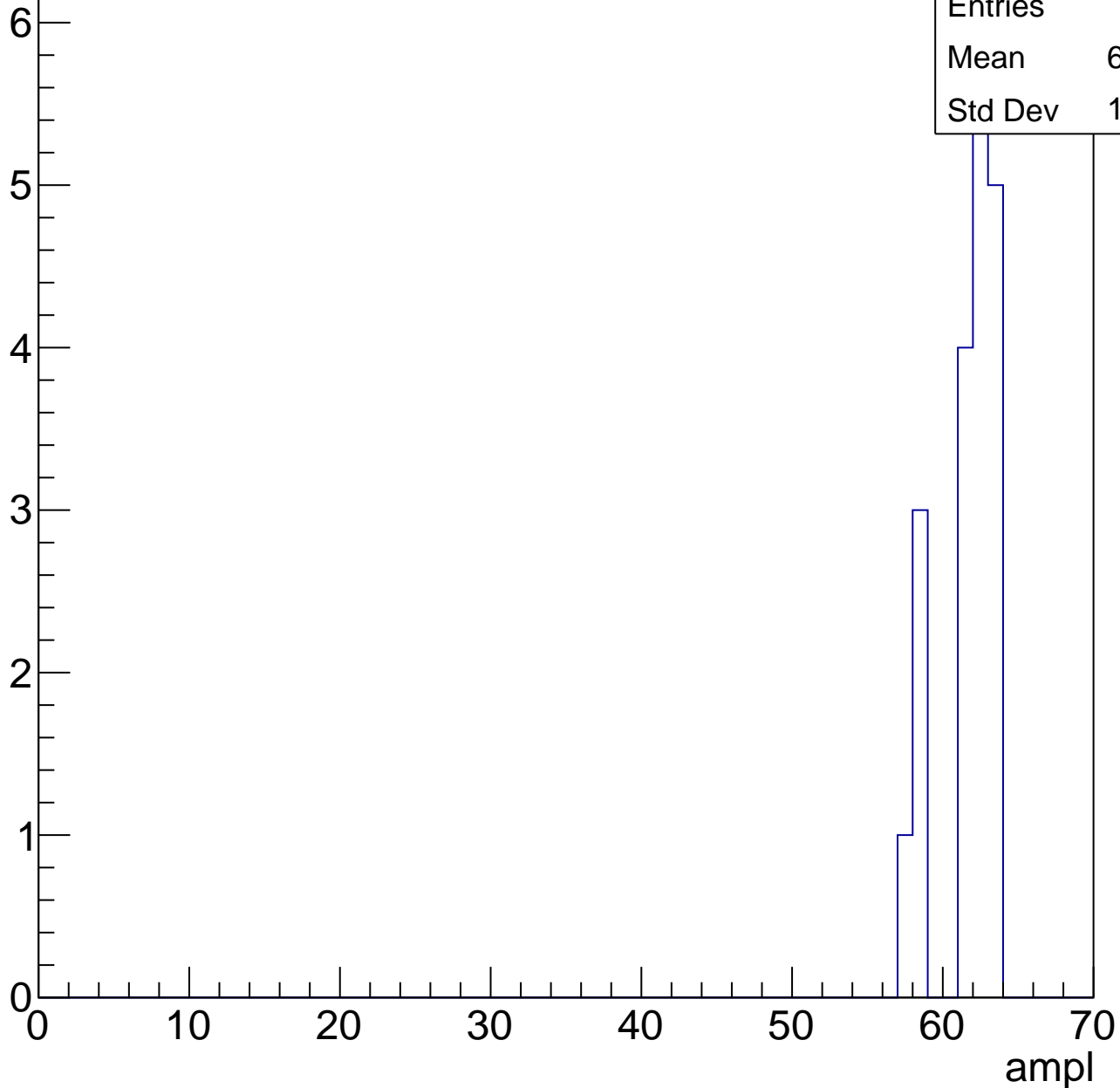


B1L102S, U18-ch4, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	61.16
Std Dev	1.899



B1L102S, U18-ch4, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch5, adc0

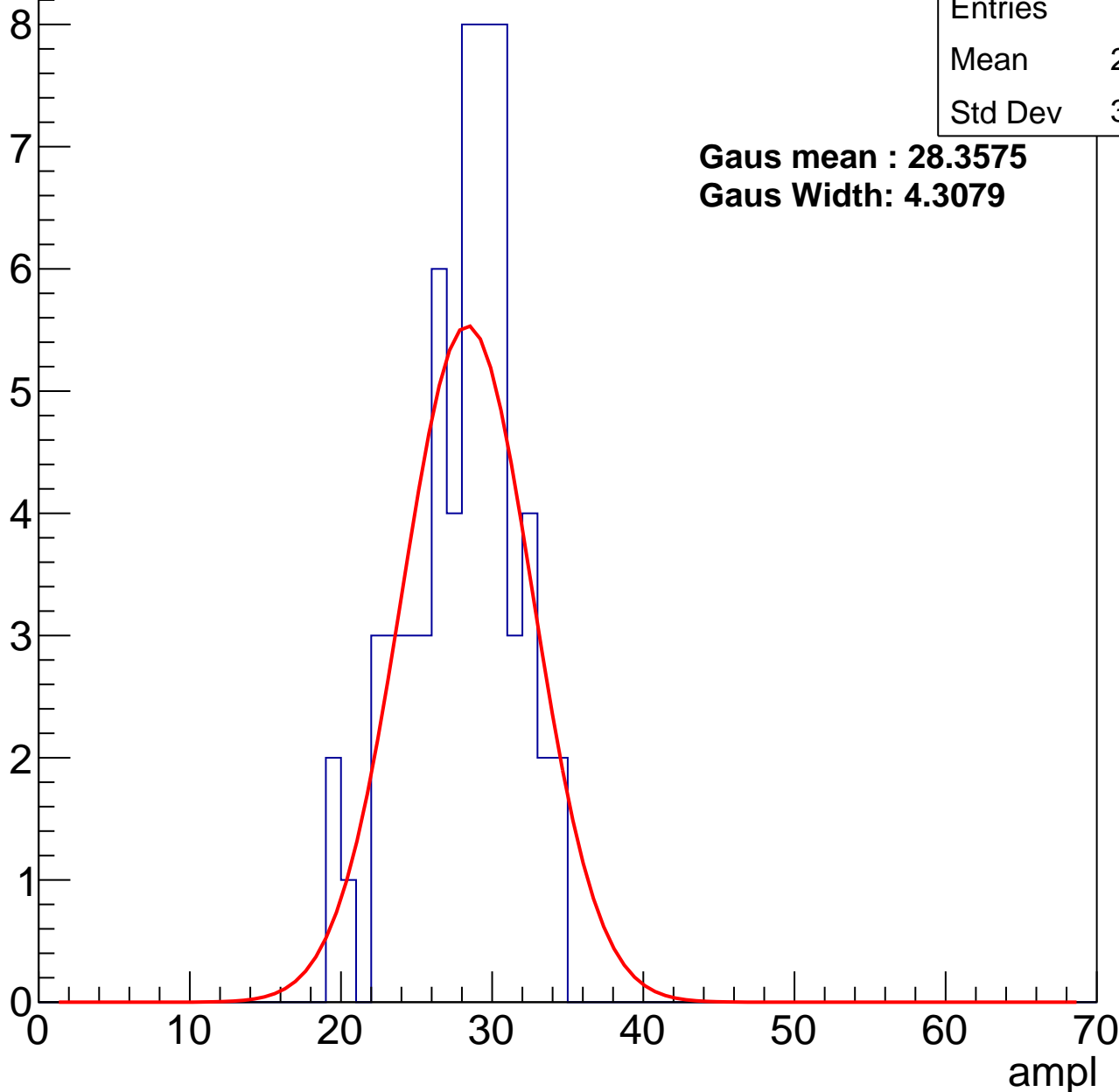
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	27.58
Std Dev	3.546

Gaus mean : 28.3575

Gaus Width: 4.3079



B1L102S, U18-ch5, adc1

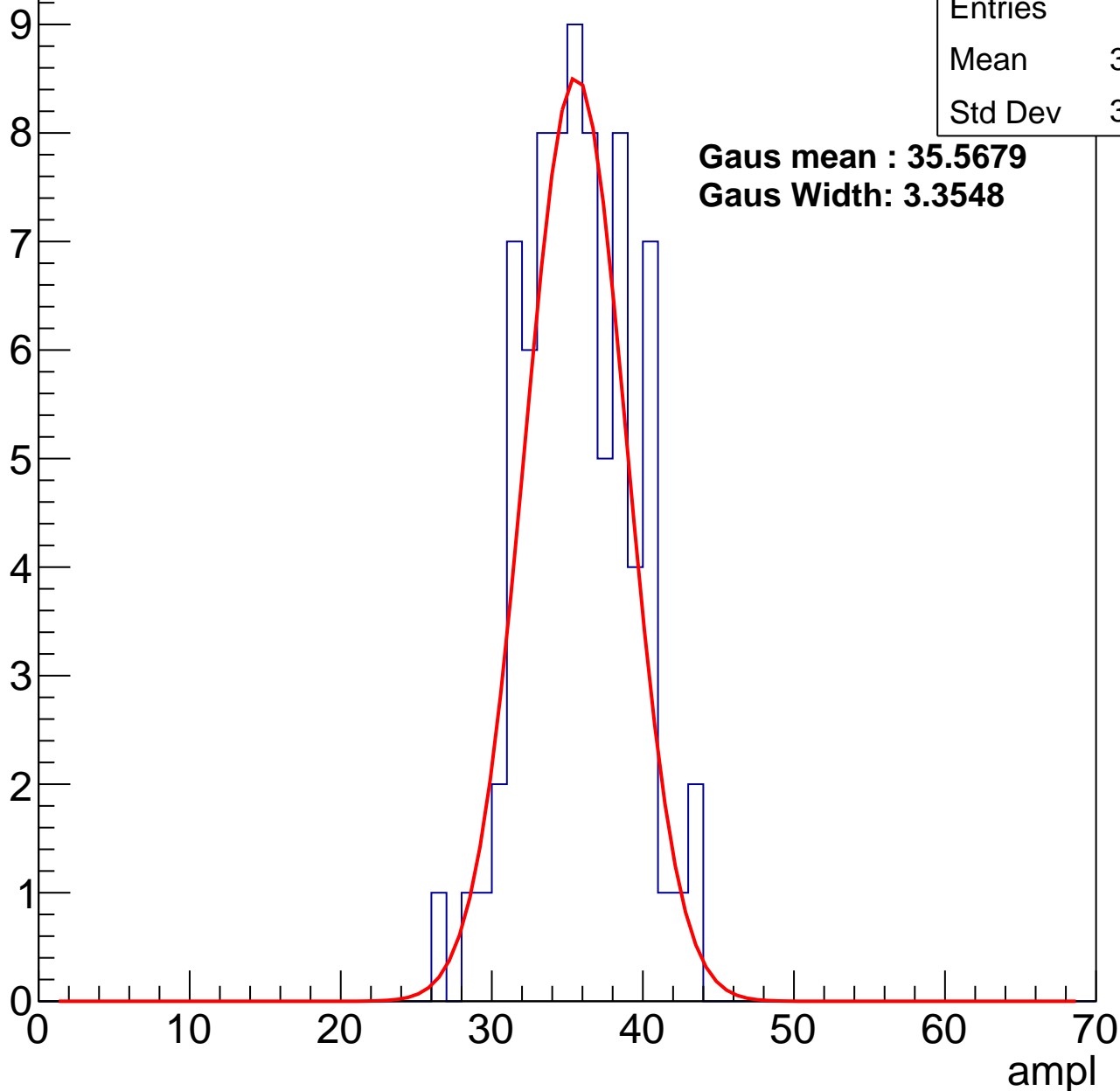
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.25
Std Dev	3.502

Gaus mean : 35.5679

Gaus Width: 3.3548



B1L102S, U18-ch5, adc2

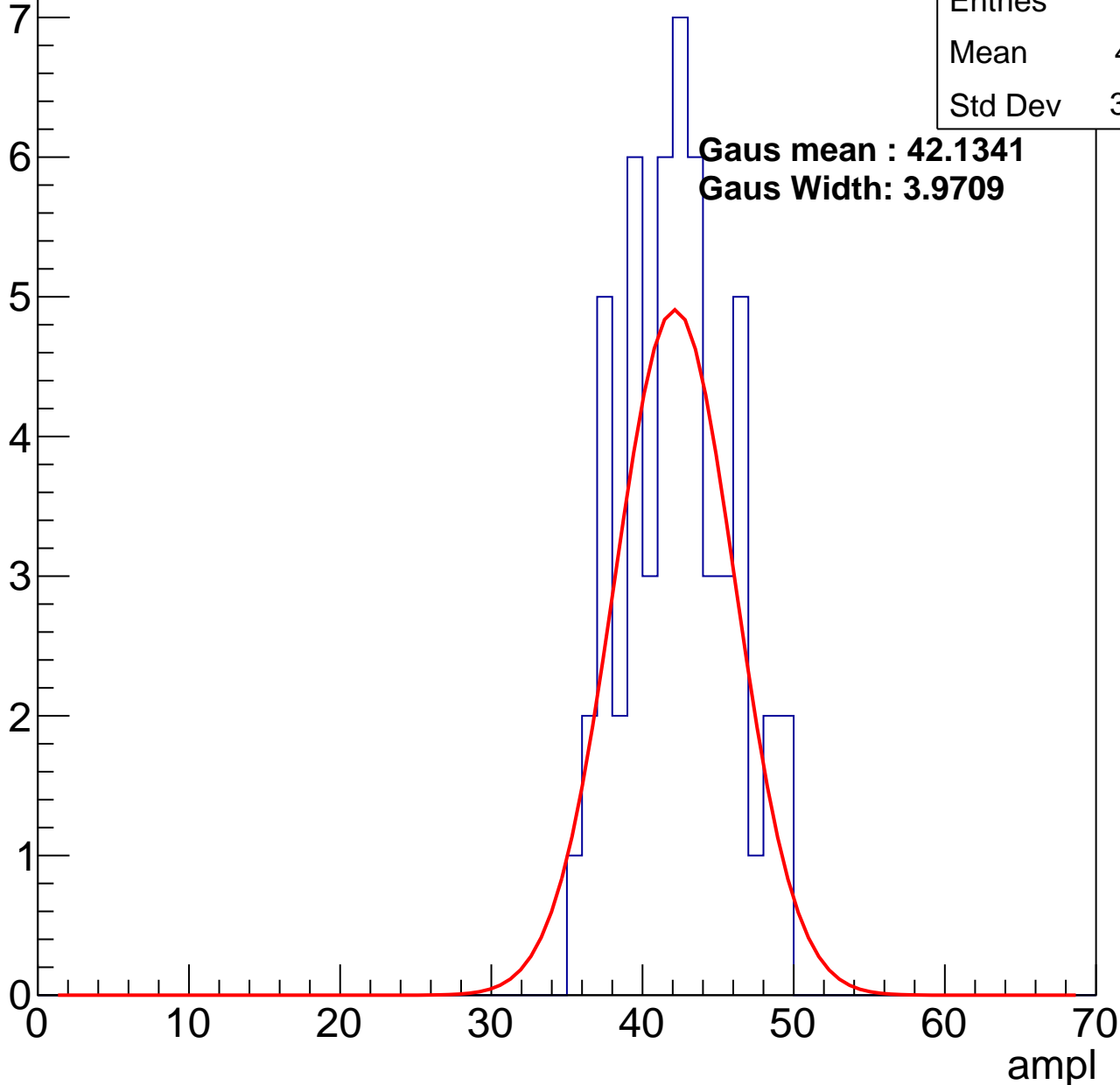
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.81
Std Dev	3.518

Gaus mean : 42.1341

Gaus Width: 3.9709

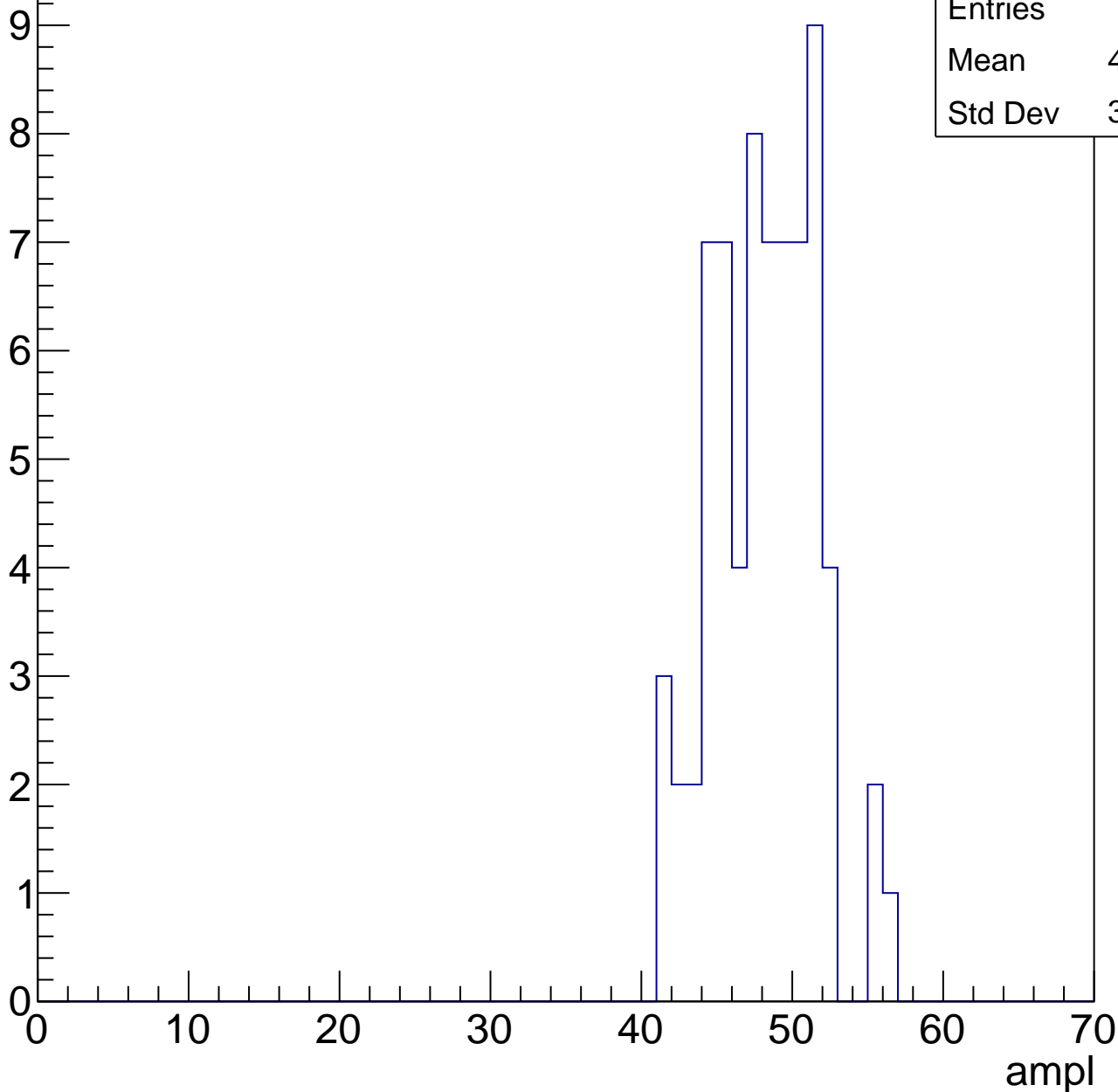


B1L102S, U18-ch5, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.69
Std Dev	3.387



B1L102S, U18-ch5, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	54.99
Std Dev	3.28

Entry

10

8

6

4

2

0

0

10

20

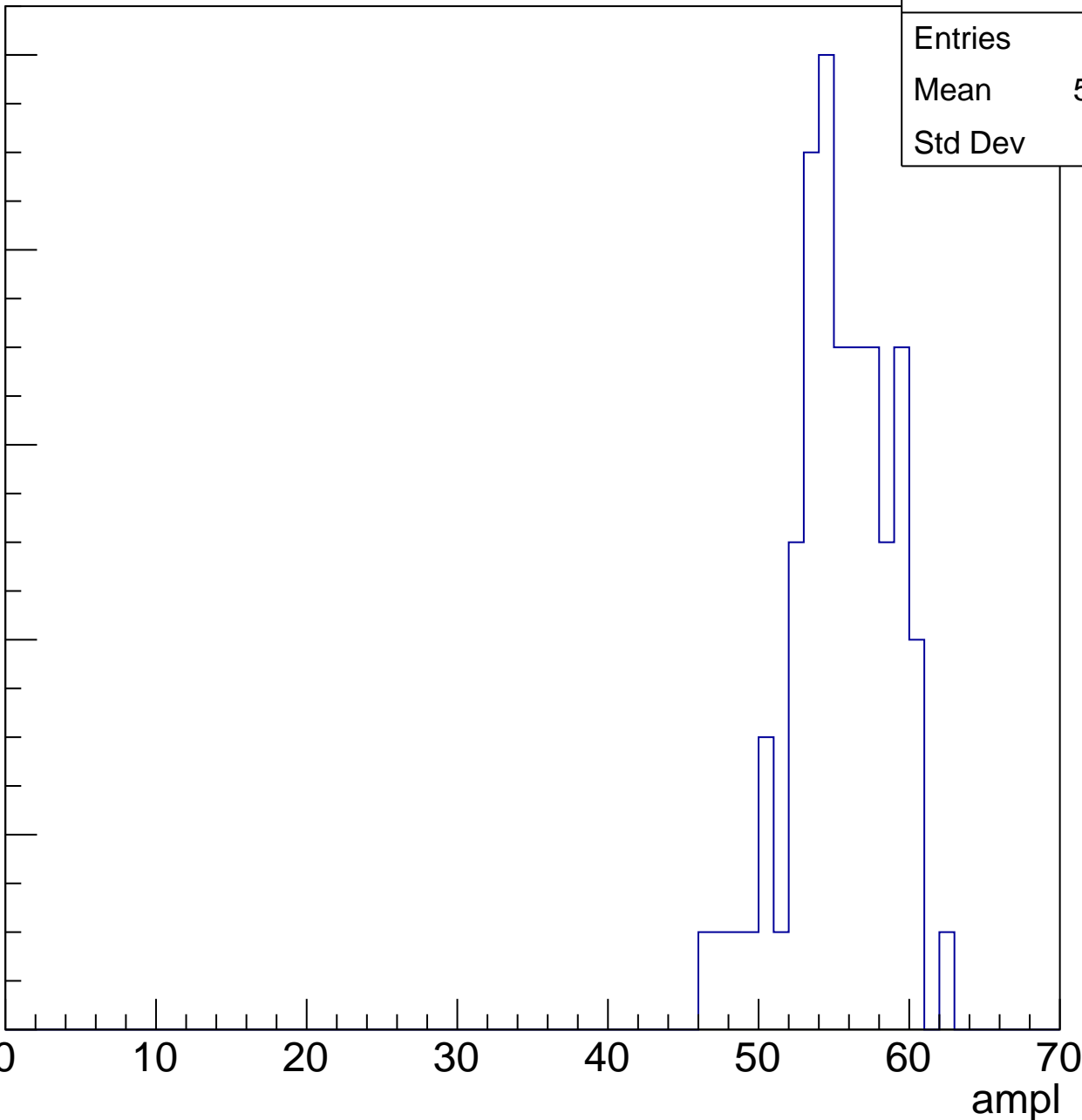
30

40

50

60

ampl

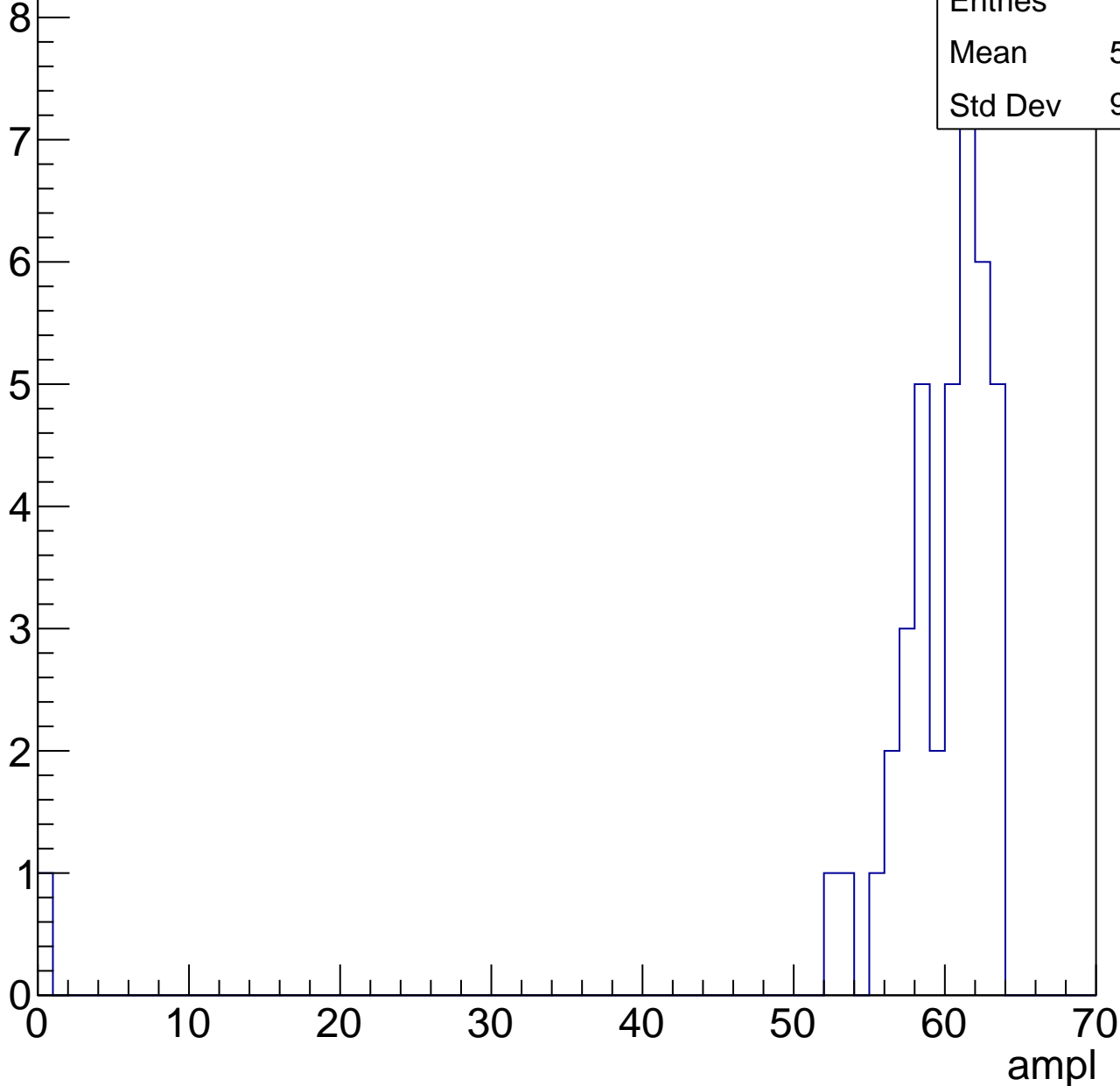


B1L102S, U18-ch5, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	58.15
Std Dev	9.694

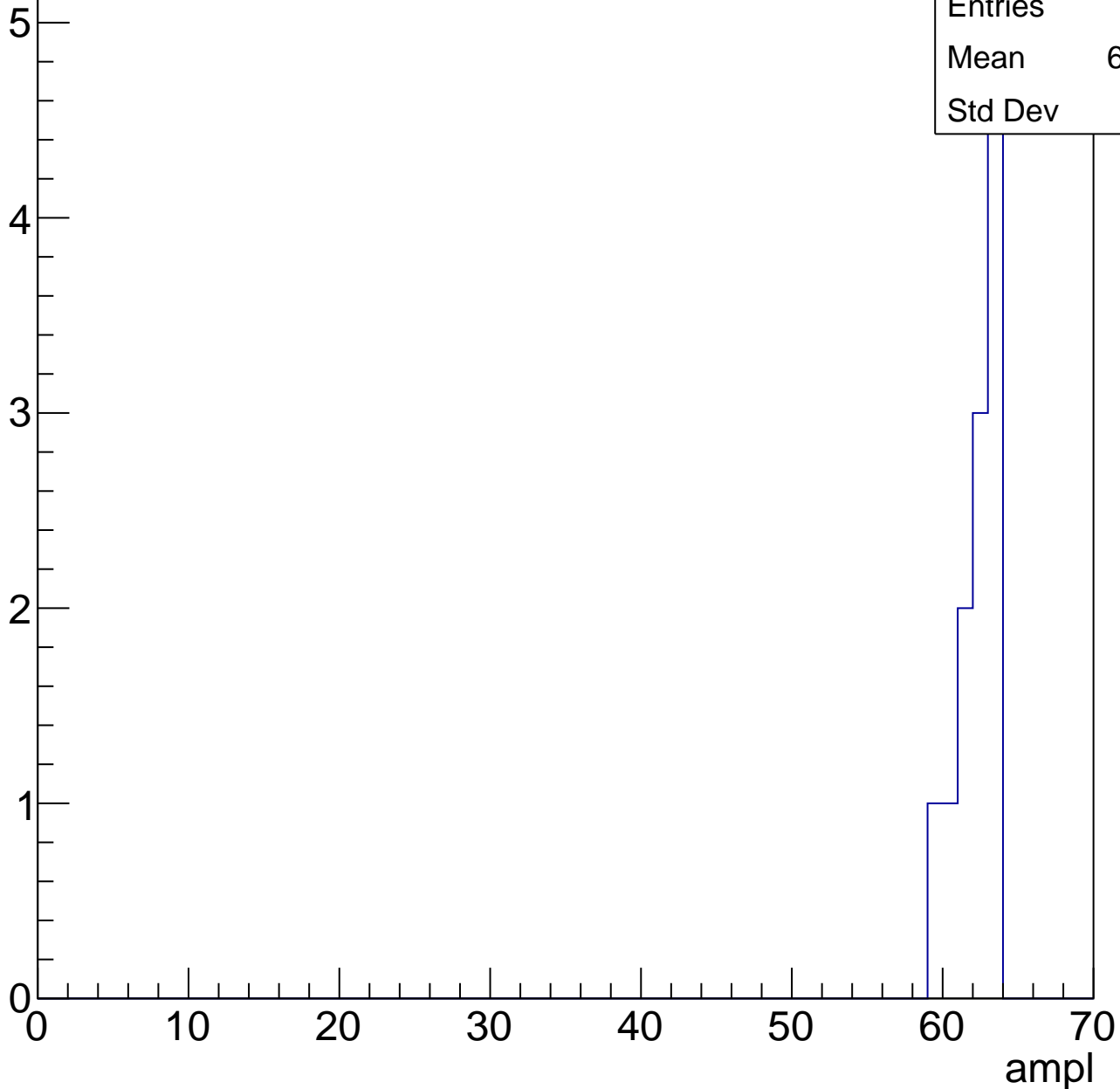


B1L102S, U18-ch5, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.83
Std Dev	1.28



B1L102S, U18-ch5, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch6, adc0

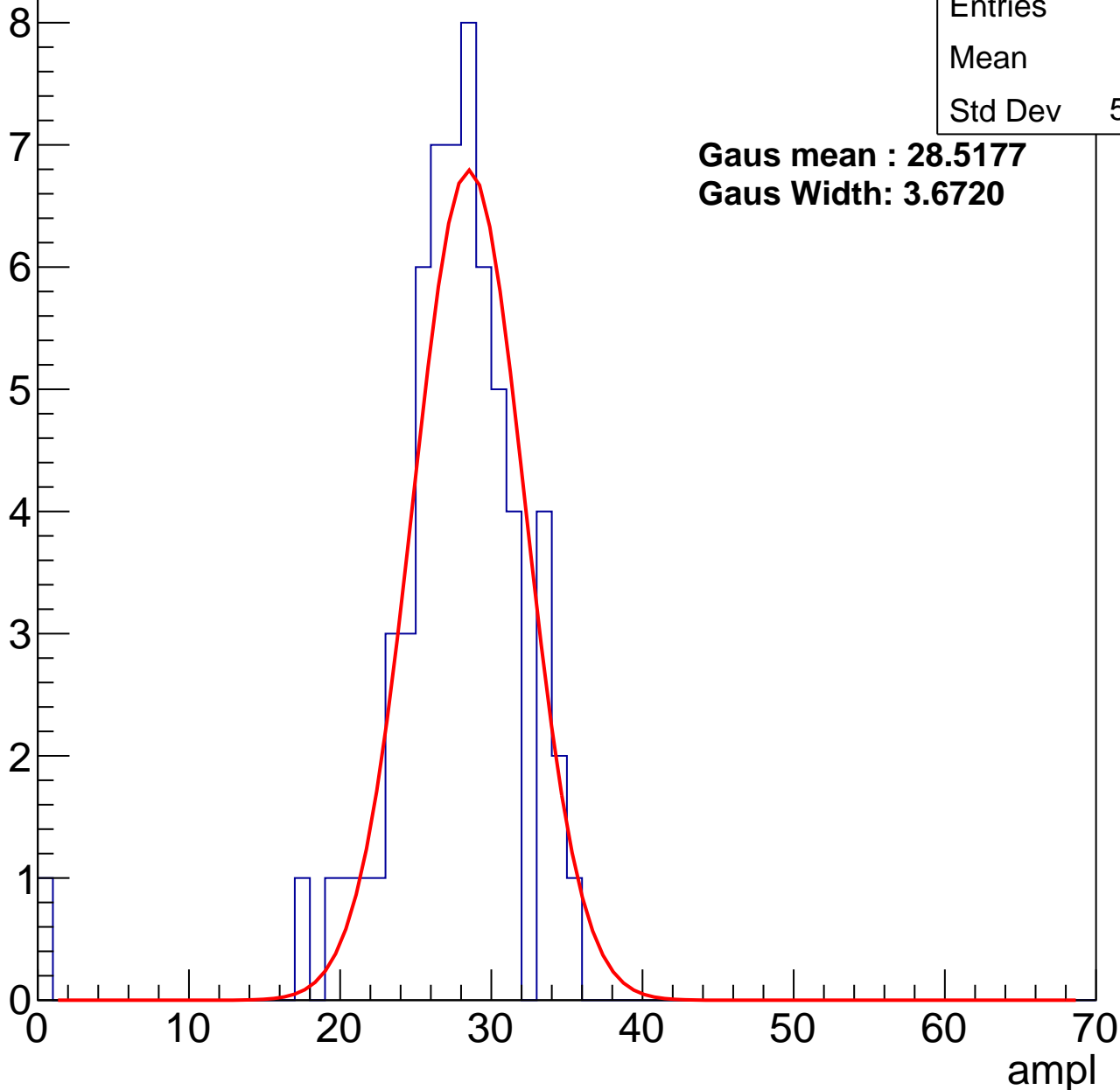
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	26.9
Std Dev	5.009

Gaus mean : 28.5177

Gaus Width: 3.6720



B1L102S, U18-ch6, adc1

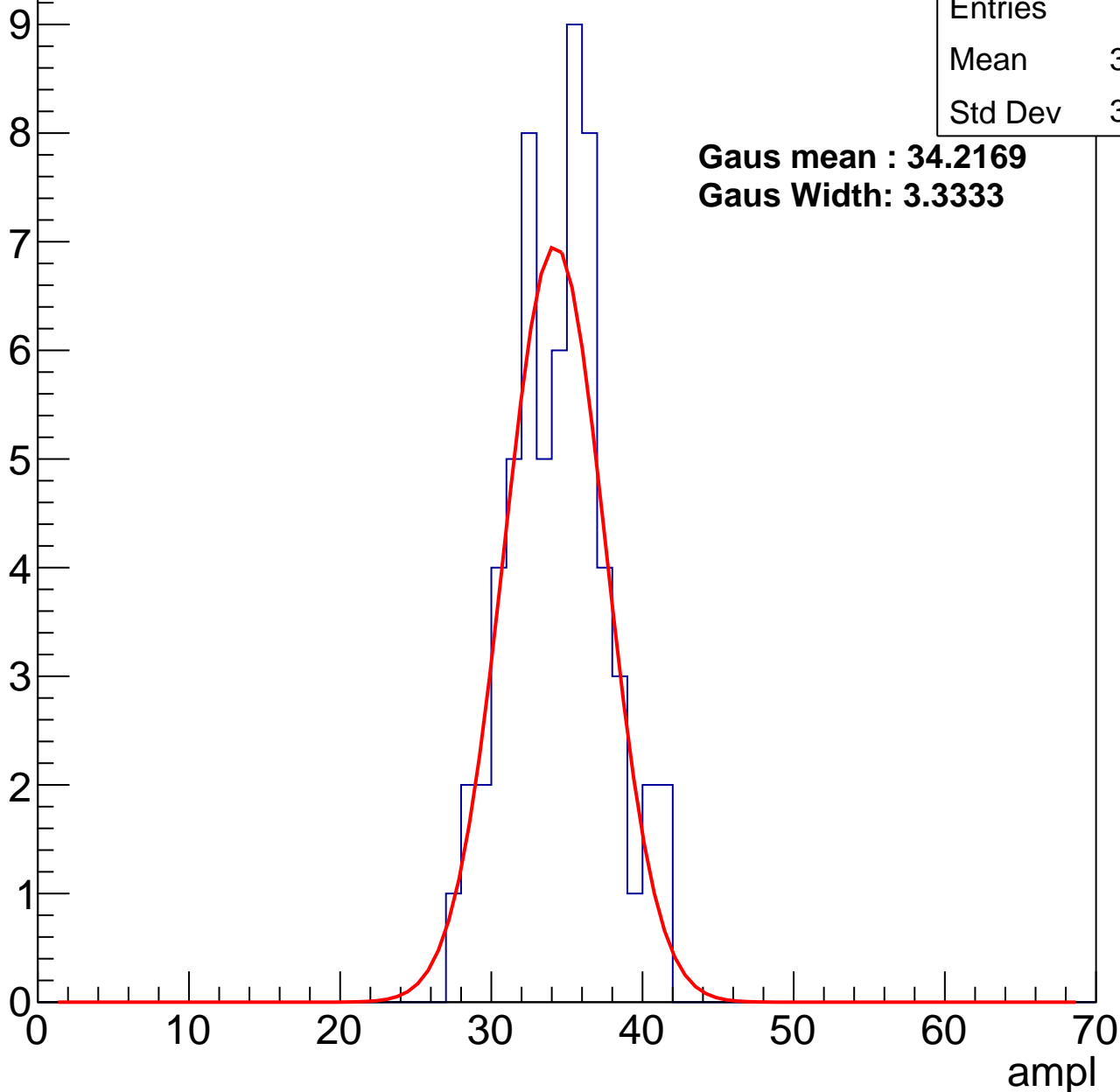
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	33.98
Std Dev	3.205

Gaus mean : 34.2169

Gaus Width: 3.3333



B1L102S, U18-ch6, adc2

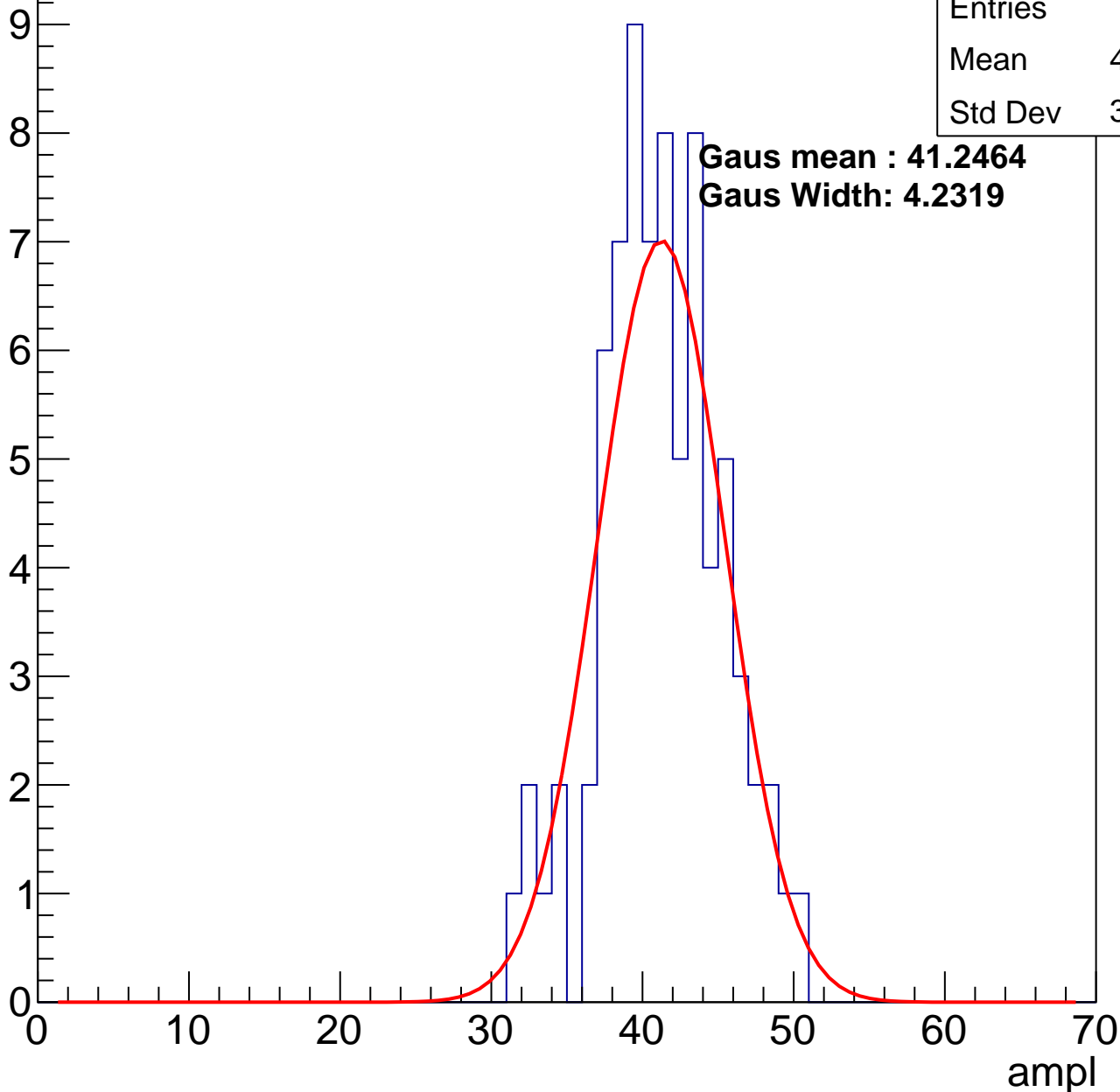
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	40.75
Std Dev	3.997

Gaus mean : 41.2464

Gaus Width: 4.2319

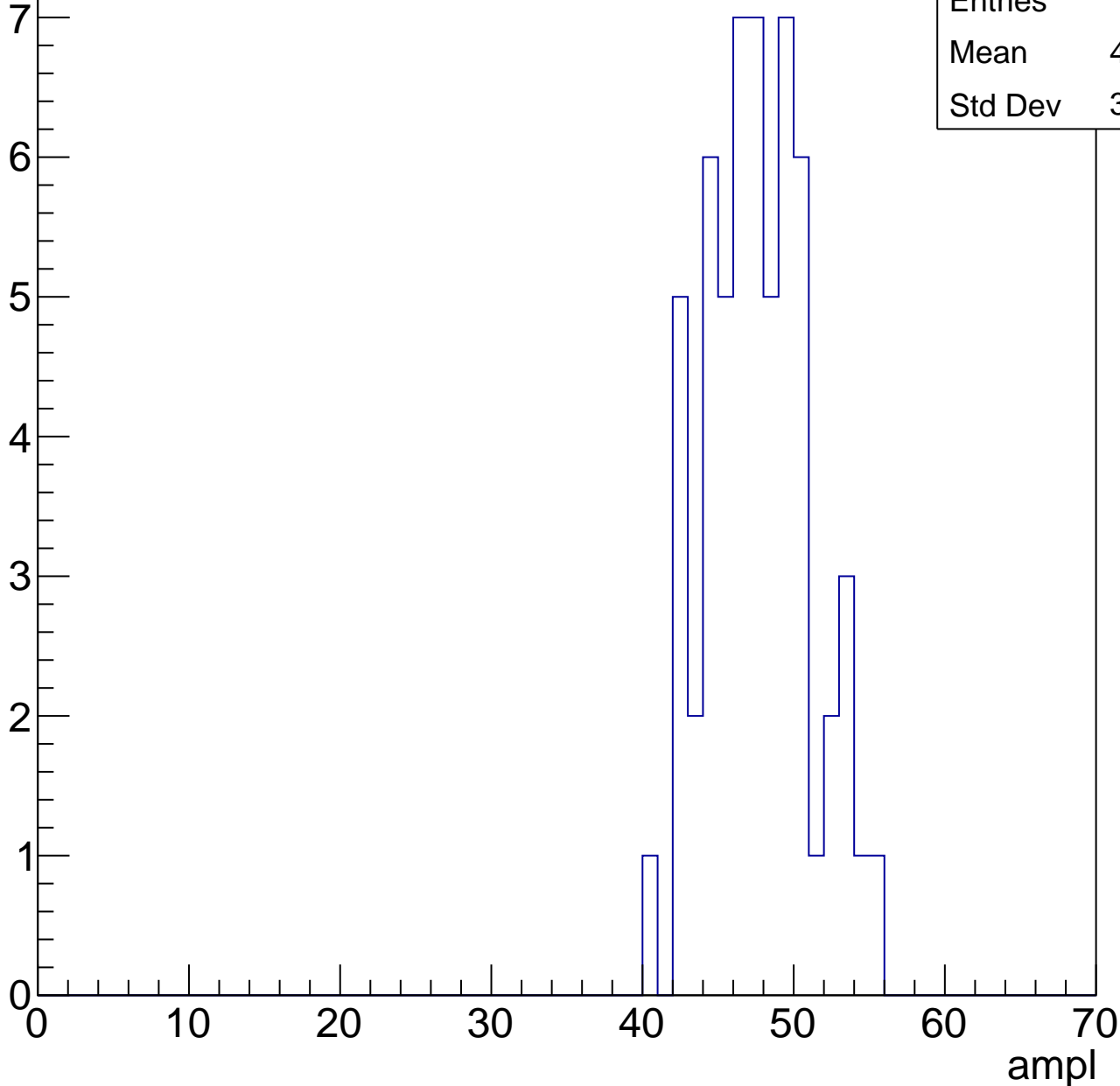


B1L102S, U18-ch6, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.15
Std Dev	3.344

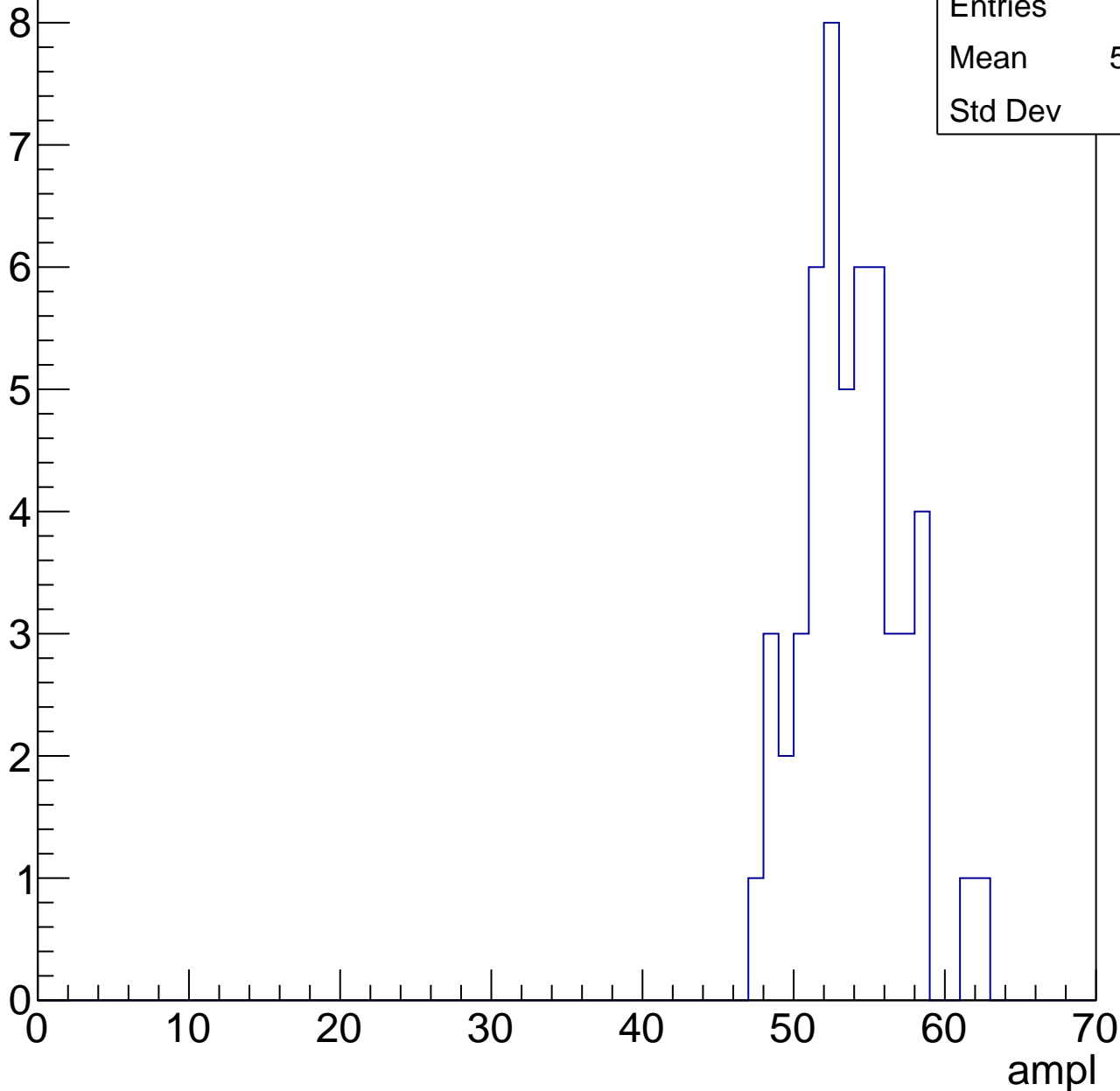


B1L102S, U18-ch6, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.35
Std Dev	3.24

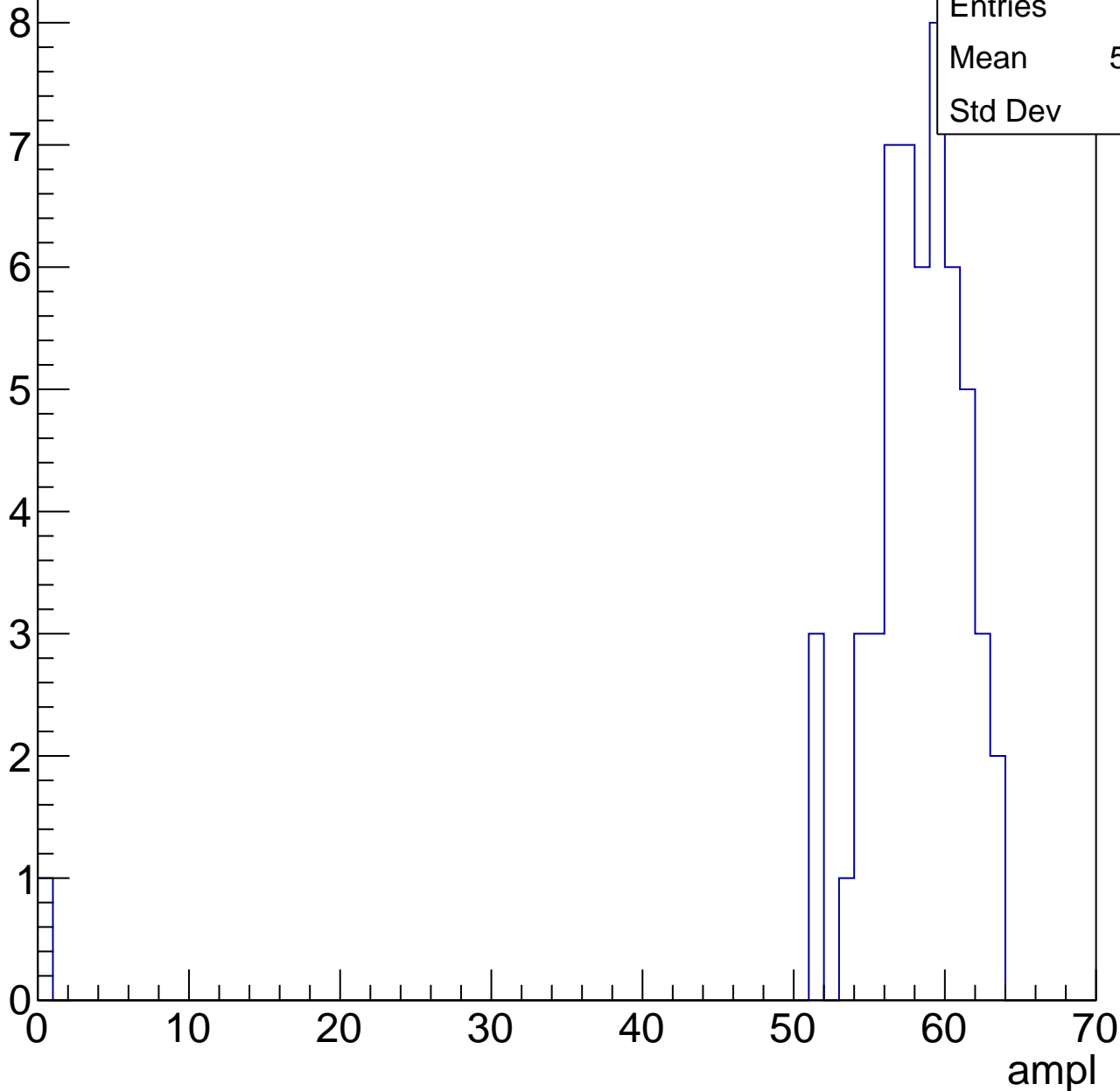


B1L102S, U18-ch6, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	56.75
Std Dev	8.24

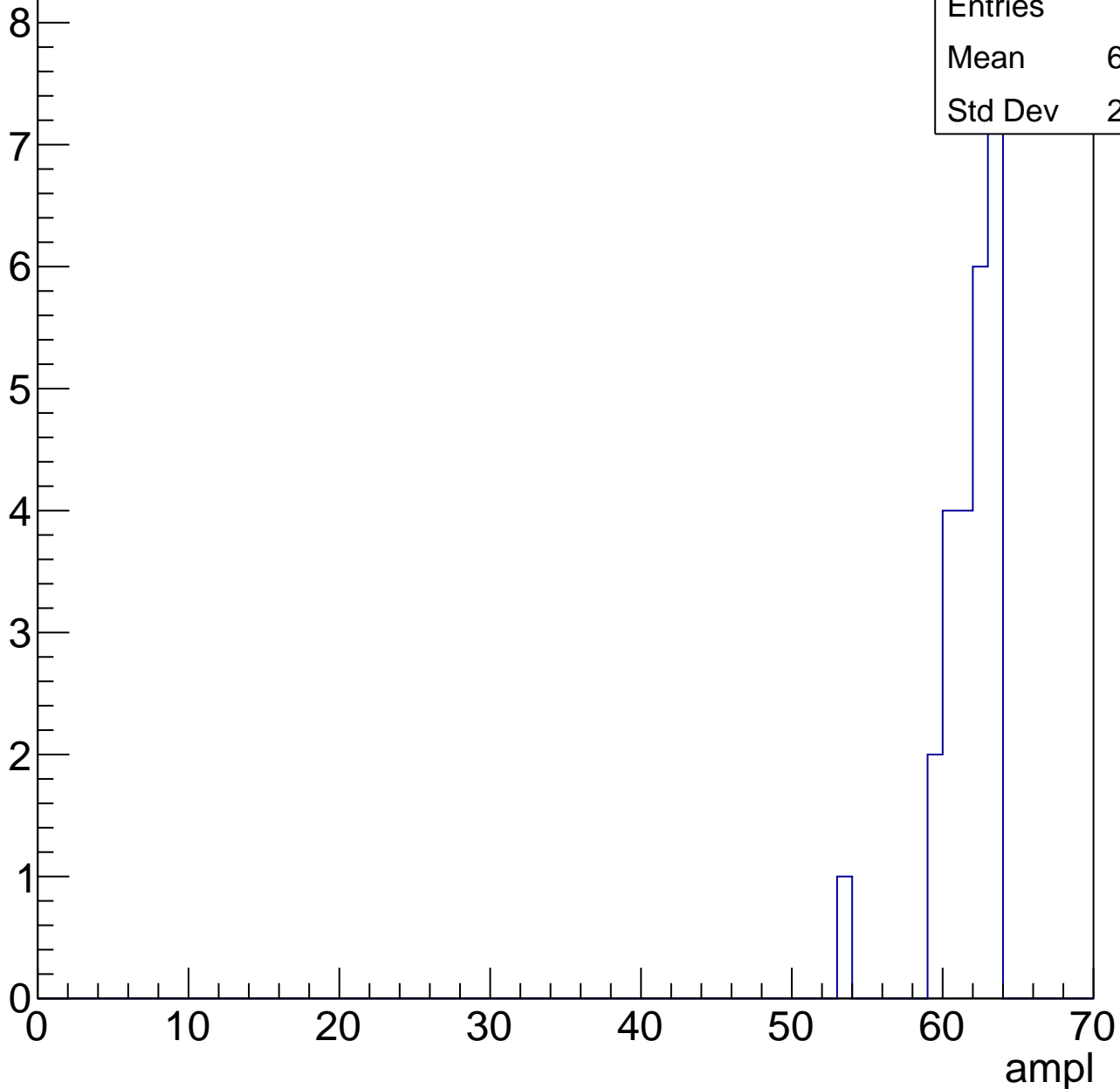


B1L102S, U18-ch6, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	61.24
Std Dev	2.122



B1L102S, U18-ch6, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

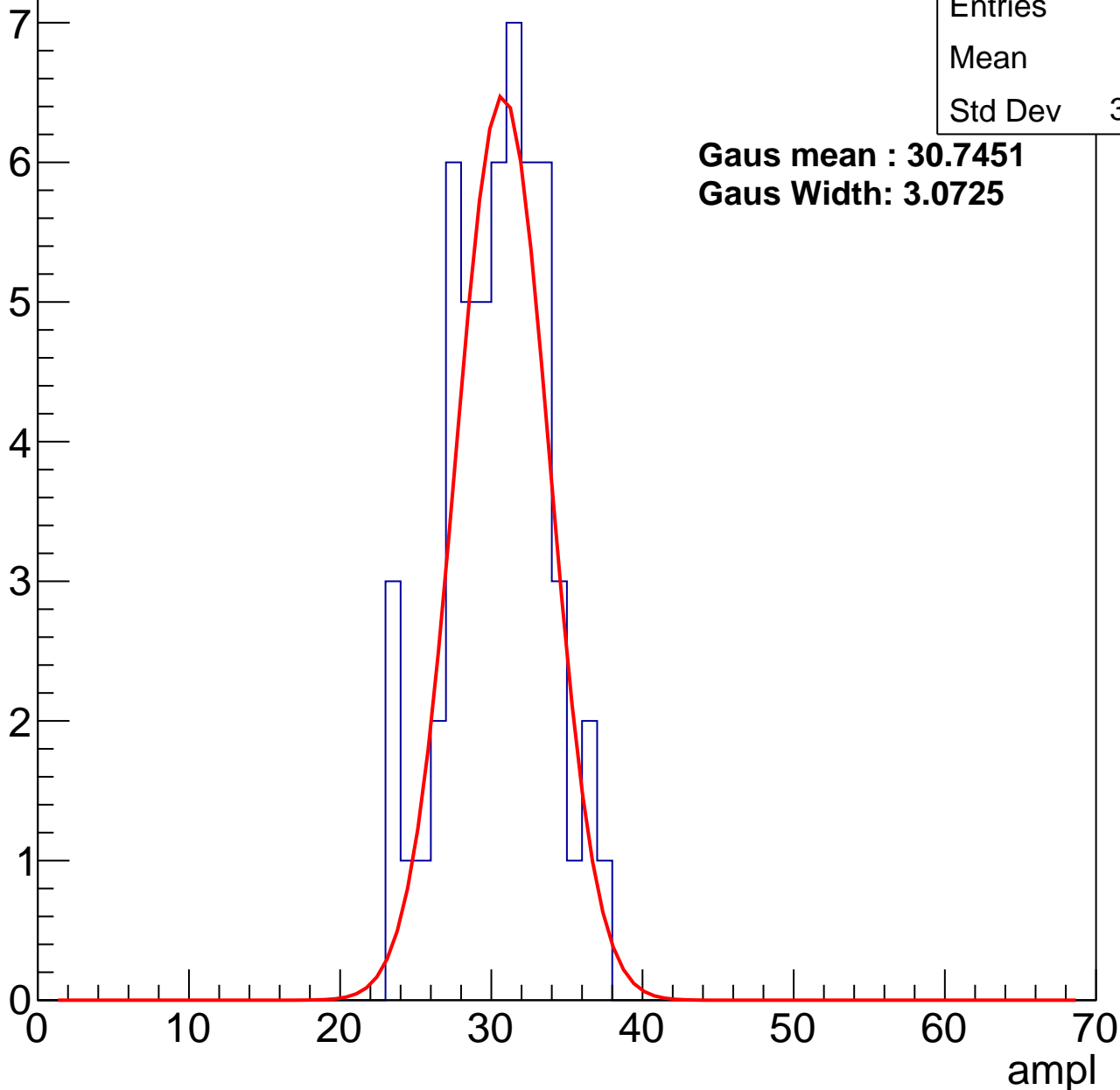
B1L102S, U18-ch7, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	30
Std Dev	3.297

Gaus mean : 30.7451
Gaus Width: 3.0725



B1L102S, U18-ch7, adc1

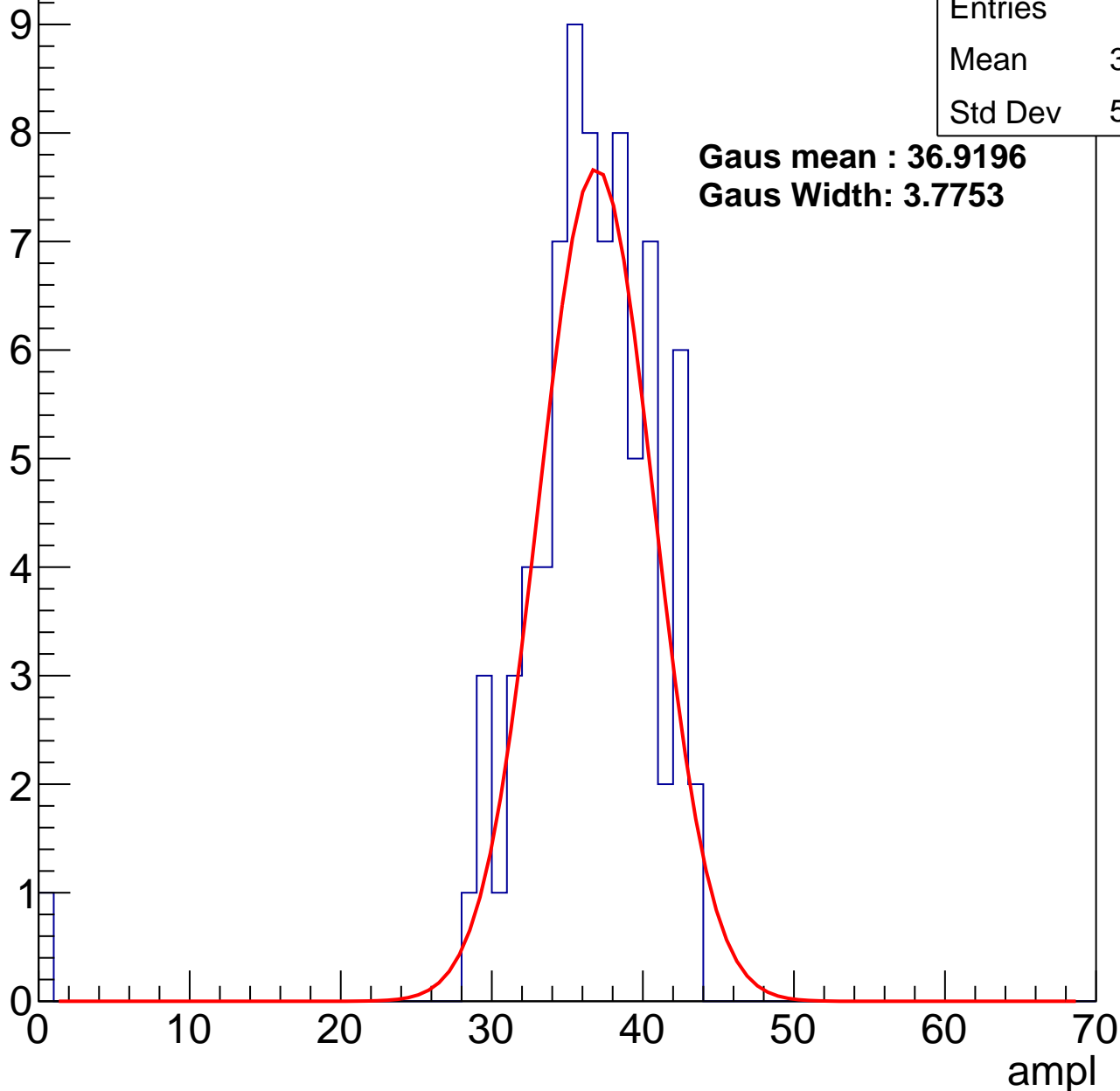
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	35.86
Std Dev	5.446

Gaus mean : 36.9196

Gaus Width: 3.7753



B1L102S, U18-ch7, adc2

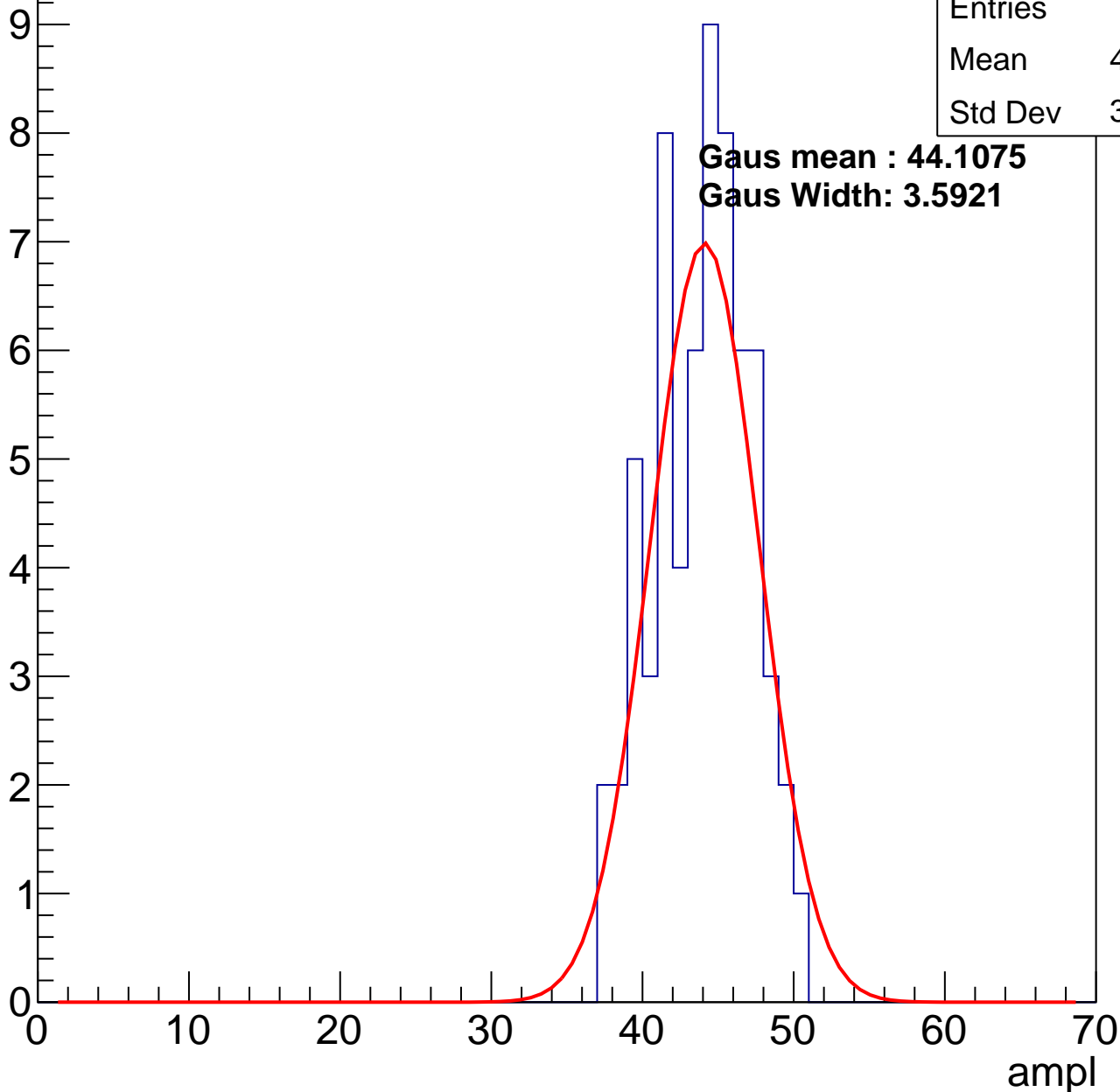
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	43.46
Std Dev	3.133

Gaus mean : 44.1075

Gaus Width: 3.5921

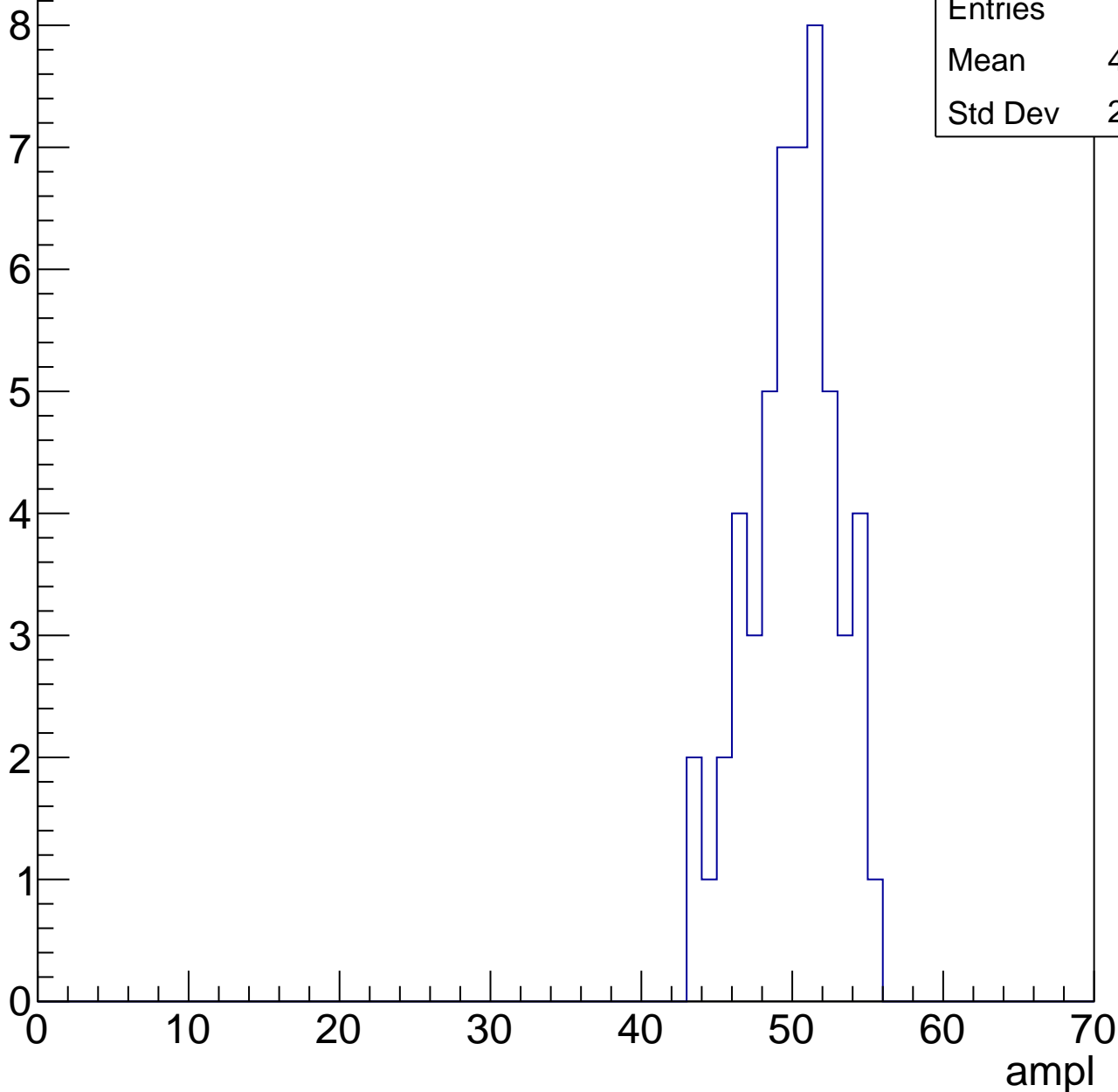


B1L102S, U18-ch7, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	49.54
Std Dev	2.892

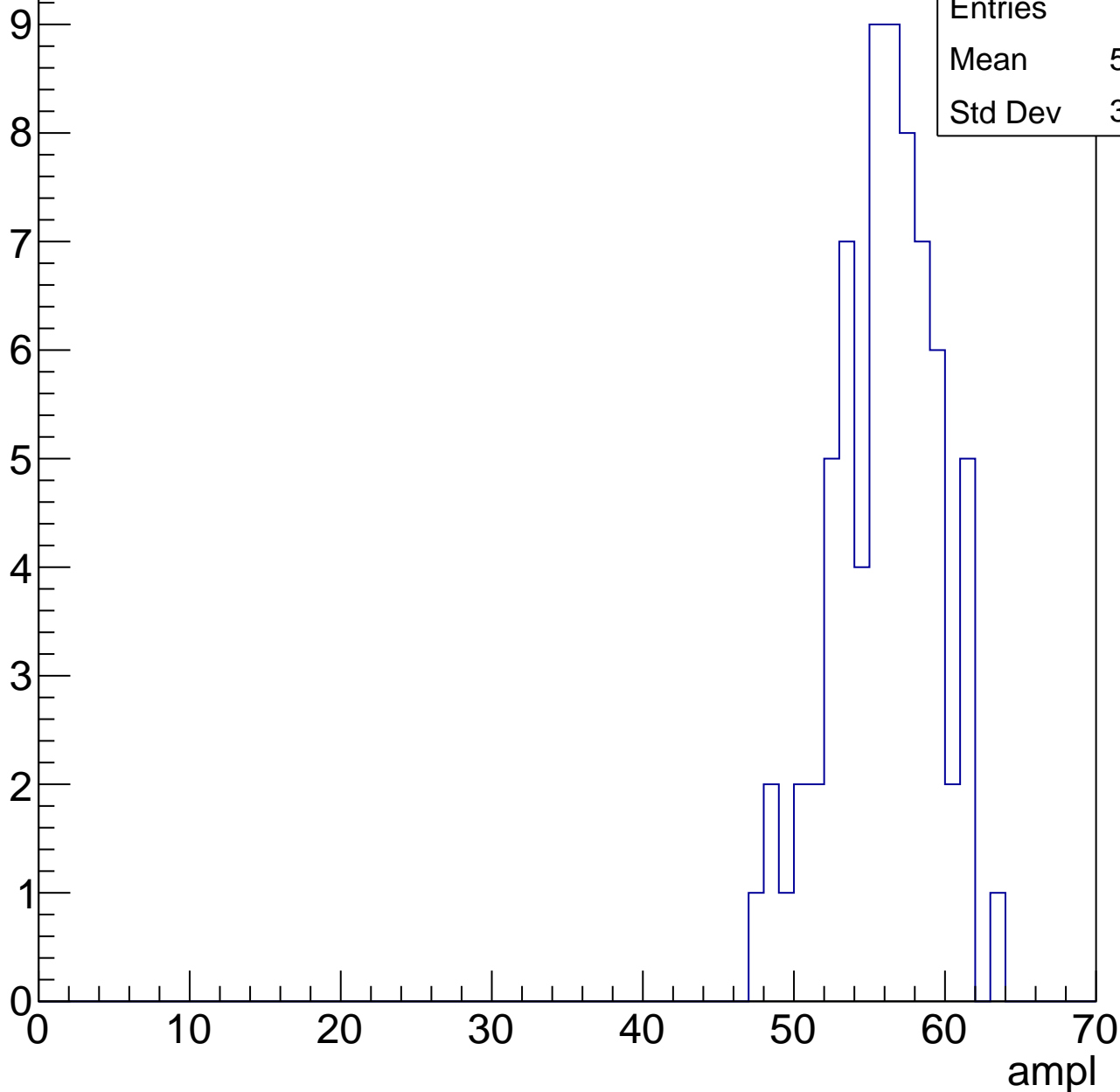


B1L102S, U18-ch7, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	55.55
Std Dev	3.418

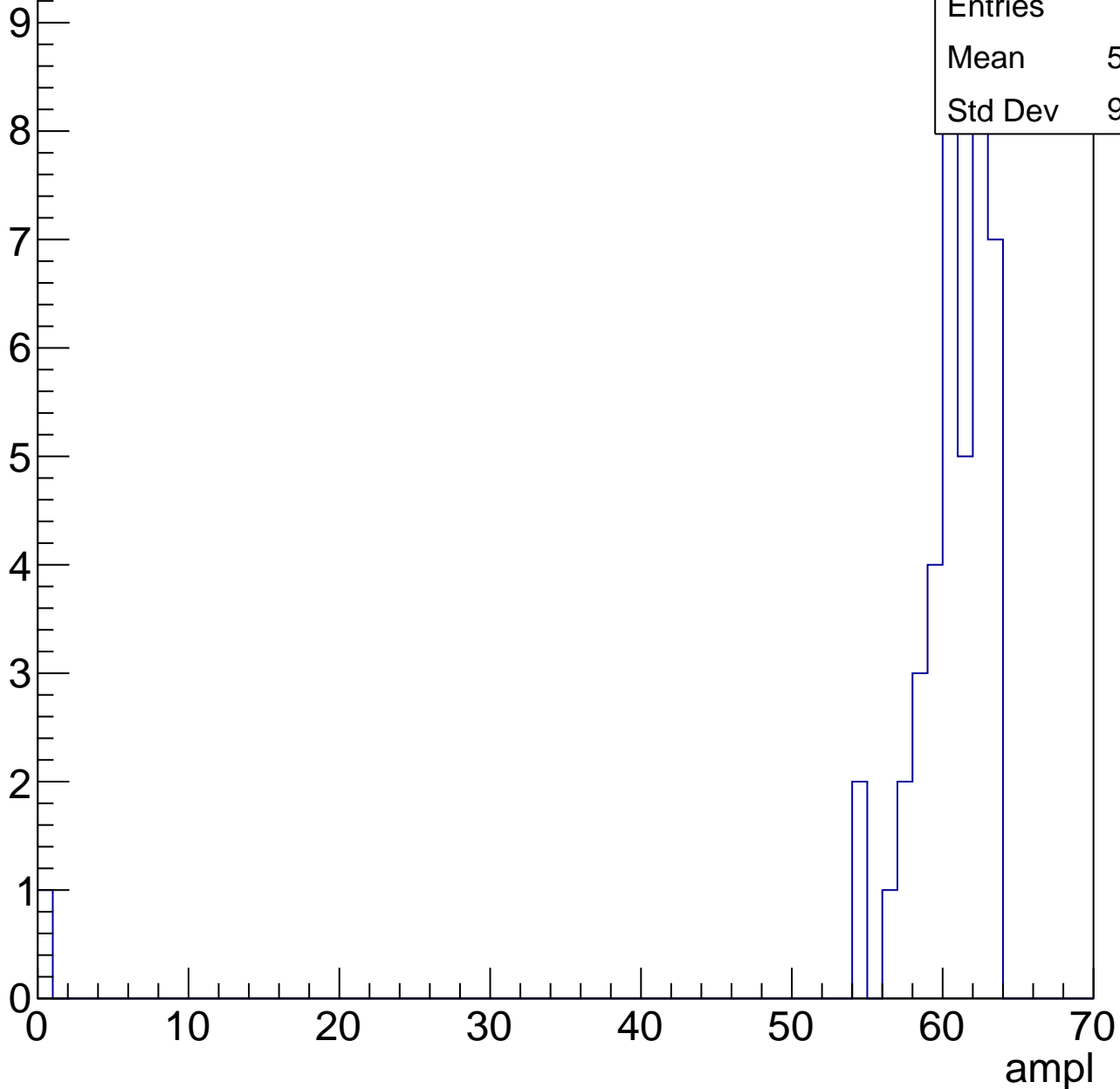


B1L102S, U18-ch7, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	58.86
Std Dev	9.476



B1L102S, U18-ch7, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U18-ch7, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch8, adc0

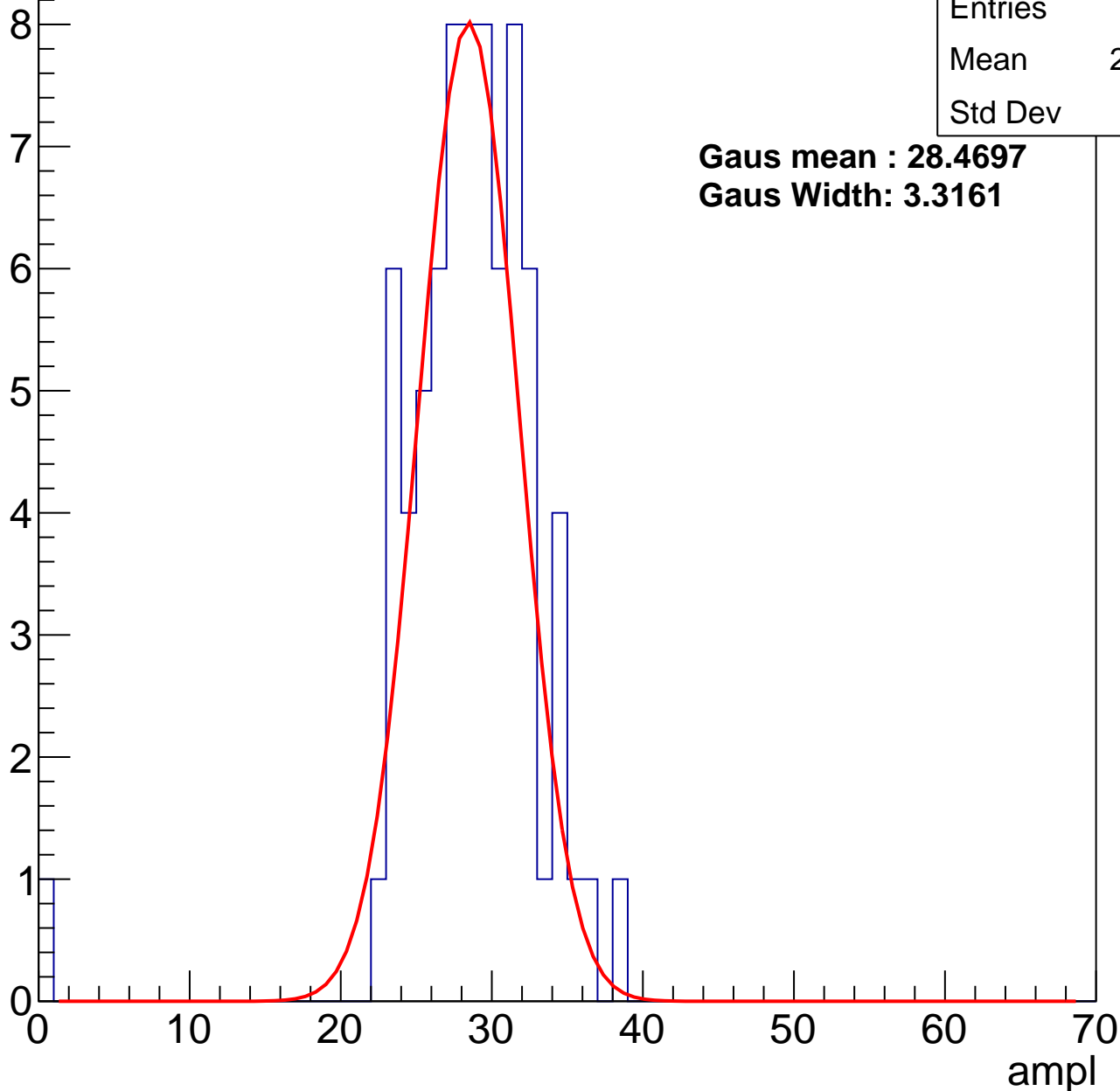
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.09
Std Dev	4.75

Gaus mean : 28.4697

Gaus Width: 3.3161



B1L102S, U18-ch8, adc1

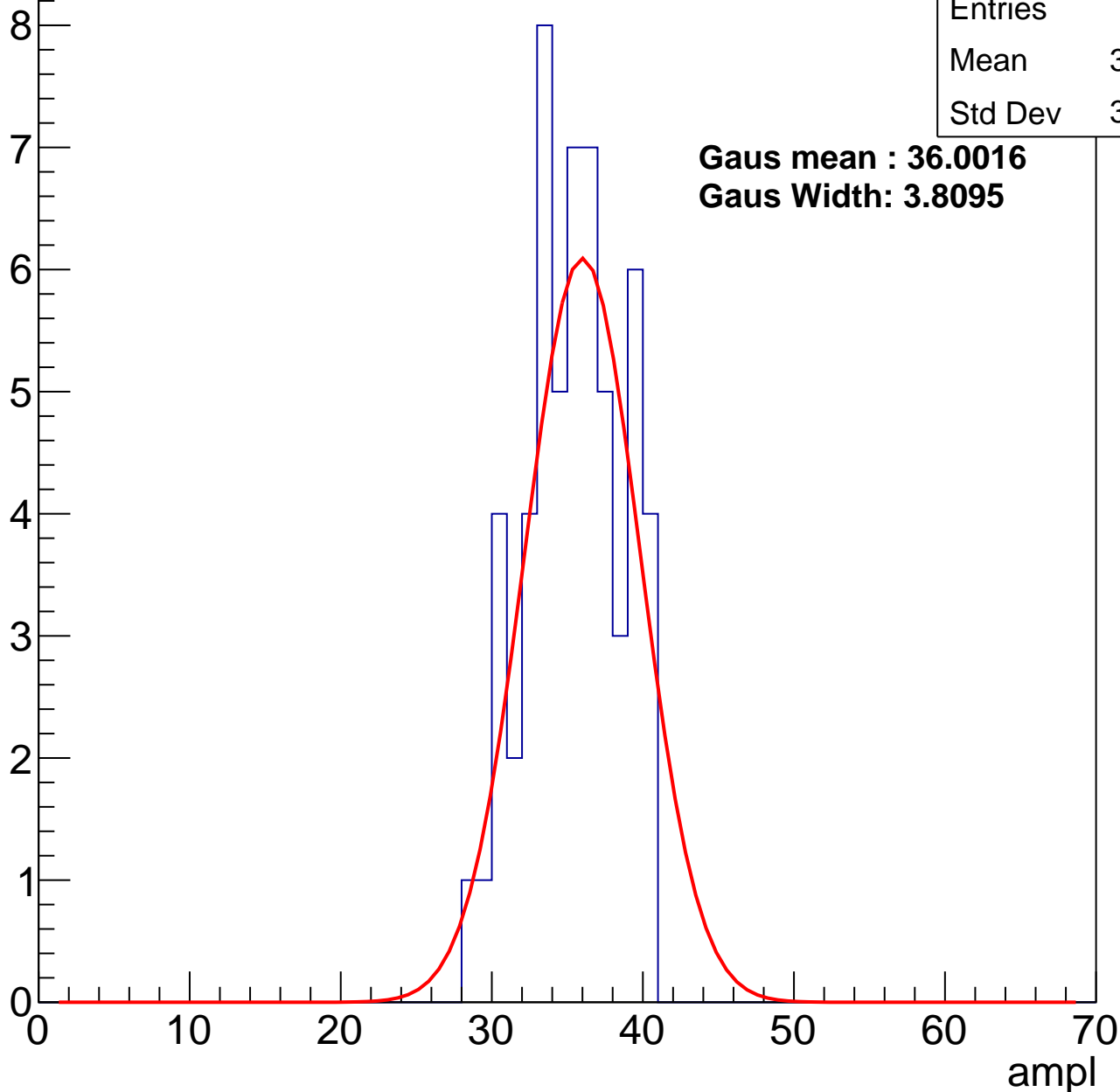
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.93
Std Dev	3.077

Gaus mean : 36.0016

Gaus Width: 3.8095



B1L102S, U18-ch8, adc2

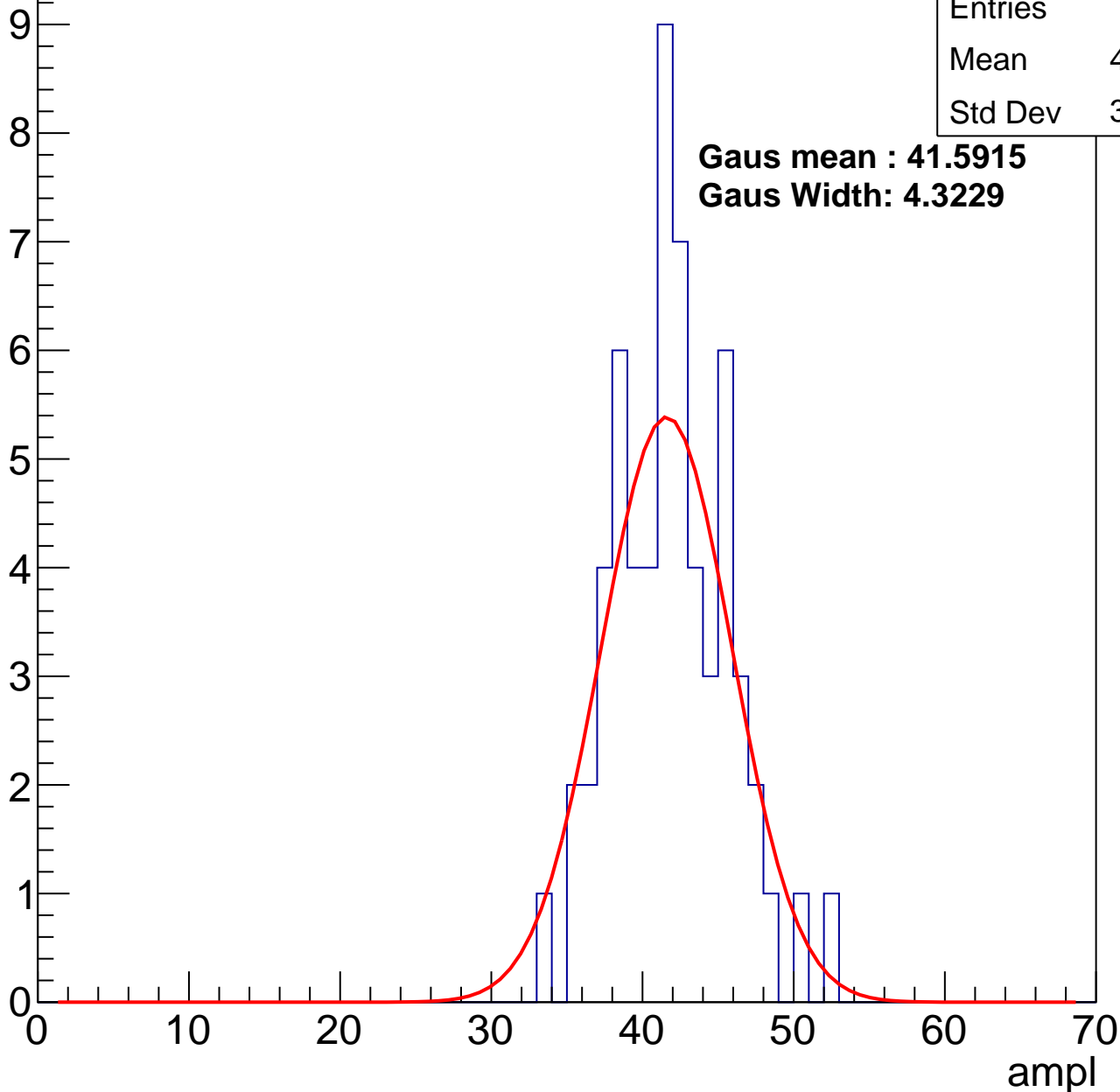
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.43
Std Dev	3.779

Gaus mean : 41.5915

Gaus Width: 4.3229

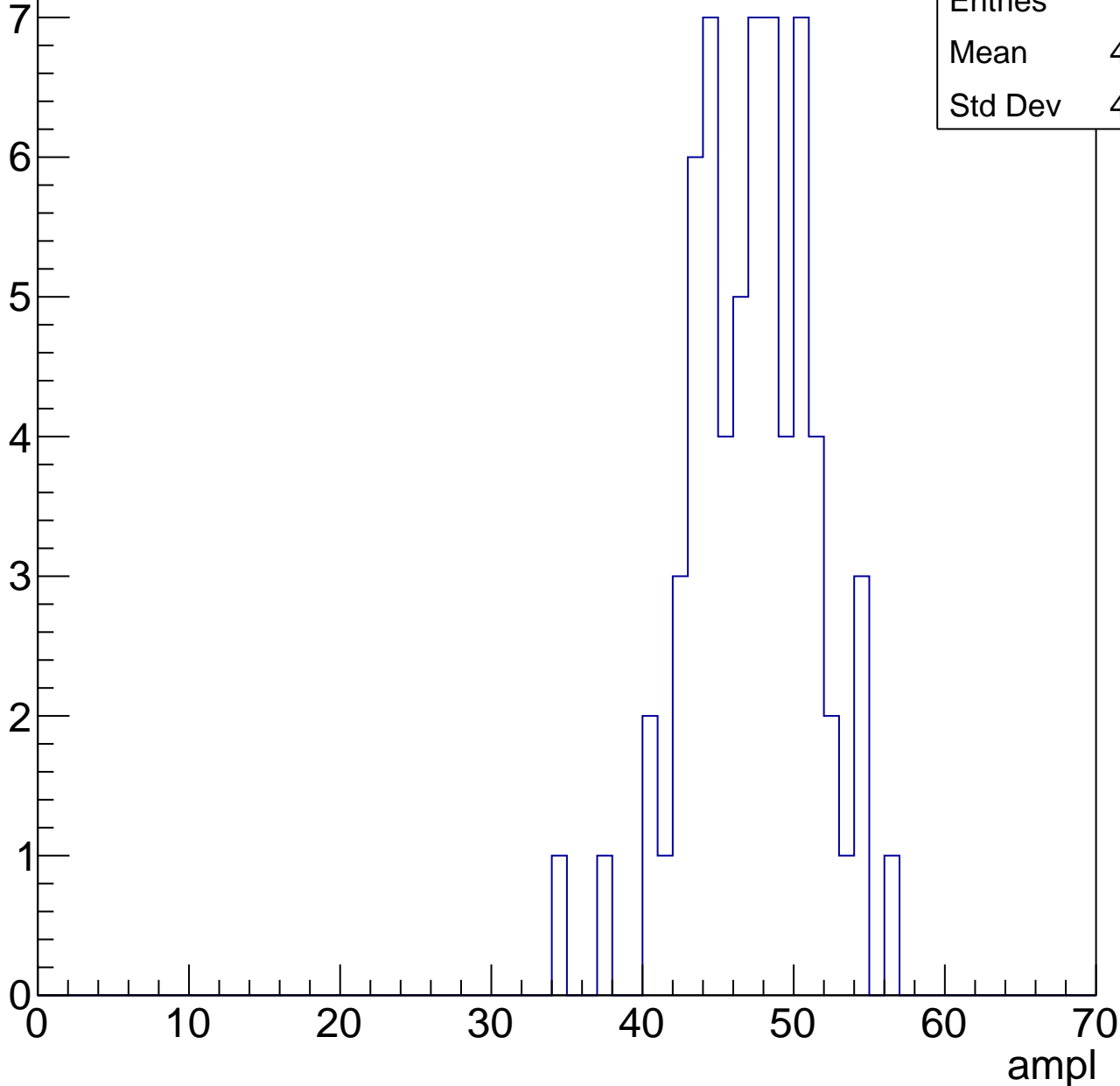


B1L102S, U18-ch8, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	46.73
Std Dev	4.114

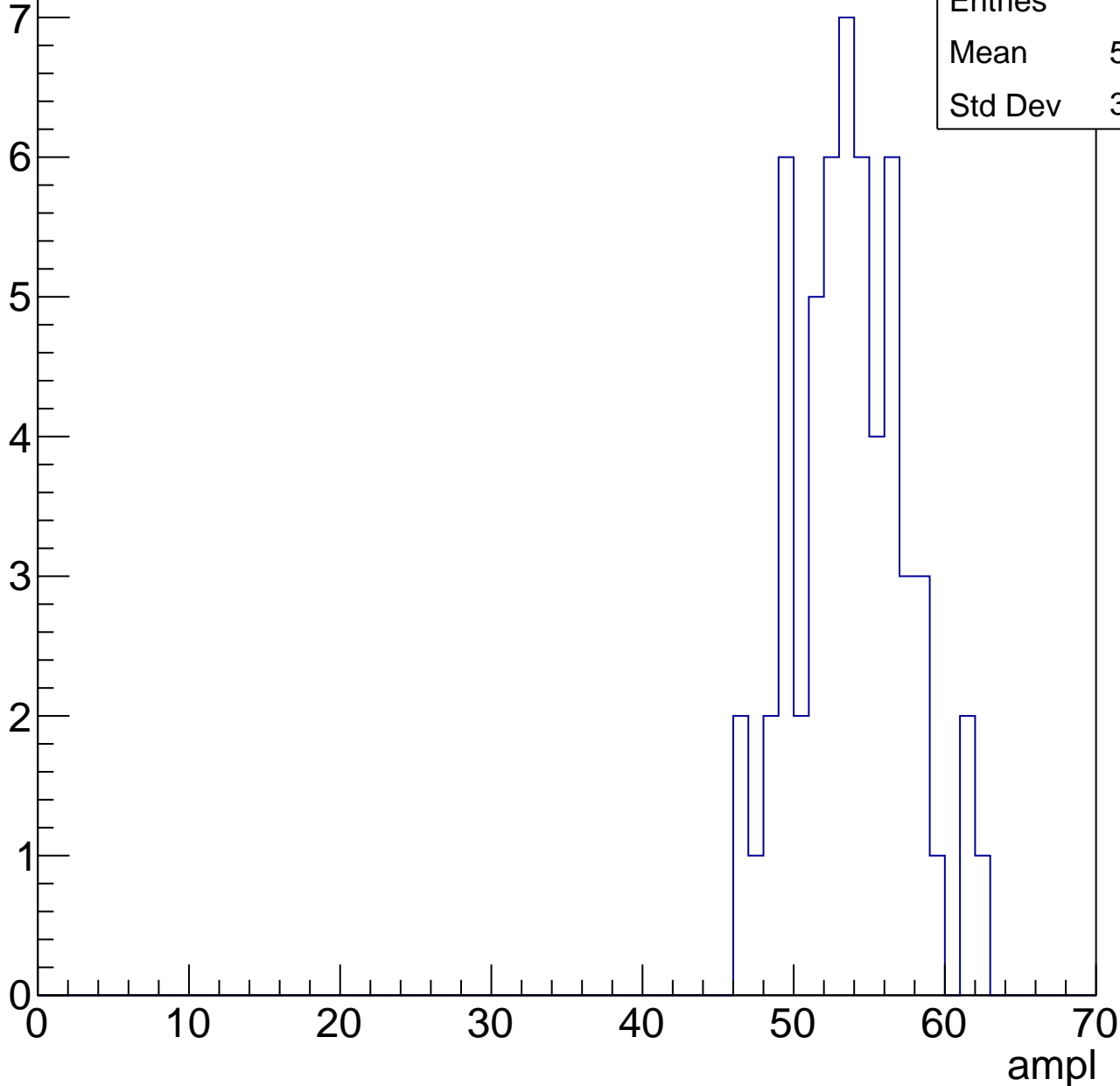


B1L102S, U18-ch8, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

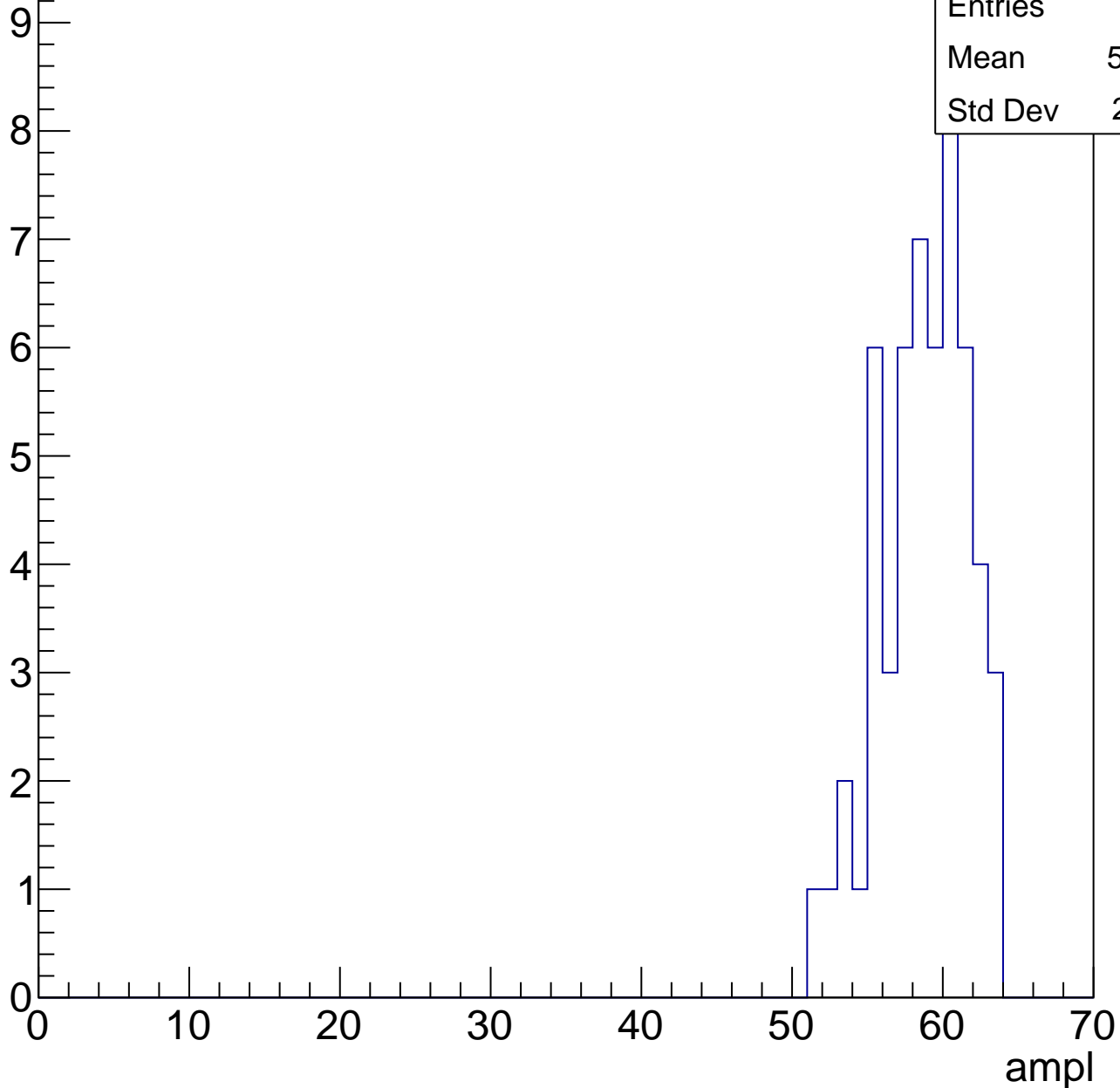
Entries	57
Mean	53.25
Std Dev	3.662



B1L102S, U18-ch8, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



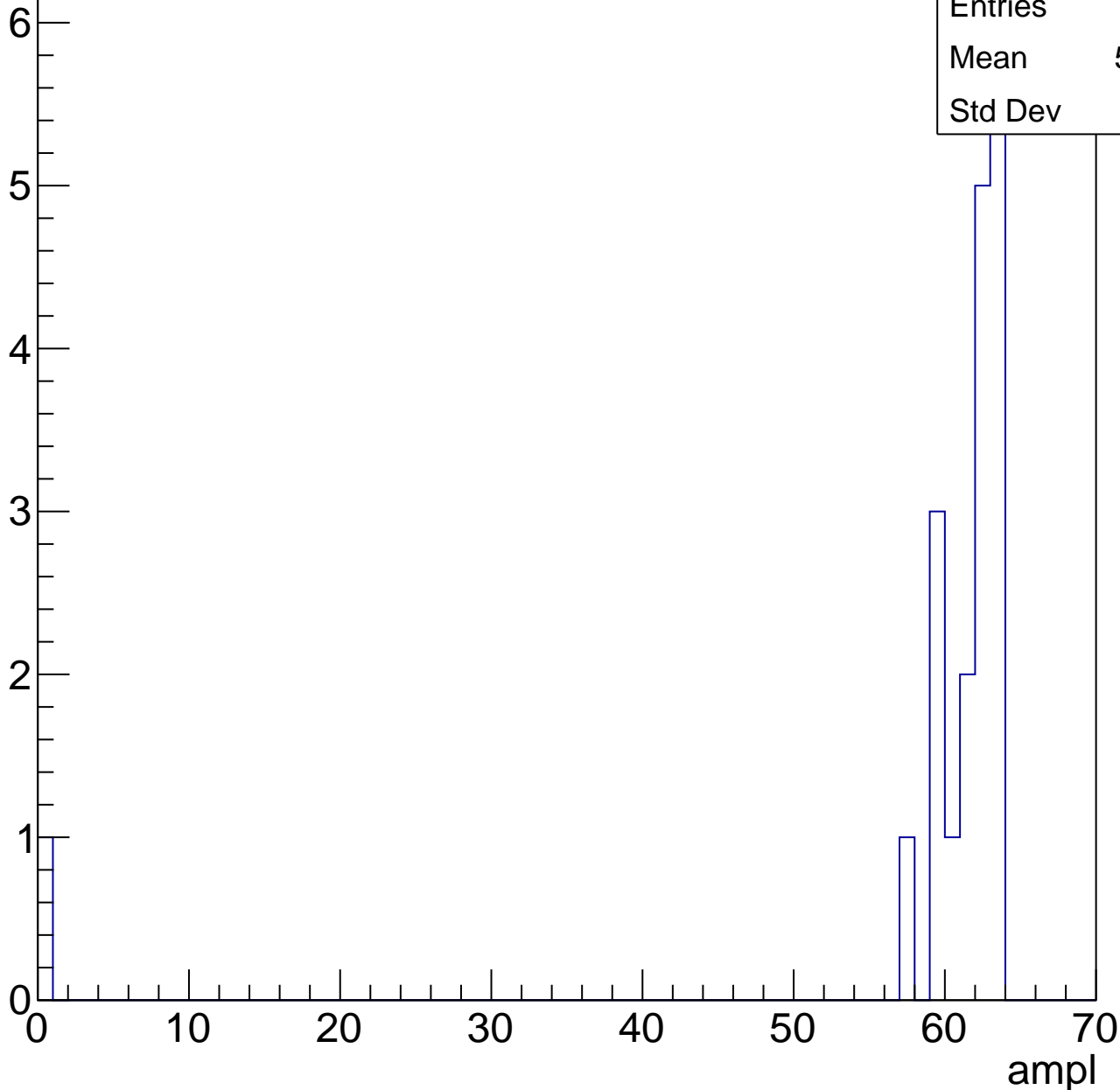
Entries	55
Mean	58.29
Std Dev	2.871

B1L102S, U18-ch8, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	19
Mean	58.11
Std Dev	13.8



B1L102S, U18-ch8, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl

B1L102S, U18-ch9, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	28.93
Std Dev	4.93

Gaus mean : 29.9479

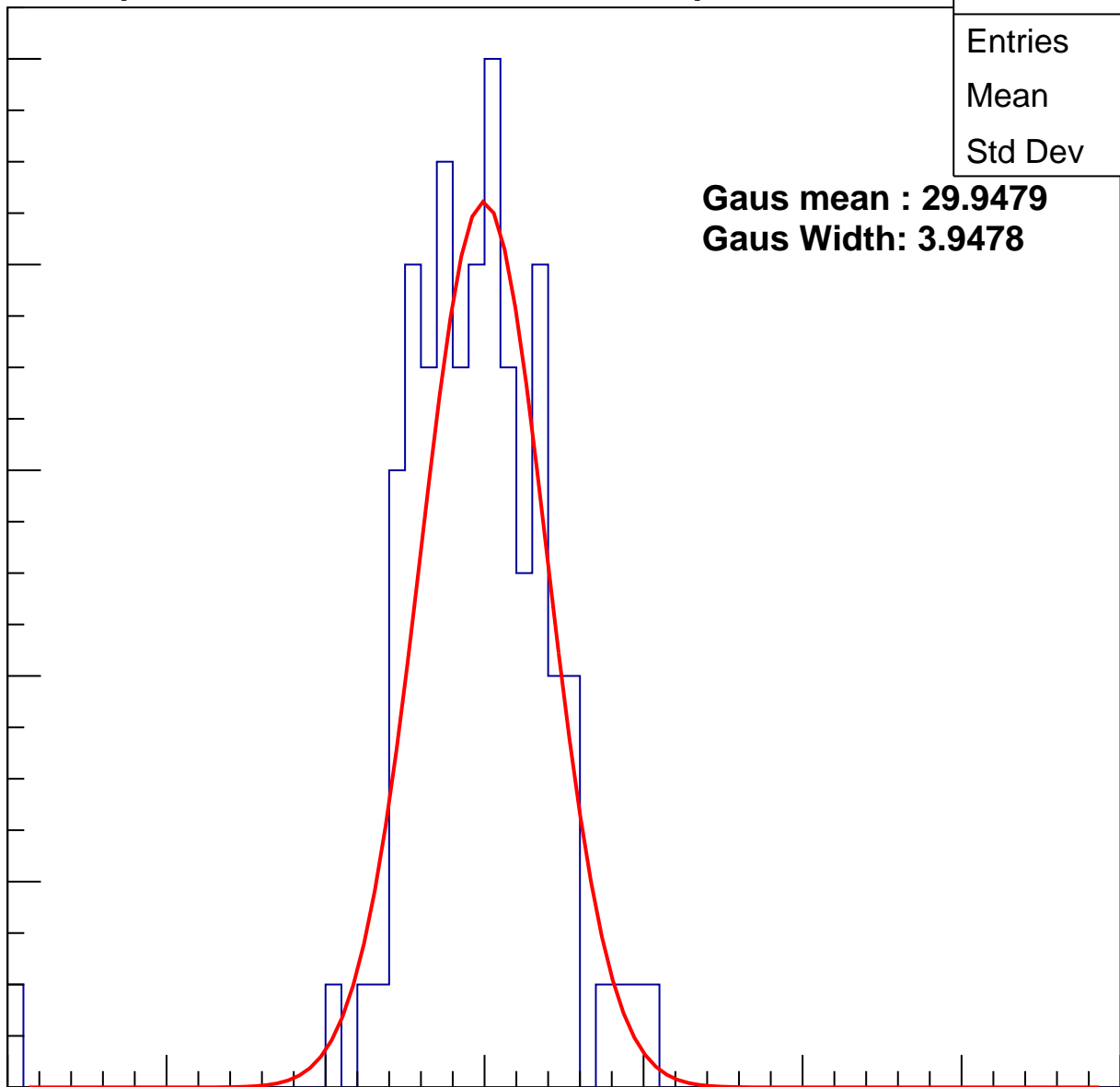
Gaus Width: 3.9478

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch9, adc1

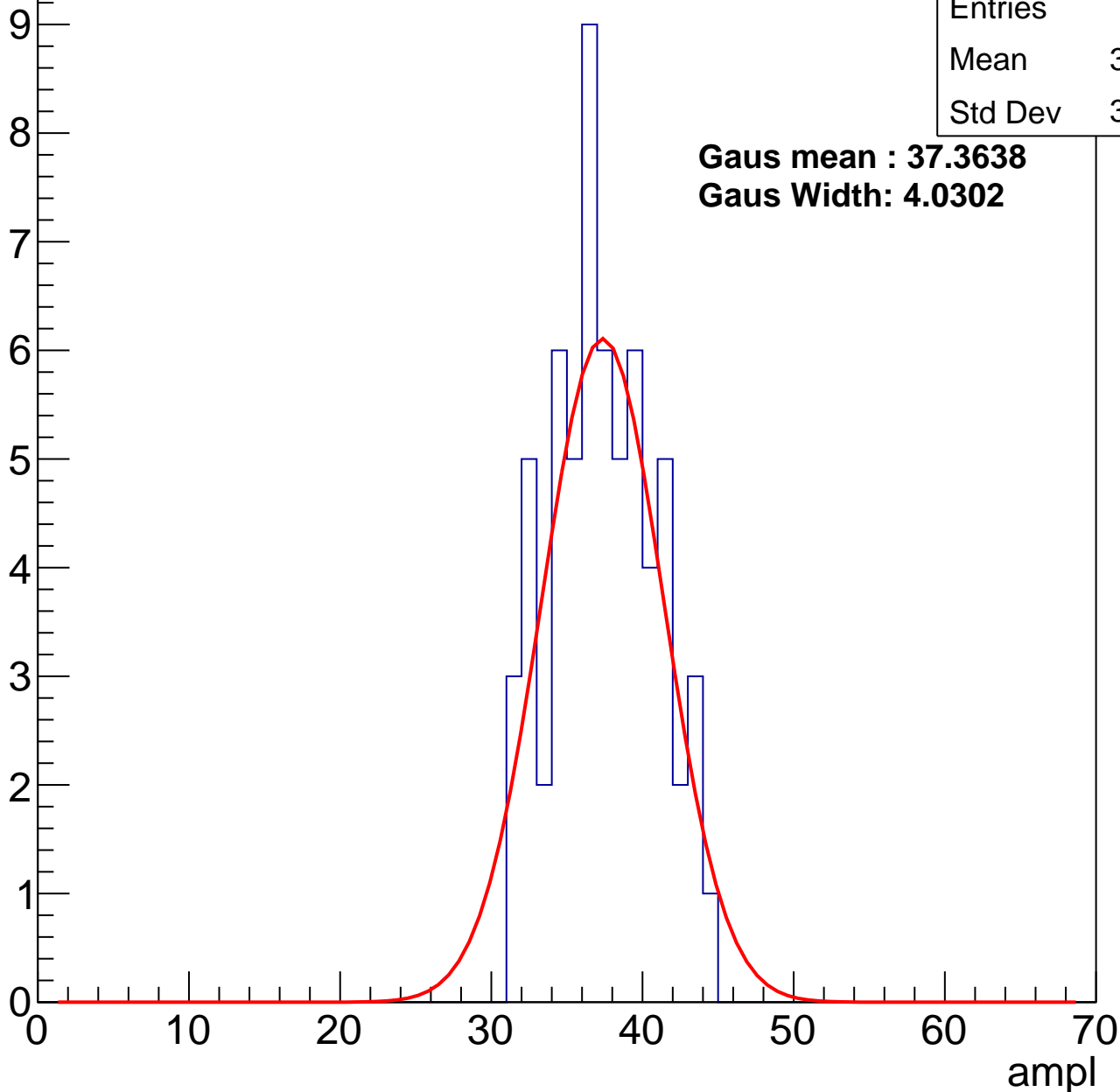
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	36.94
Std Dev	3.359

Gaus mean : 37.3638

Gaus Width: 4.0302



B1L102S, U18-ch9, adc2

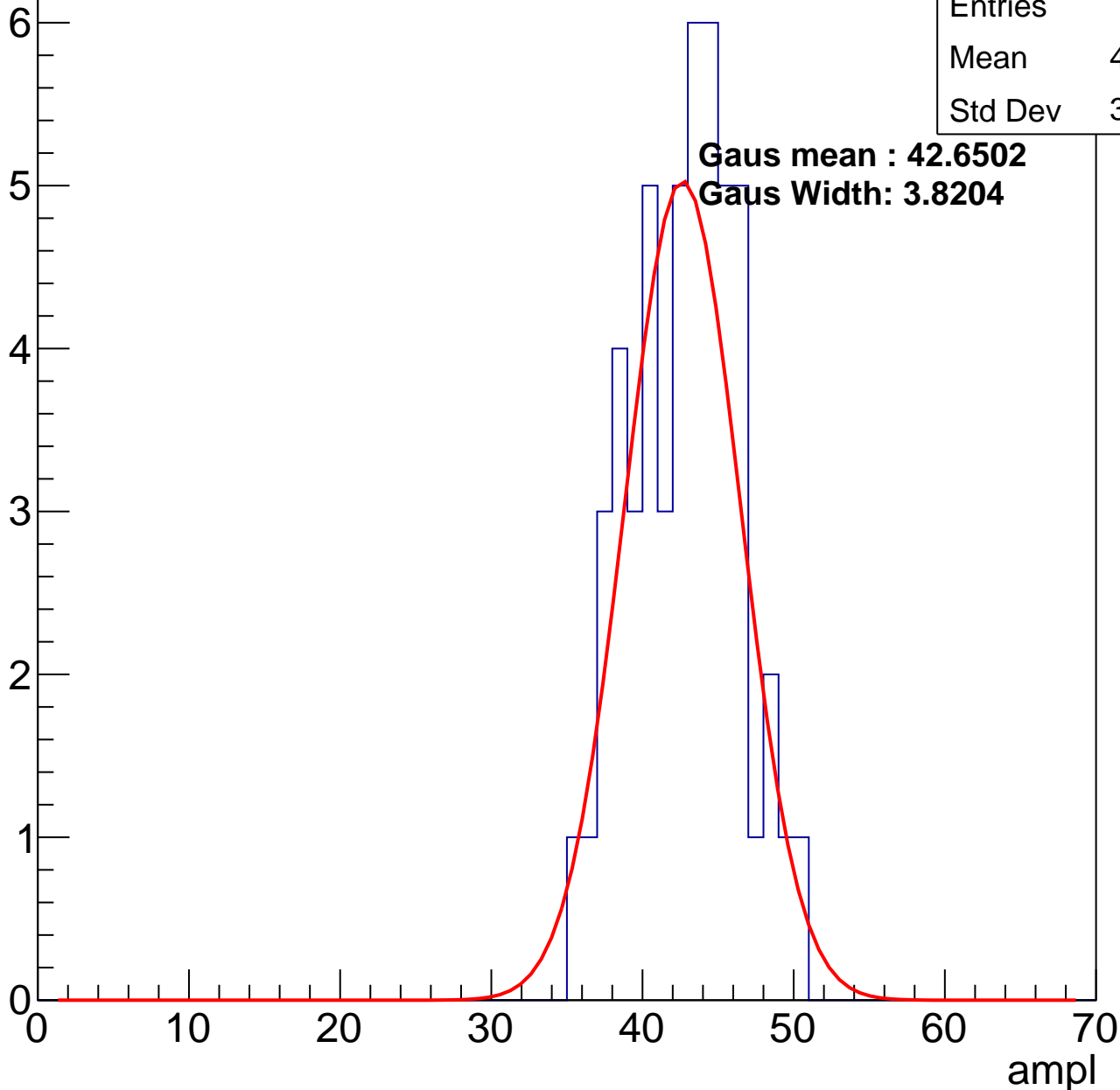
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	42.37
Std Dev	3.486

Gaus mean : 42.6502

Gaus Width: 3.8204

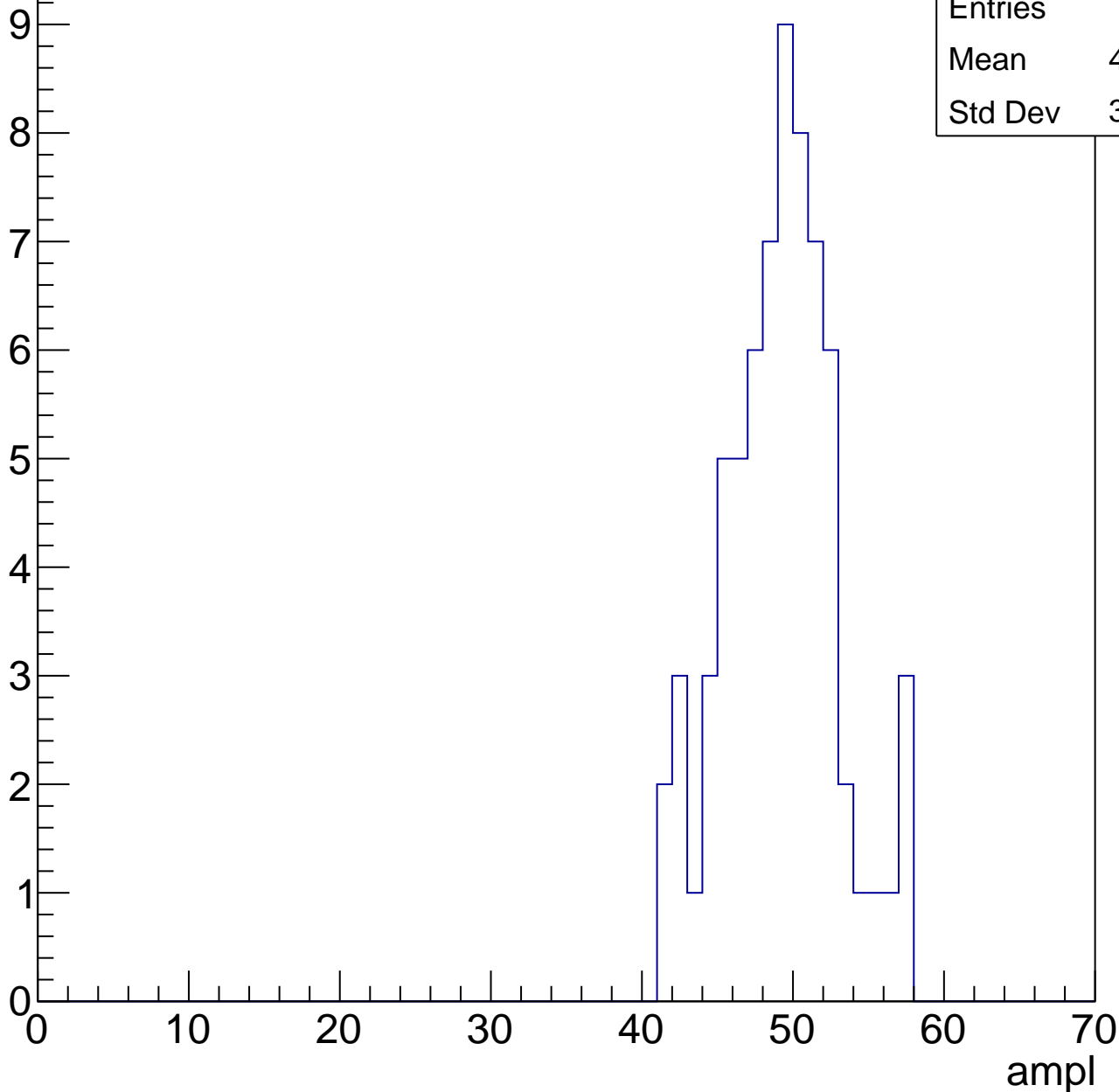


B1L102S, U18-ch9, adc3

calib_packv5_042523_0143.root, FC#11, port A2

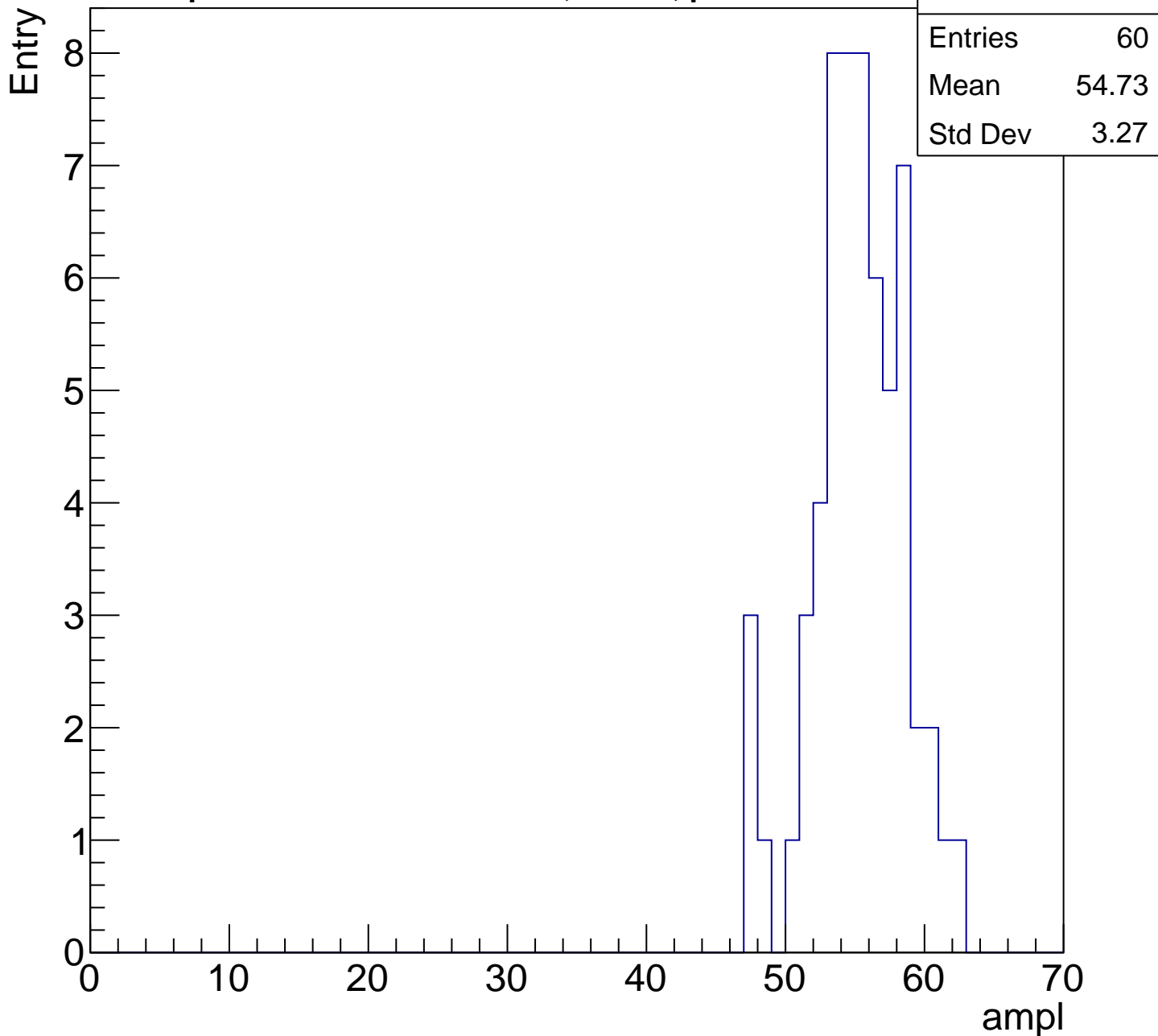
Entry

Entries	70
Mean	48.69
Std Dev	3.702



B1L102S, U18-ch9, adc4

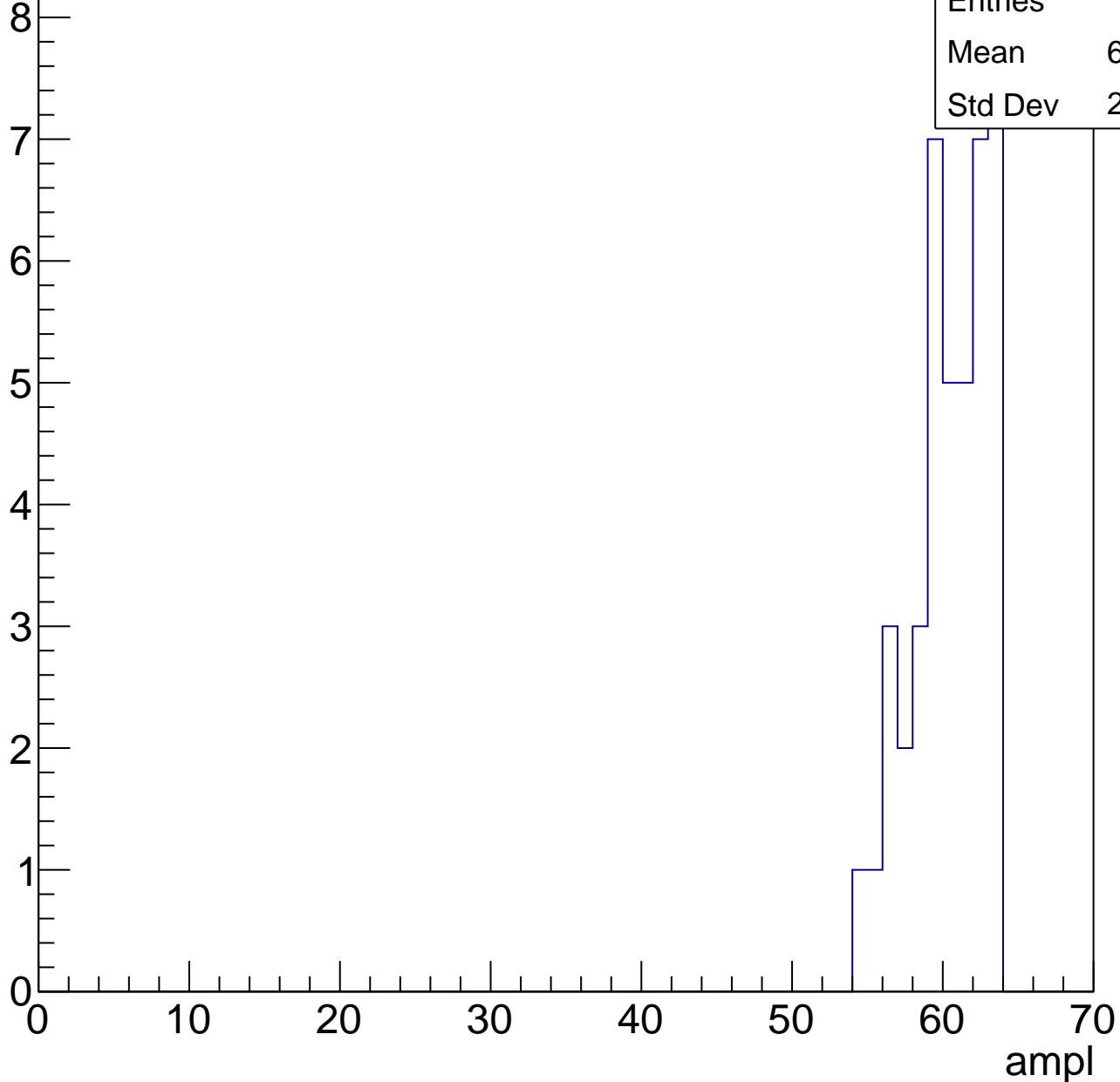
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U18-ch9, adc5

calib_packv5_042523_0143.root, FC#11, port A2

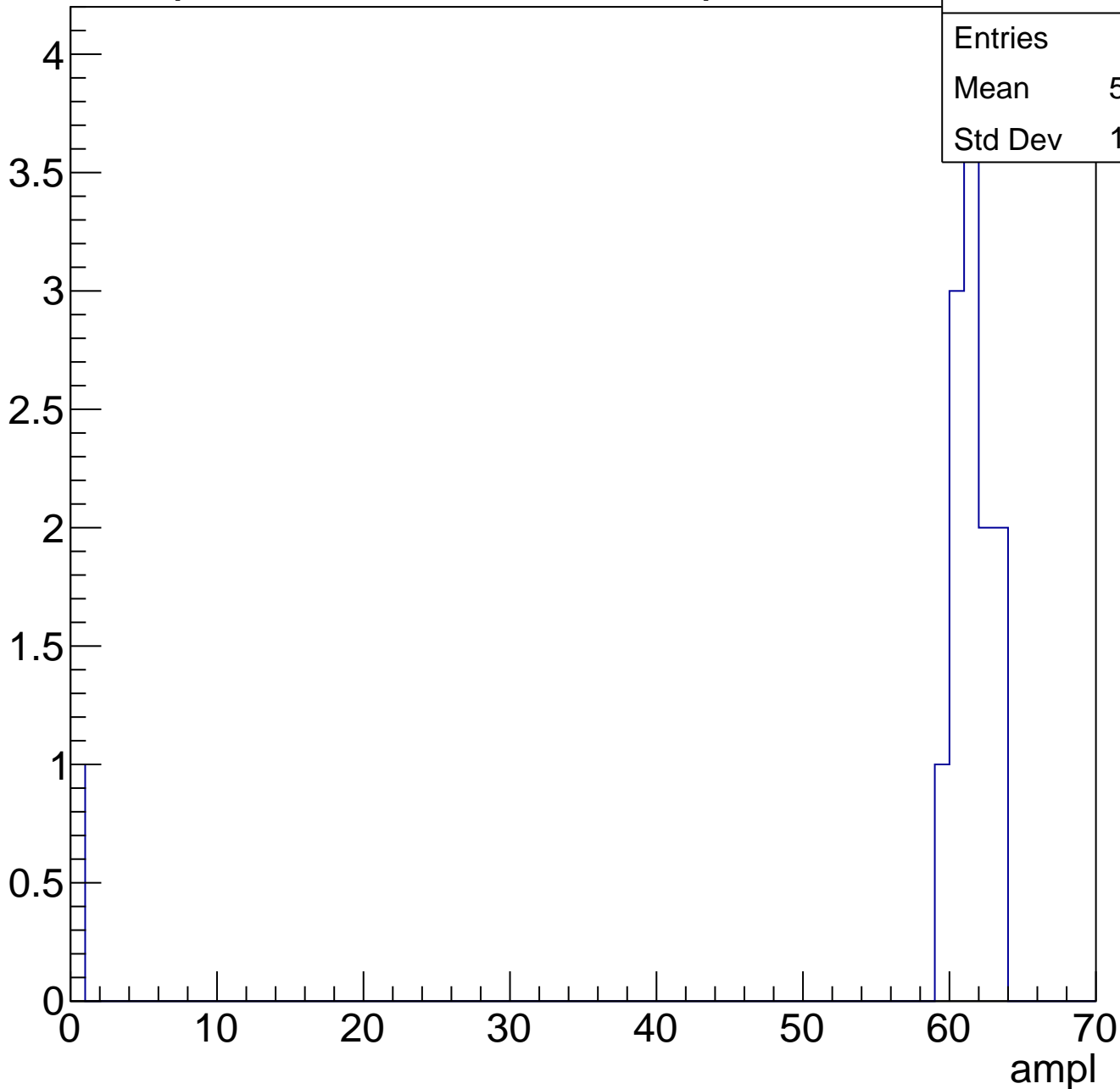
Entry



B1L102S, U18-ch9, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch9, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch10, adc0

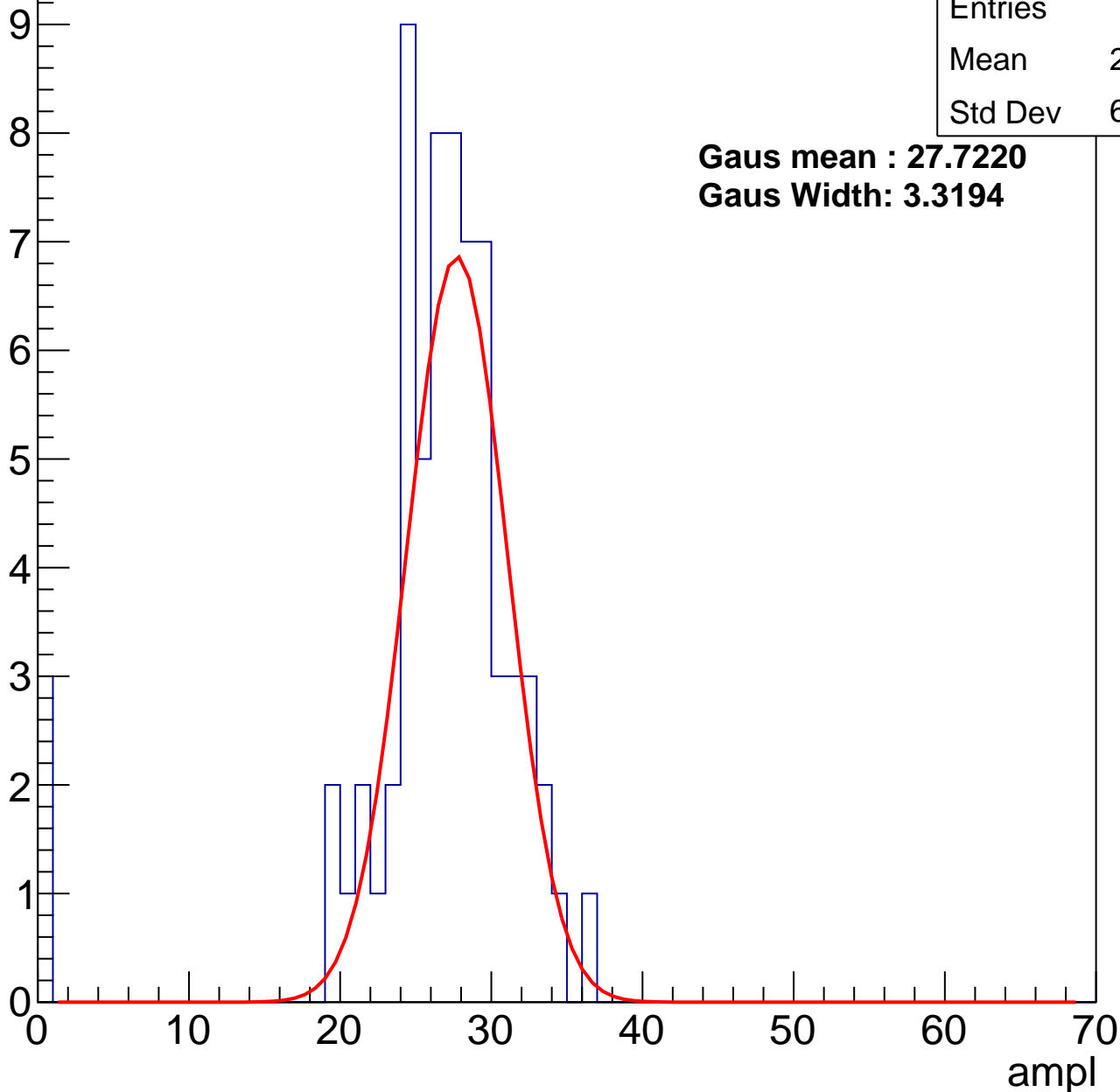
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	25.69
Std Dev	6.499

Gaus mean : 27.7220

Gaus Width: 3.3194



B1L102S, U18-ch10, adc1

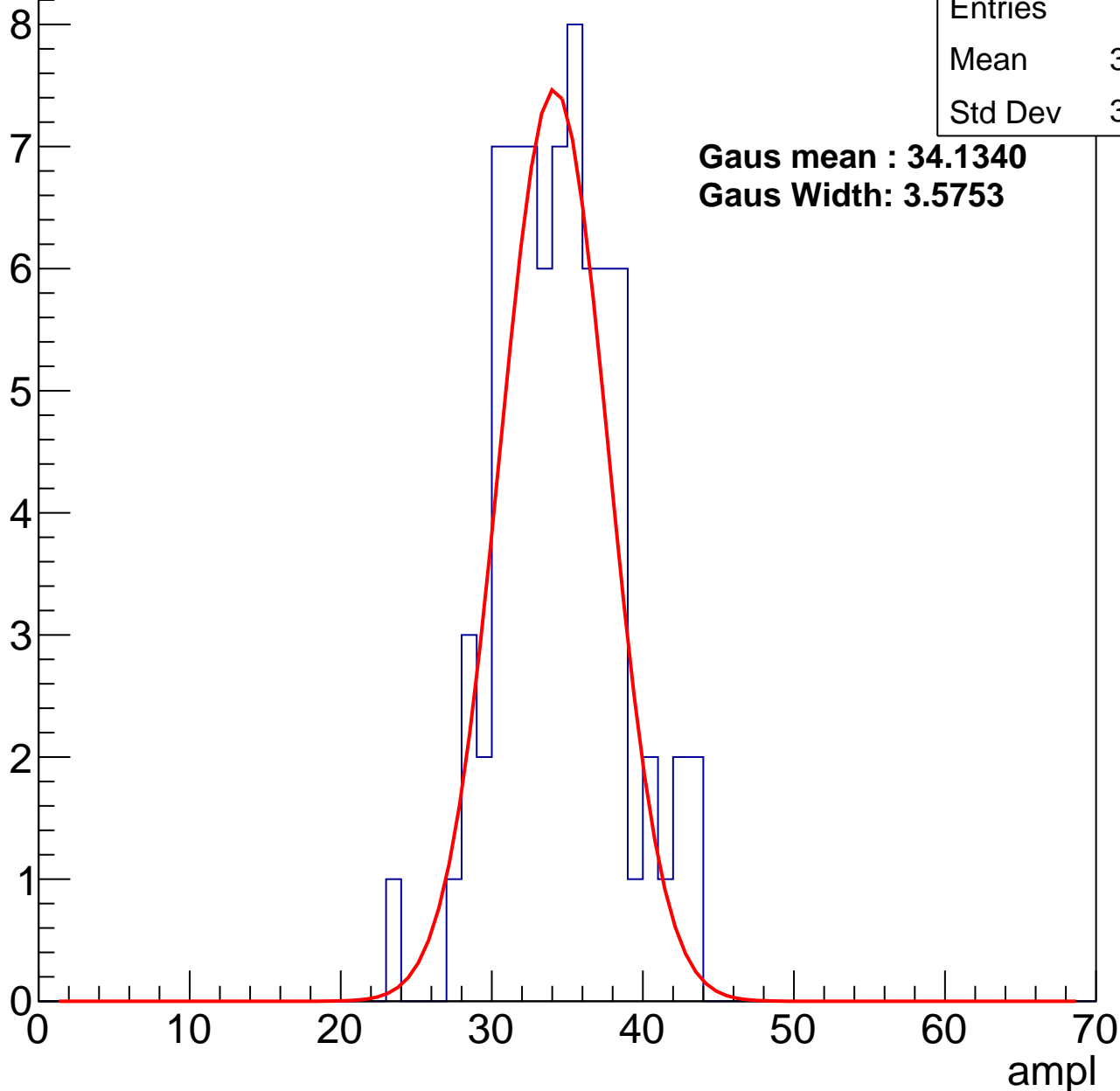
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.07
Std Dev	3.924

Gaus mean : 34.1340

Gaus Width: 3.5753



B1L102S, U18-ch10, adc2

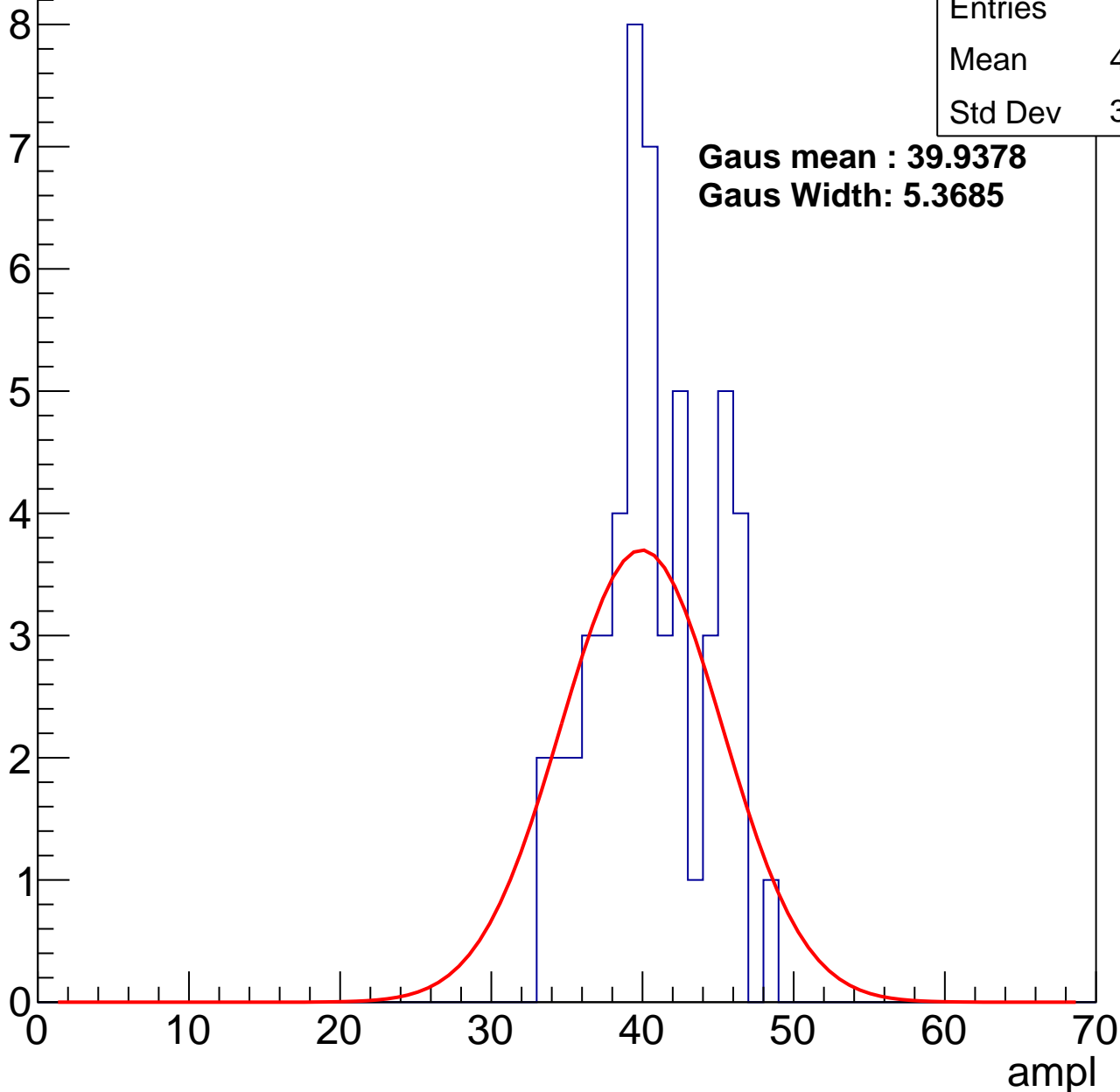
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	40.23
Std Dev	3.709

Gaus mean : 39.9378

Gaus Width: 5.3685

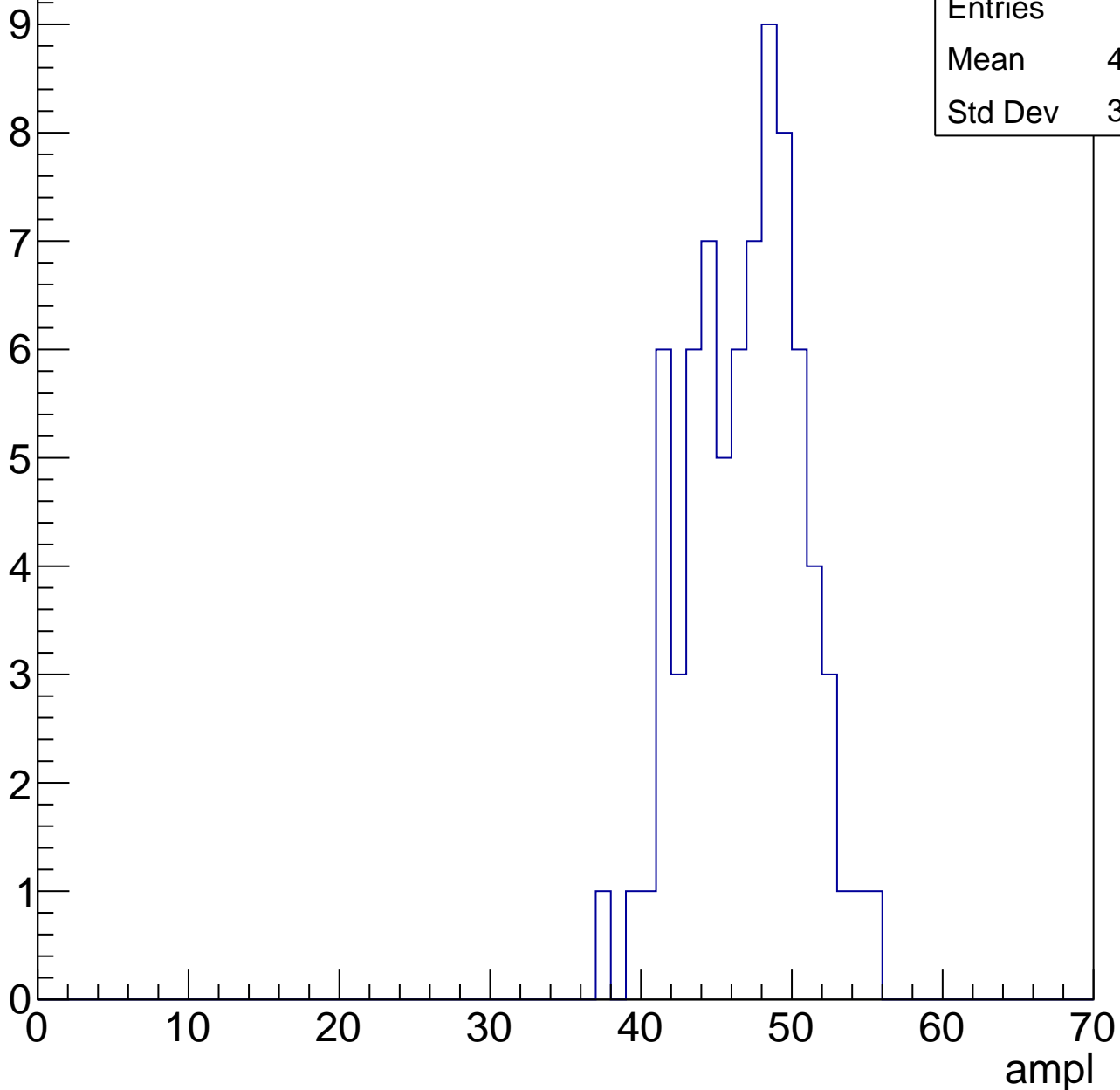


B1L102S, U18-ch10, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	46.45
Std Dev	3.715

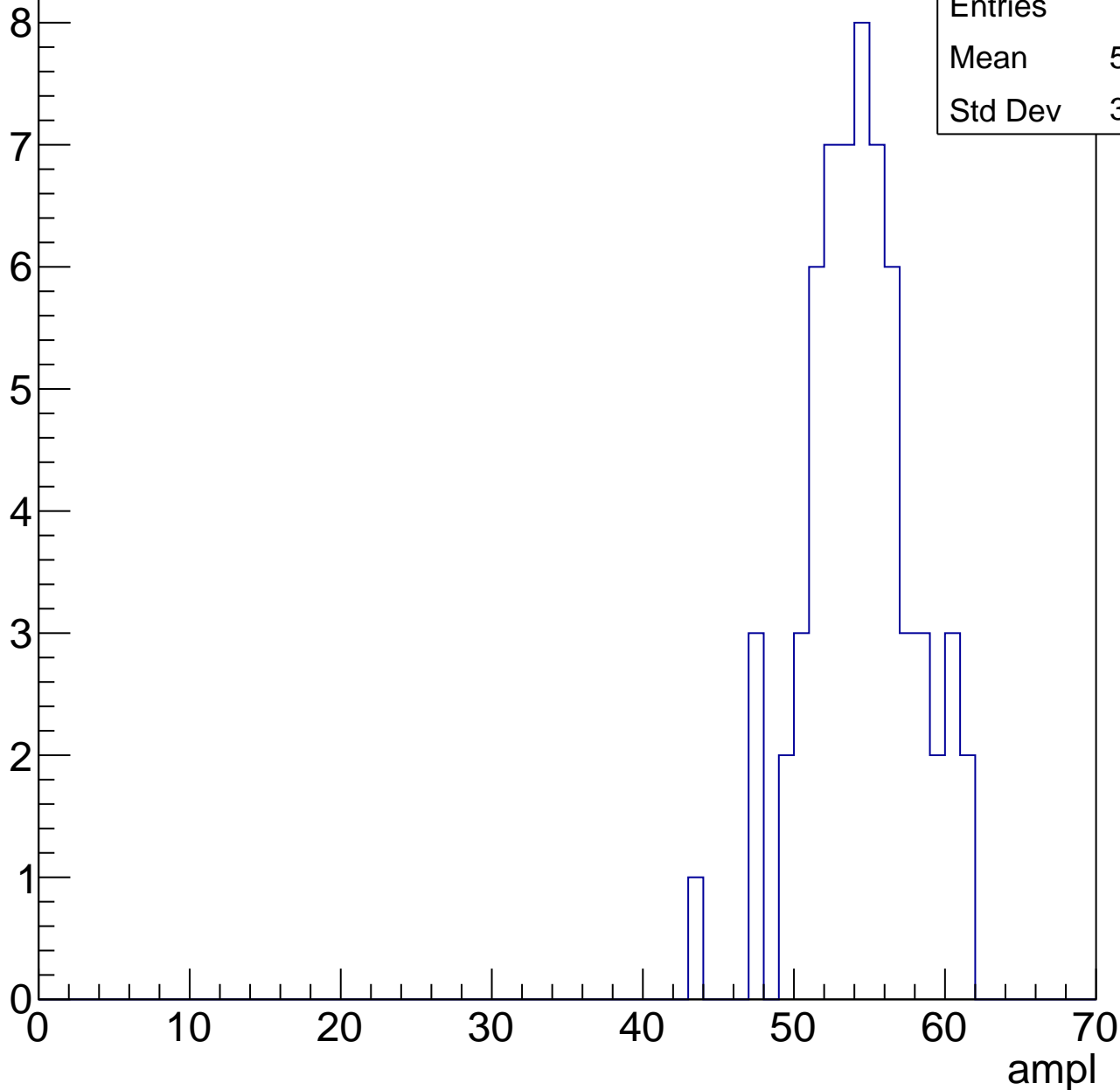


B1L102S, U18-ch10, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.83
Std Dev	3.597

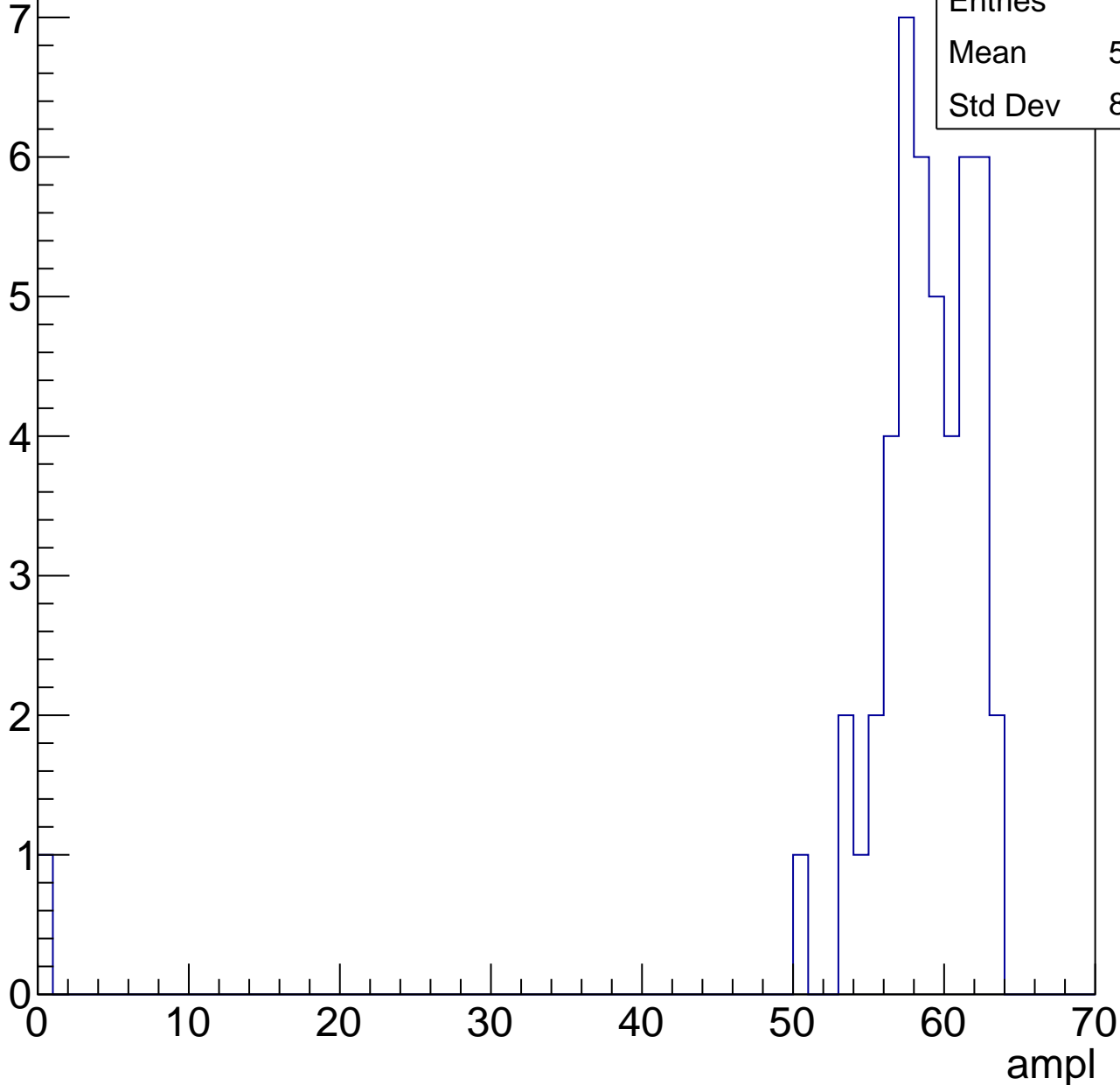


B1L102S, U18-ch10, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	57.23
Std Dev	8.907

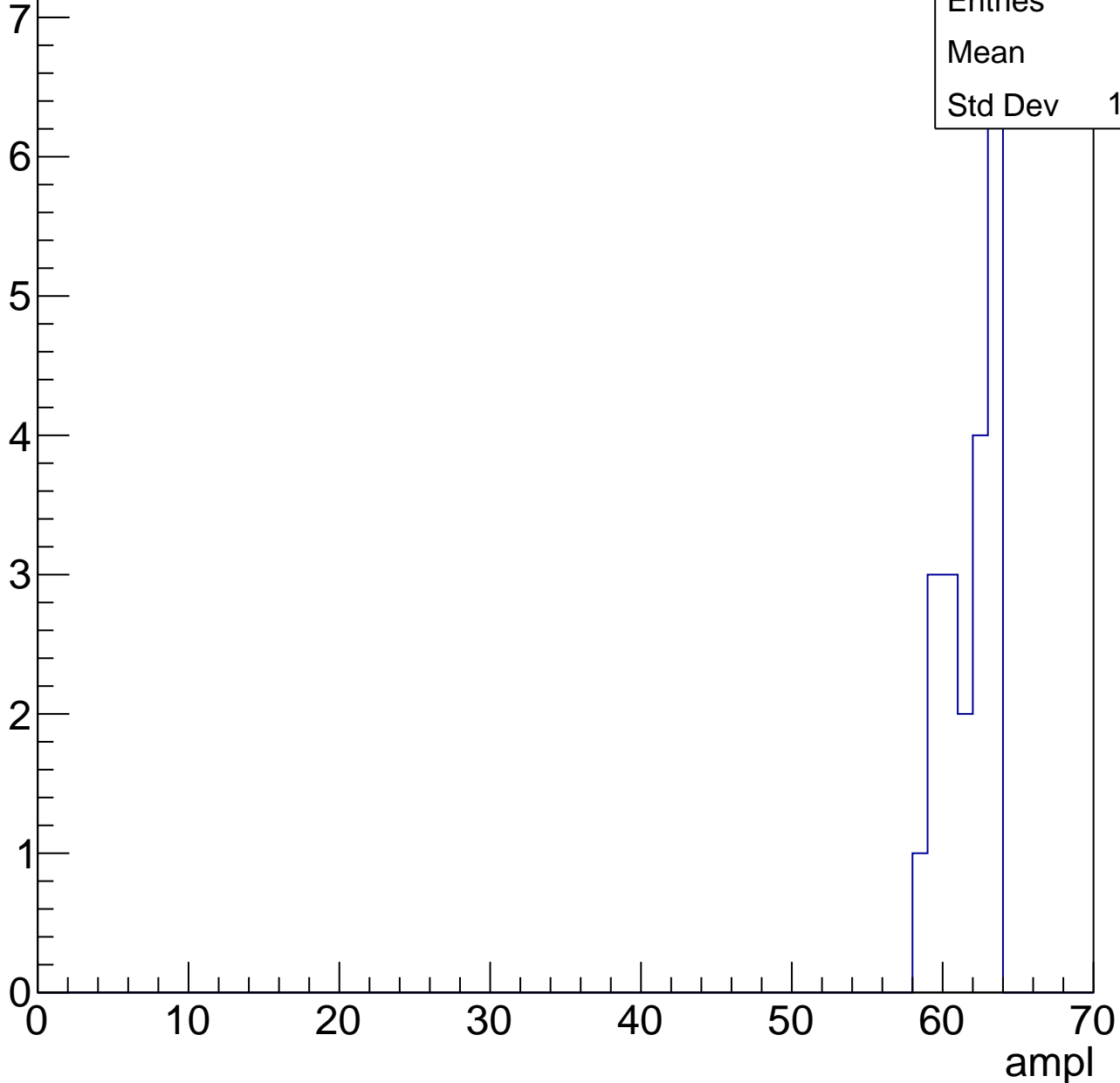


B1L102S, U18-ch10, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

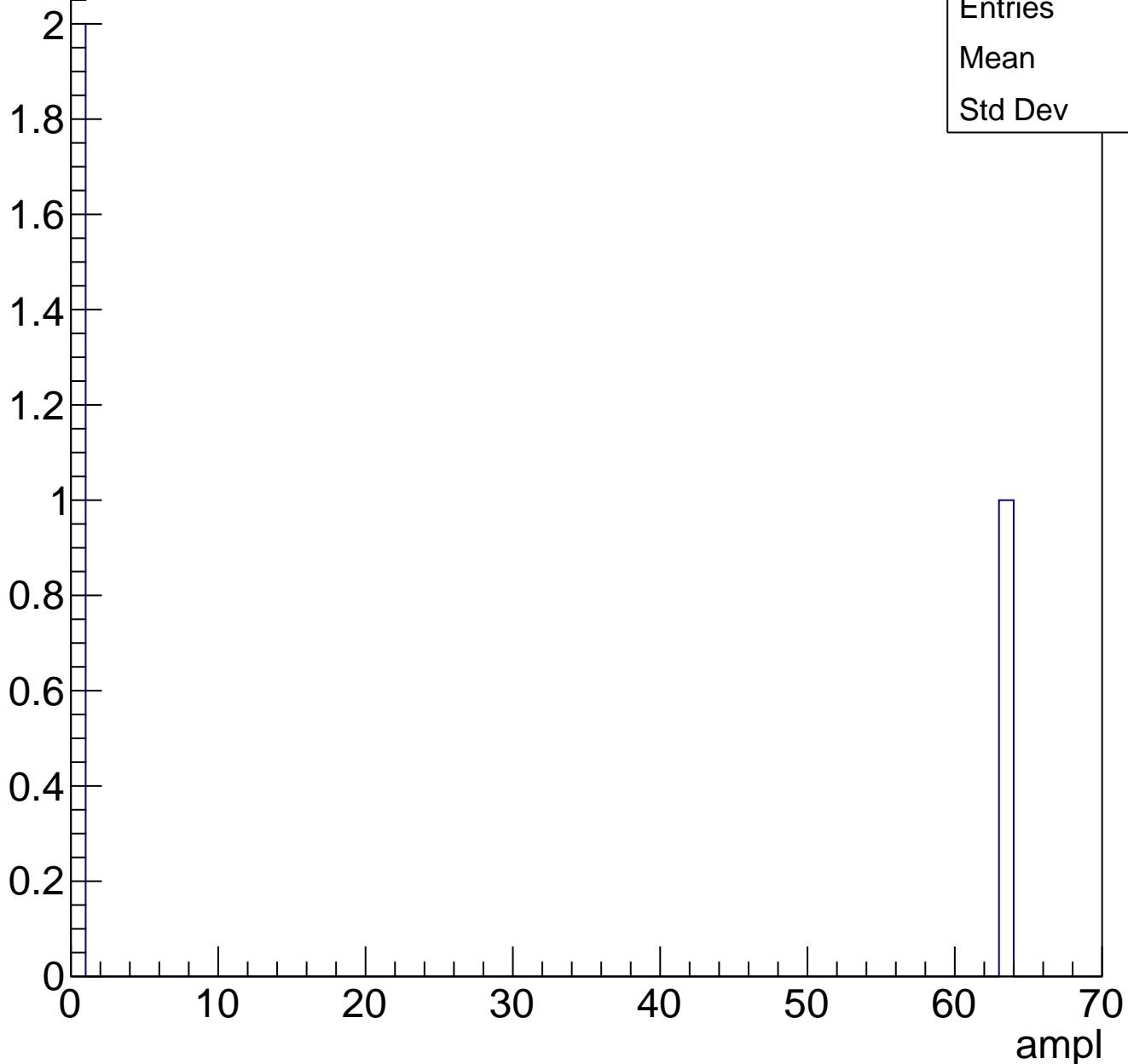
Entries	20
Mean	61.3
Std Dev	1.646



B1L102S, U18-ch10, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U18-ch11, adc0

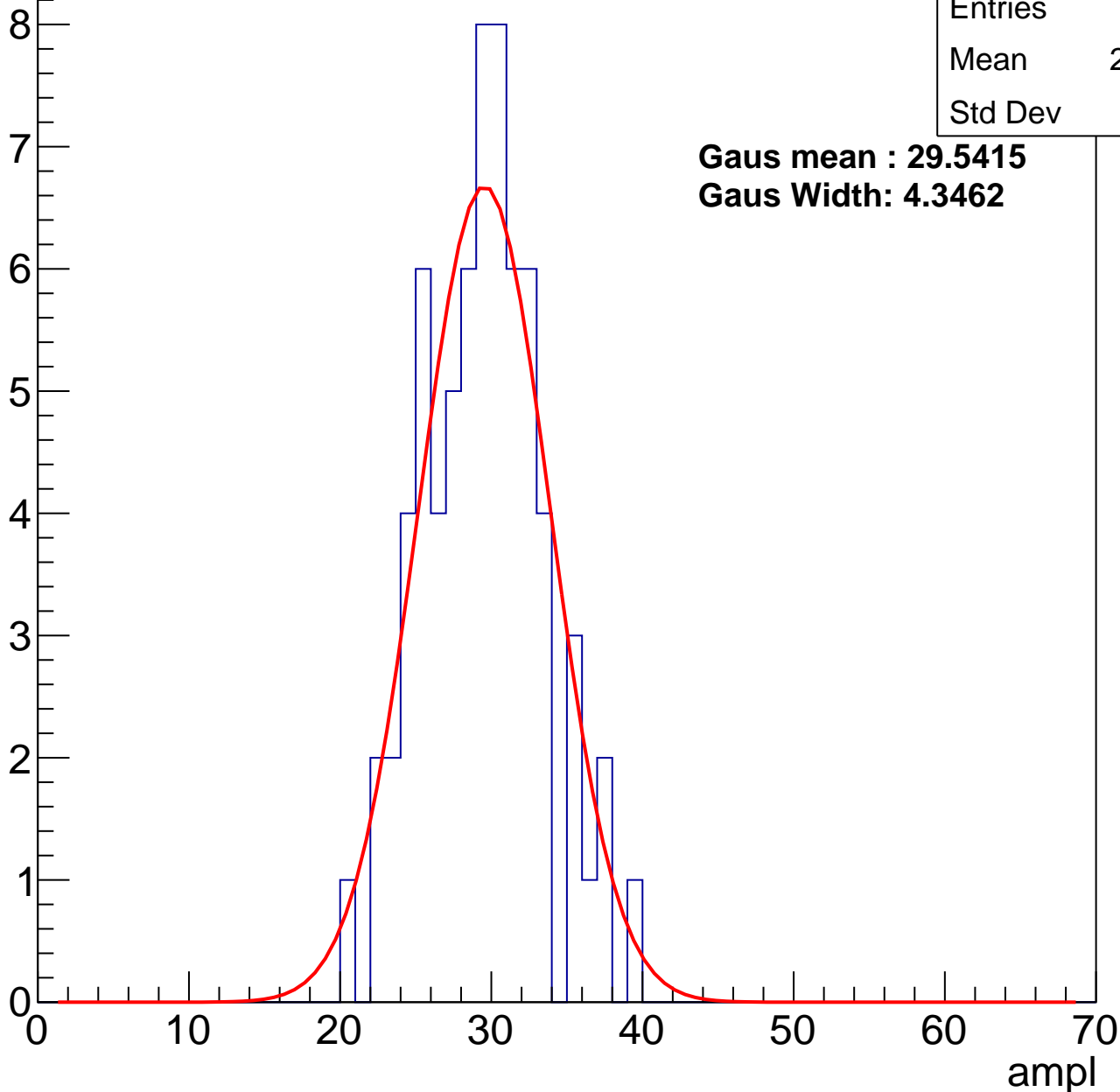
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.97
Std Dev	3.89

Gaus mean : 29.5415

Gaus Width: 4.3462



B1L102S, U18-ch11, adc1

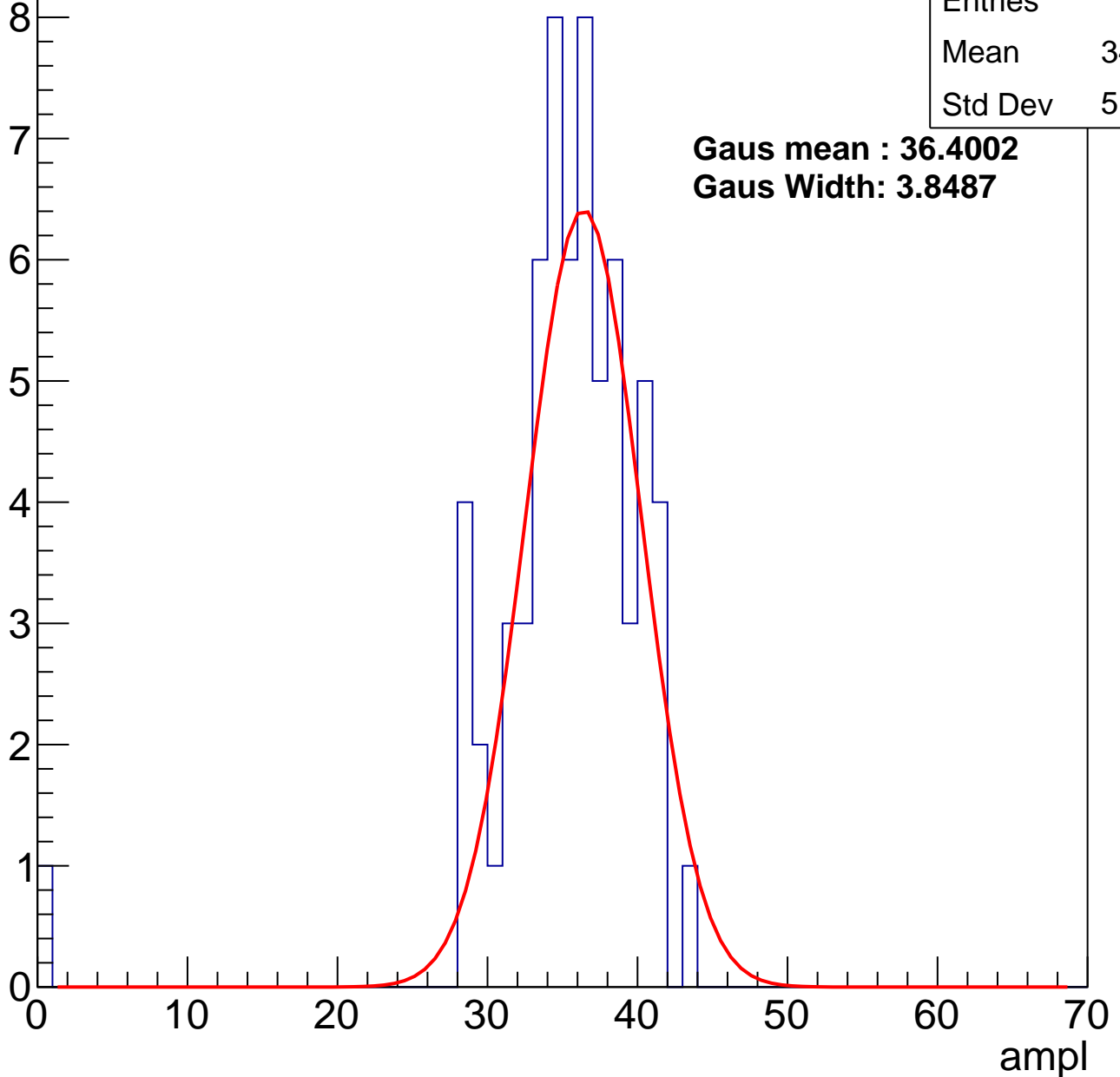
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.76
Std Dev	5.619

Gaus mean : 36.4002

Gaus Width: 3.8487



B1L102S, U18-ch11, adc2

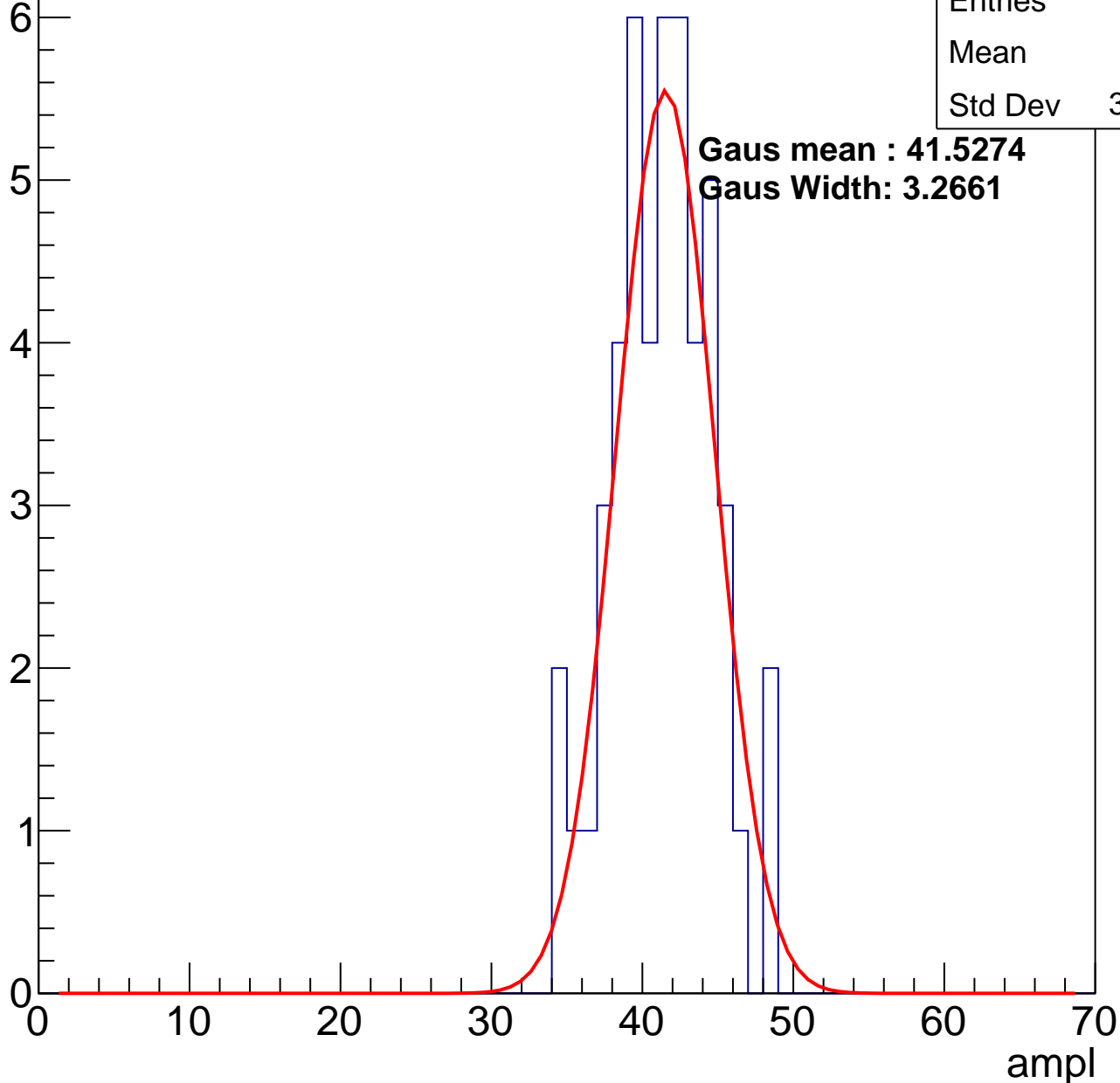
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	40.9
Std Dev	3.255

Gaus mean : 41.5274

Gaus Width: 3.2661

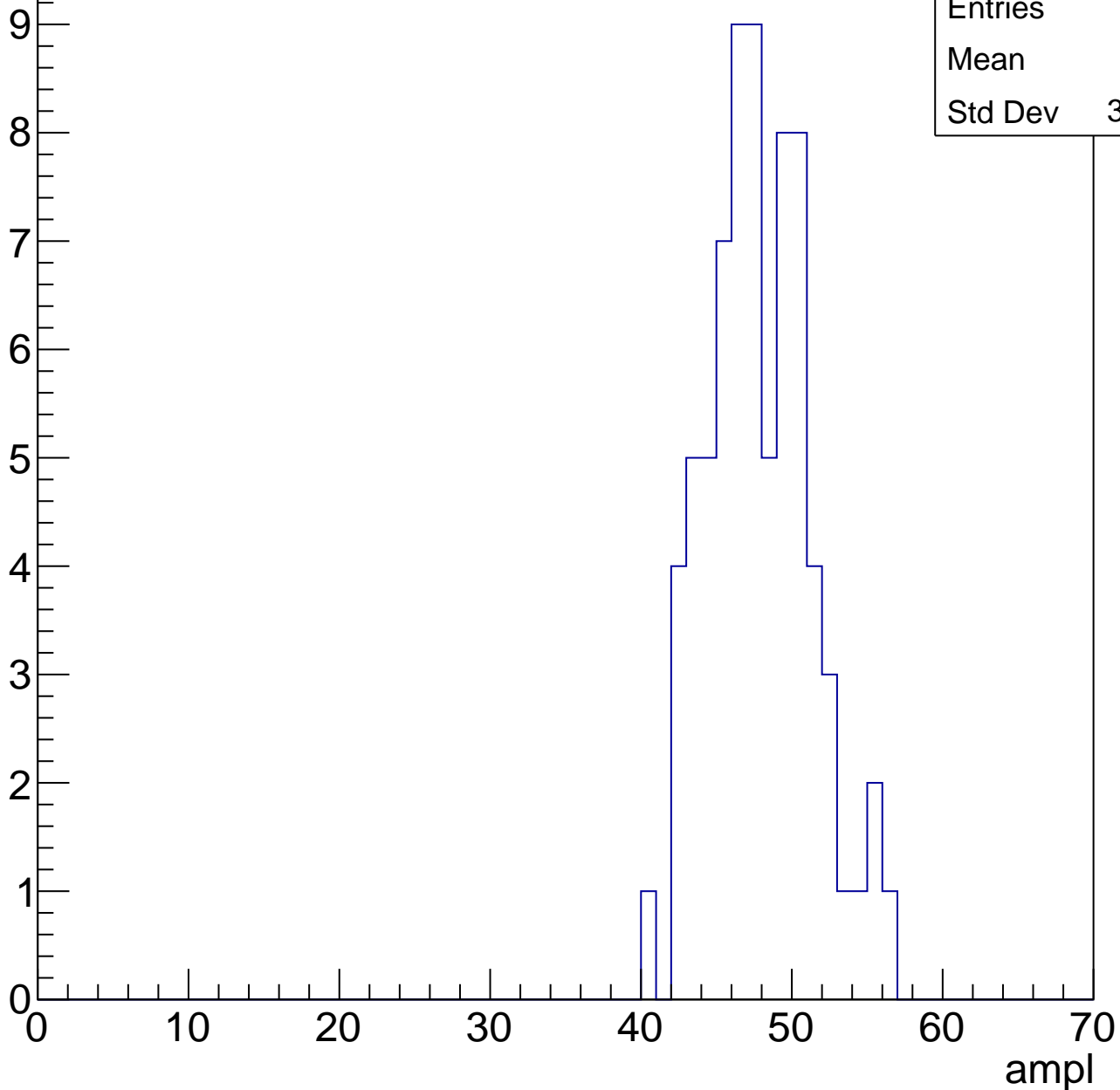


B1L102S, U18-ch11, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

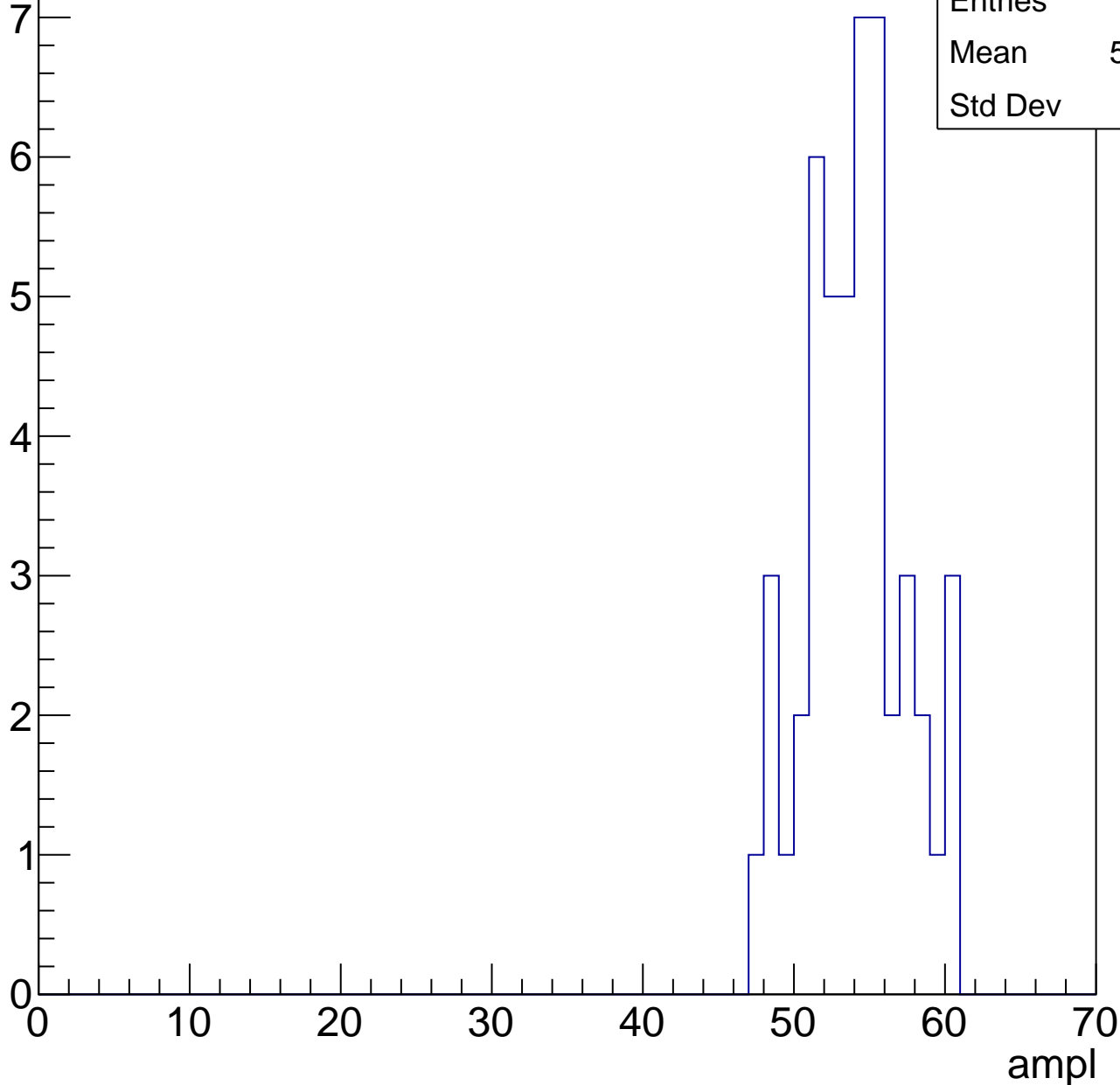
Entries	73
Mean	47.4
Std Dev	3.395



B1L102S, U18-ch11, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

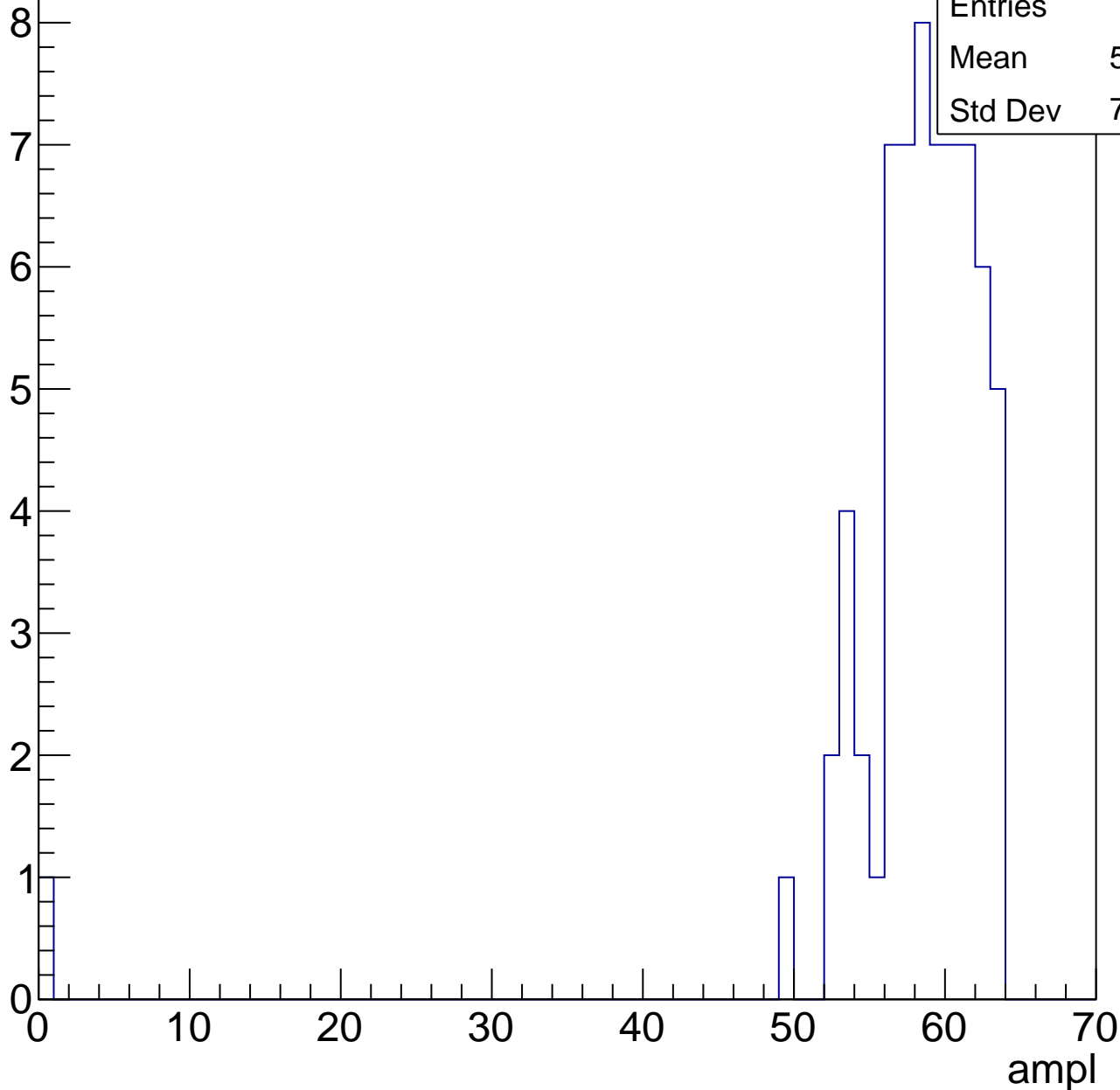


B1L102S, U18-ch11, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	57.38
Std Dev	7.832

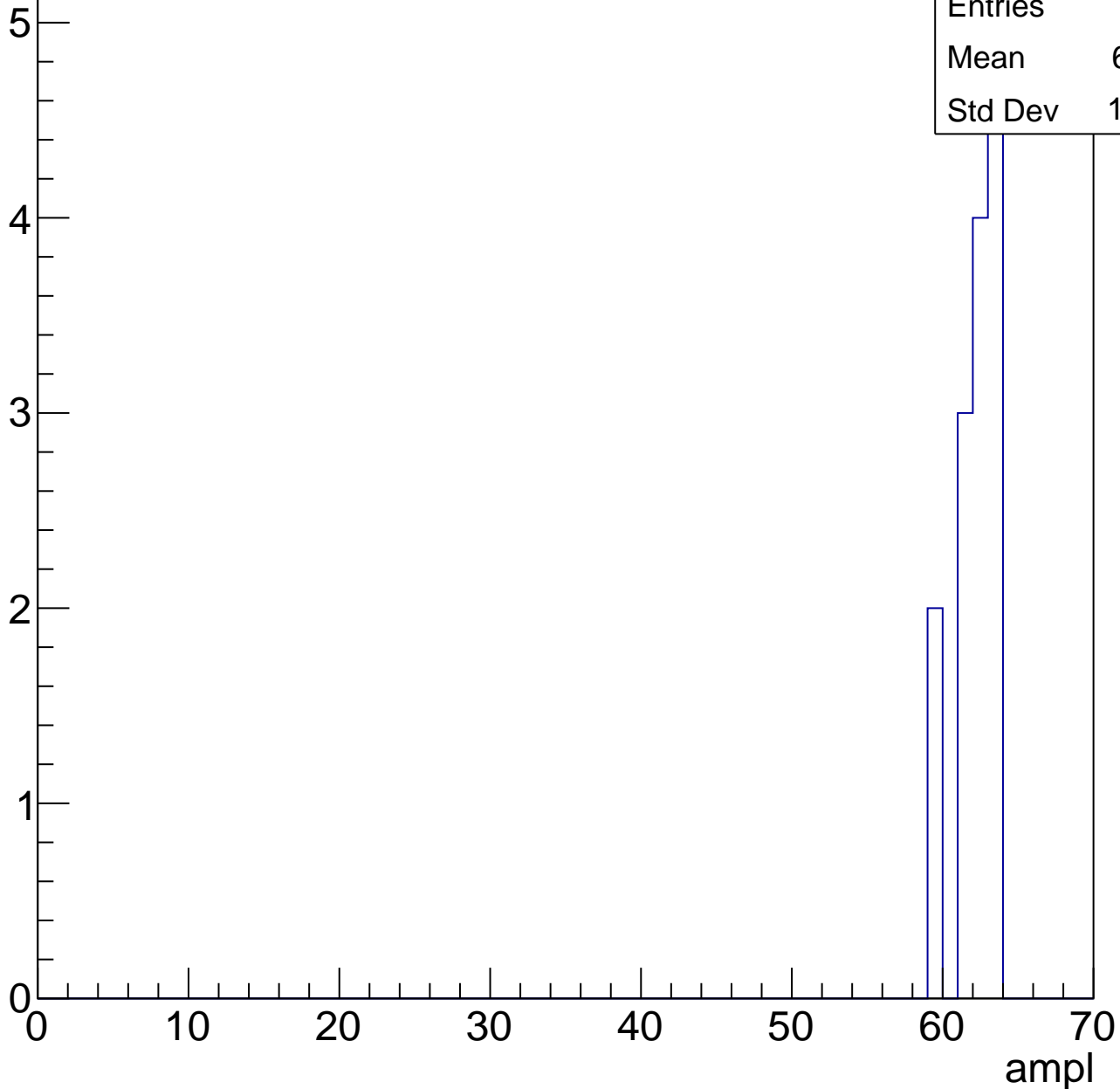


B1L102S, U18-ch11, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.71
Std Dev	1.332



B1L102S, U18-ch11, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch12, adc0

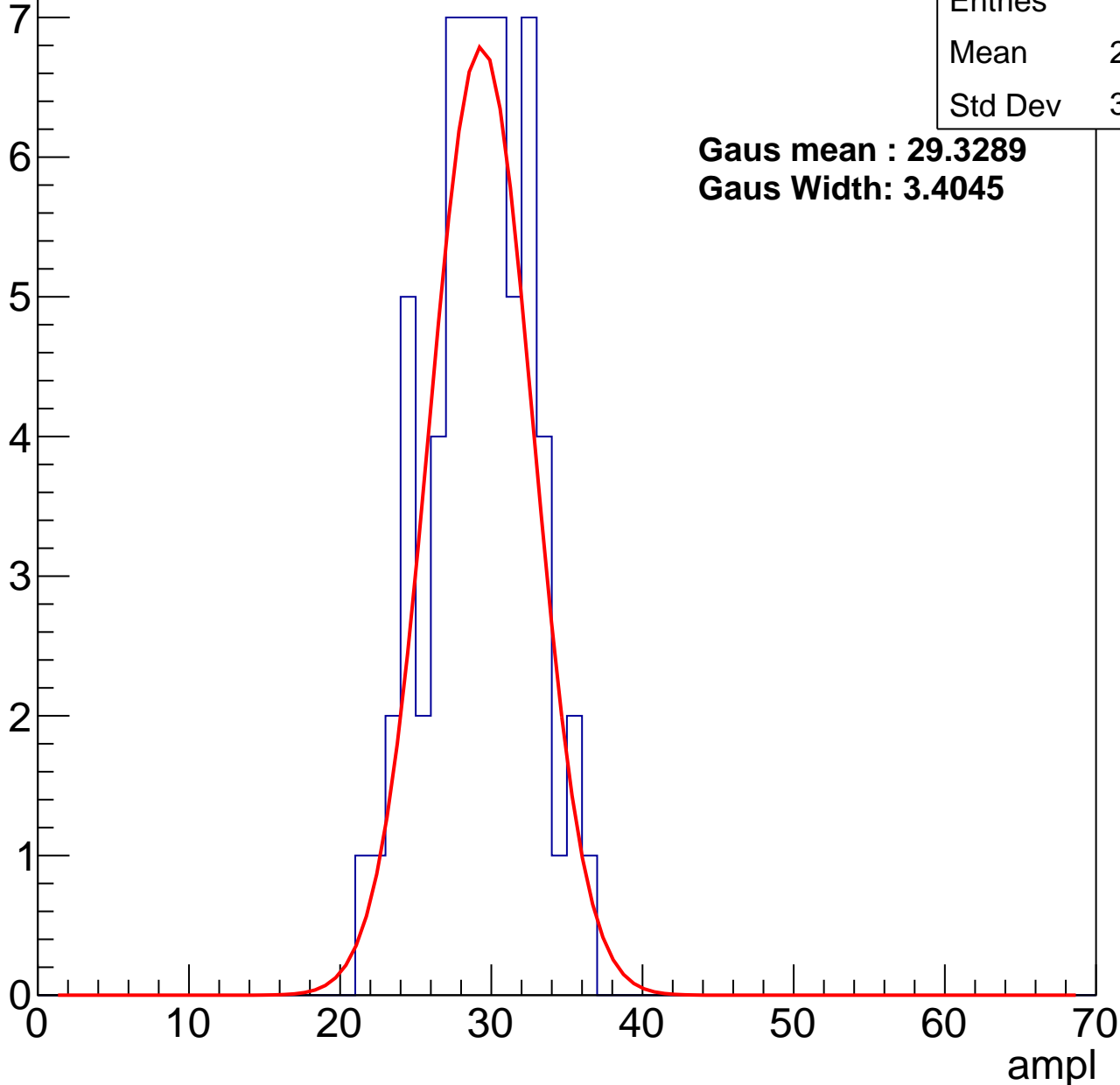
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	28.76
Std Dev	3.356

Gaus mean : 29.3289

Gaus Width: 3.4045



B1L102S, U18-ch12, adc1

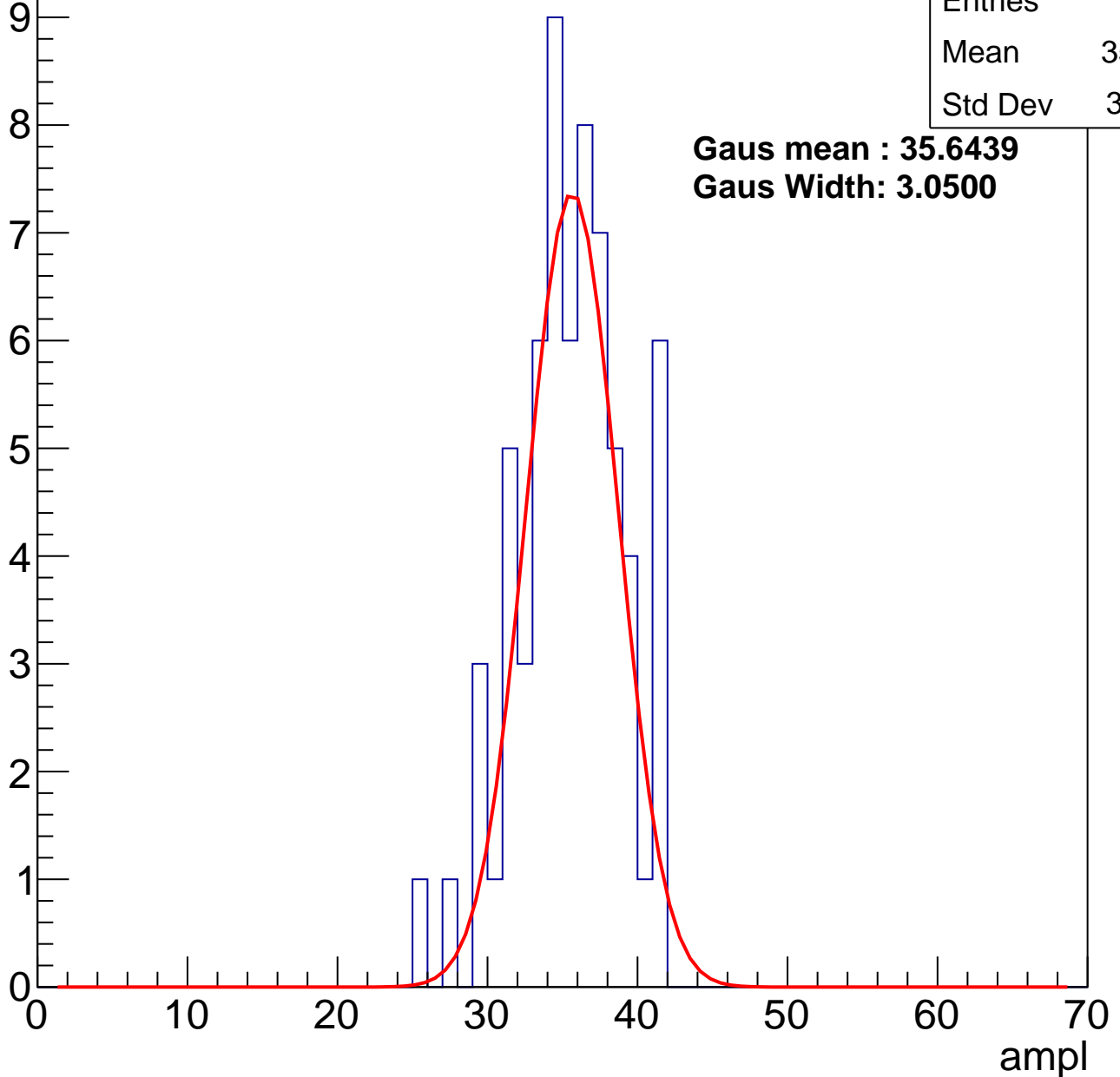
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.05
Std Dev	3.531

Gaus mean : 35.6439

Gaus Width: 3.0500



B1L102S, U18-ch12, adc2

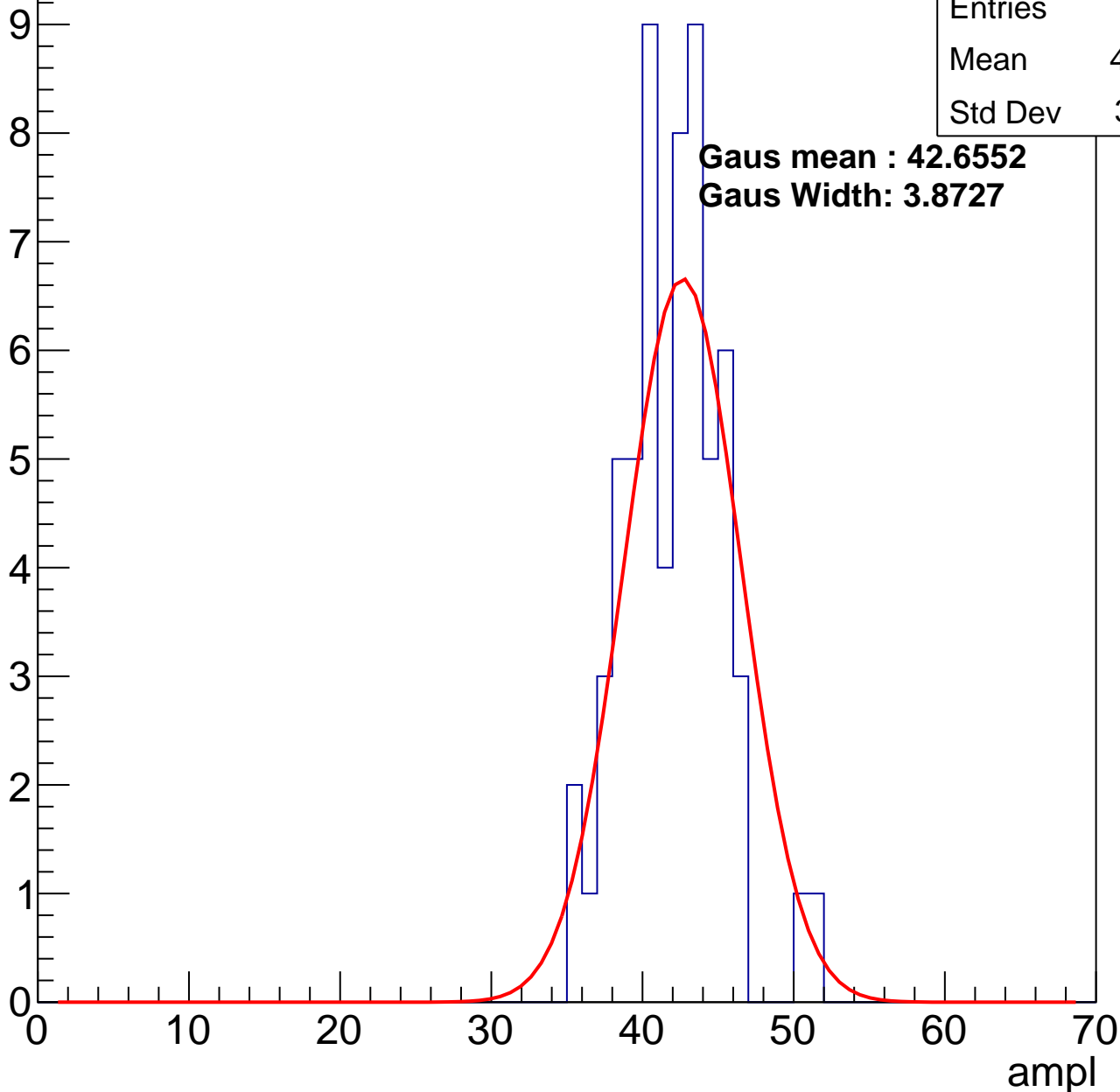
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.58
Std Dev	3.211

Gaus mean : 42.6552

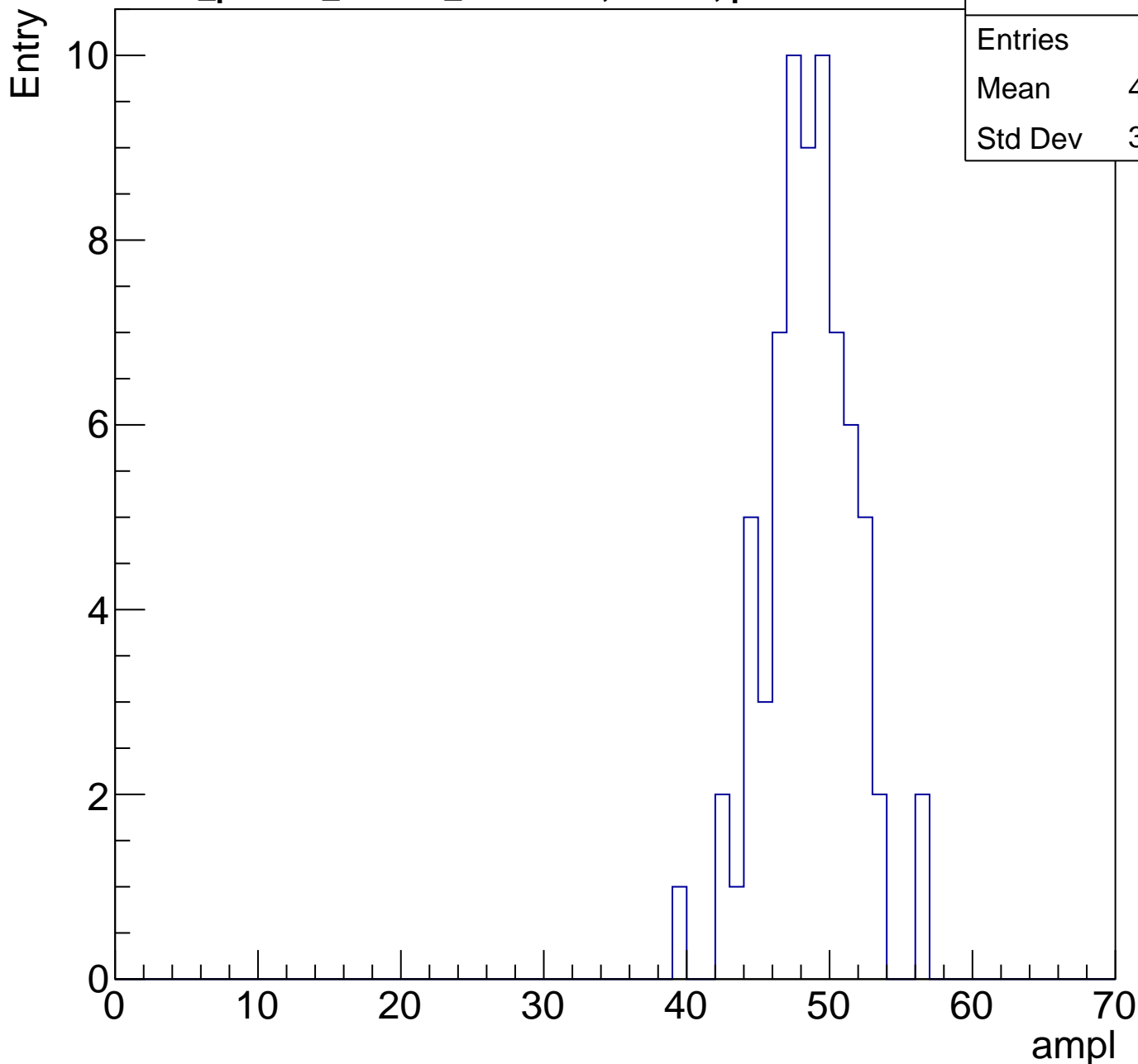
Gaus Width: 3.8727



B1L102S, U18-ch12, adc3

calib_packv5_042523_0143.root, FC#11, port A2

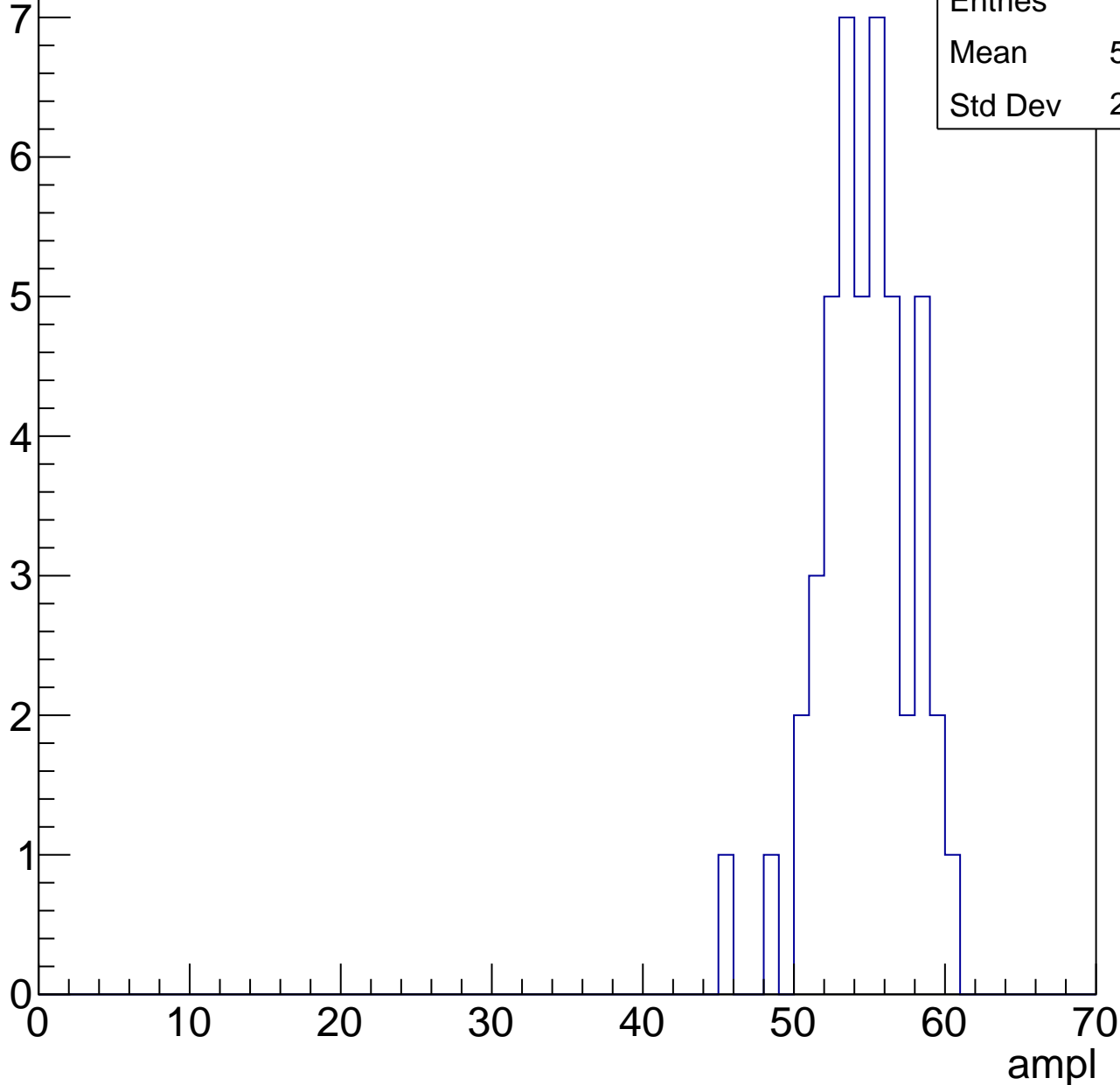
Entries	70
Mean	48.13
Std Dev	3.098



B1L102S, U18-ch12, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

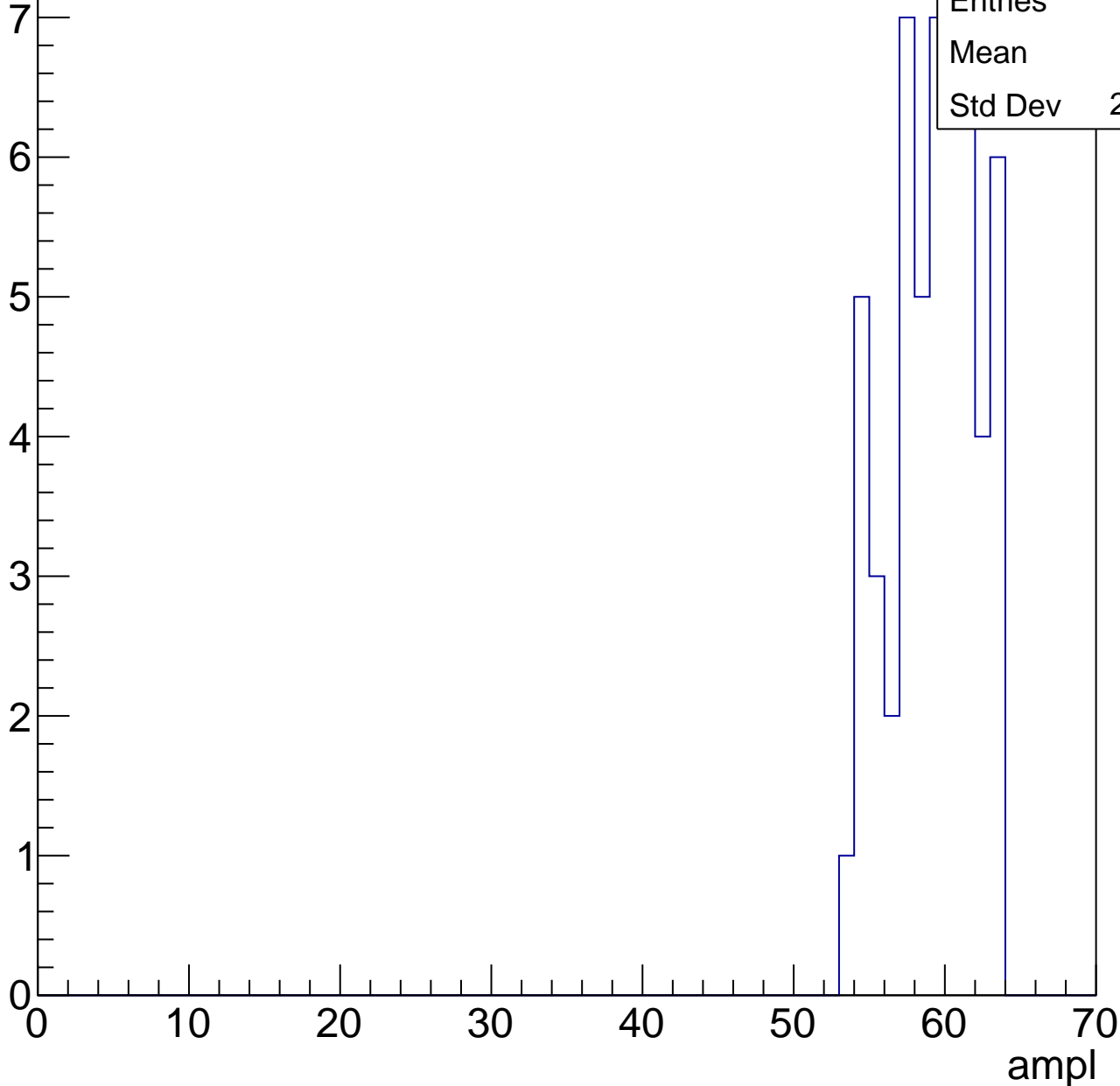


B1L102S, U18-ch12, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.8
Std Dev	2.805

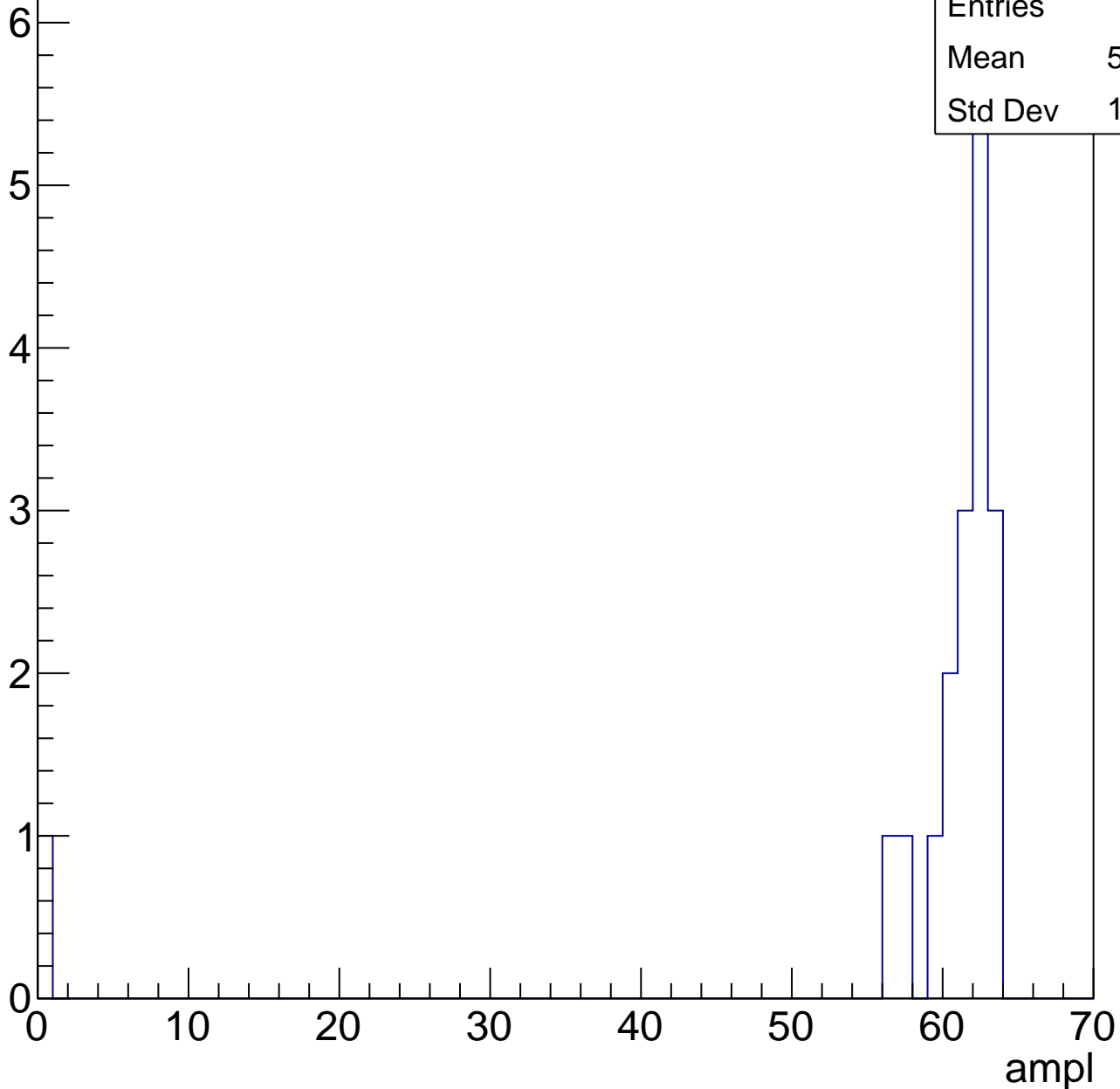


B1L102S, U18-ch12, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	57.56
Std Dev	14.09



B1L102S, U18-ch12, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch13, adc0

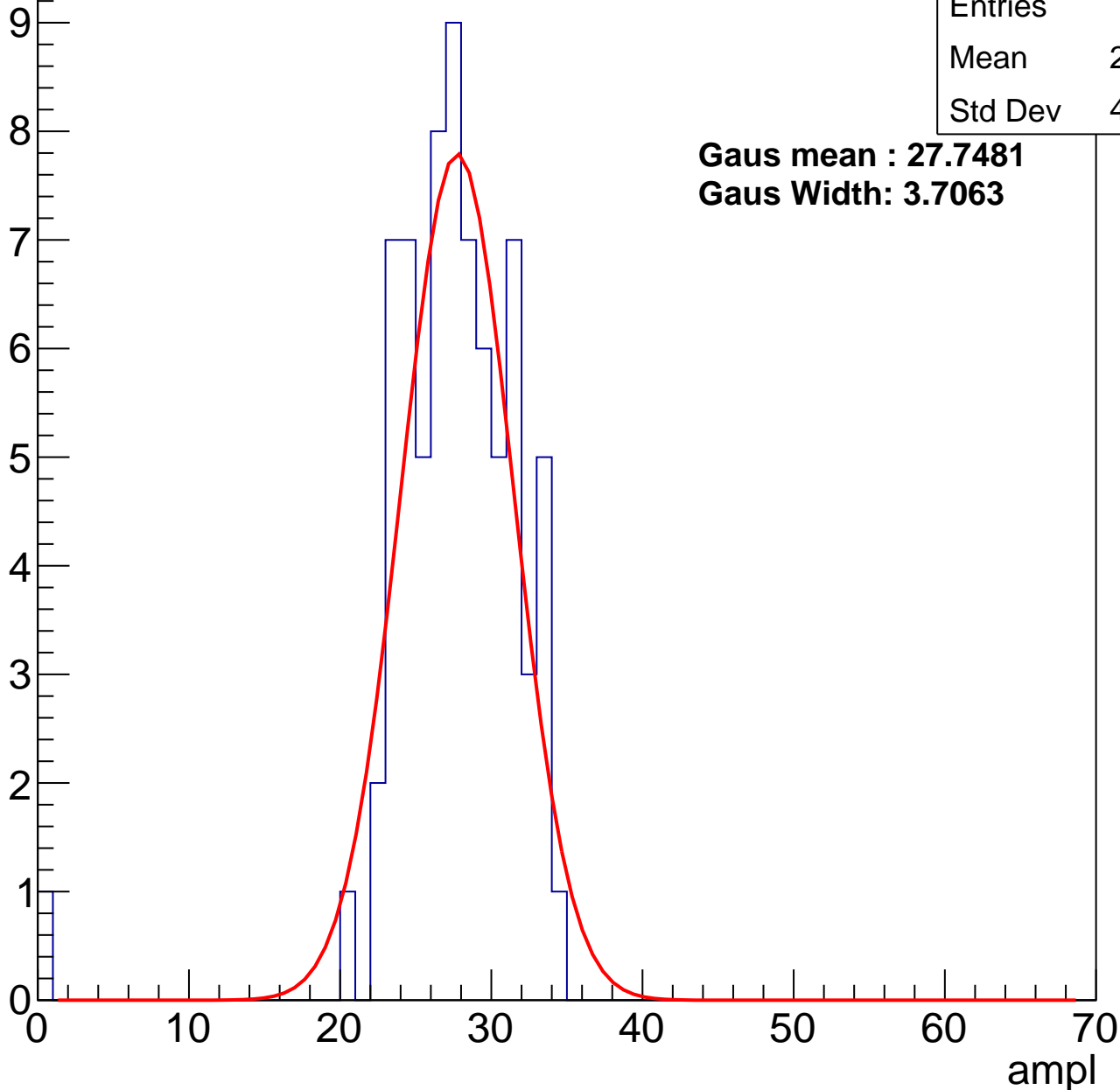
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	27.04
Std Dev	4.536

Gaus mean : 27.7481

Gaus Width: 3.7063



B1L102S, U18-ch13, adc1

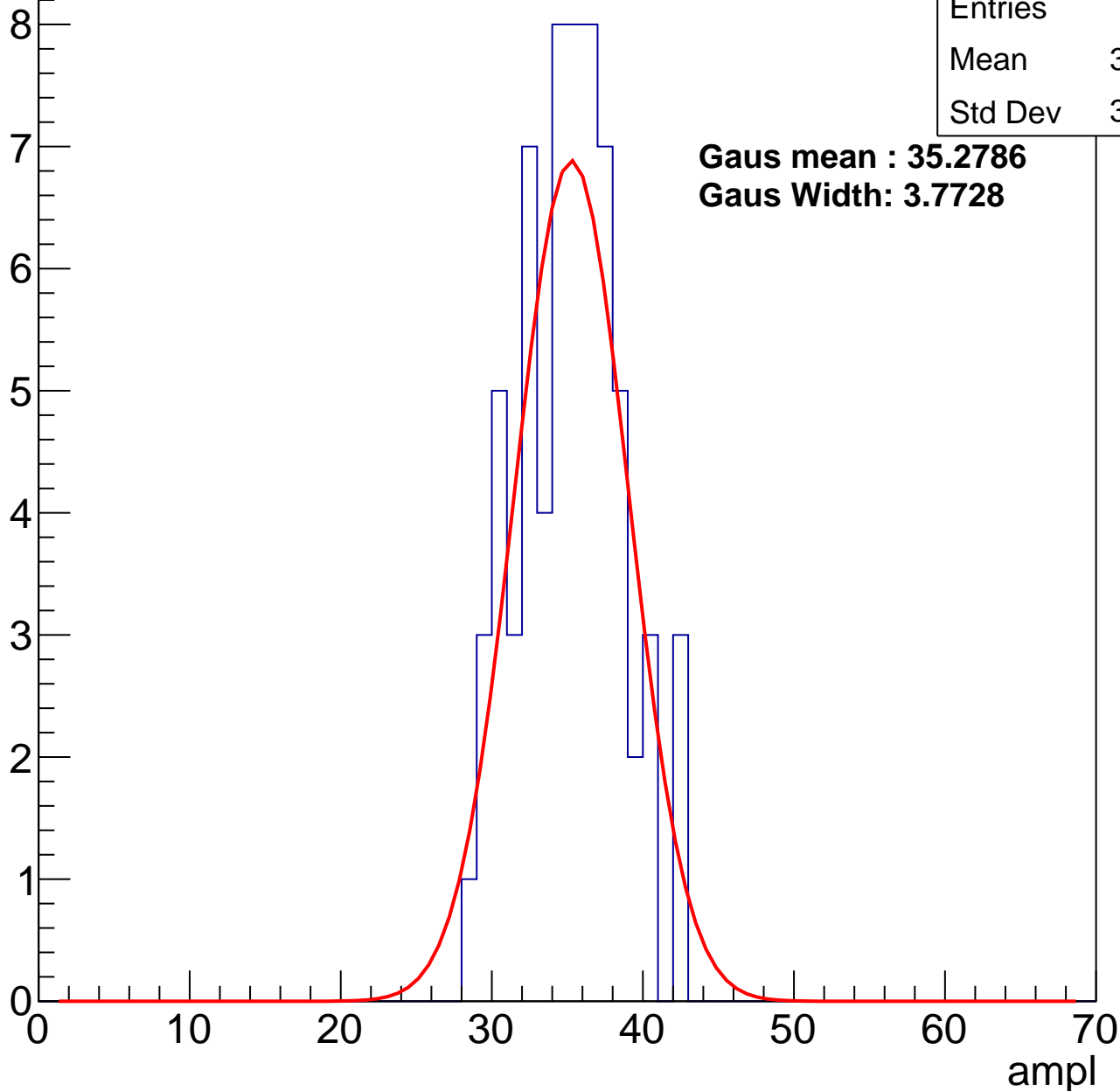
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.73
Std Dev	3.339

Gaus mean : 35.2786

Gaus Width: 3.7728



B1L102S, U18-ch13, adc2

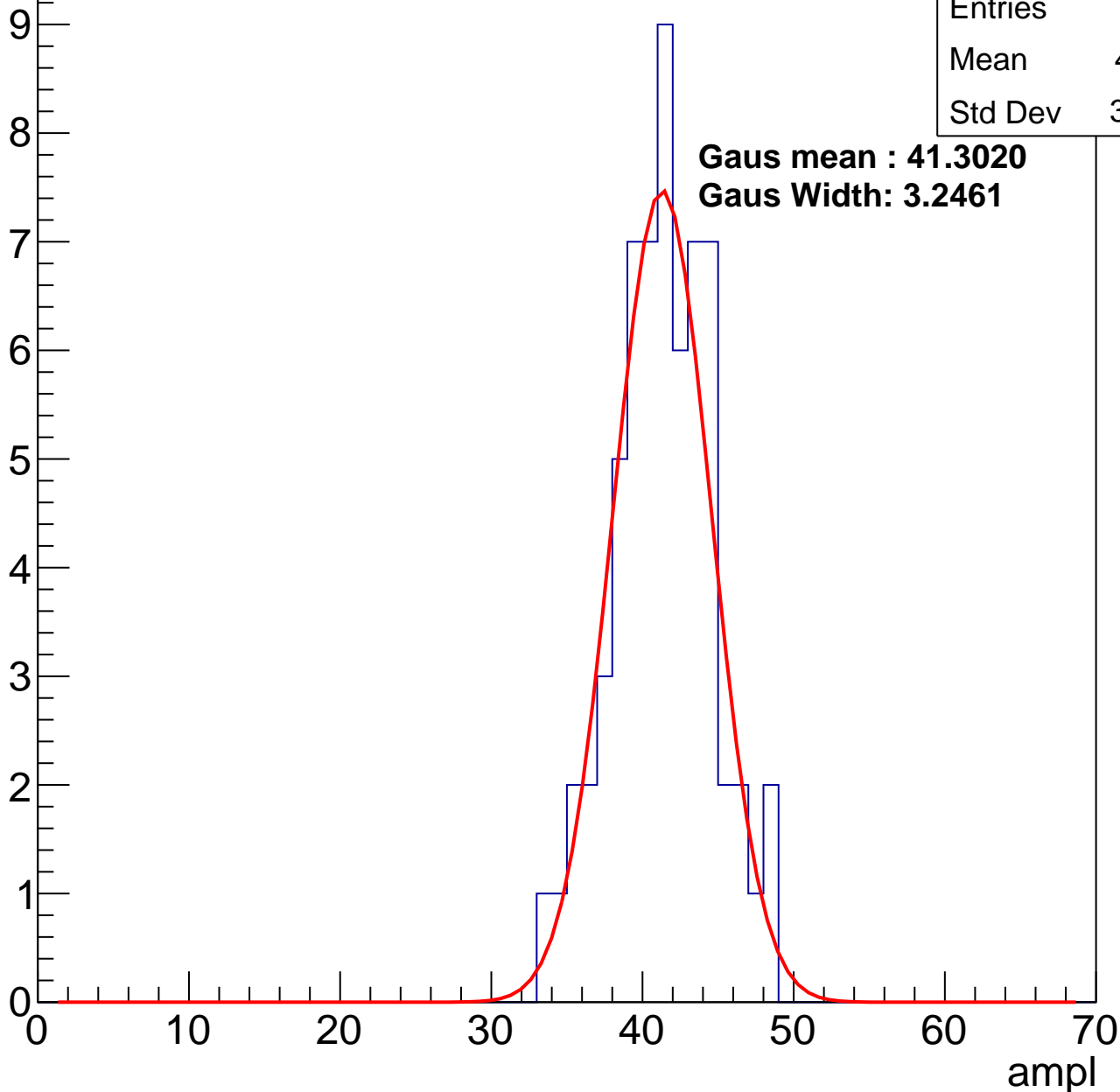
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	40.91
Std Dev	3.249

Gaus mean : 41.3020

Gaus Width: 3.2461

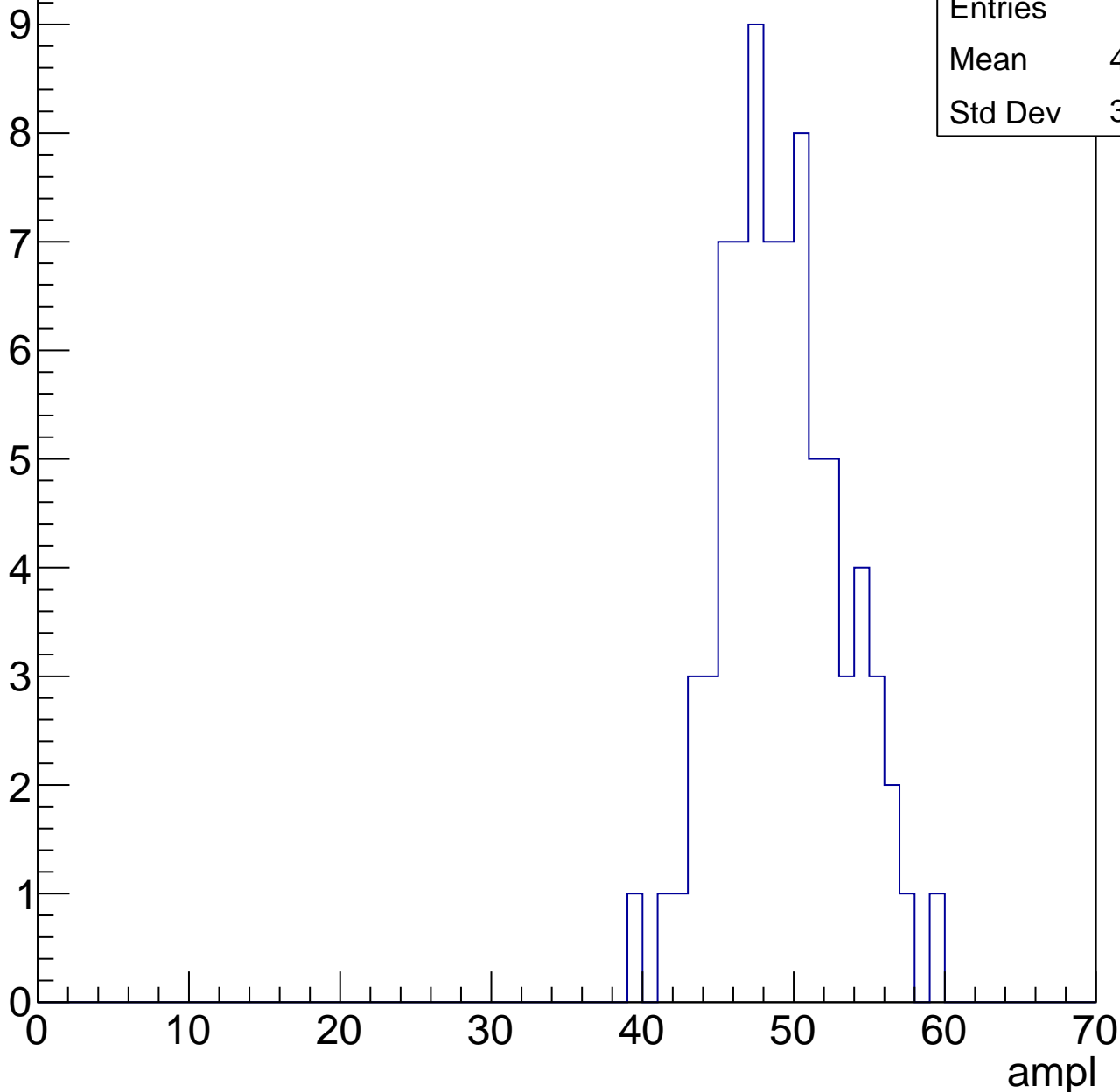


B1L102S, U18-ch13, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	48.78
Std Dev	3.924

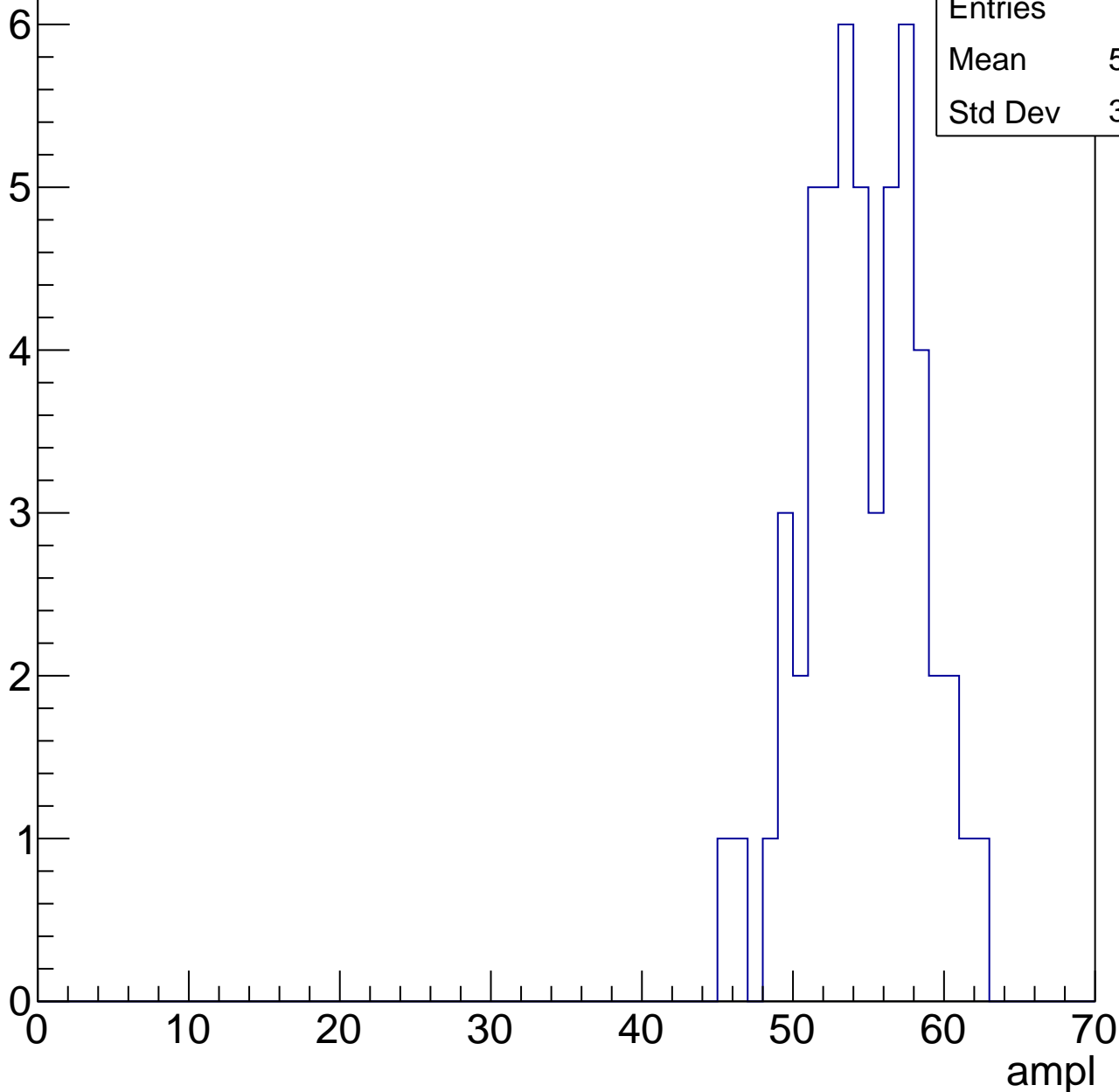


B1L102S, U18-ch13, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.13
Std Dev	3.722



B1L102S, U18-ch13, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	45
Mean	59.18
Std Dev	2.702

ampl

0

10

20

30

40

50

60

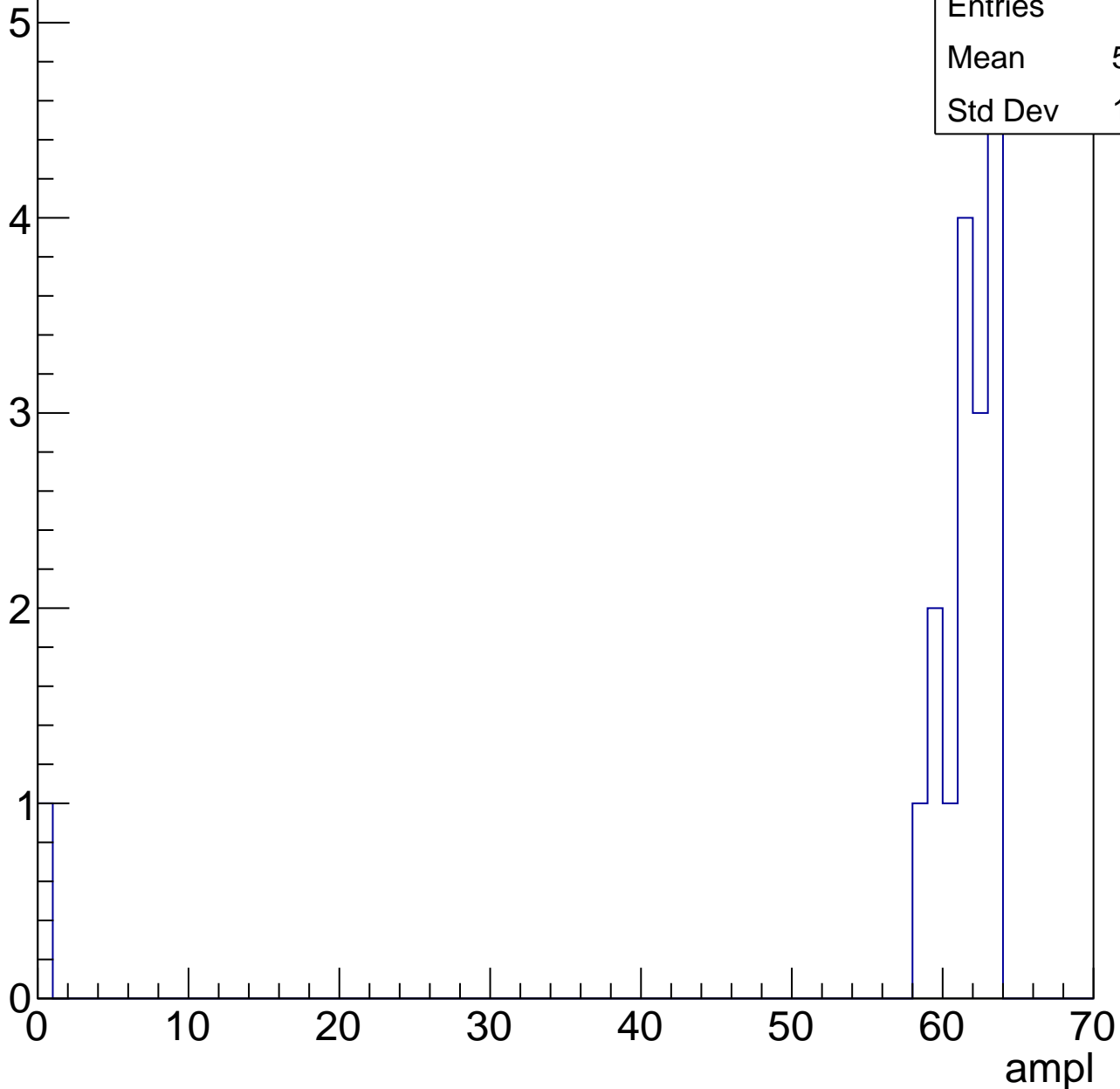
70

B1L102S, U18-ch13, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.71
Std Dev	14.51



B1L102S, U18-ch13, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch14, adc0

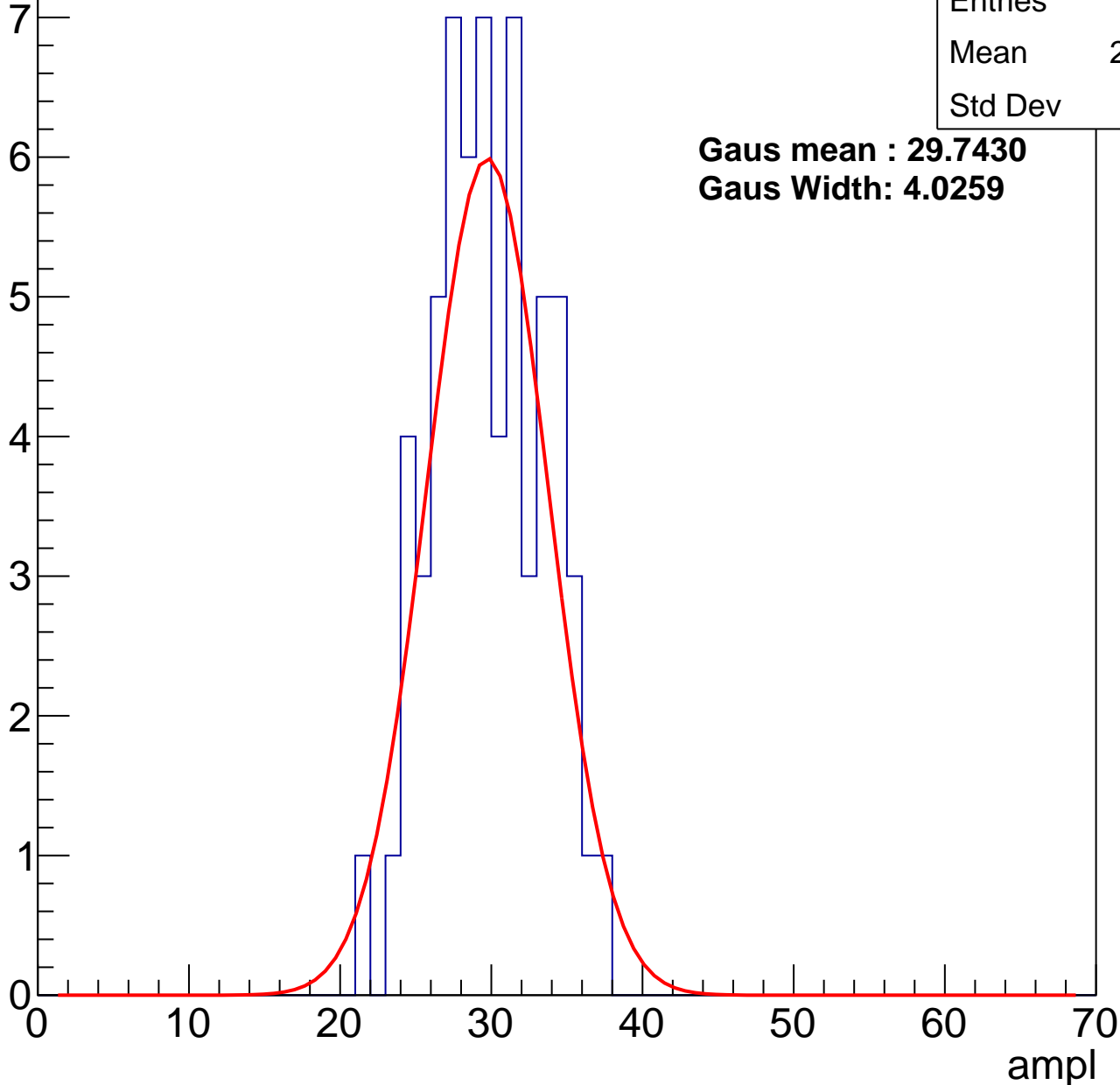
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	29.38
Std Dev	3.57

Gaus mean : 29.7430

Gaus Width: 4.0259



B1L102S, U18-ch14, adc1

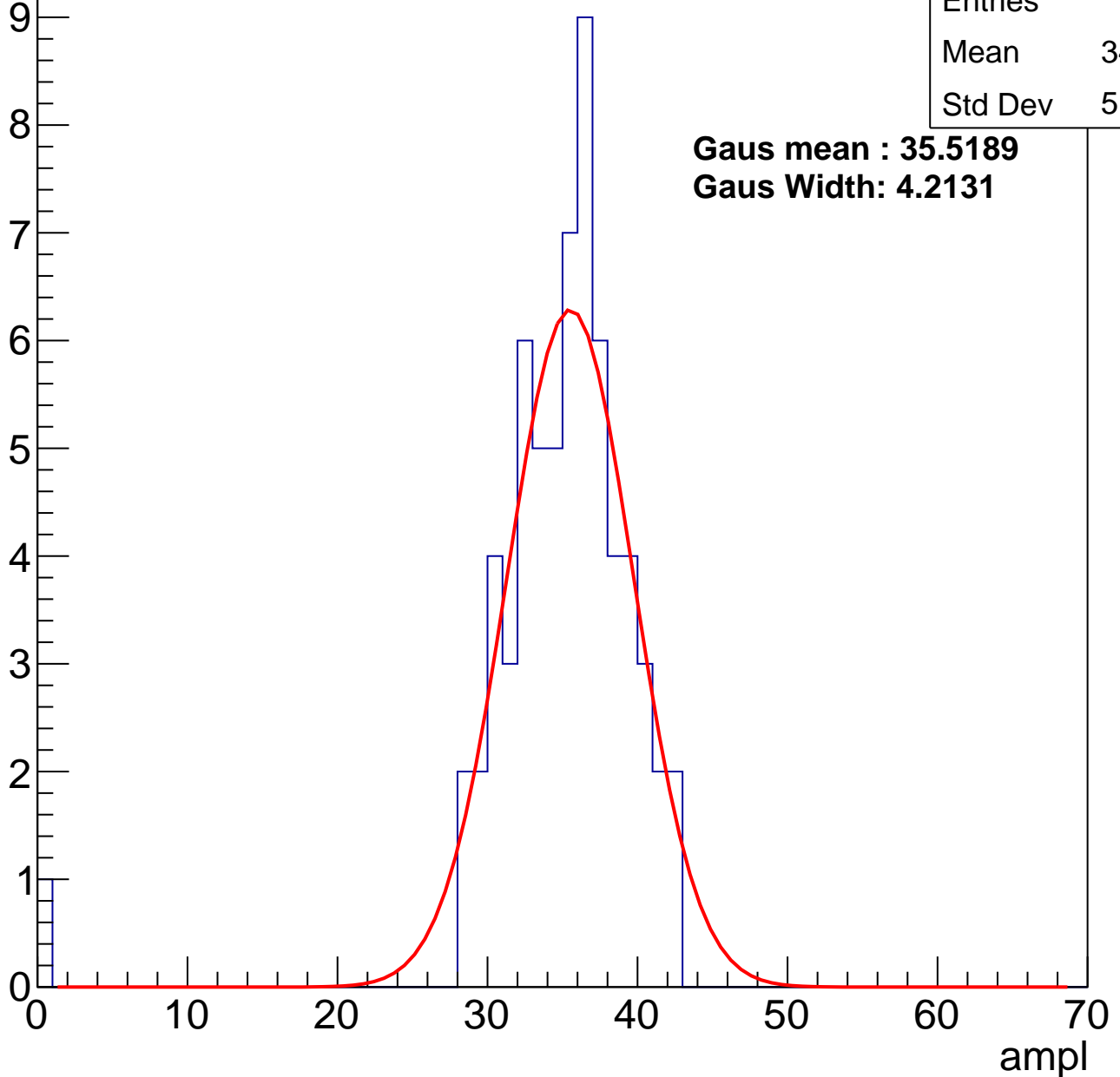
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.45
Std Dev	5.519

Gaus mean : 35.5189

Gaus Width: 4.2131



B1L102S, U18-ch14, adc2

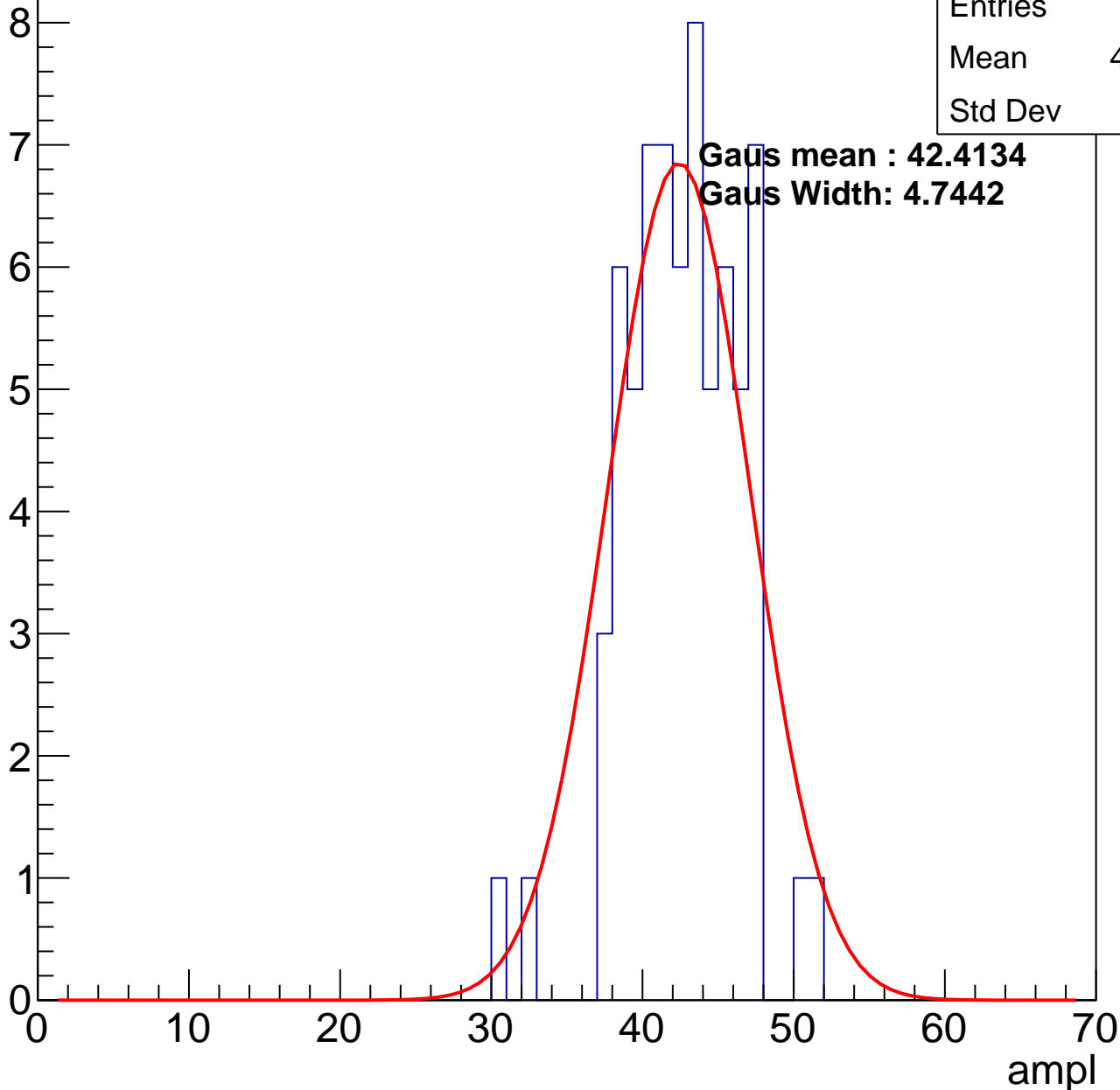
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	42.16
Std Dev	3.76

Gaus mean : 42.4134

Gaus Width: 4.7442

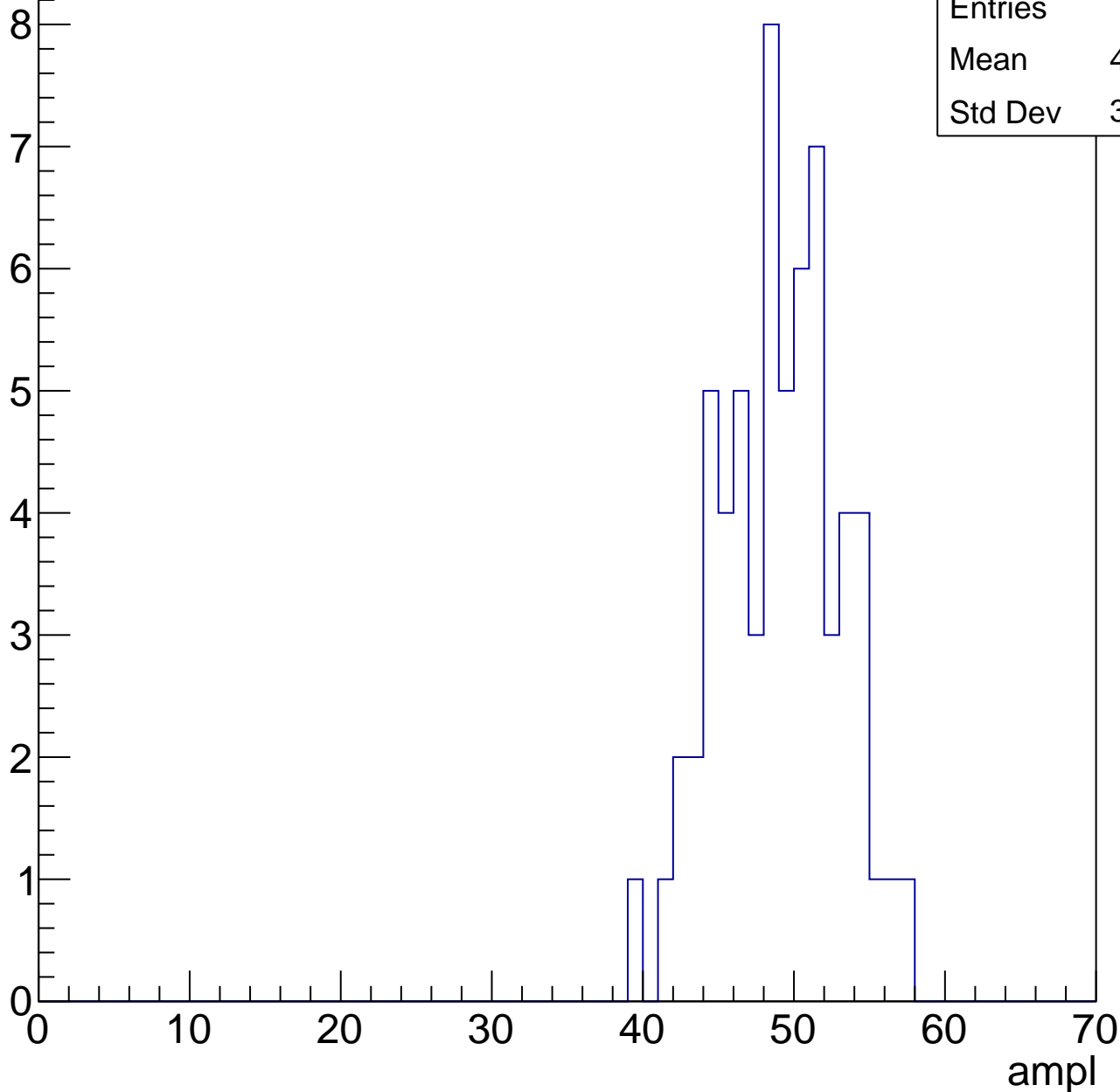


B1L102S, U18-ch14, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	48.56
Std Dev	3.882

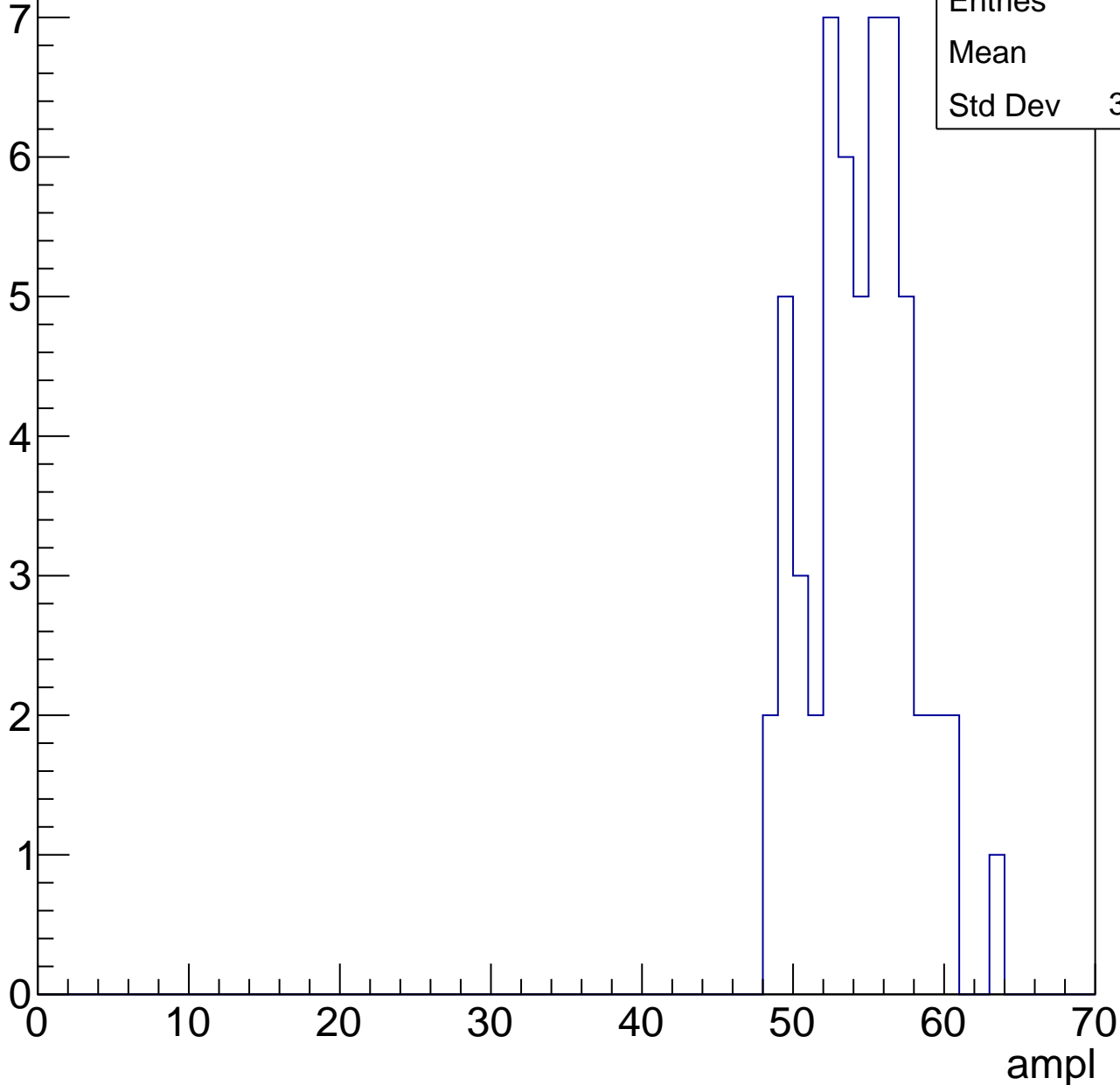


B1L102S, U18-ch14, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54
Std Dev	3.306

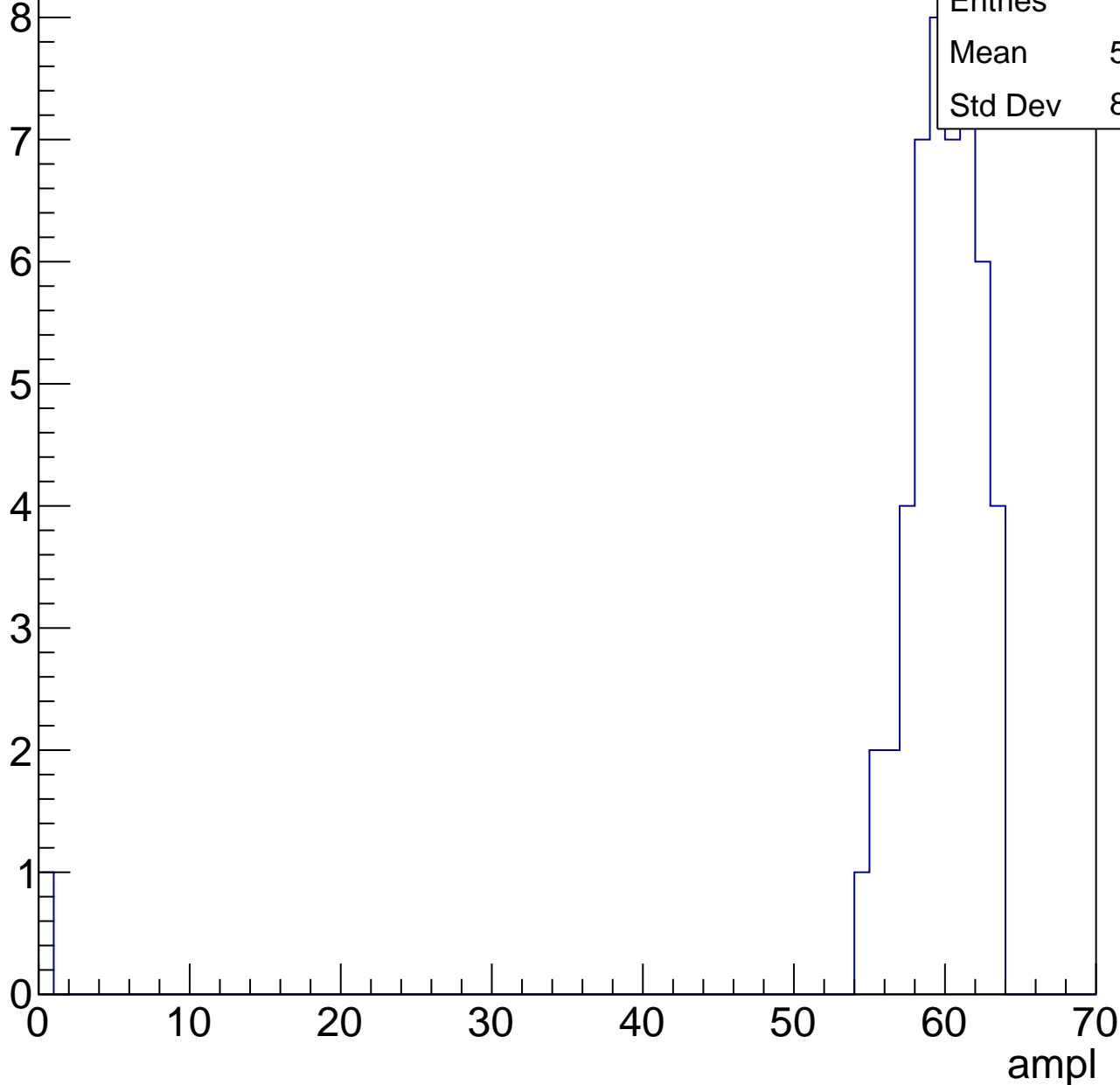


B1L102S, U18-ch14, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

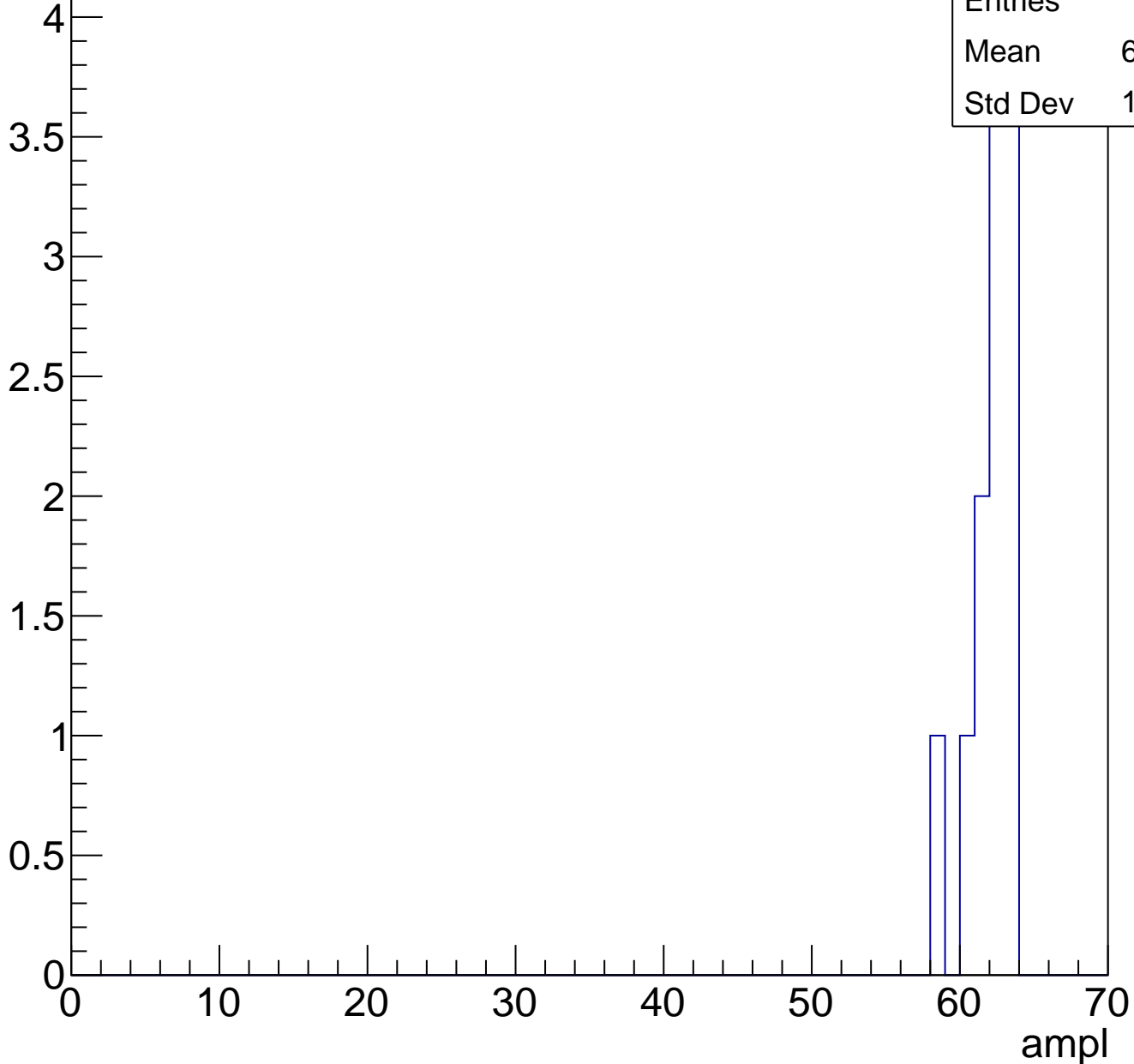
Entries	50
Mean	58.28
Std Dev	8.614



B1L102S, U18-ch14, adc6

calib_packv5_042523_0143.root, FC#11, port A2

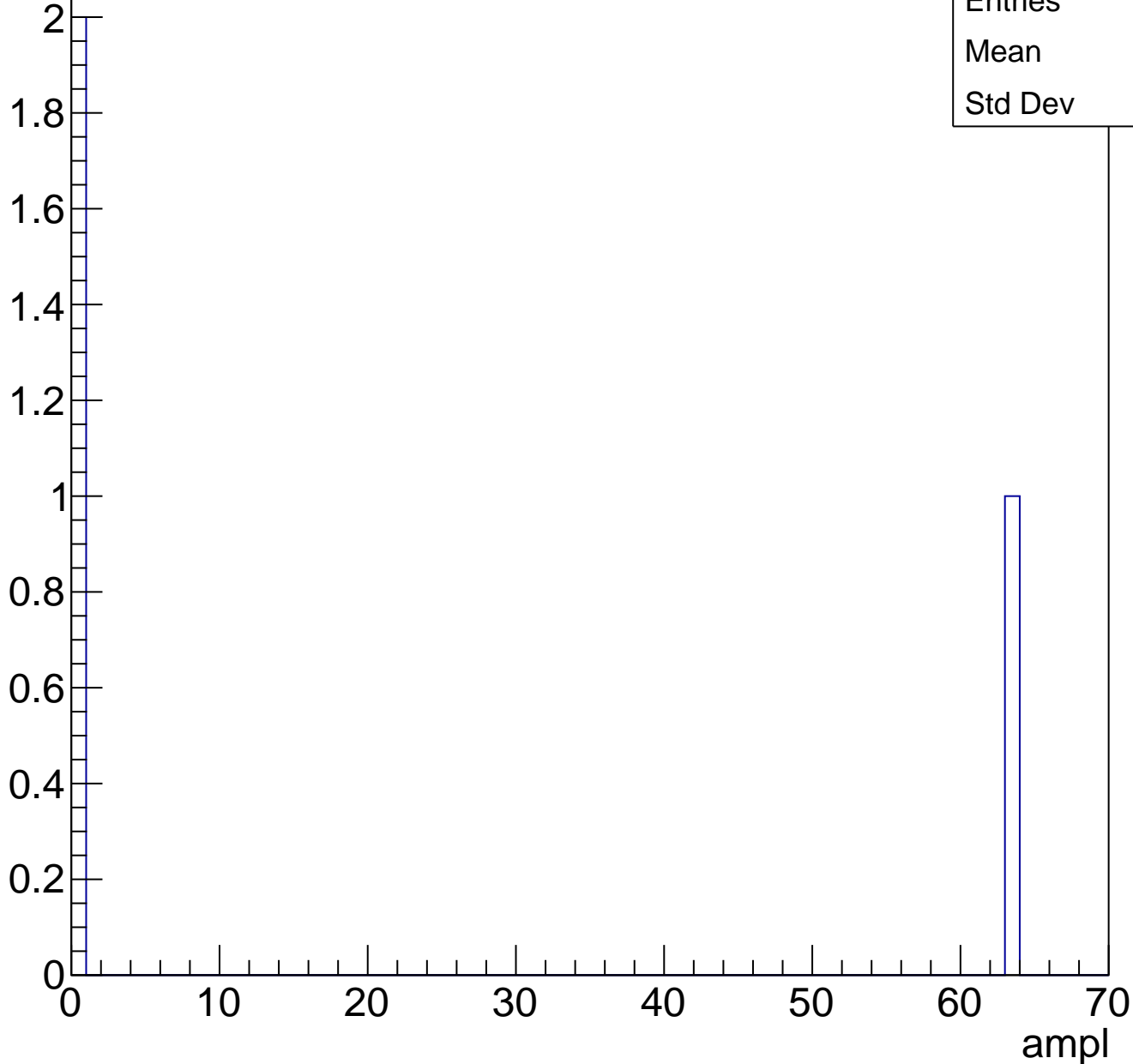
Entry



B1L102S, U18-ch14, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch15, adc0

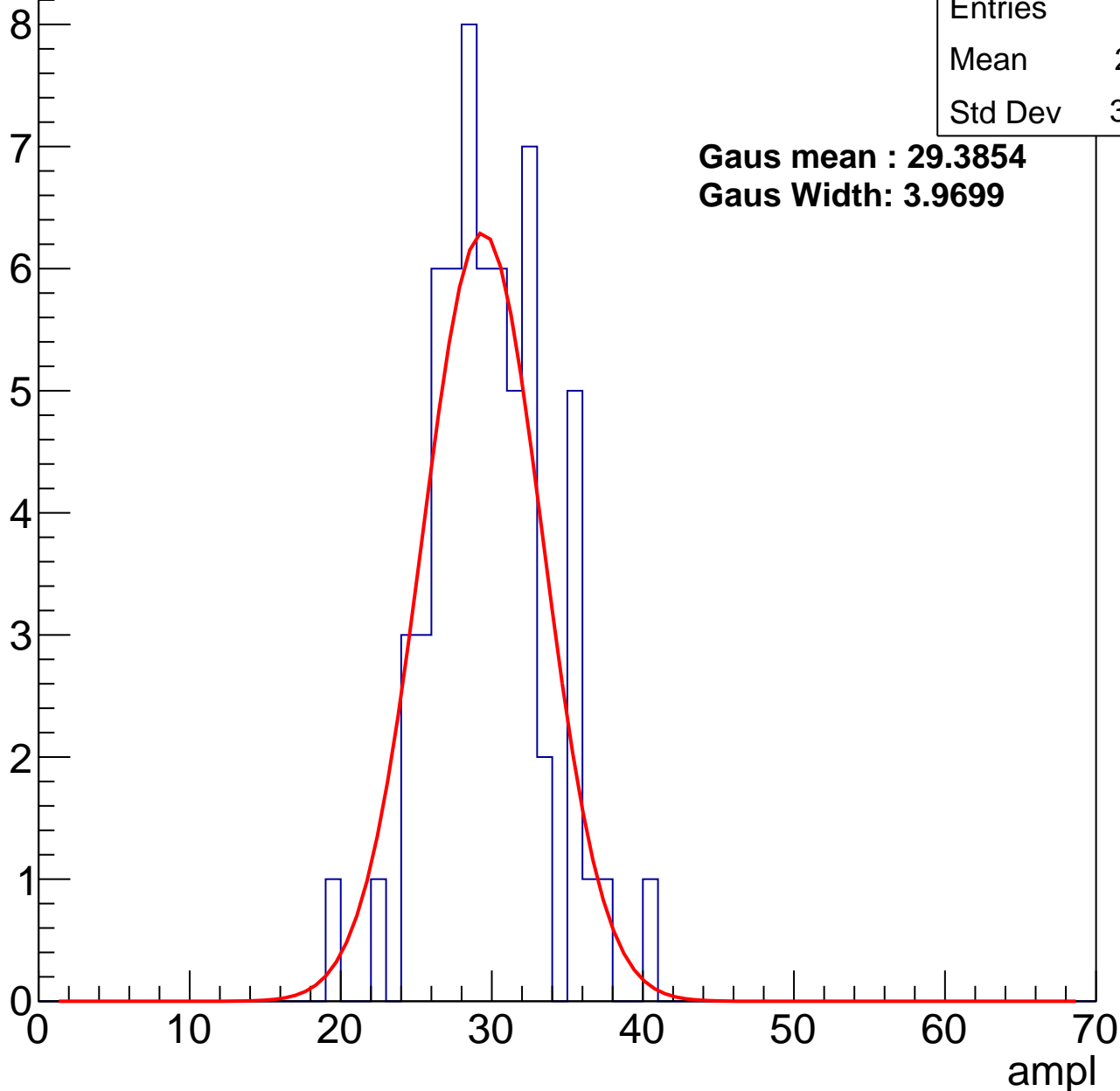
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	29.31
Std Dev	3.783

Gaus mean : 29.3854

Gaus Width: 3.9699



B1L102S, U18-ch15, adc1

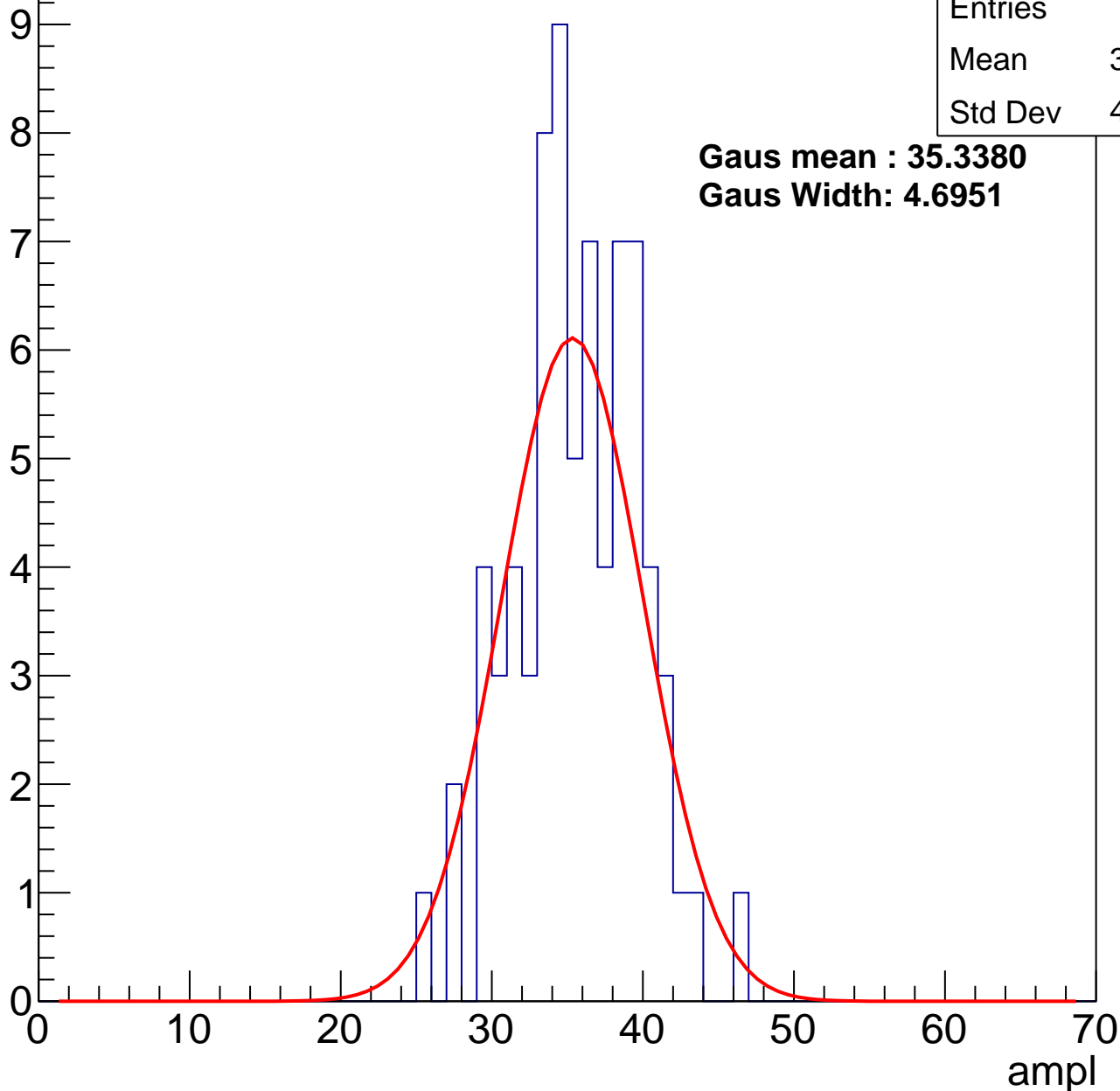
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	35.18
Std Dev	4.055

Gaus mean : 35.3380

Gaus Width: 4.6951



B1L102S, U18-ch15, adc2

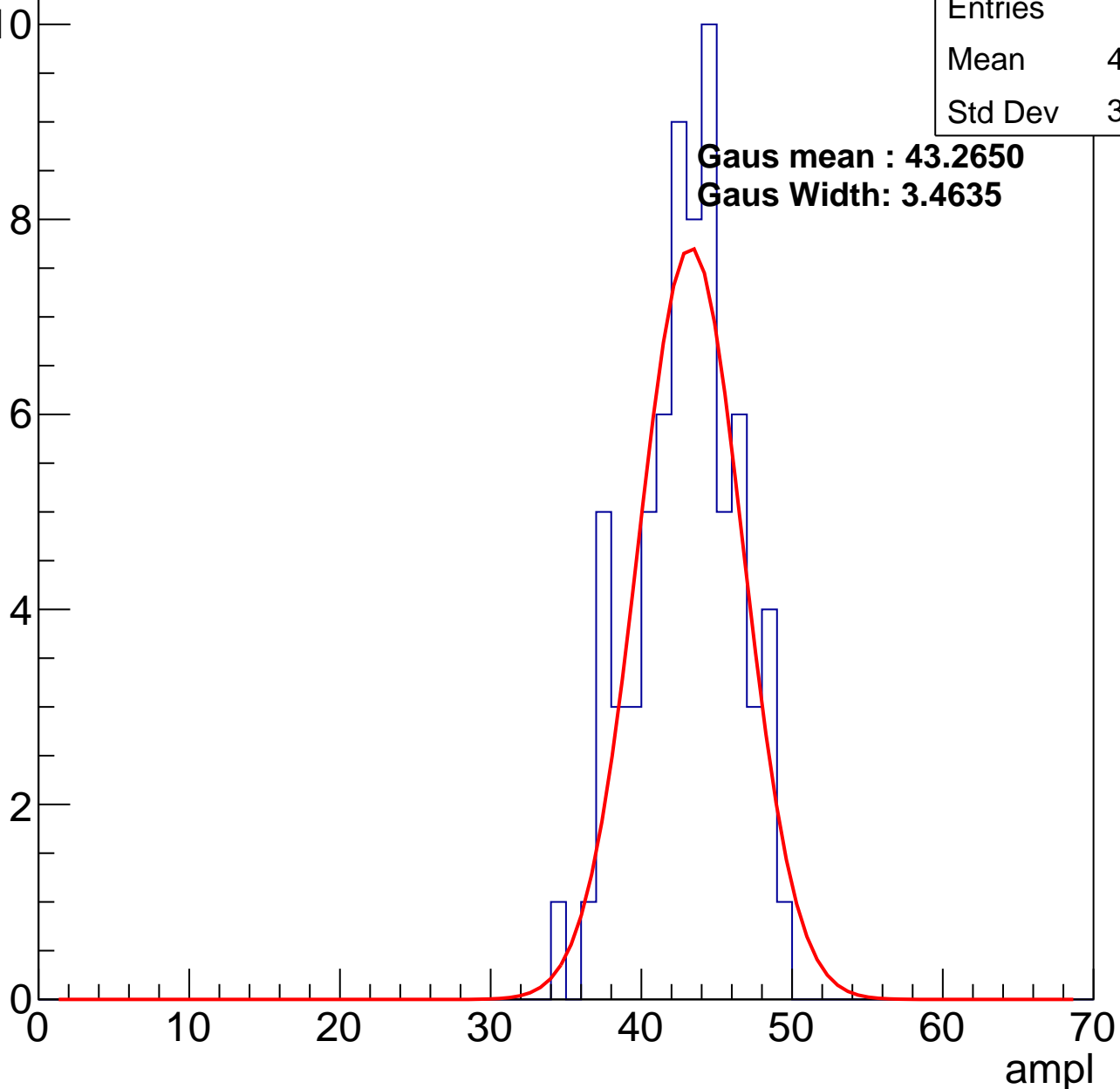
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.53
Std Dev	3.302

Gaus mean : 43.2650

Gaus Width: 3.4635

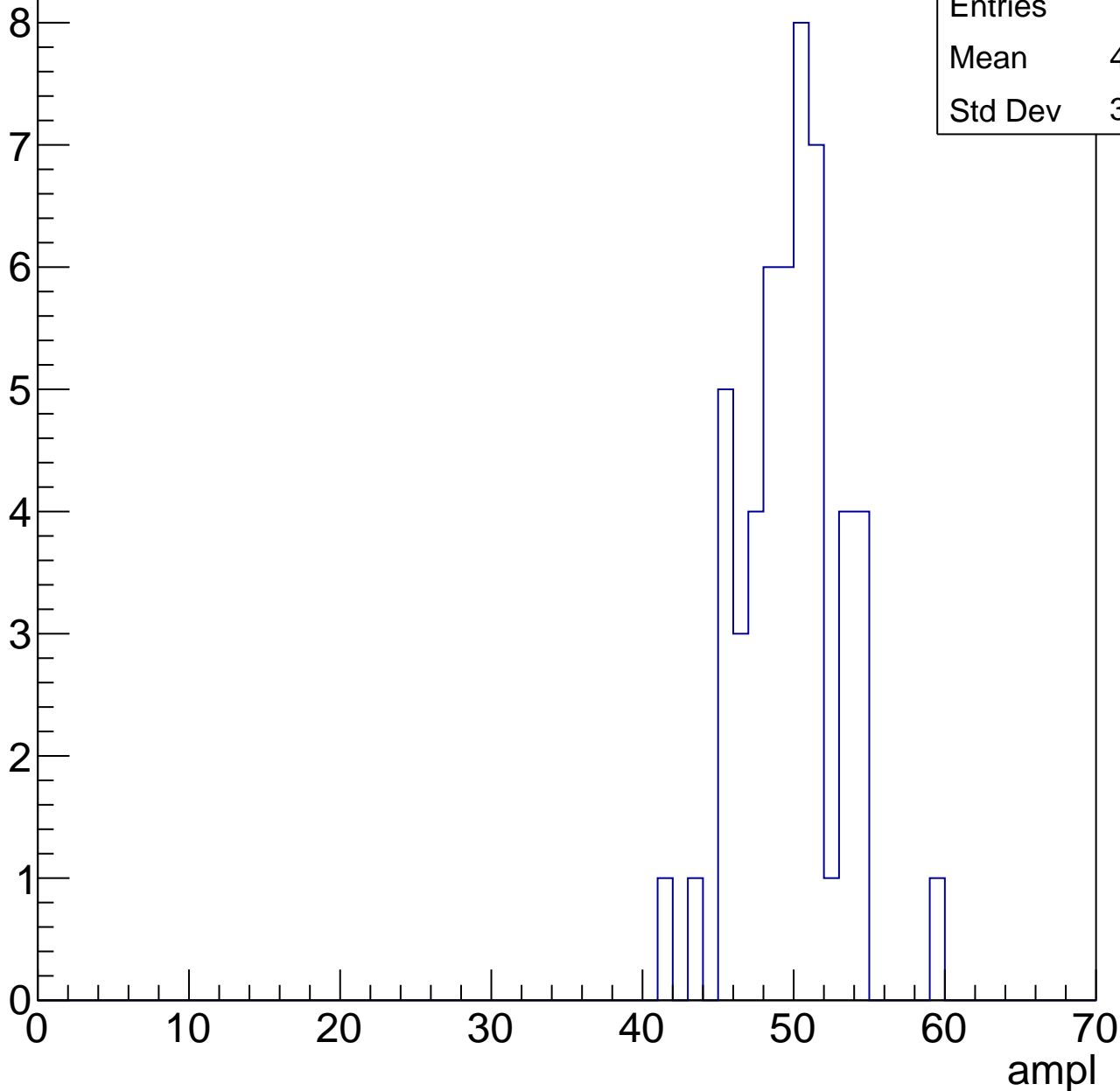


B1L102S, U18-ch15, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	49.27
Std Dev	3.236

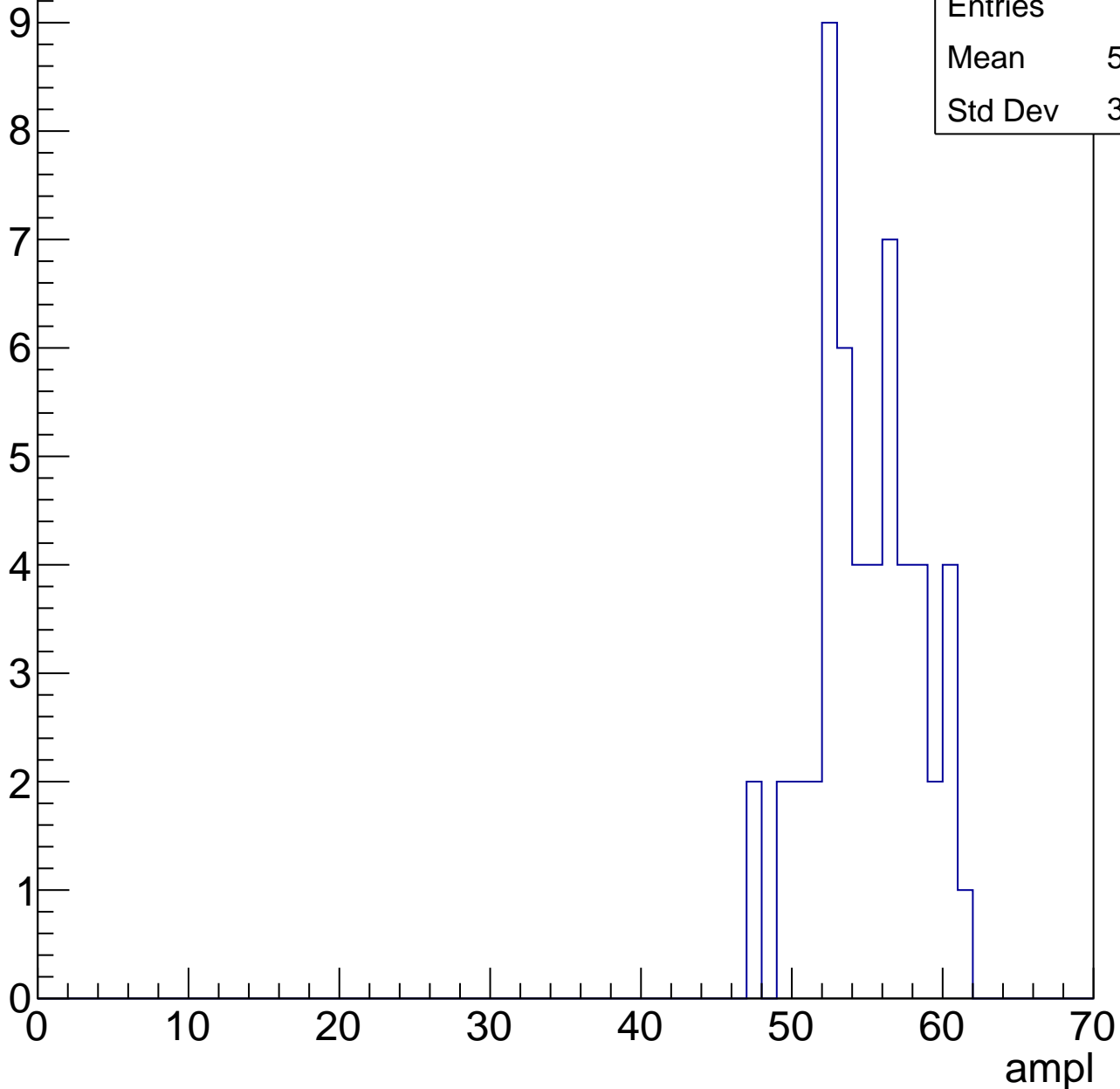


B1L102S, U18-ch15, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.47
Std Dev	3.374

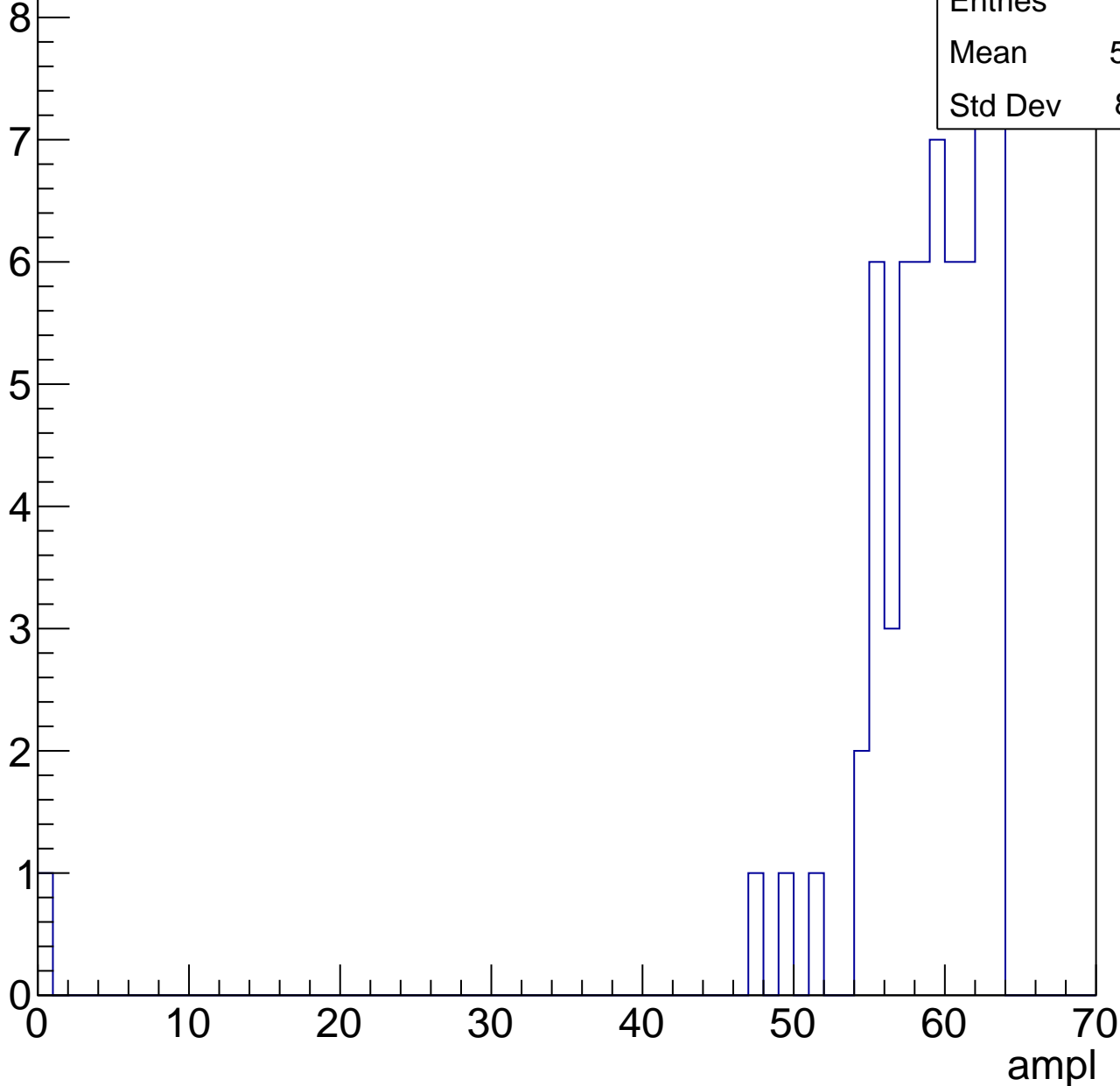


B1L102S, U18-ch15, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.77
Std Dev	8.161



B1L102S, U18-ch15, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch15, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch16, adc0

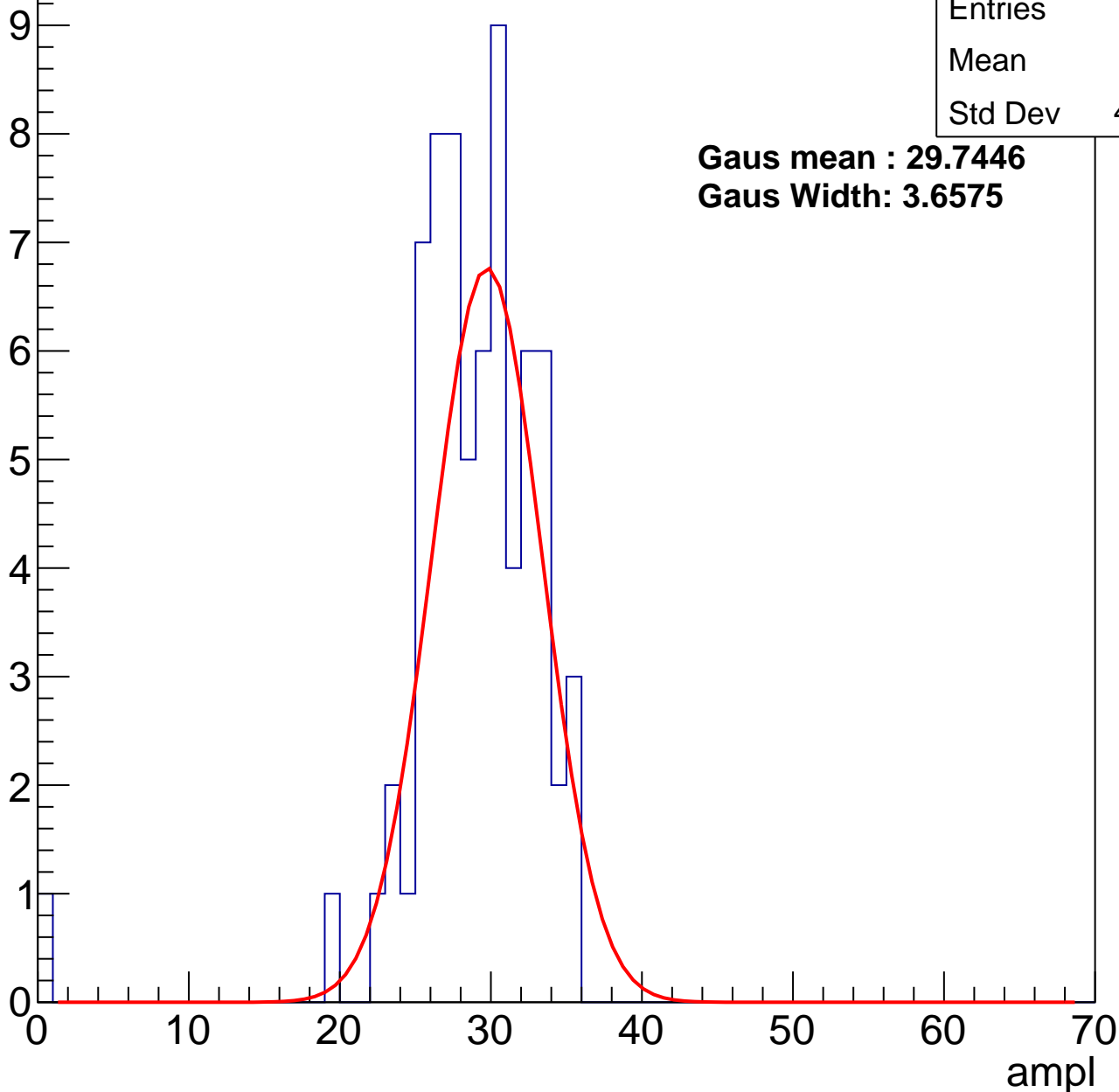
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	28.3
Std Dev	4.791

Gaus mean : 29.7446

Gaus Width: 3.6575



B1L102S, U18-ch16, adc1

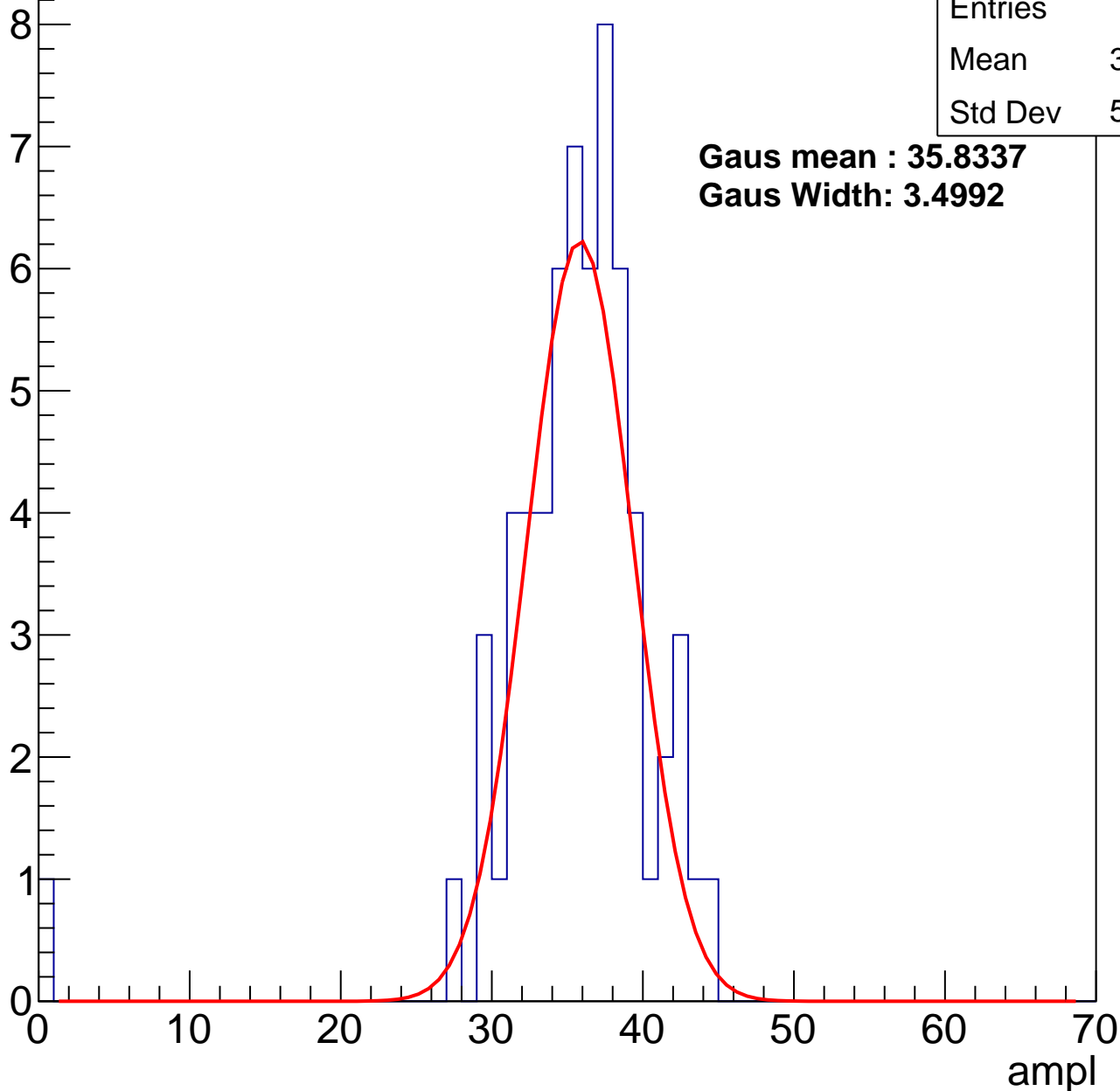
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.05
Std Dev	5.758

Gaus mean : 35.8337

Gaus Width: 3.4992



B1L102S, U18-ch16, adc2

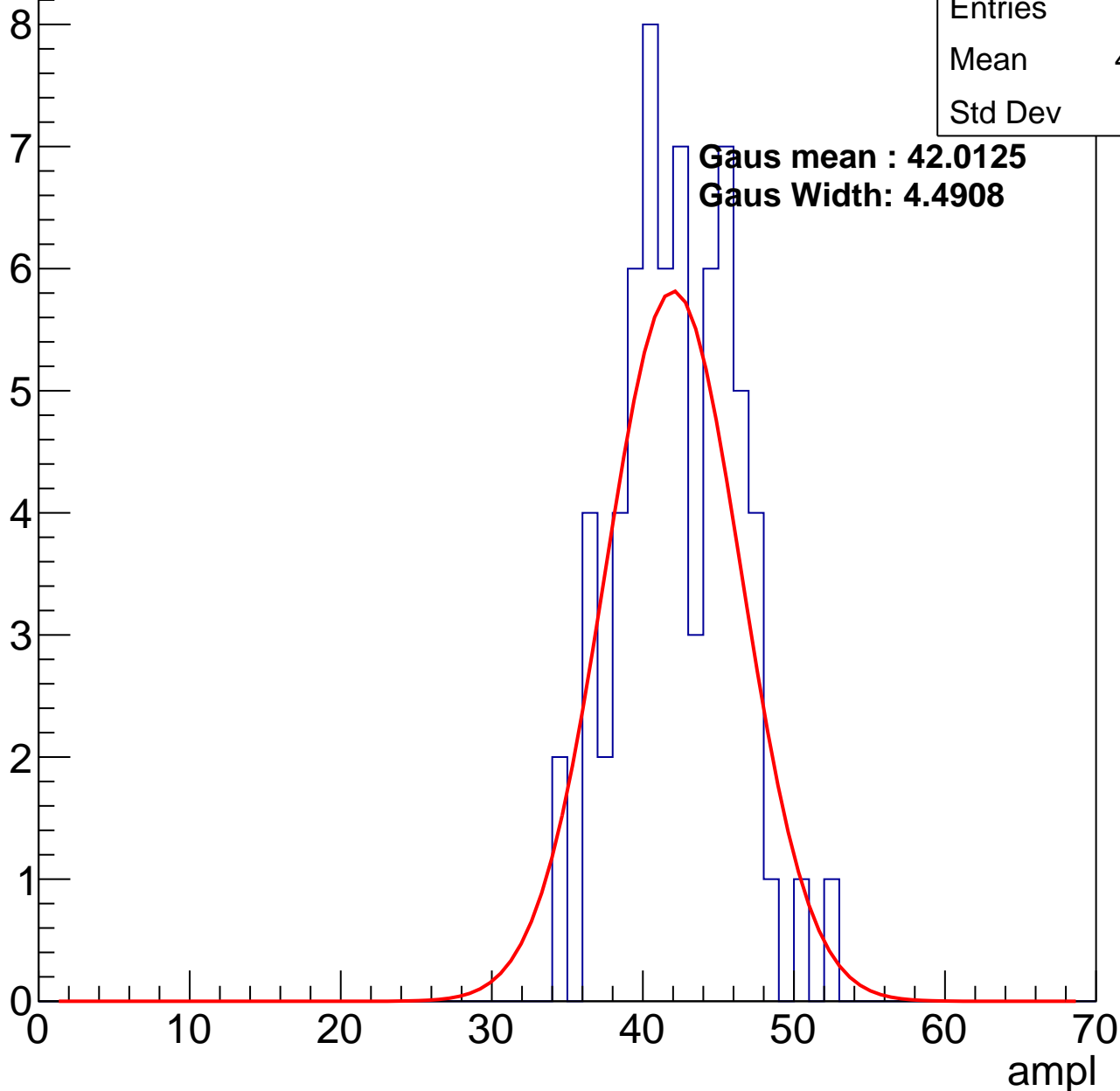
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.91
Std Dev	3.76

Gaus mean : 42.0125

Gaus Width: 4.4908

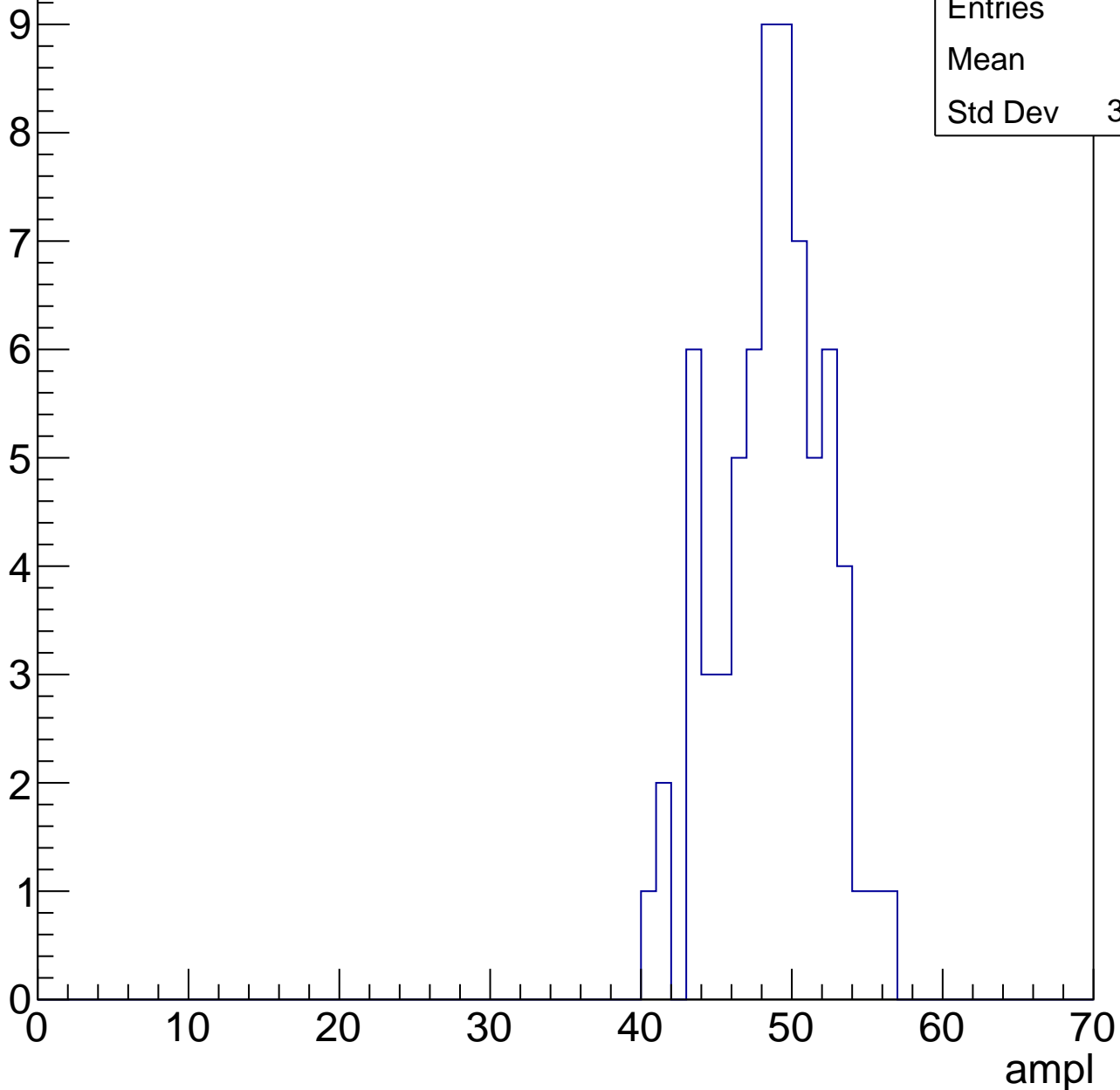


B1L102S, U18-ch16, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	48.2
Std Dev	3.483

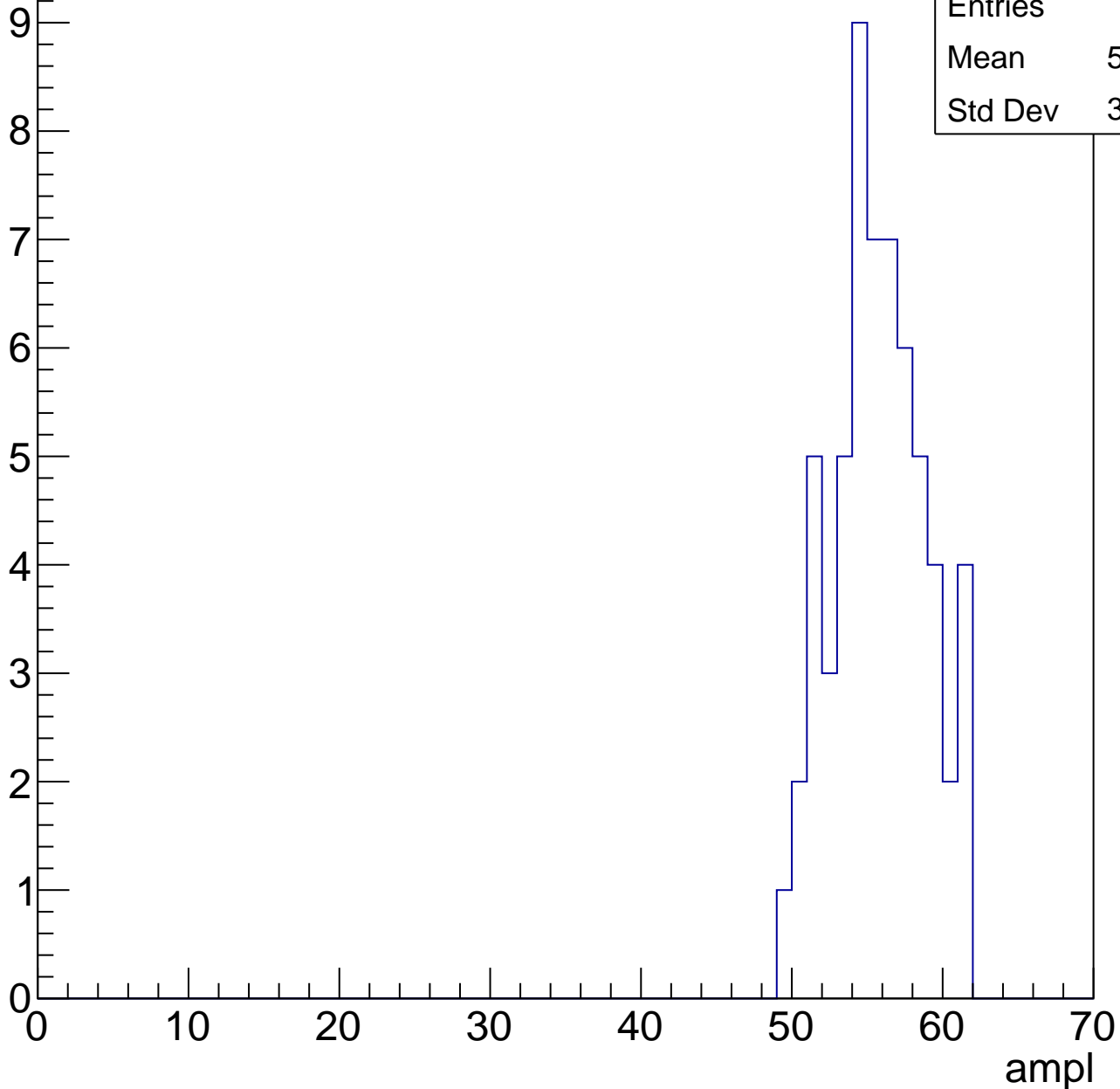


B1L102S, U18-ch16, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.33
Std Dev	3.026



B1L102S, U18-ch16, adc5

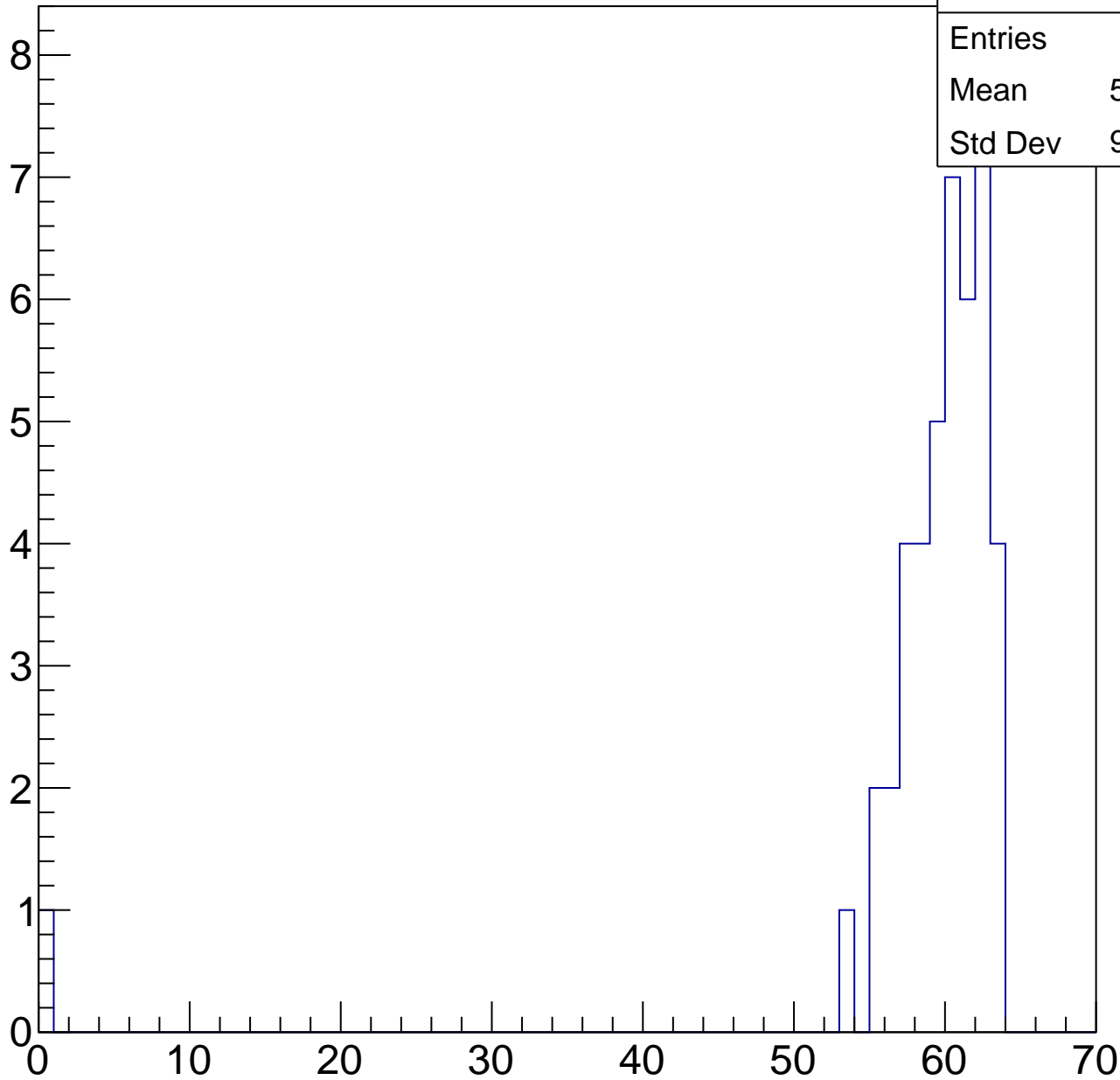
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	44
Mean	58.27
Std Dev	9.208

ampl

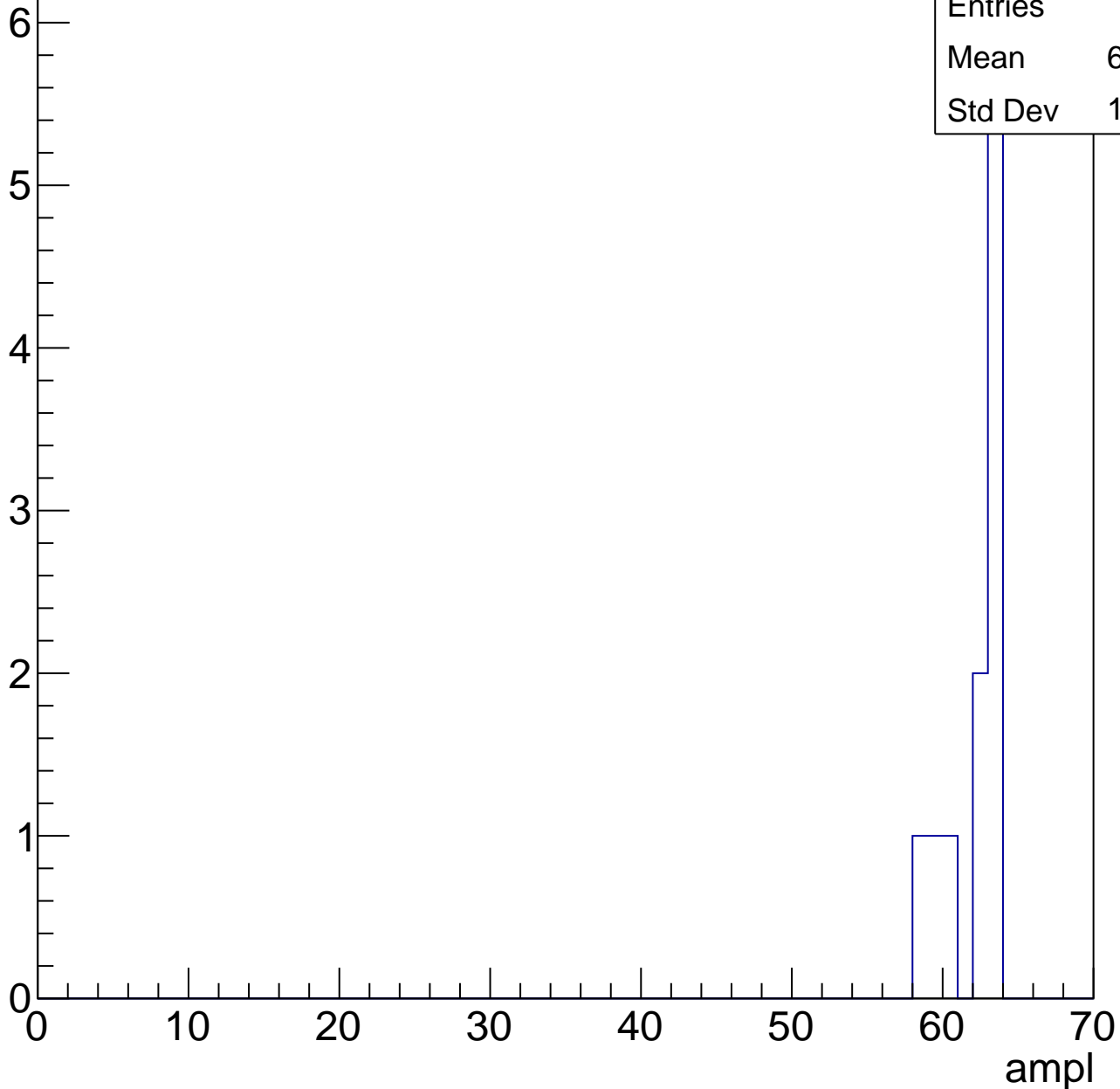


B1L102S, U18-ch16, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.73
Std Dev	1.763



B1L102S, U18-ch16, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch17, adc0

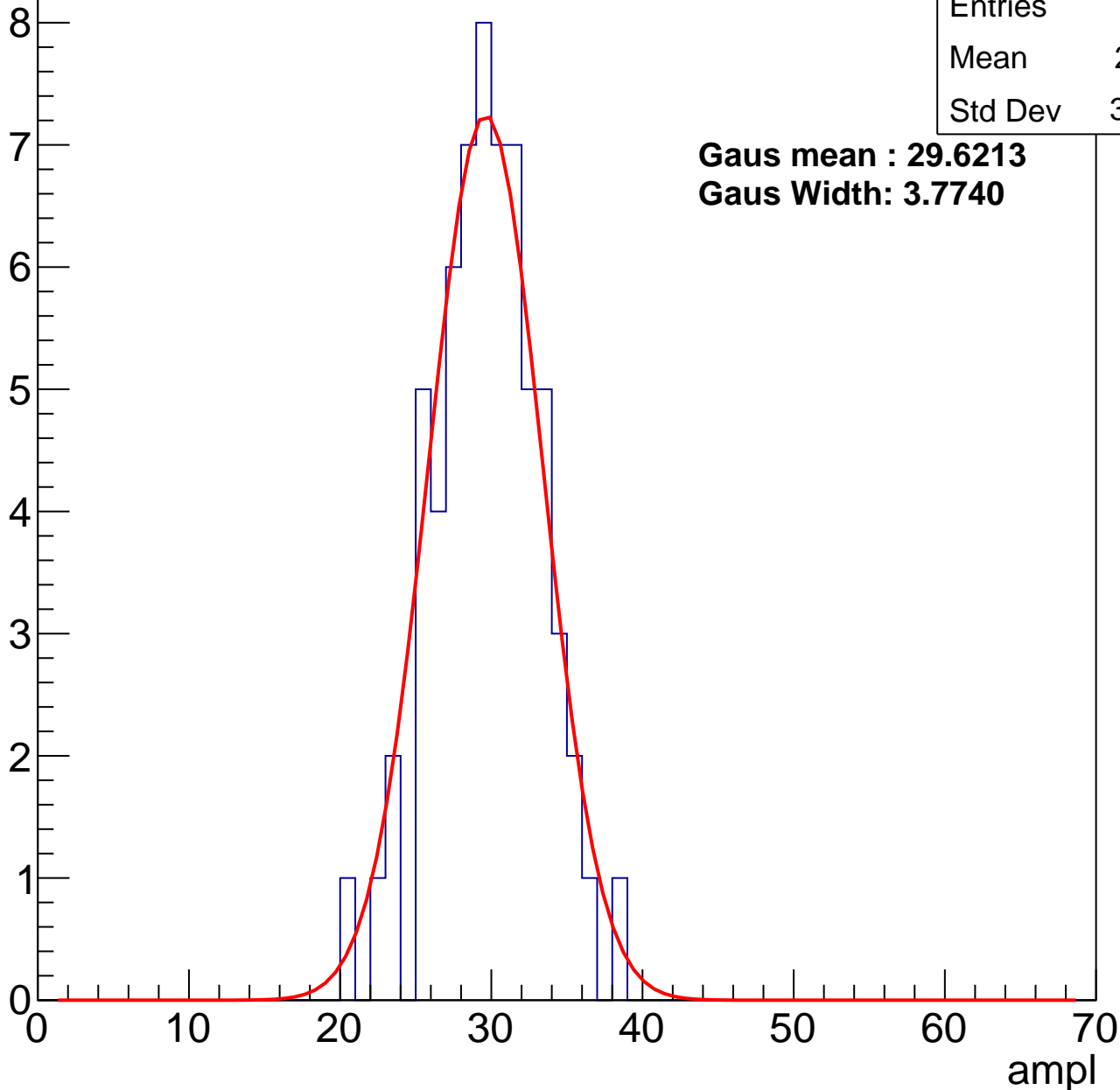
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.31
Std Dev	3.464

Gaus mean : 29.6213

Gaus Width: 3.7740



B1L102S, U18-ch17, adc1

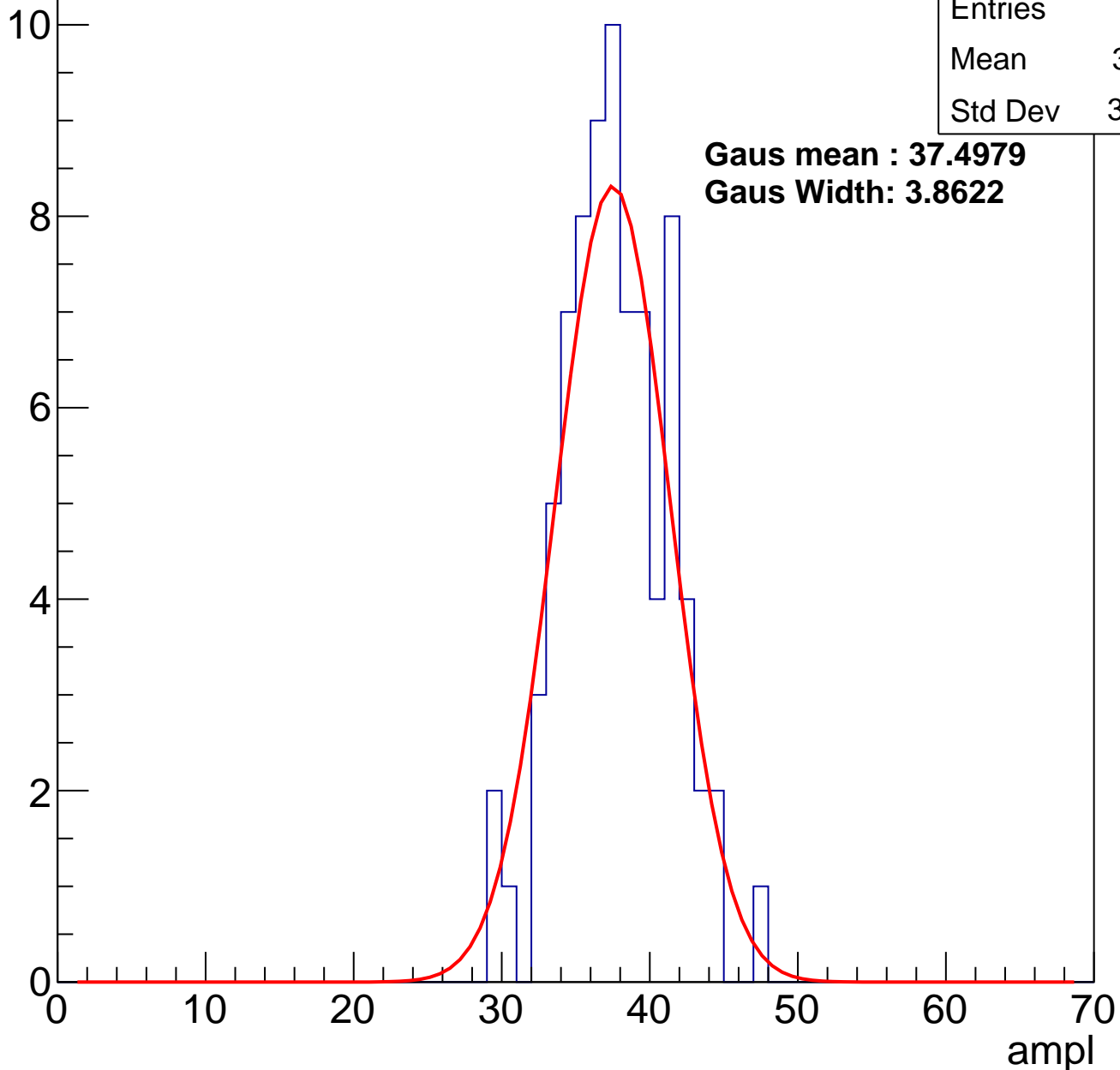
calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	37.21
Std Dev	3.538

Gaus mean : 37.4979

Gaus Width: 3.8622

Entry



B1L102S, U18-ch17, adc2

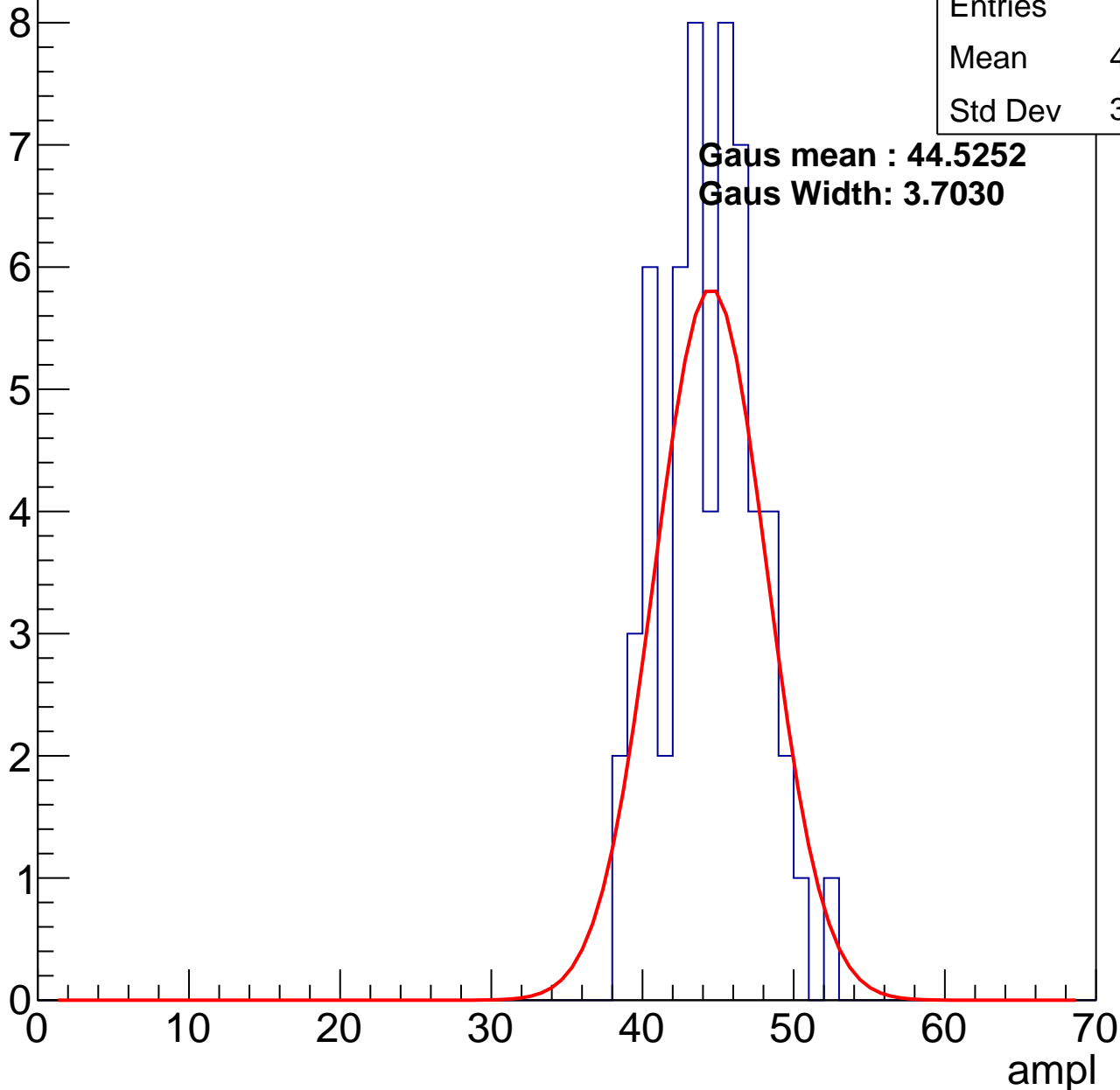
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	43.95
Std Dev	3.159

Gaus mean : 44.5252

Gaus Width: 3.7030

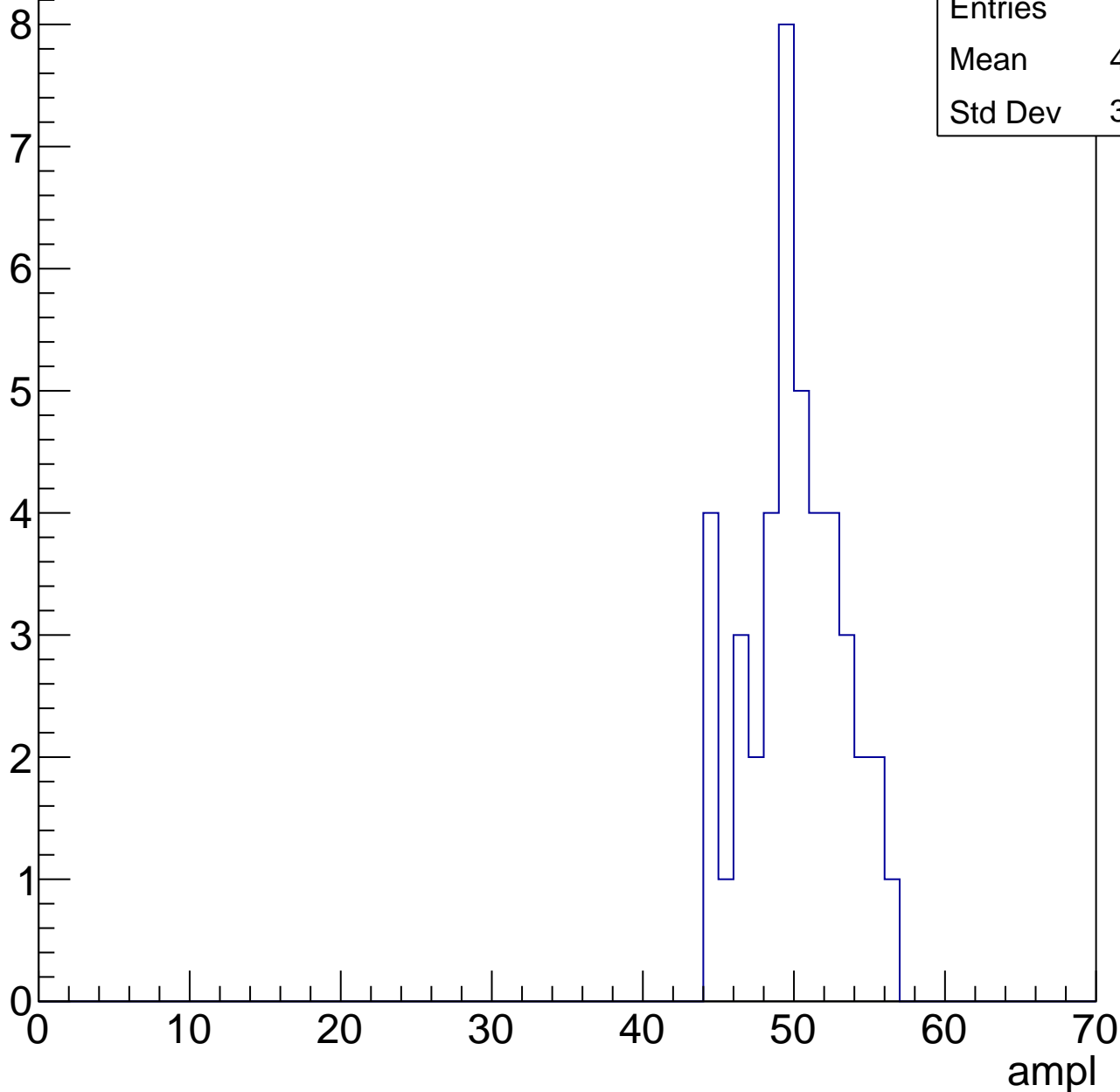


B1L102S, U18-ch17, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	49.58
Std Dev	3.112

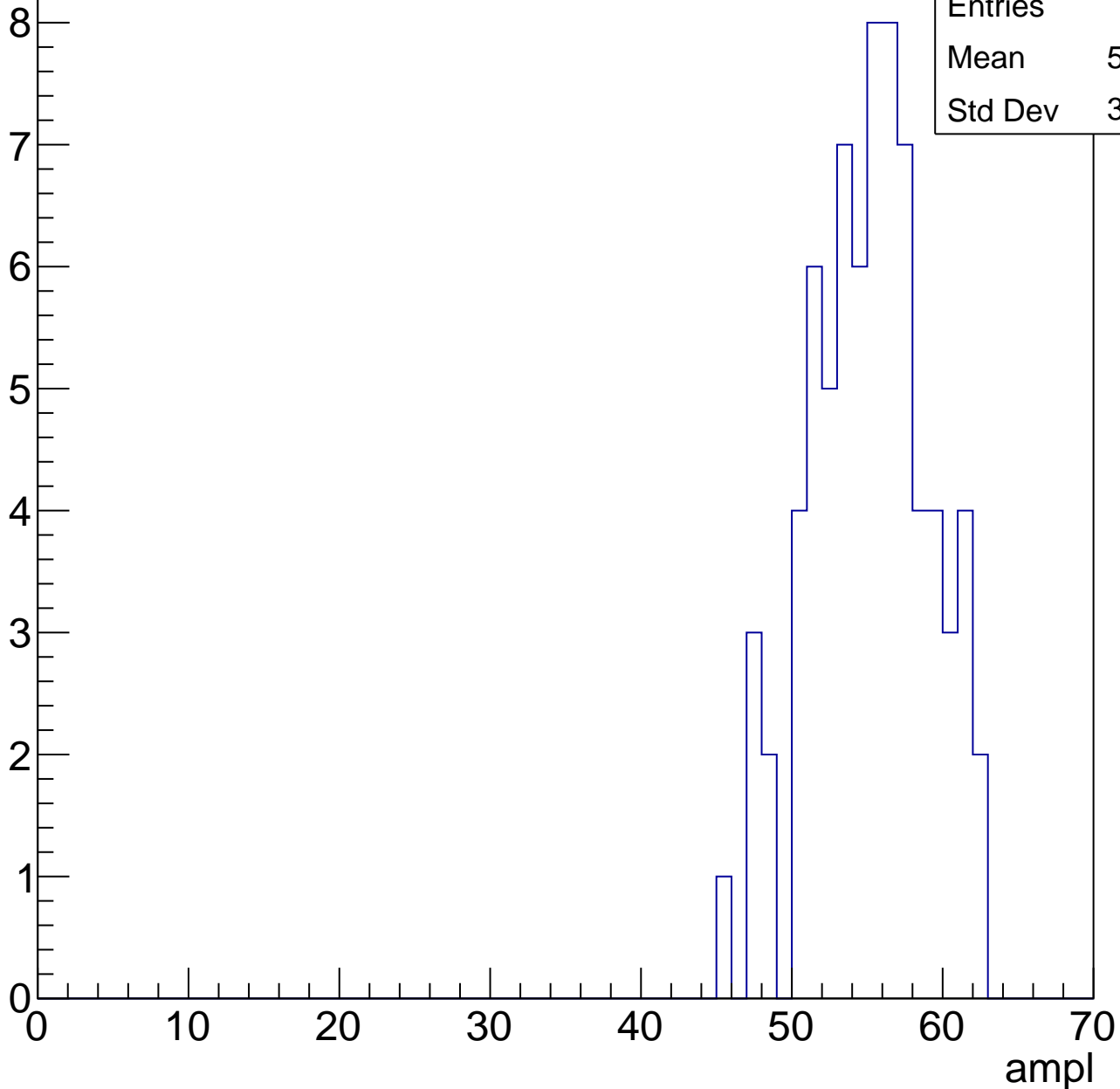


B1L102S, U18-ch17, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	54.68
Std Dev	3.873

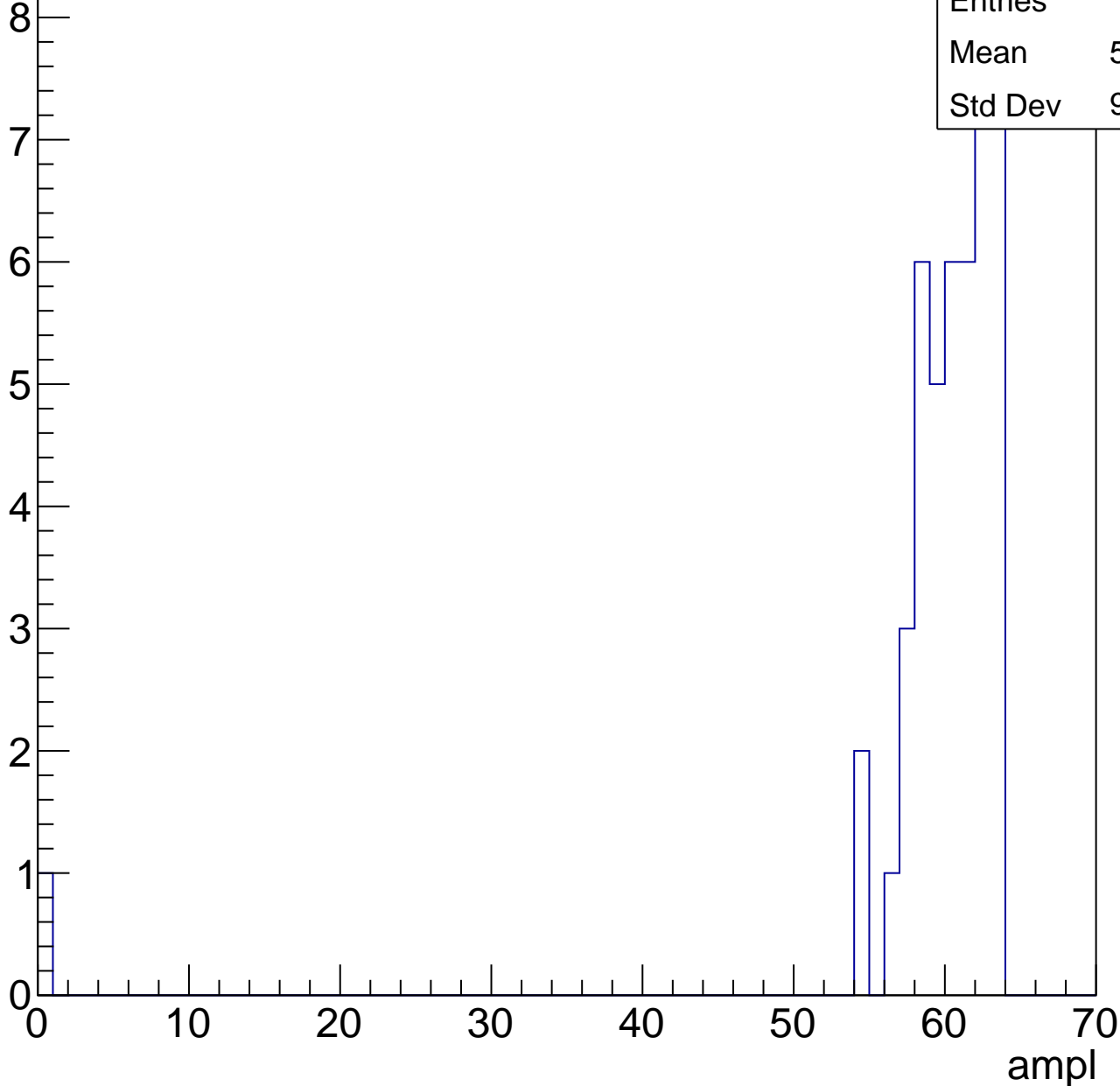


B1L102S, U18-ch17, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

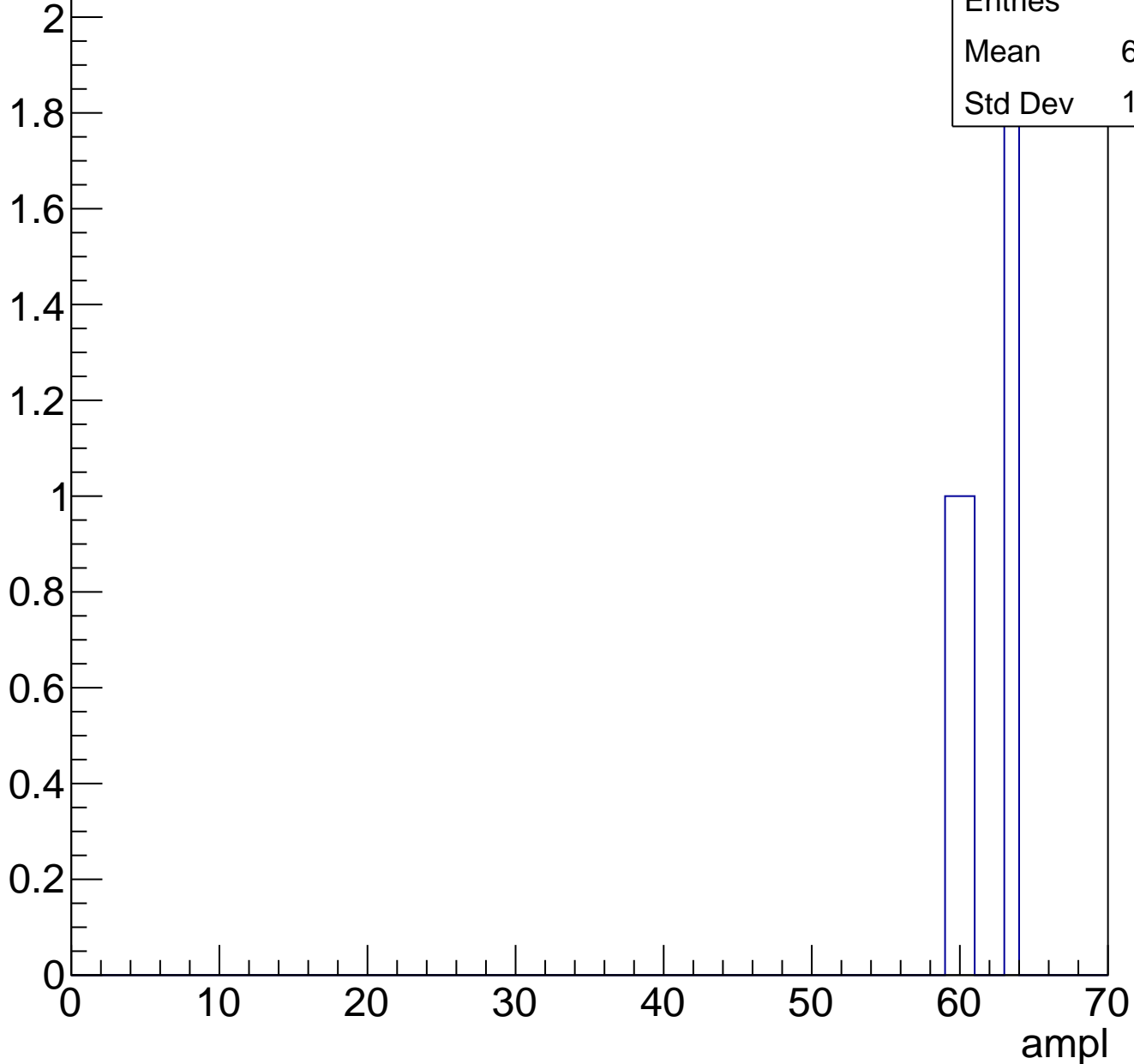
Entries	46
Mean	58.78
Std Dev	9.072



B1L102S, U18-ch17, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch17, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch18, adc0

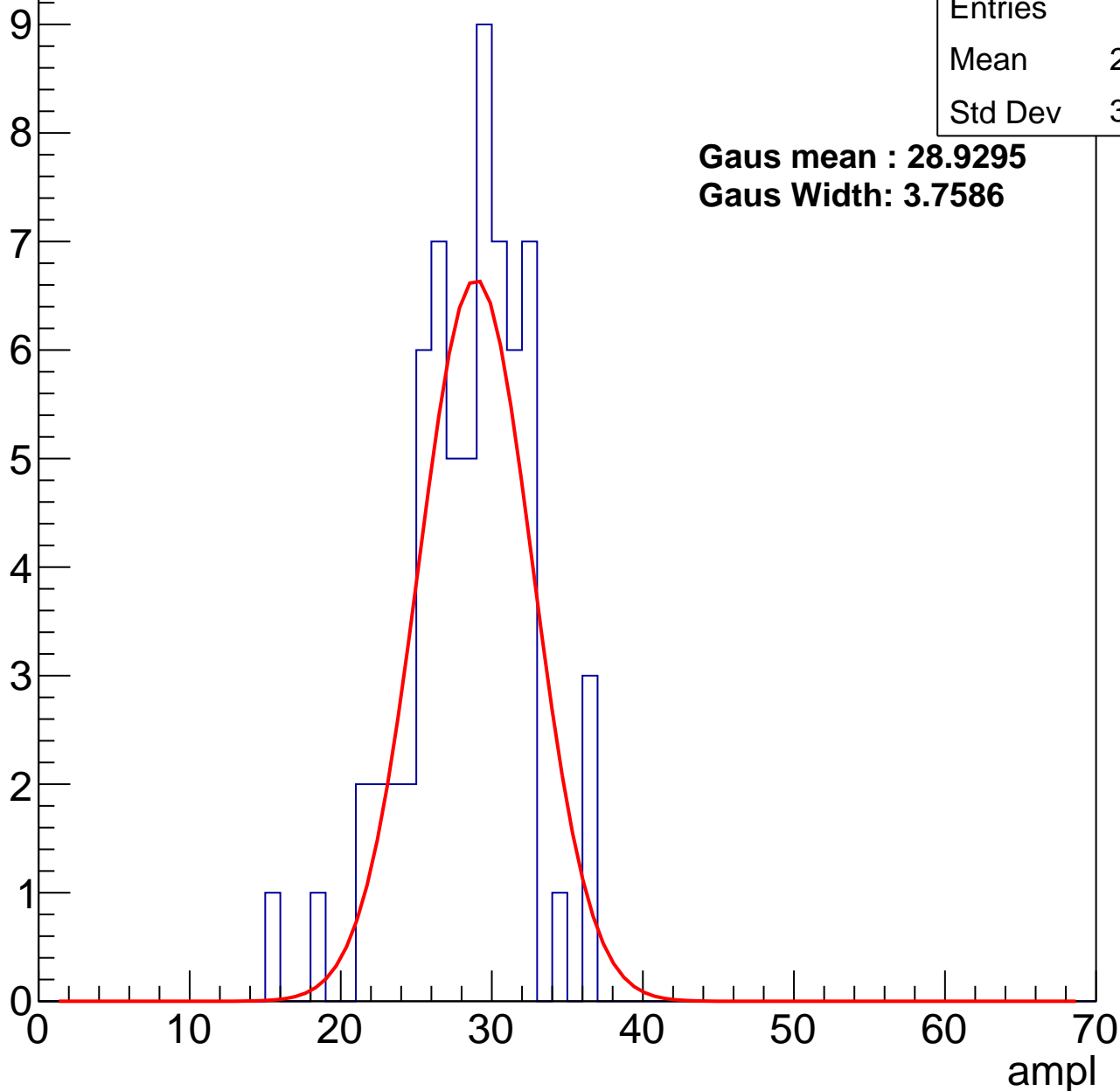
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	27.92
Std Dev	3.967

Gaus mean : 28.9295

Gaus Width: 3.7586



B1L102S, U18-ch18, adc1

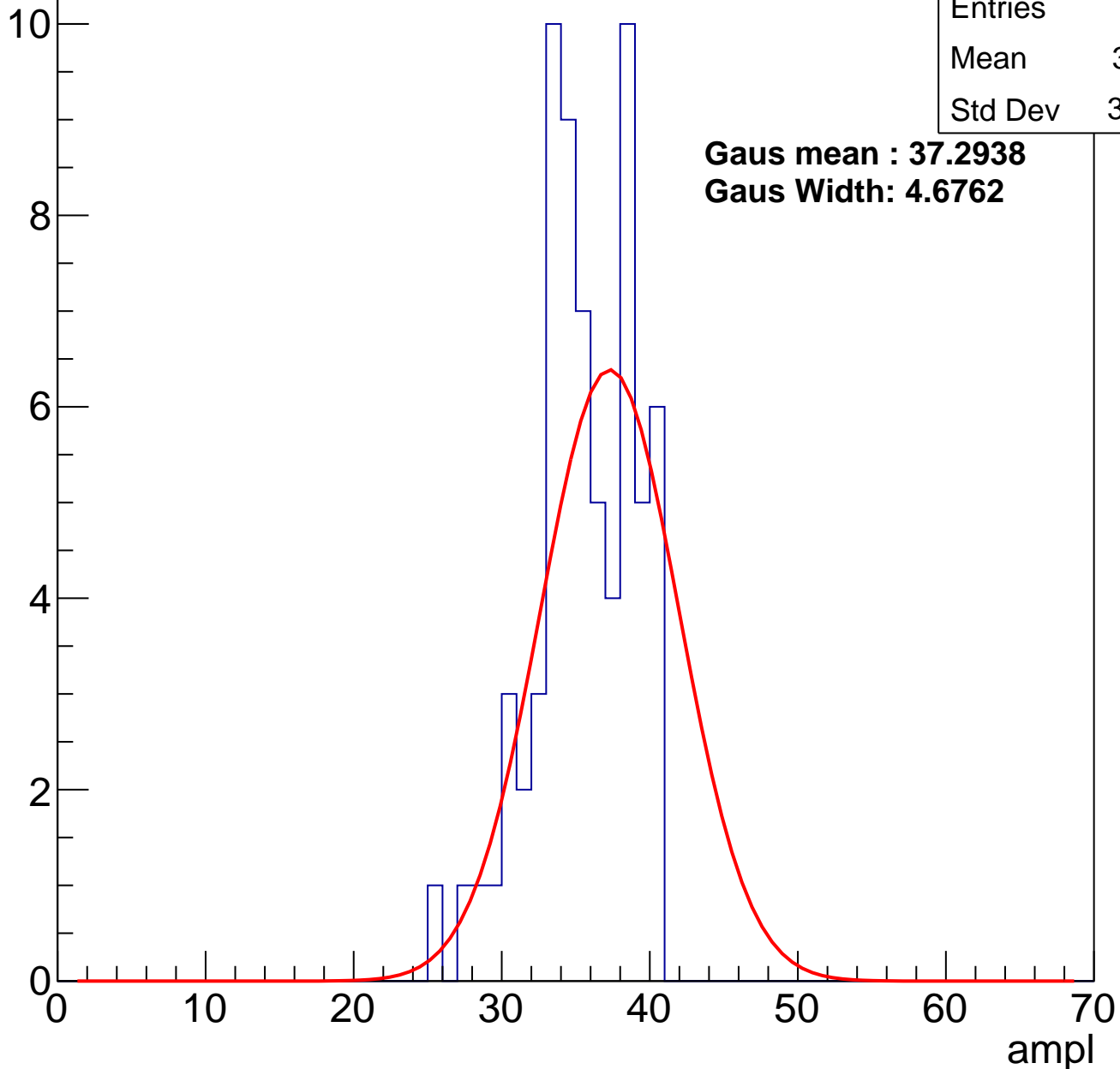
calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	35.01
Std Dev	3.372

Gaus mean : 37.2938

Gaus Width: 4.6762

Entry



B1L102S, U18-ch18, adc2

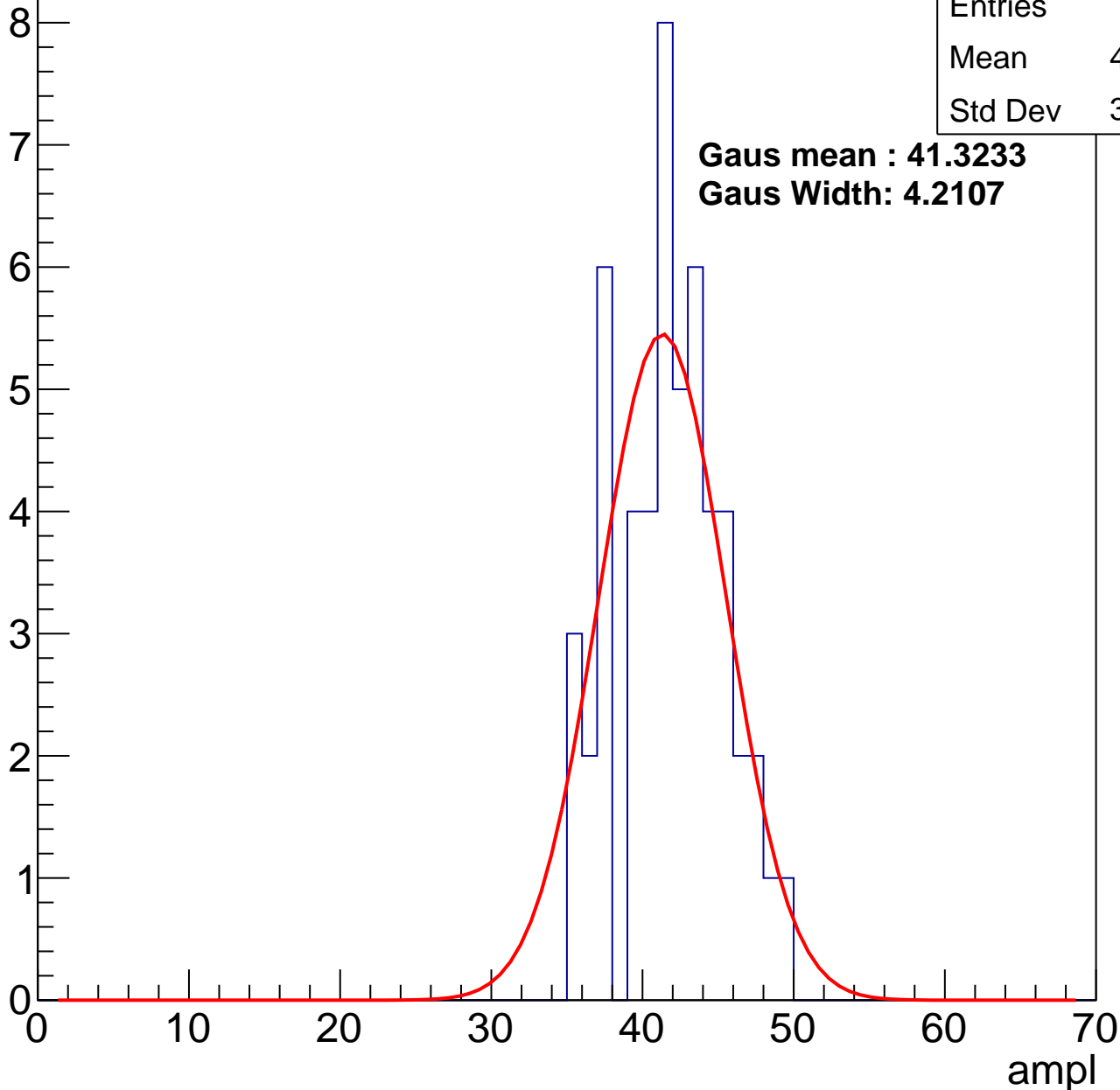
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	41.35
Std Dev	3.486

Gaus mean : 41.3233

Gaus Width: 4.2107

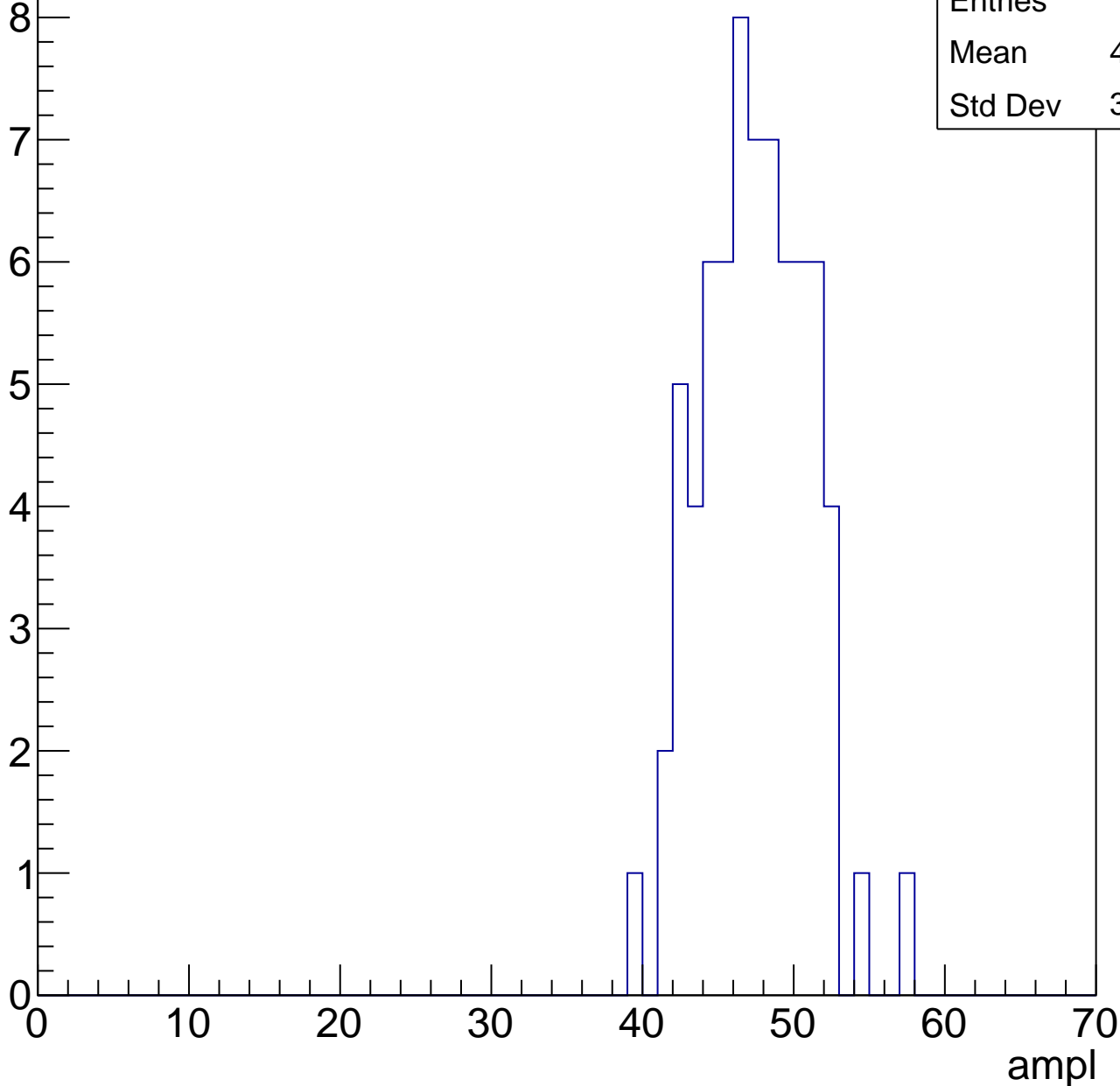


B1L102S, U18-ch18, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	46.99
Std Dev	3.466

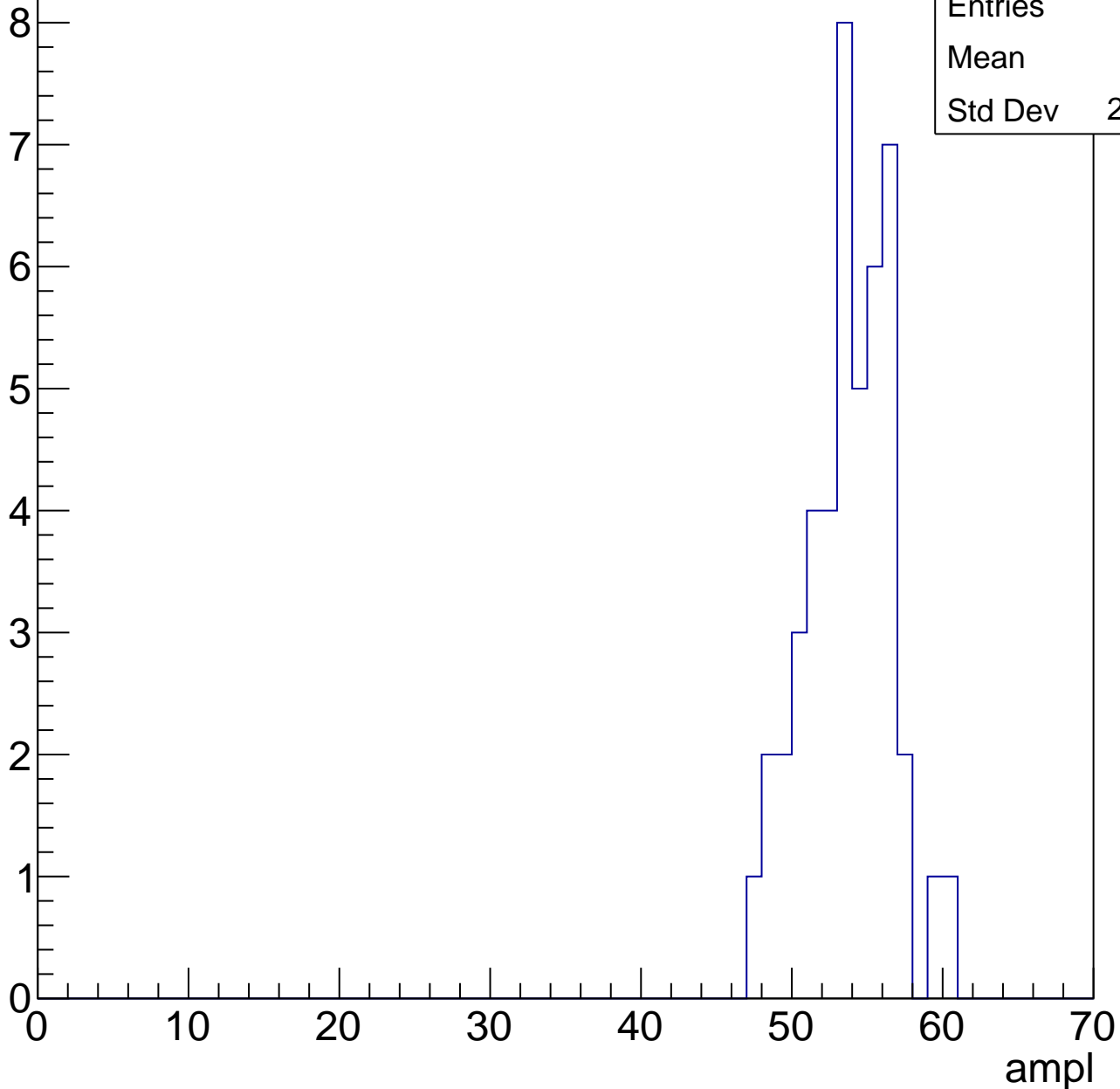


B1L102S, U18-ch18, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.3
Std Dev	2.835

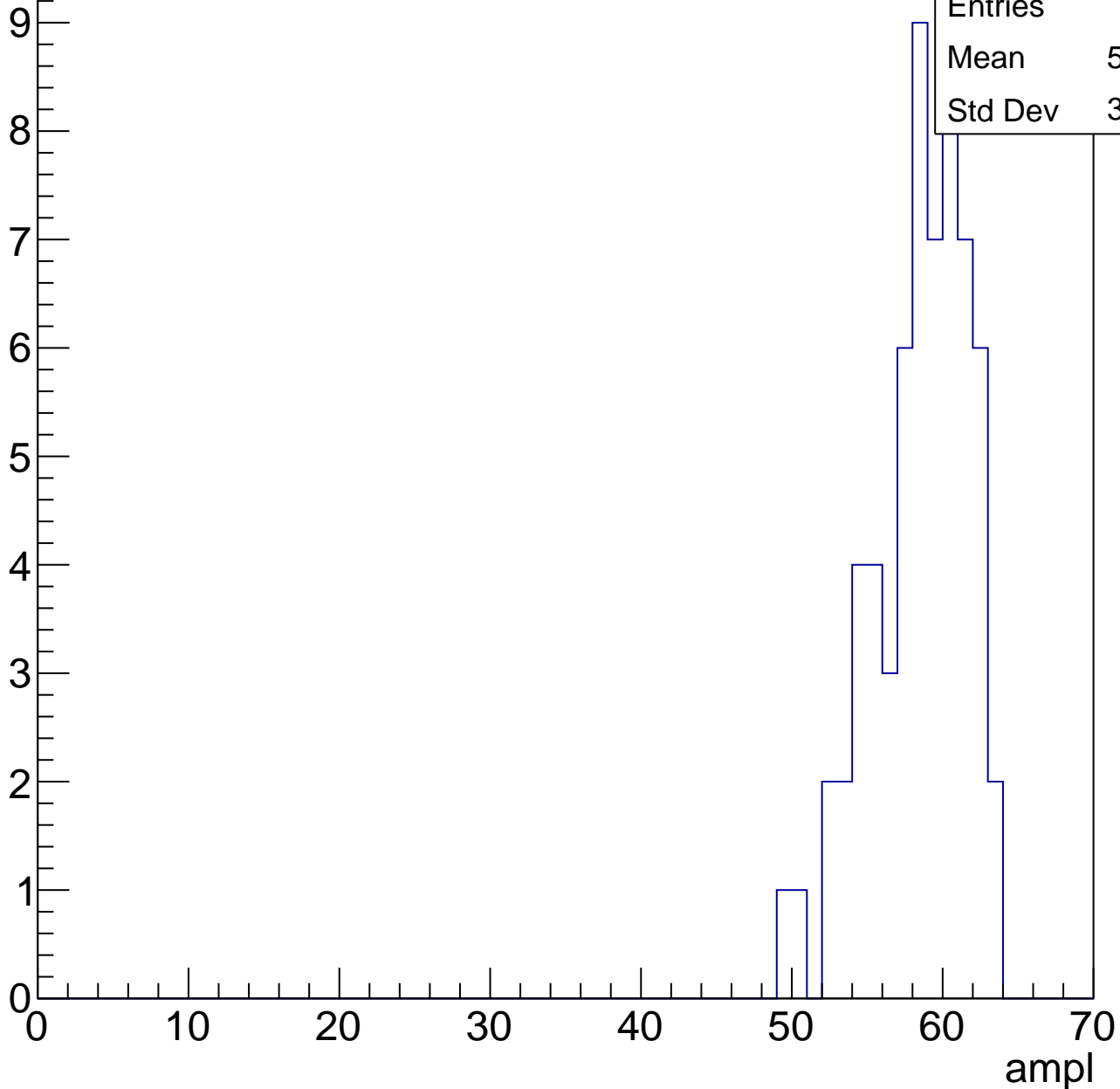


B1L102S, U18-ch18, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	57.98
Std Dev	3.195

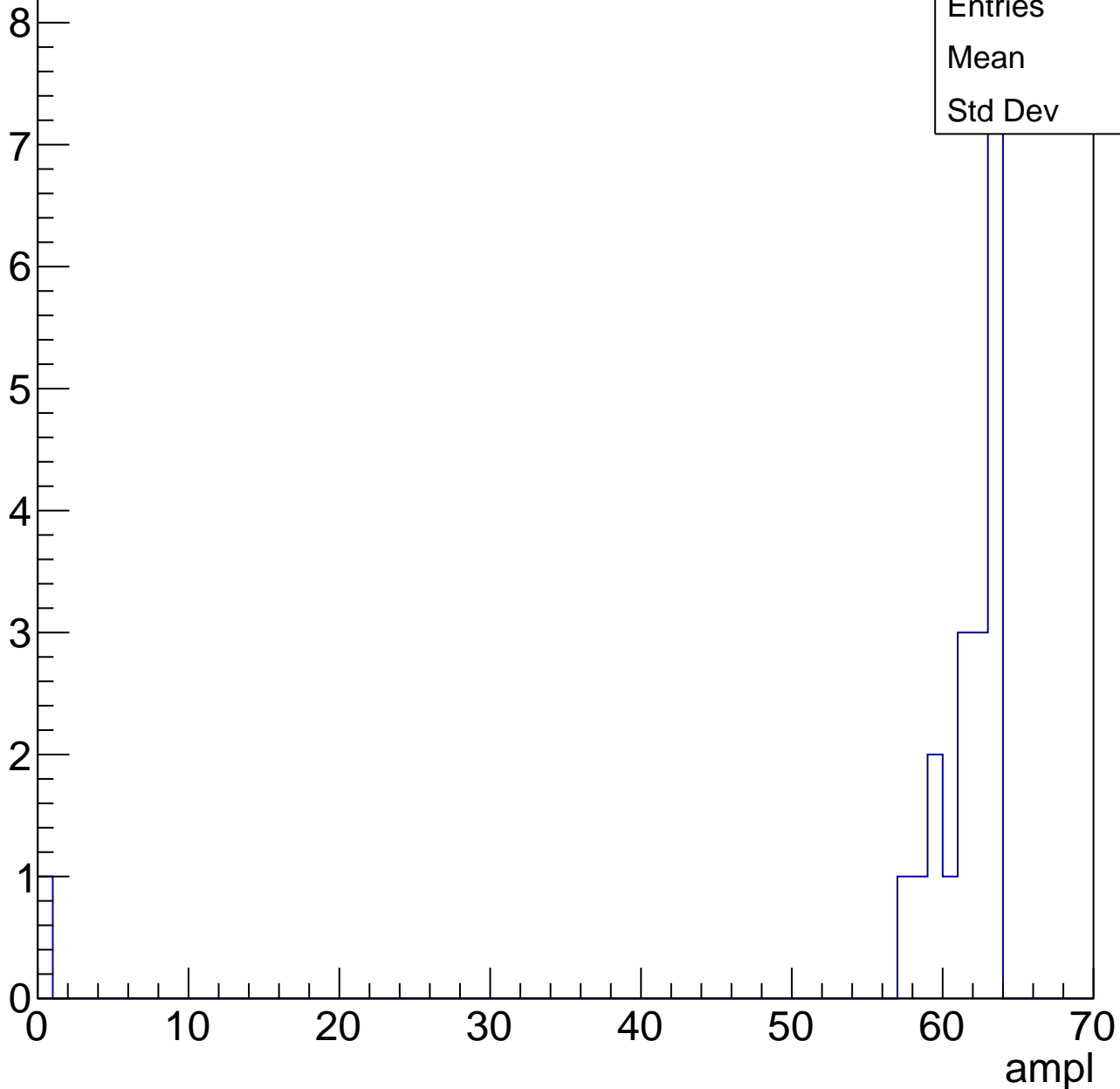


B1L102S, U18-ch18, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

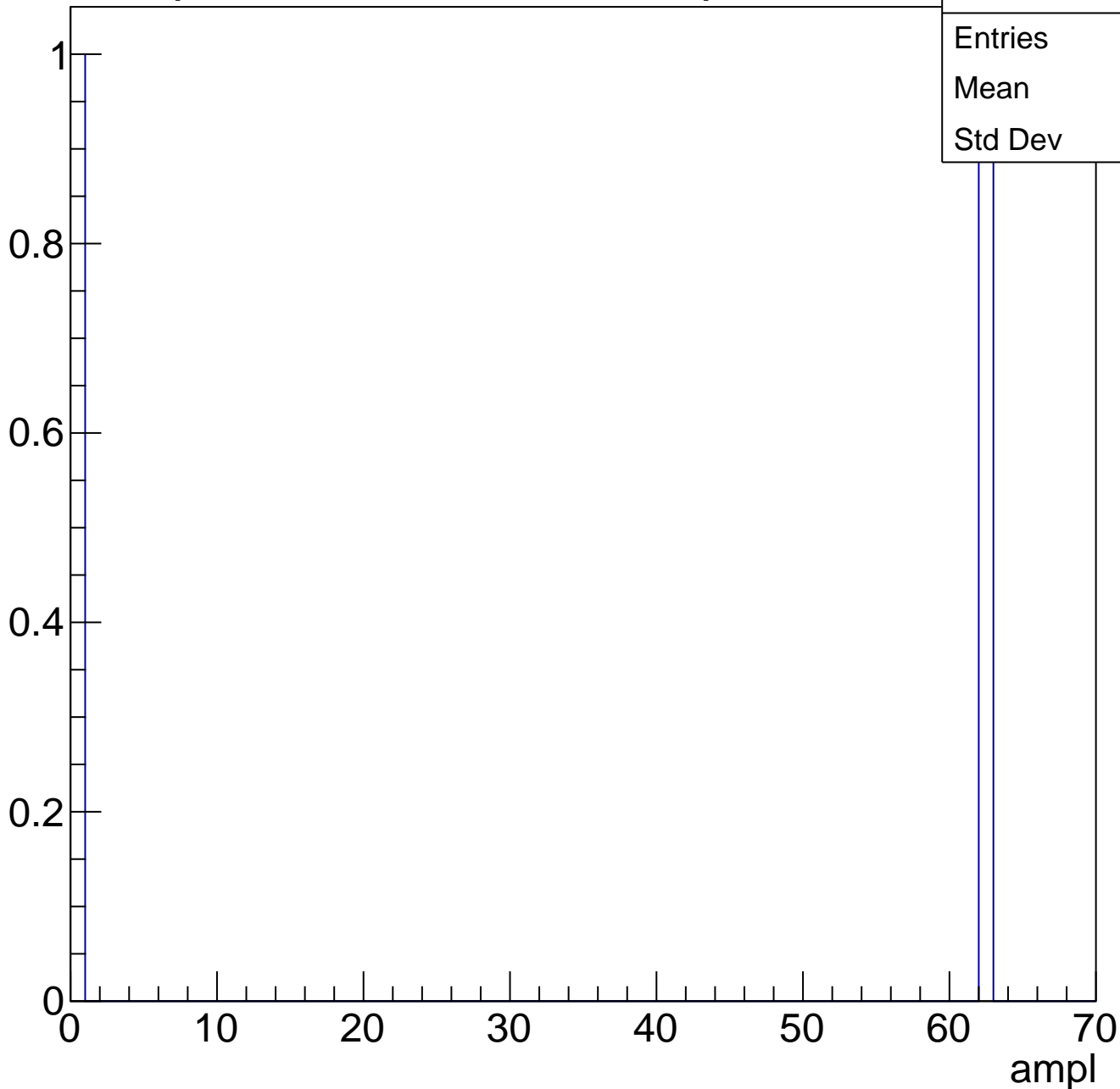
Entries	20
Mean	58.3
Std Dev	13.5



B1L102S, U18-ch18, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch19, adc0

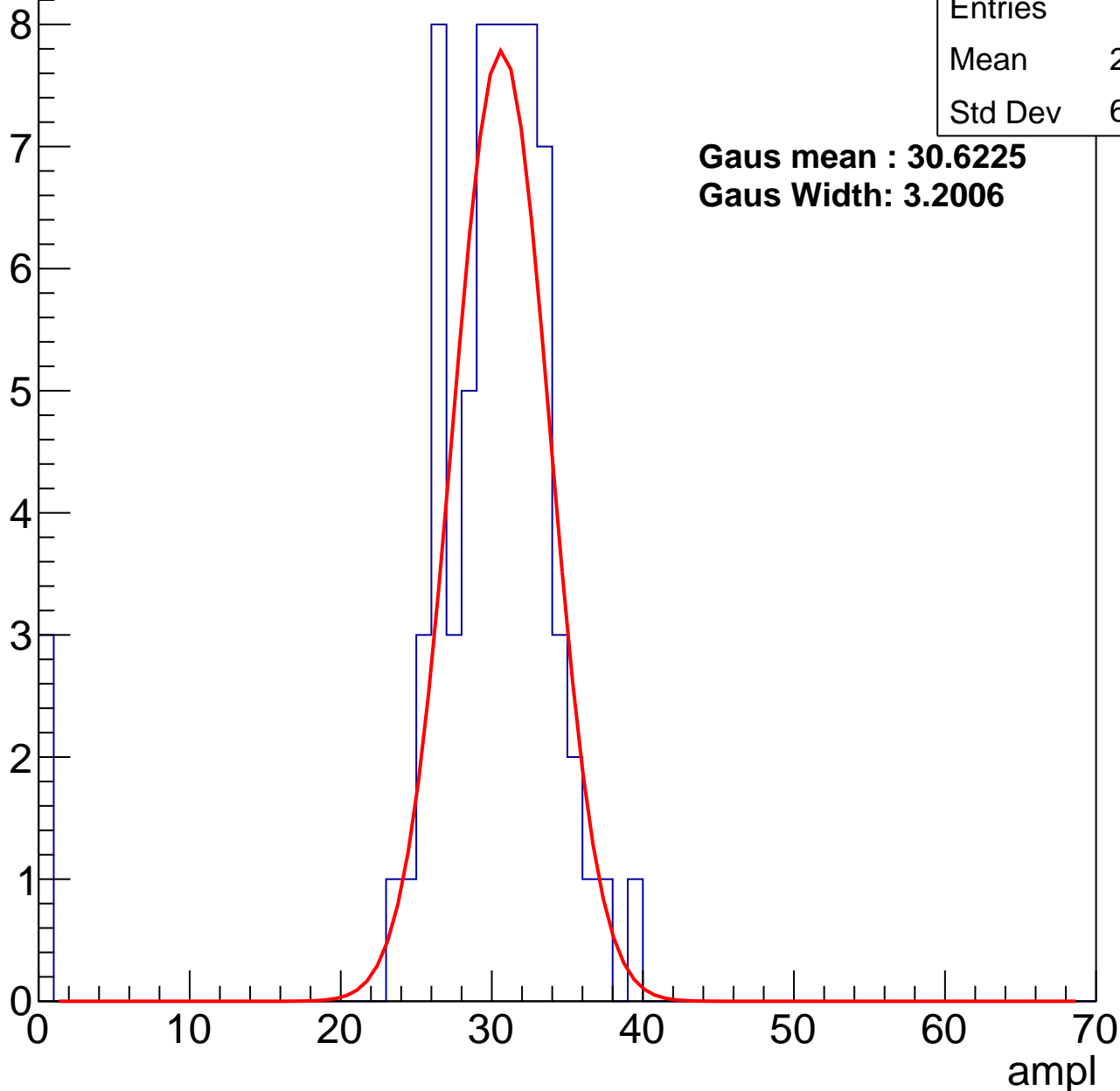
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	28.76
Std Dev	6.819

Gaus mean : 30.6225

Gaus Width: 3.2006



B1L102S, U18-ch19, adc1

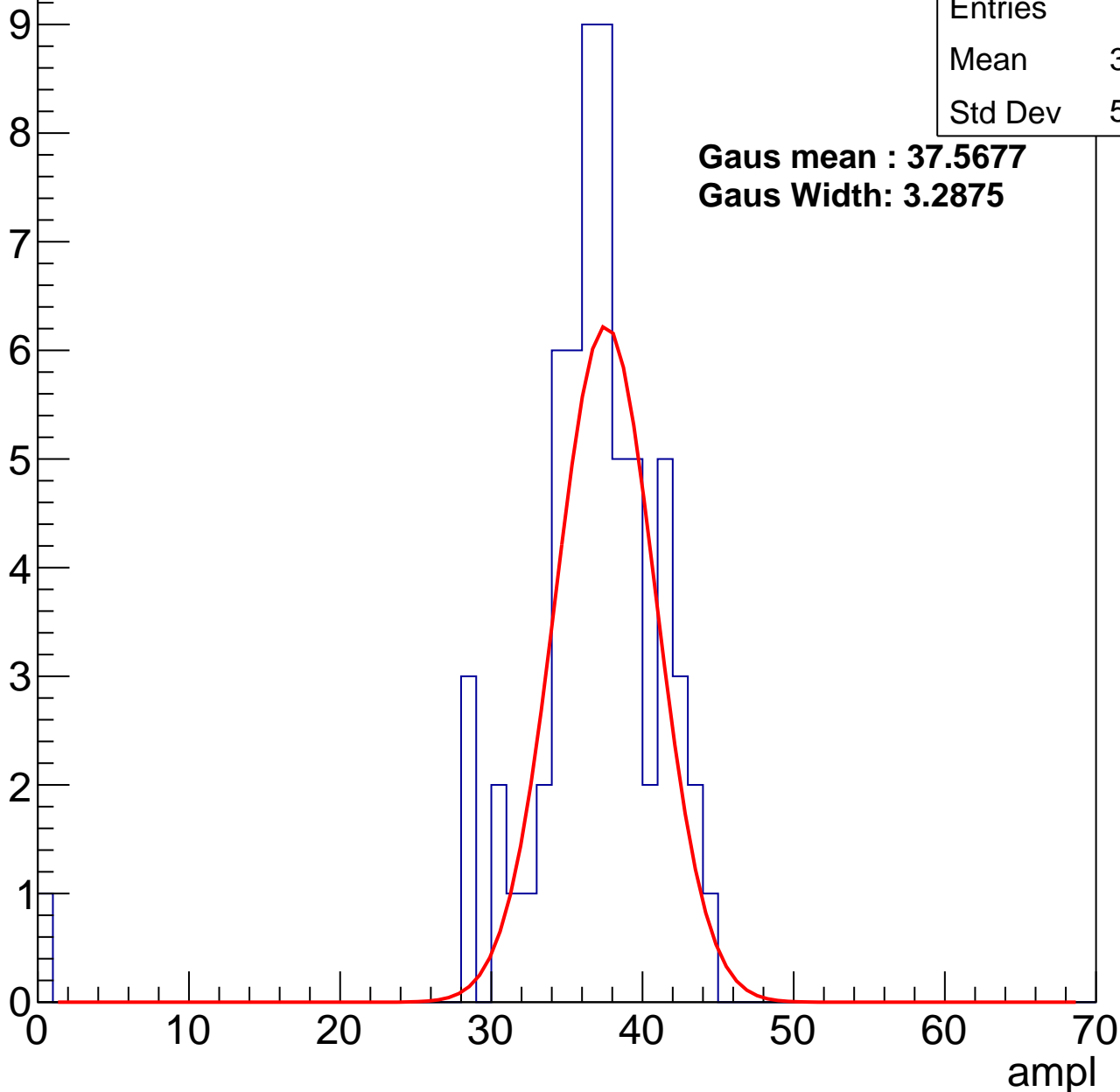
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	36.03
Std Dev	5.842

Gaus mean : 37.5677

Gaus Width: 3.2875



B1L102S, U18-ch19, adc2

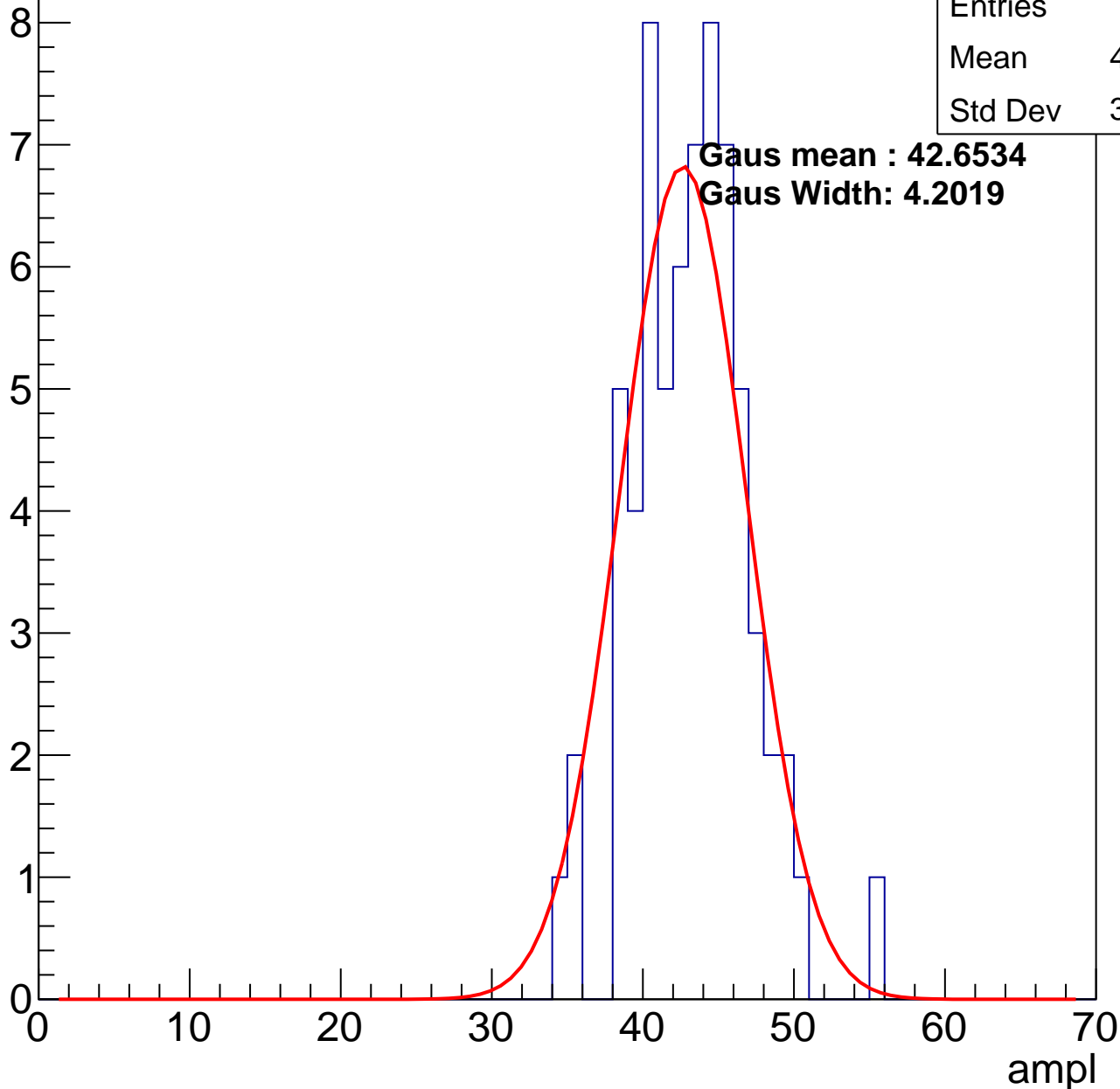
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	42.76
Std Dev	3.738

Gaus mean : 42.6534

Gaus Width: 4.2019

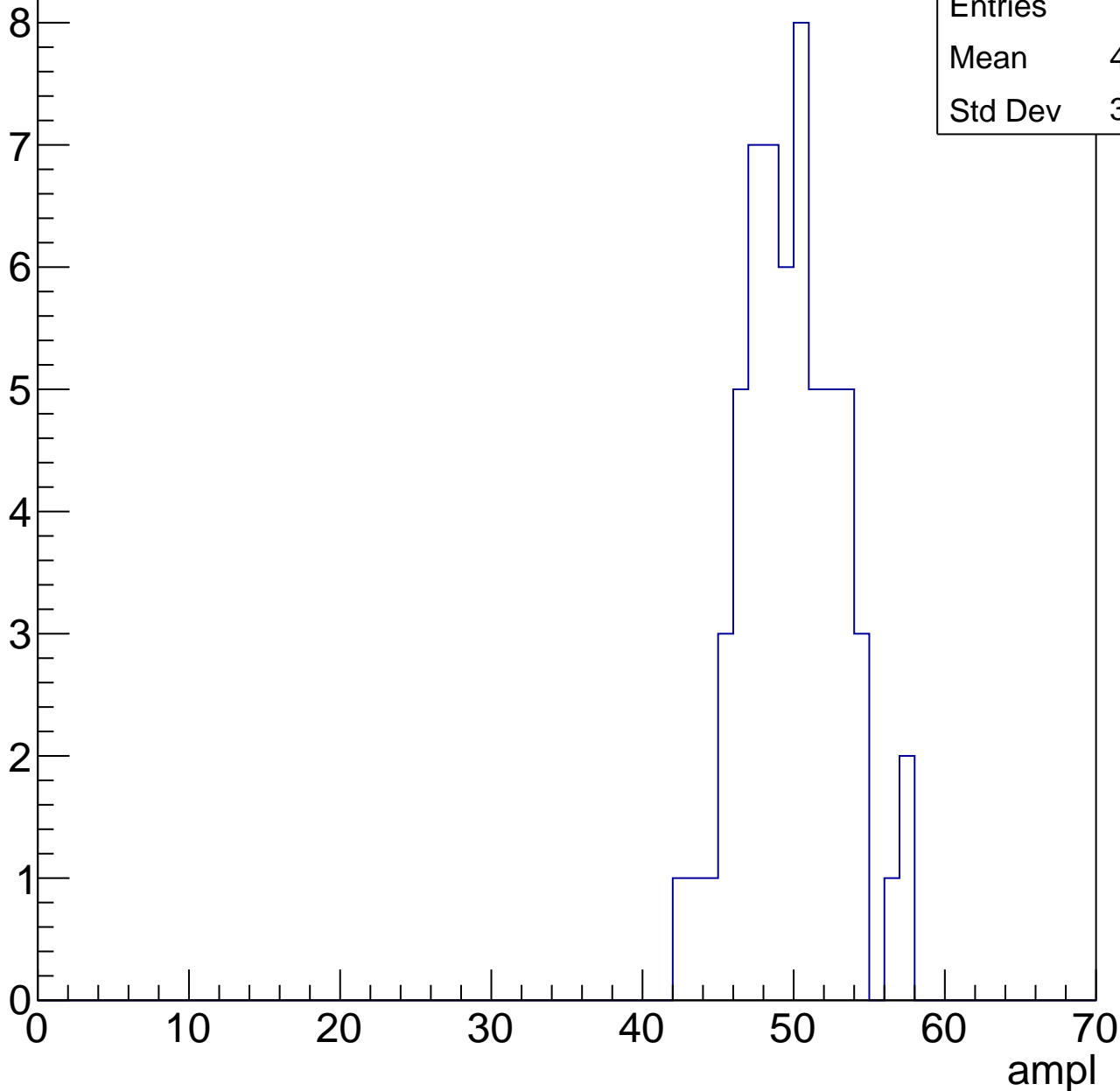


B1L102S, U18-ch19, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

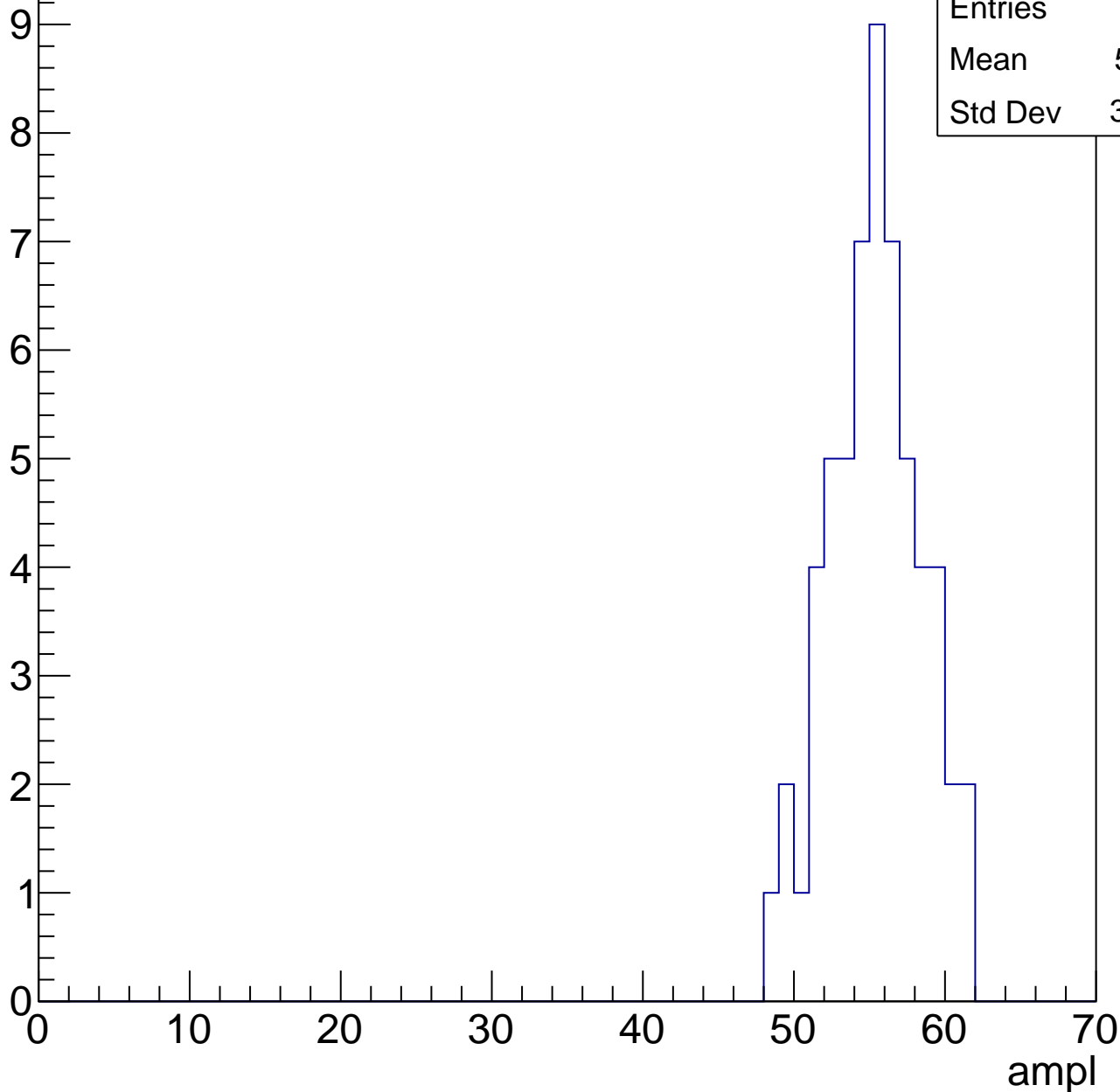
Entries	60
Mean	49.42
Std Dev	3.247



B1L102S, U18-ch19, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



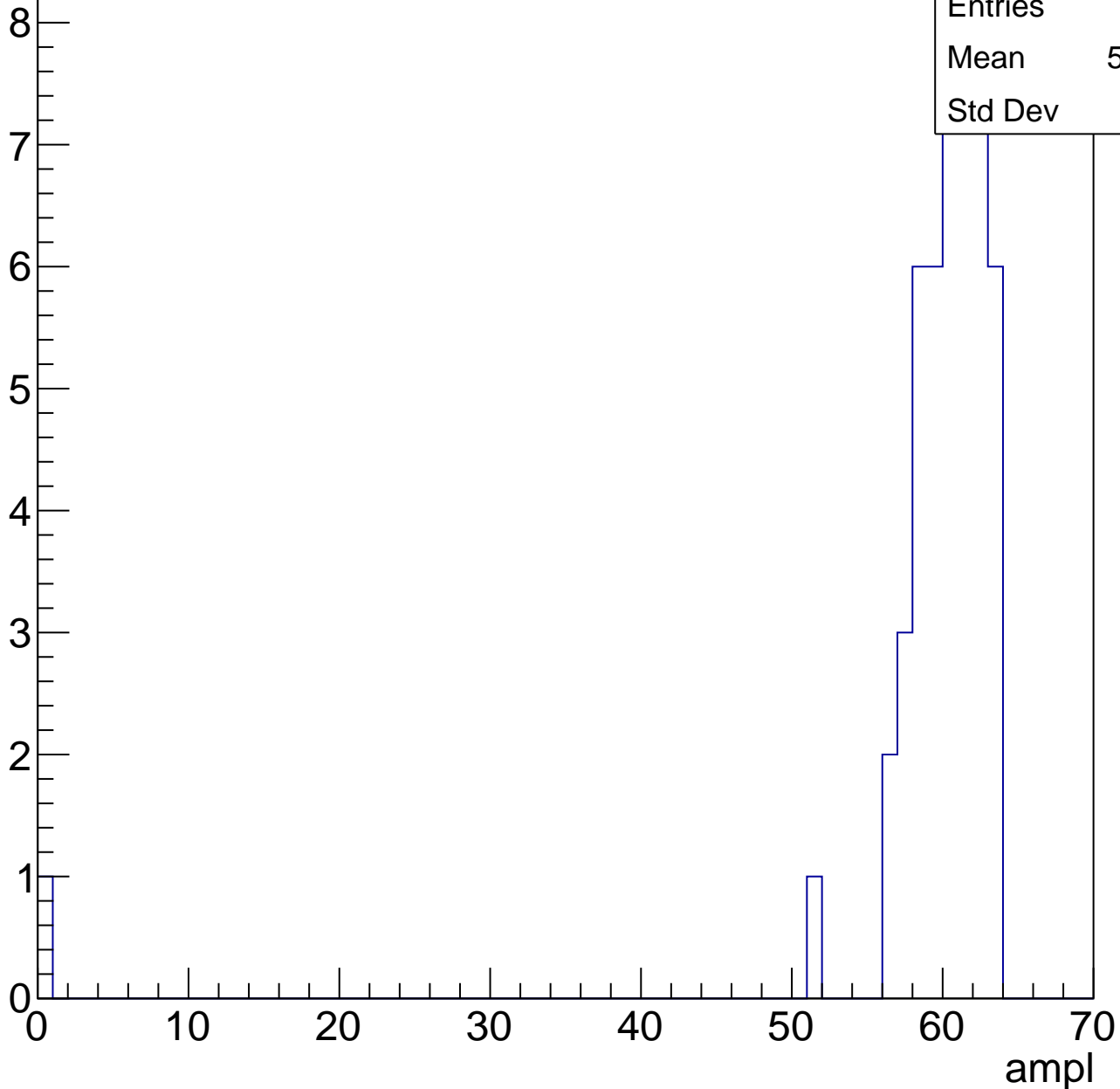
Entries	58
Mean	54.91
Std Dev	3.025

B1L102S, U18-ch19, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

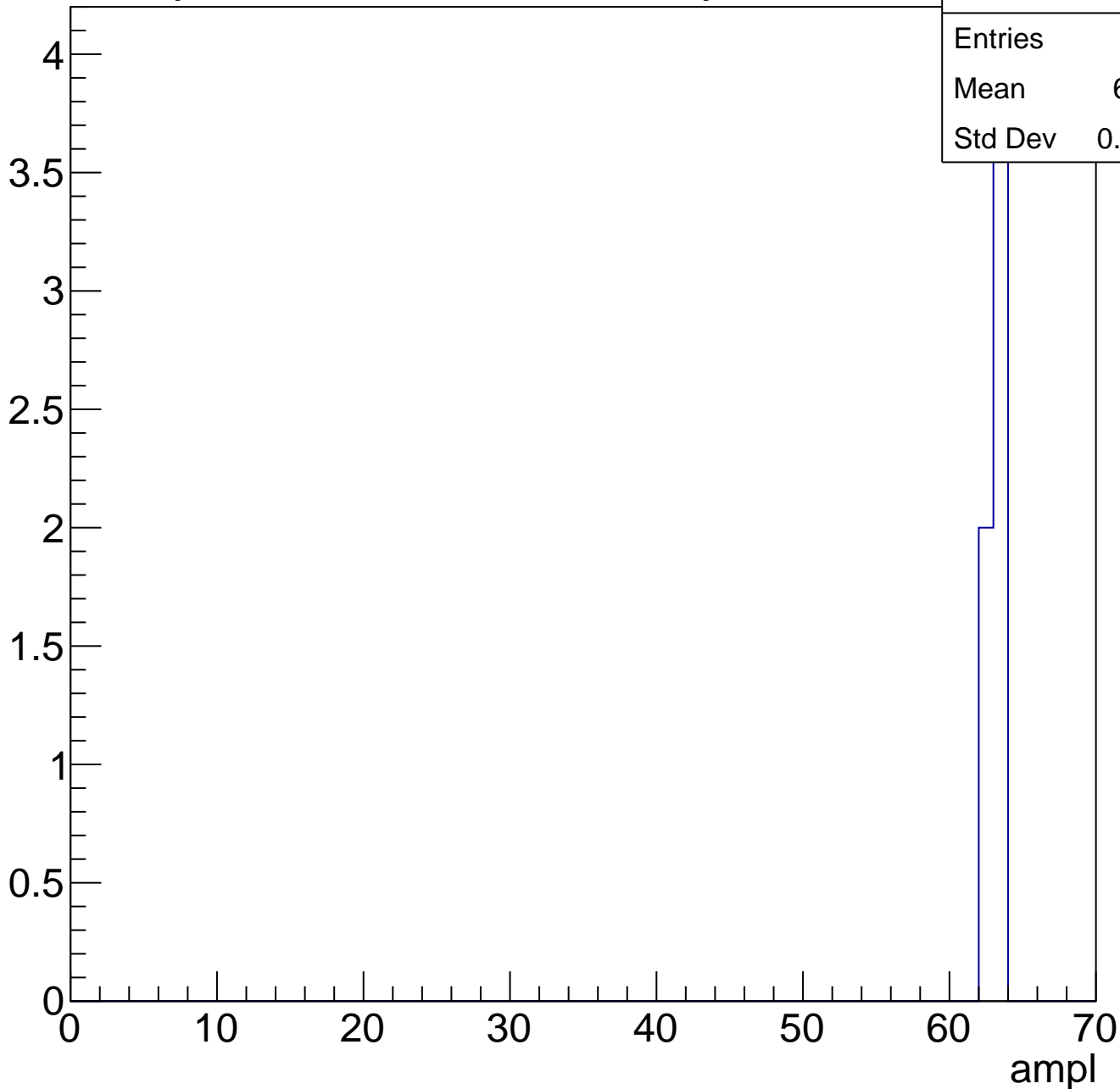
Entries	49
Mean	58.73
Std Dev	8.79



B1L102S, U18-ch19, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch19, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch20, adc0

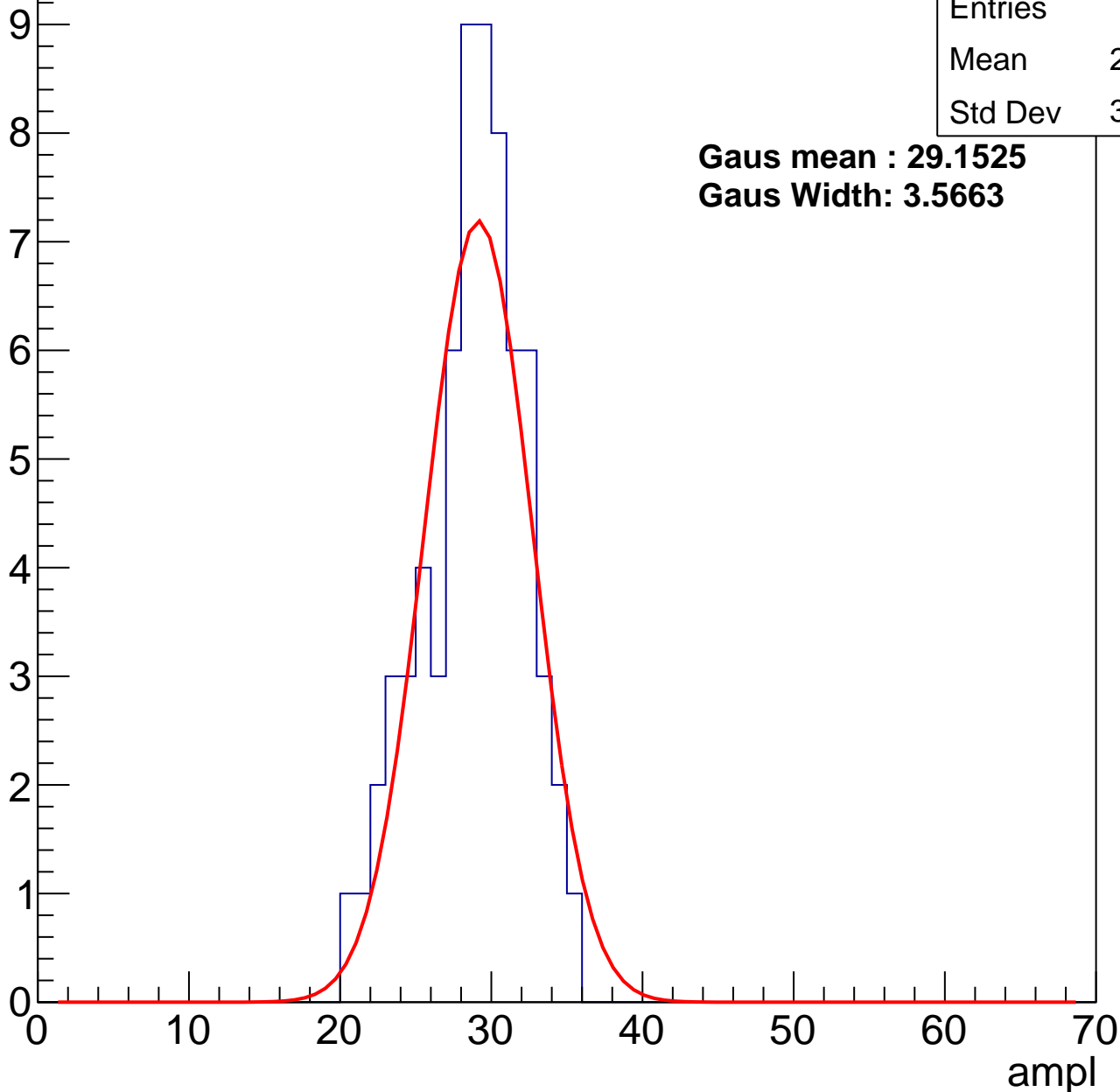
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.34
Std Dev	3.326

Gaus mean : 29.1525

Gaus Width: 3.5663



B1L102S, U18-ch20, adc1

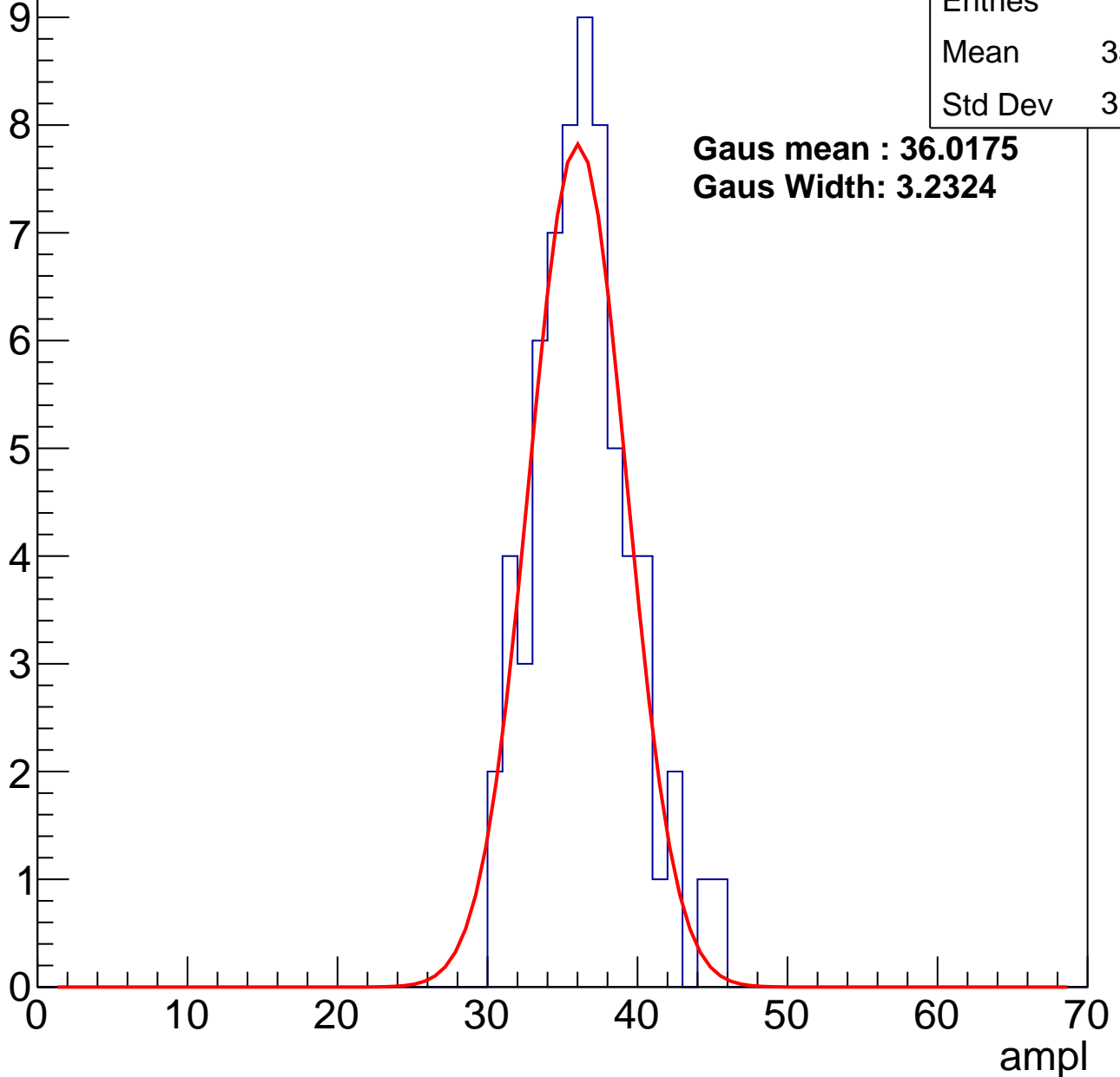
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	35.94
Std Dev	3.234

Gaus mean : 36.0175

Gaus Width: 3.2324



B1L102S, U18-ch20, adc2

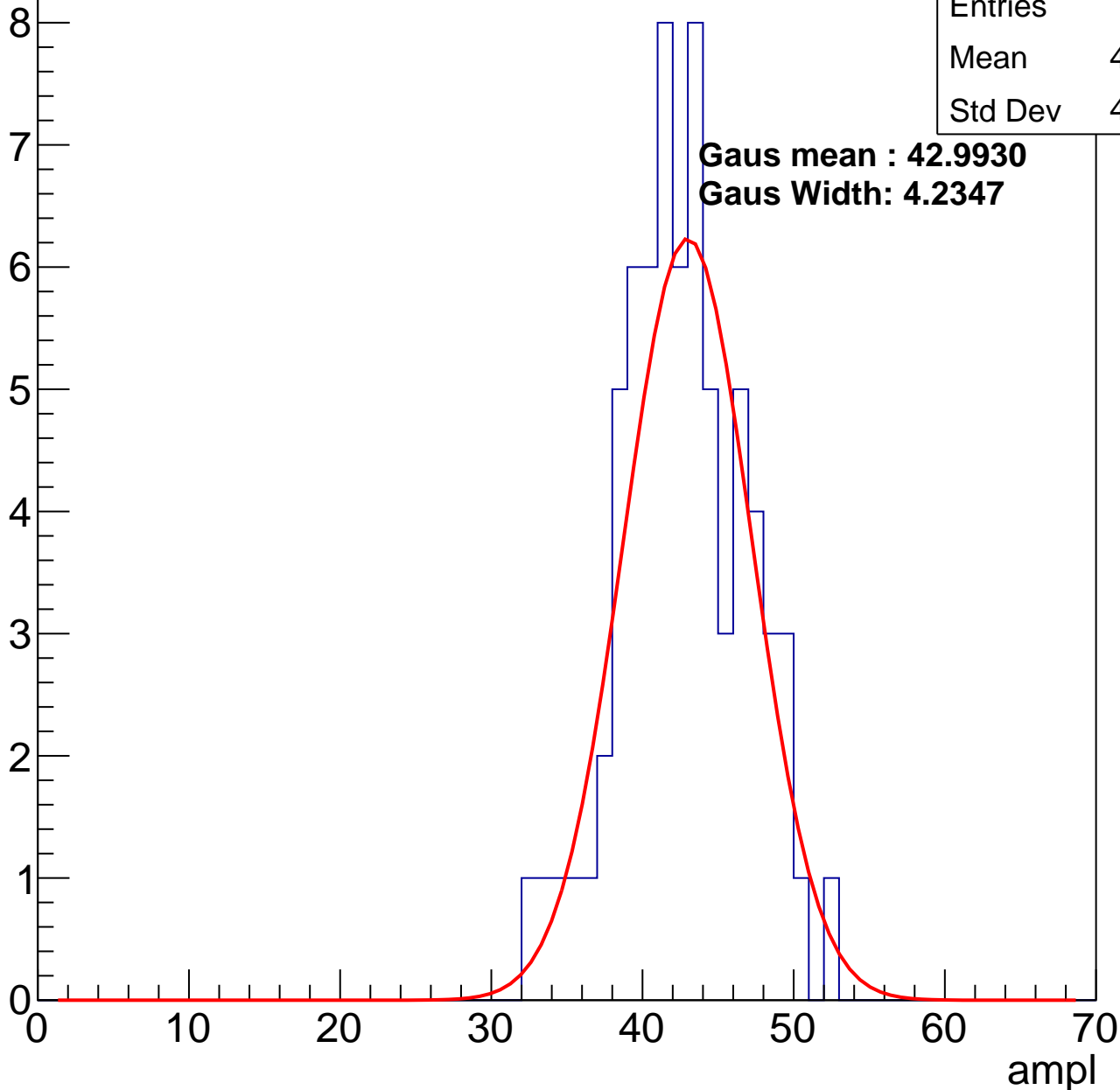
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	42.23
Std Dev	4.105

Gaus mean : 42.9930

Gaus Width: 4.2347

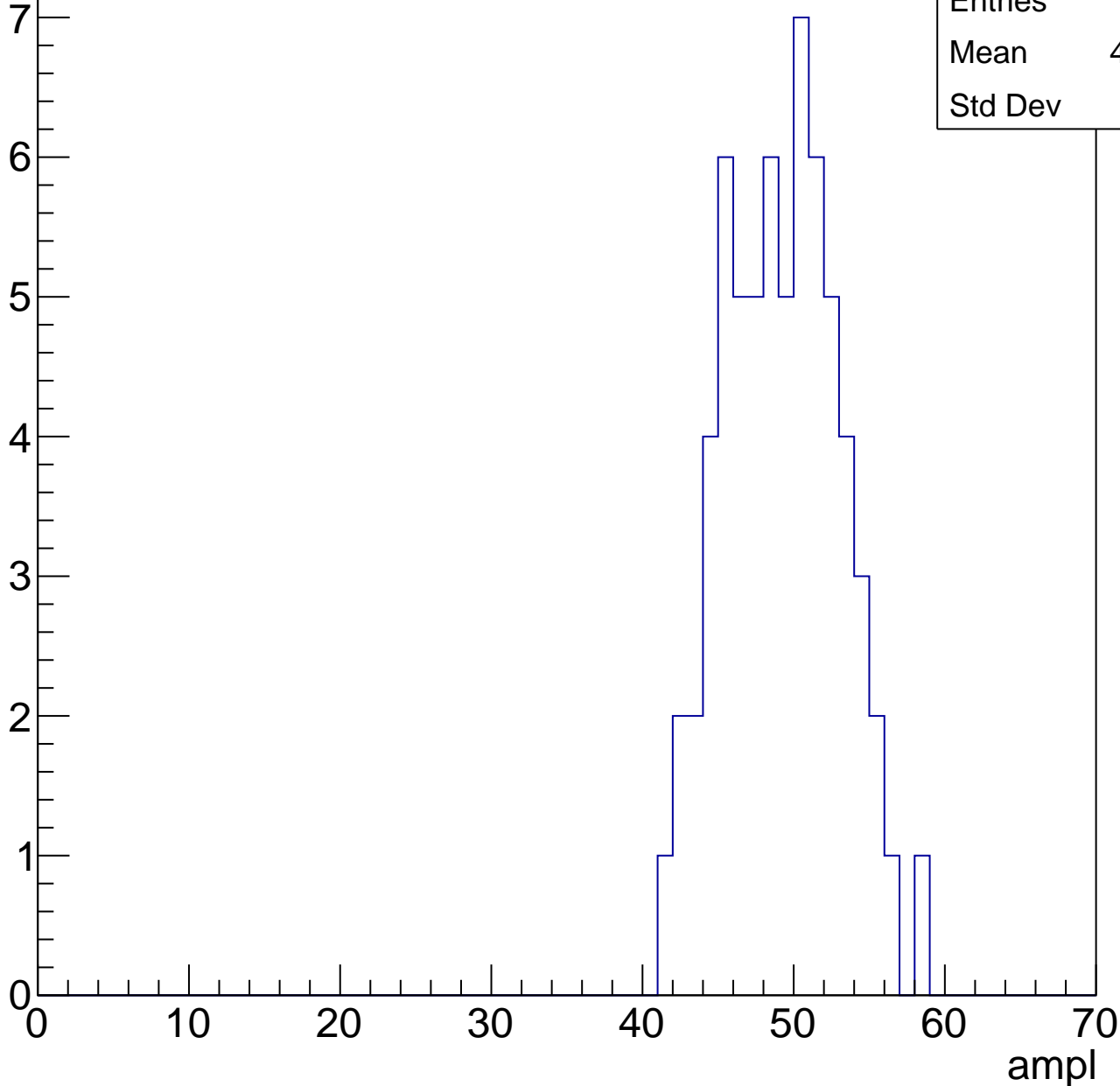


B1L102S, U18-ch20, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.75
Std Dev	3.75

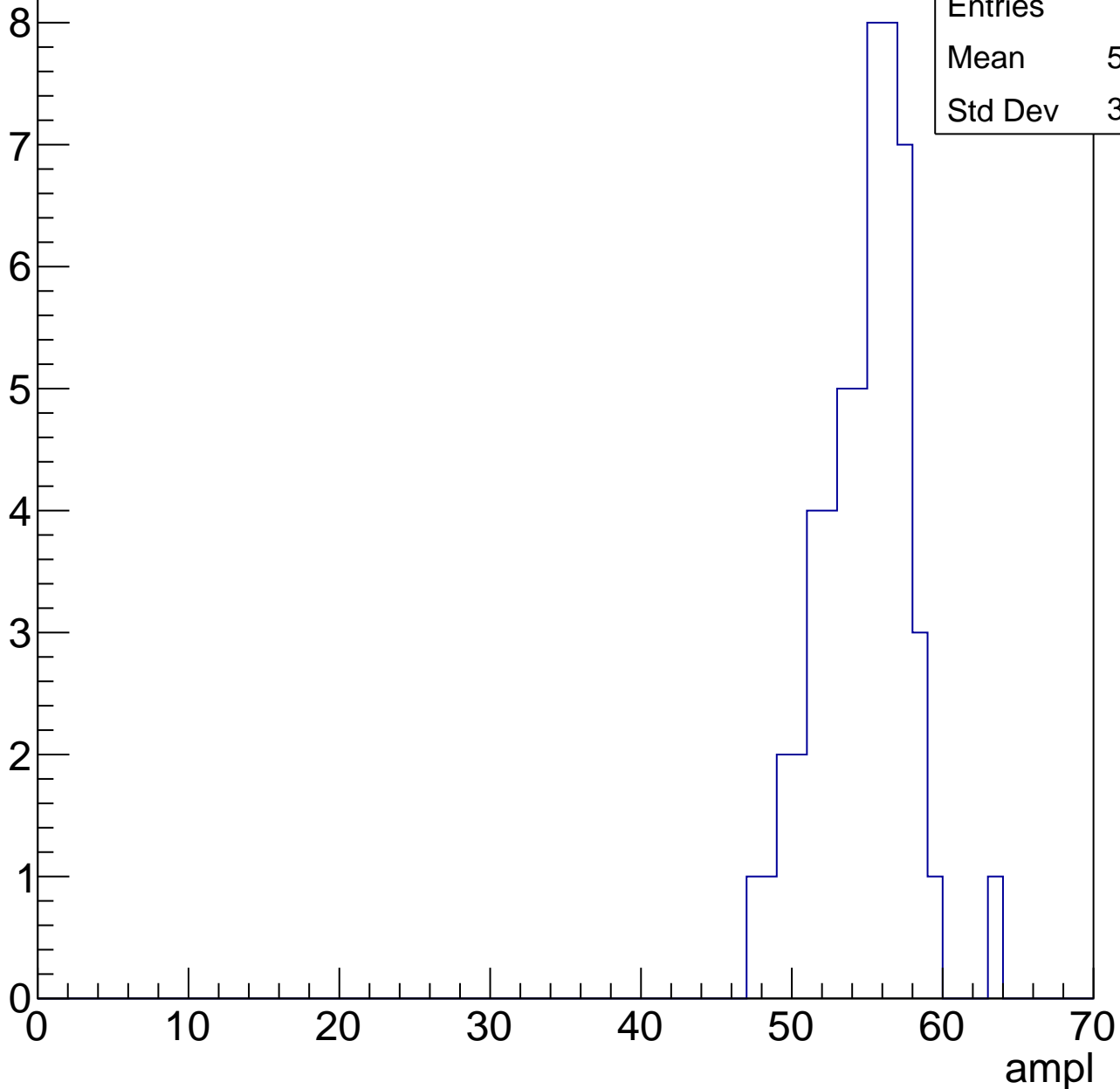


B1L102S, U18-ch20, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	54.29
Std Dev	3.028

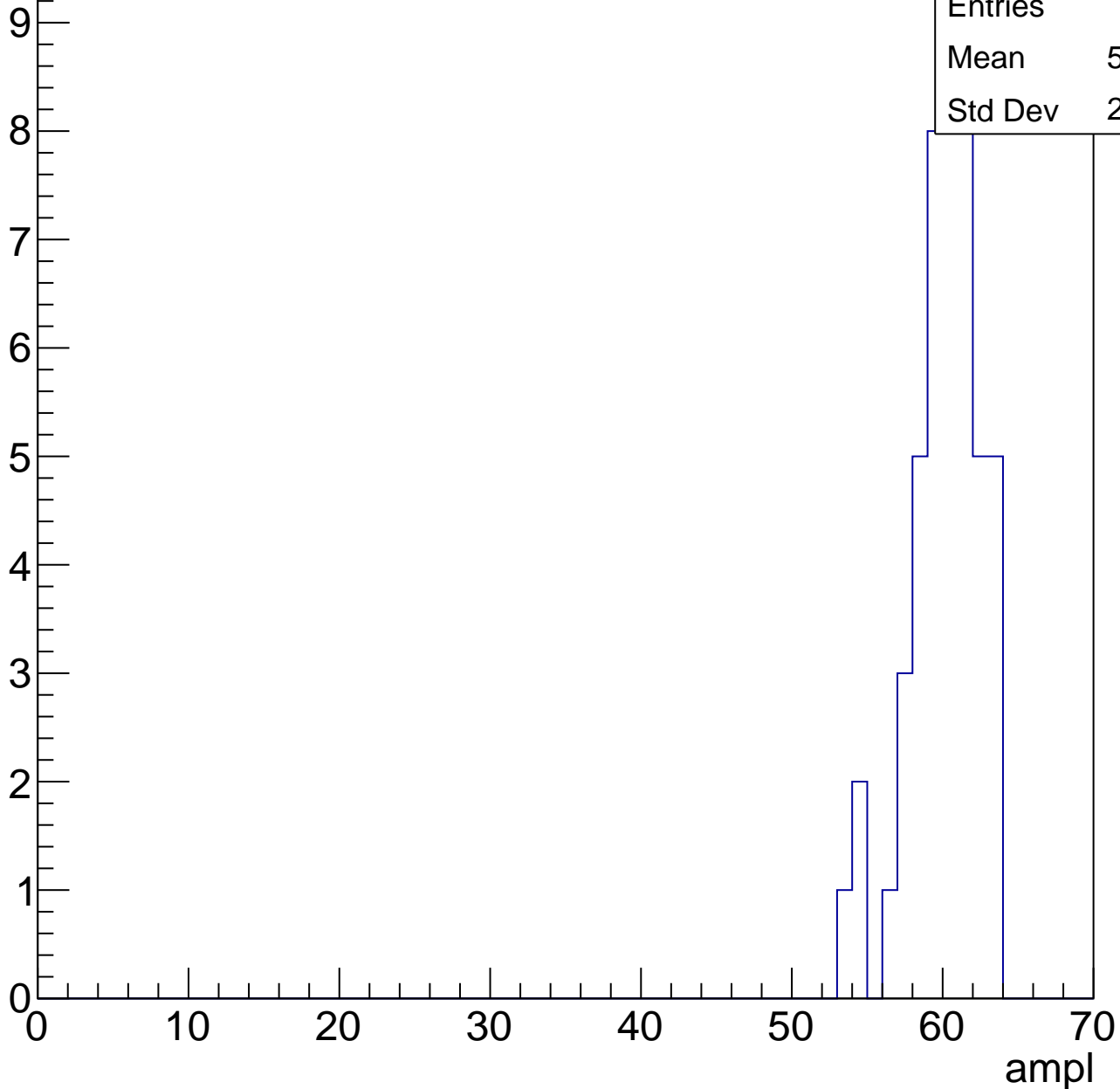


B1L102S, U18-ch20, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.66
Std Dev	2.355

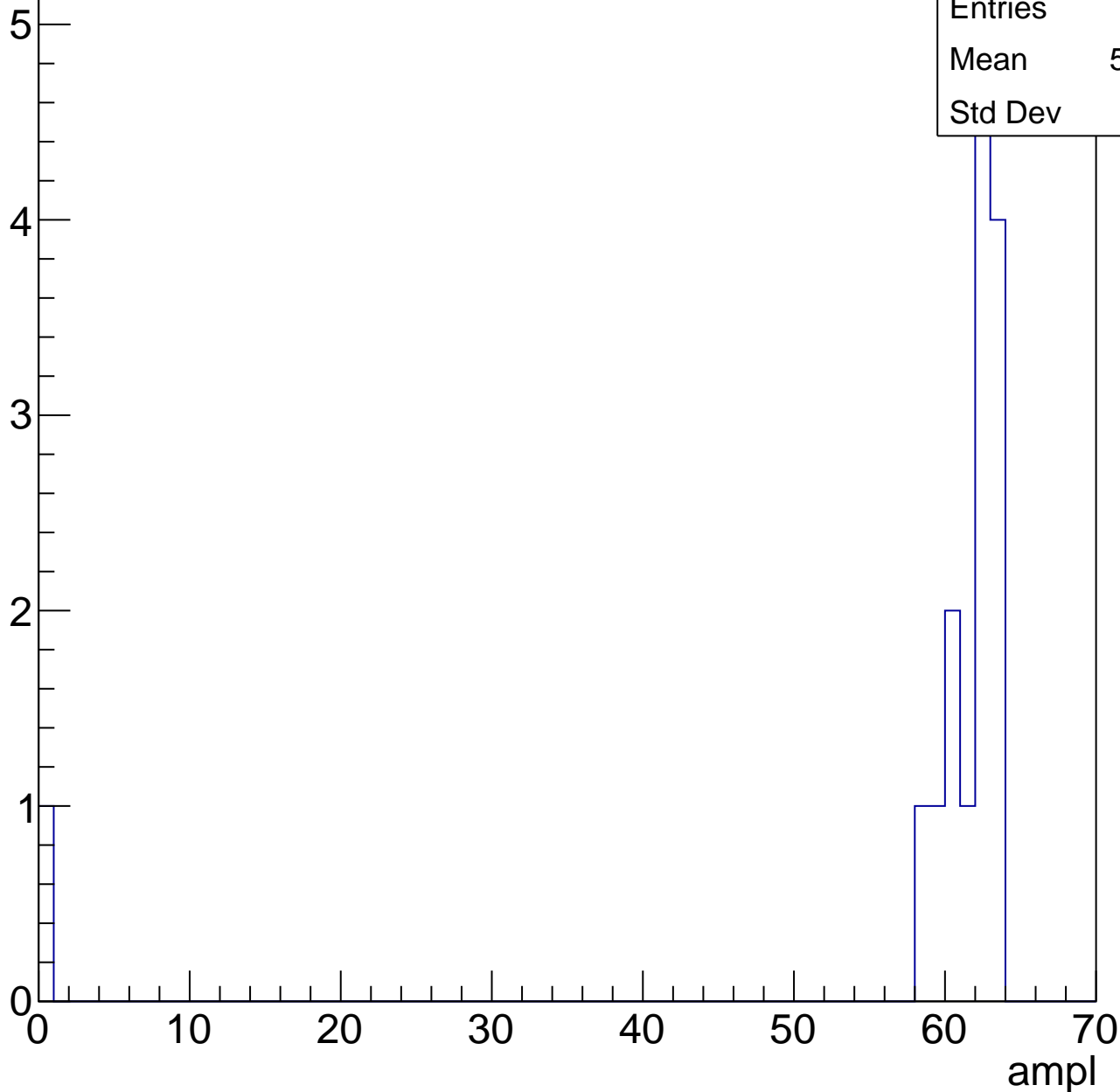


B1L102S, U18-ch20, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	57.33
Std Dev	15.4



B1L102S, U18-ch20, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch21, adc0

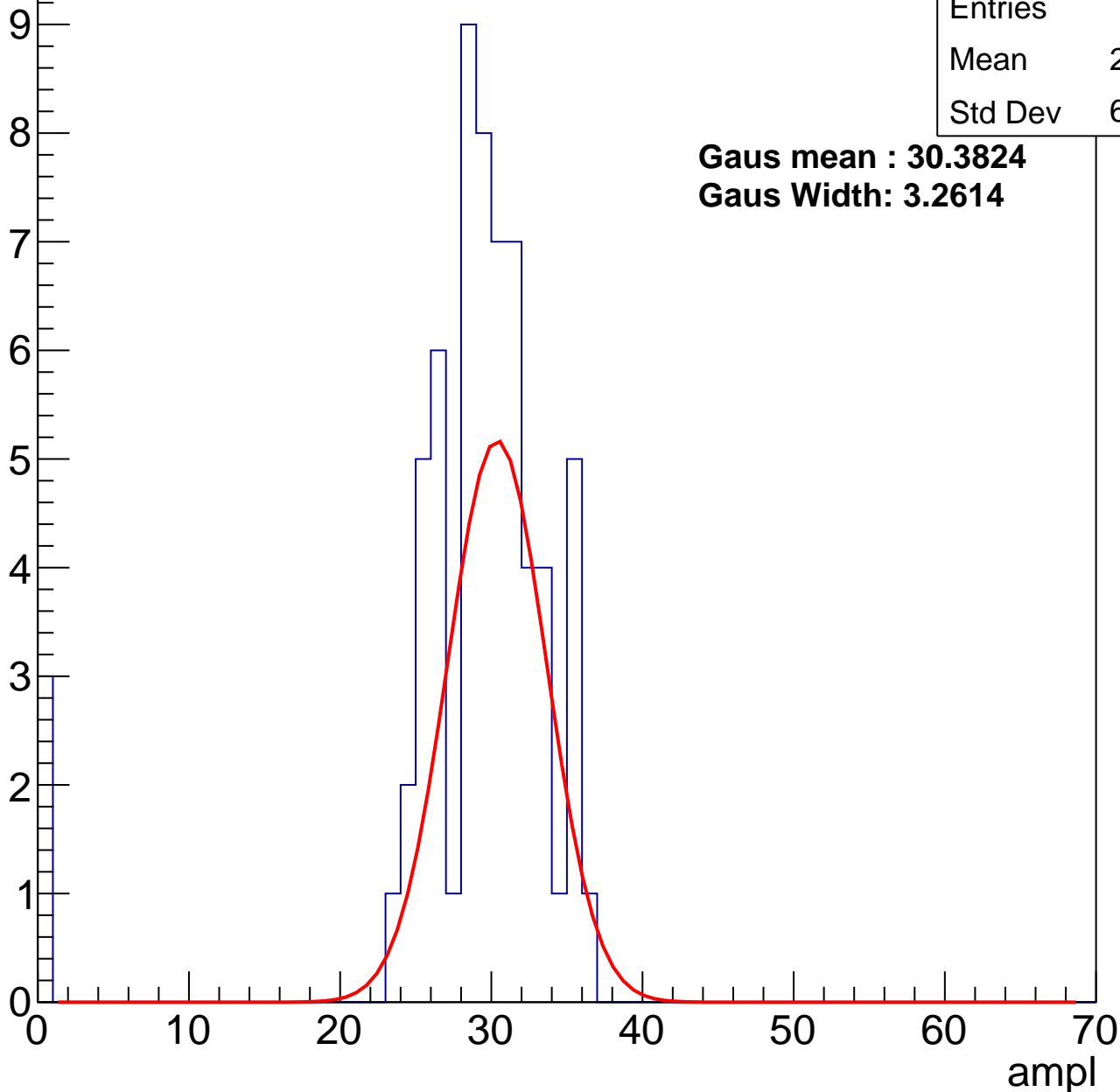
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28.05
Std Dev	6.945

Gaus mean : 30.3824

Gaus Width: 3.2614



B1L102S, U18-ch21, adc1

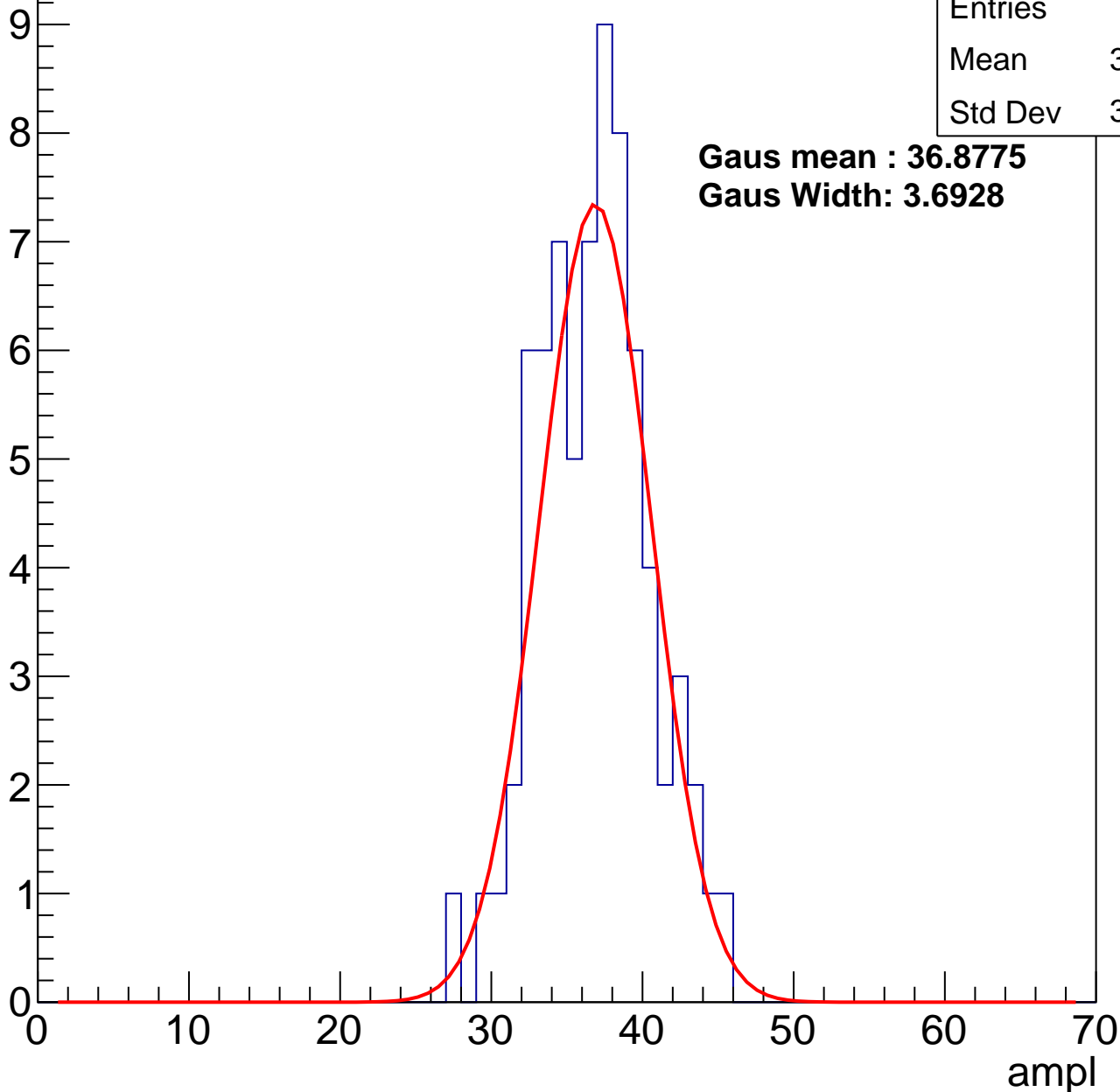
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	36.35
Std Dev	3.633

Gaus mean : 36.8775

Gaus Width: 3.6928



B1L102S, U18-ch21, adc2

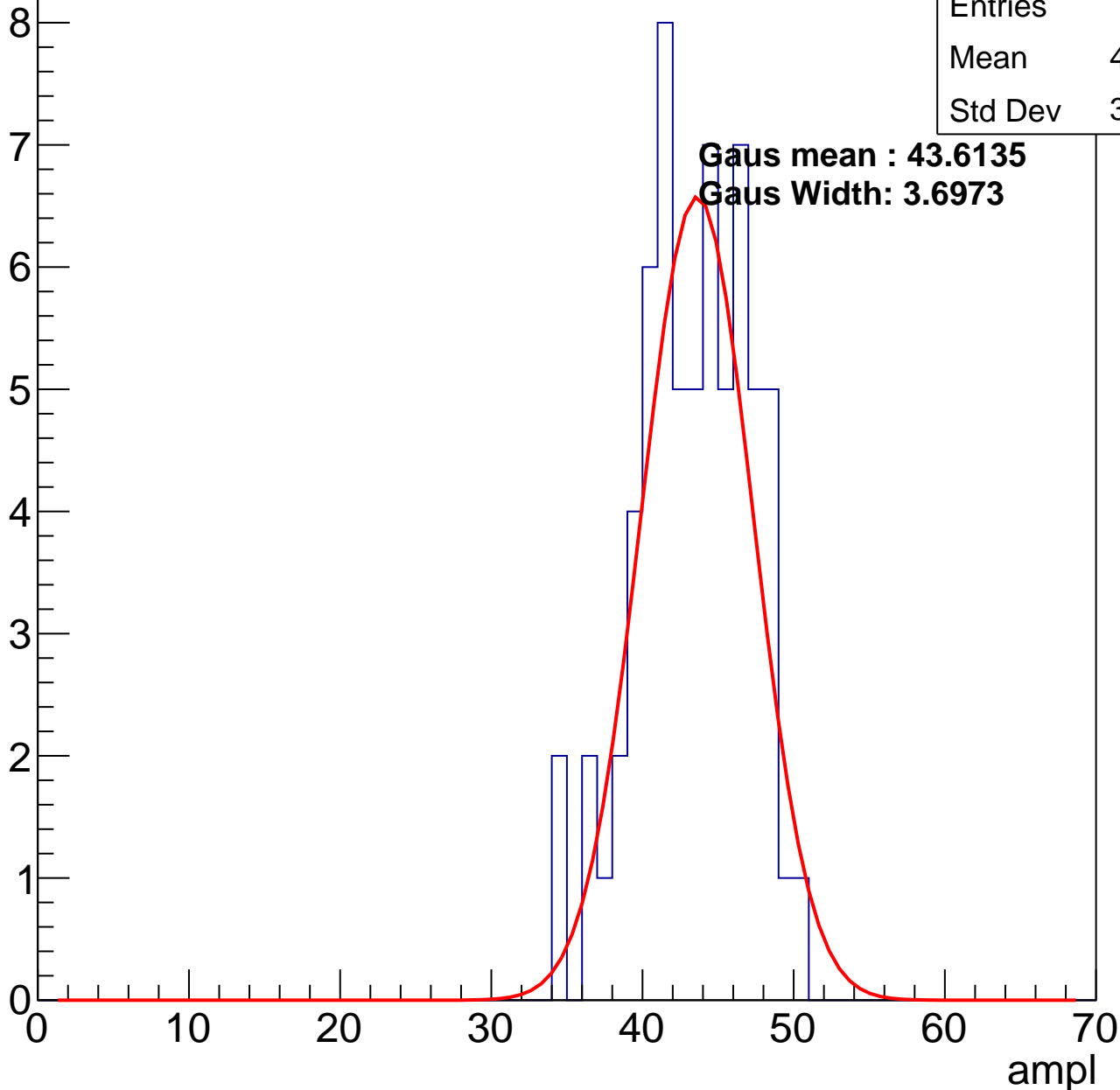
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.89
Std Dev	3.652

Gaus mean : 43.6135

Gaus Width: 3.6973

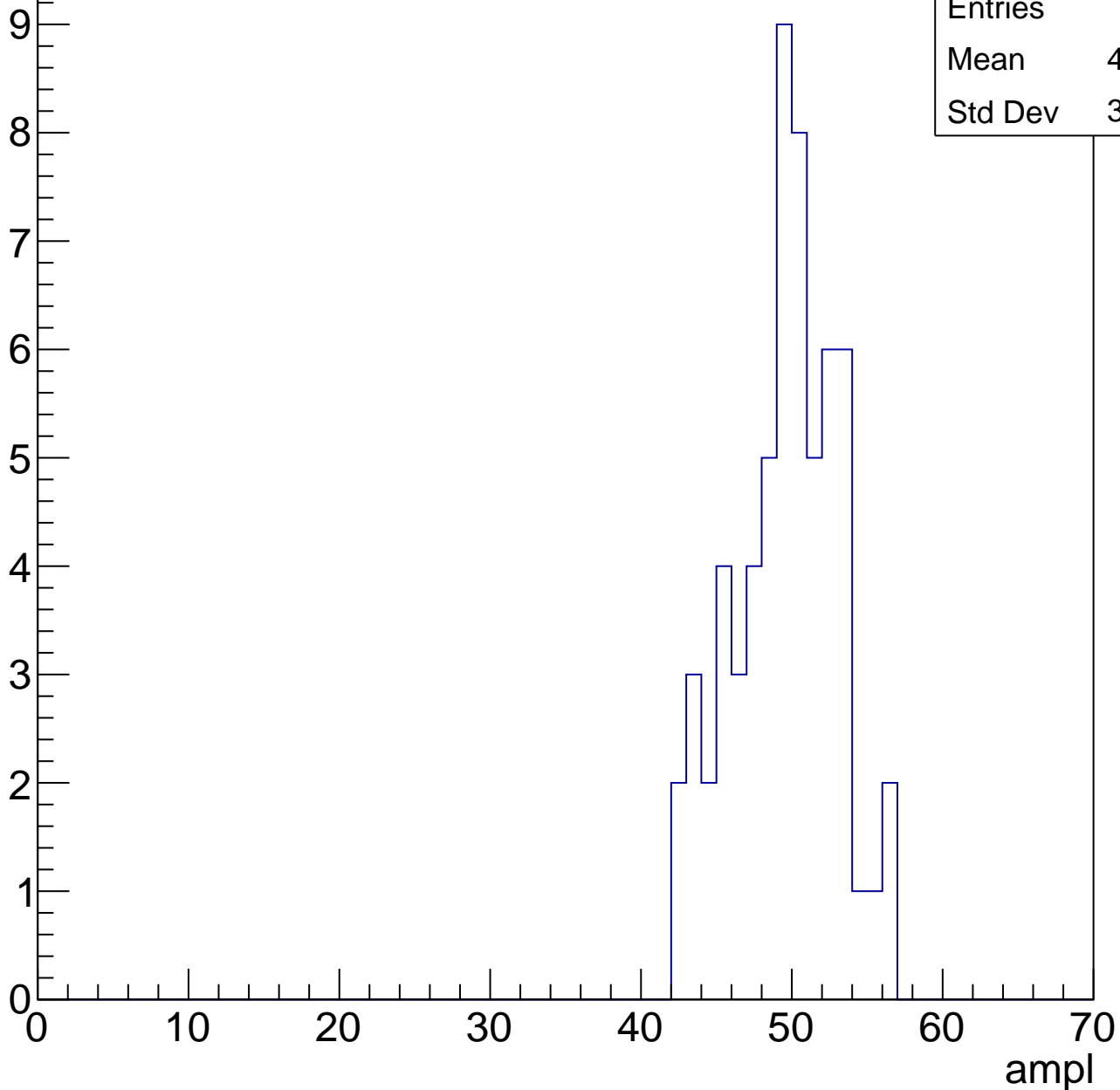


B1L102S, U18-ch21, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	49.08
Std Dev	3.399

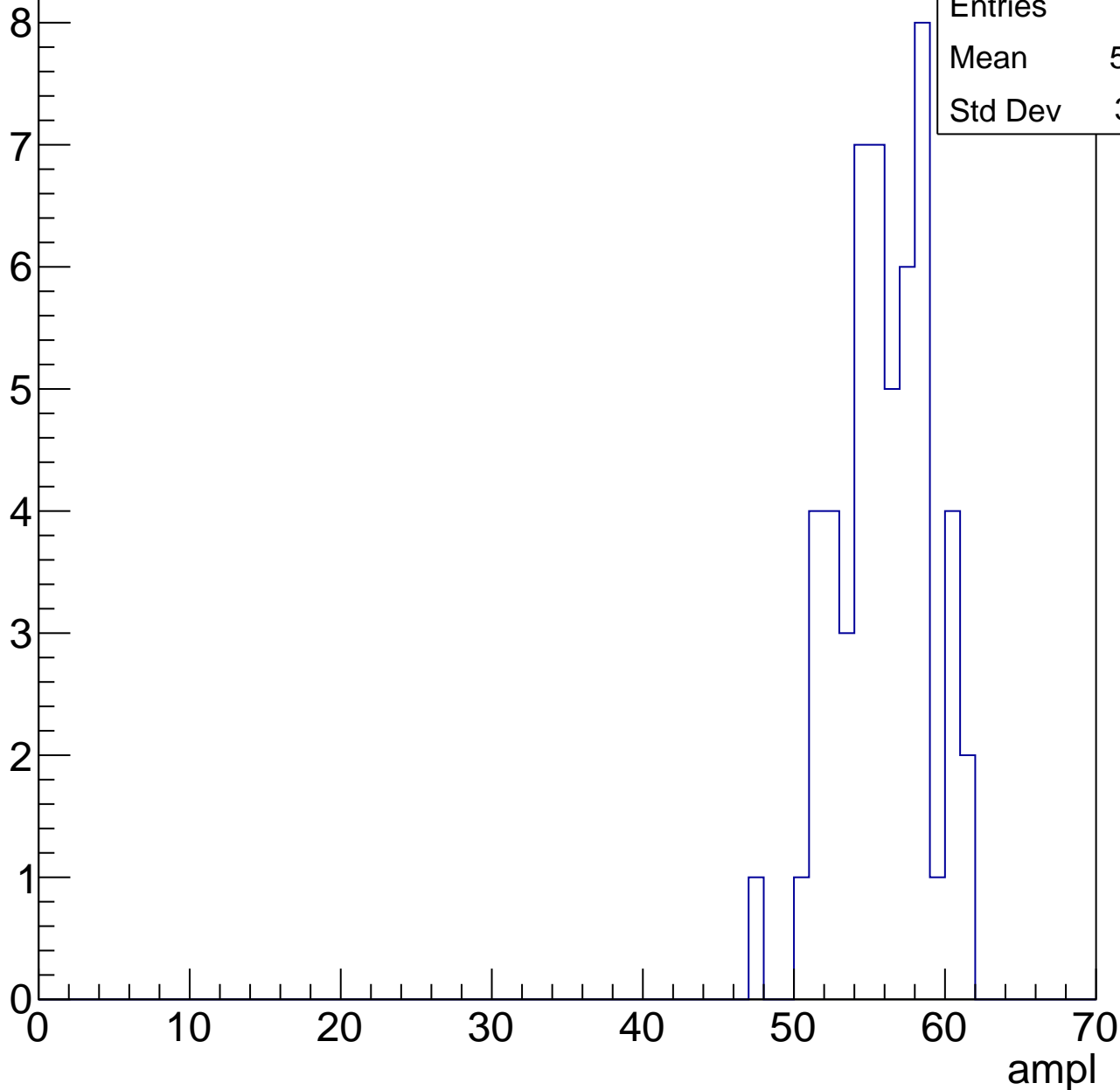


B1L102S, U18-ch21, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.43
Std Dev	3.031

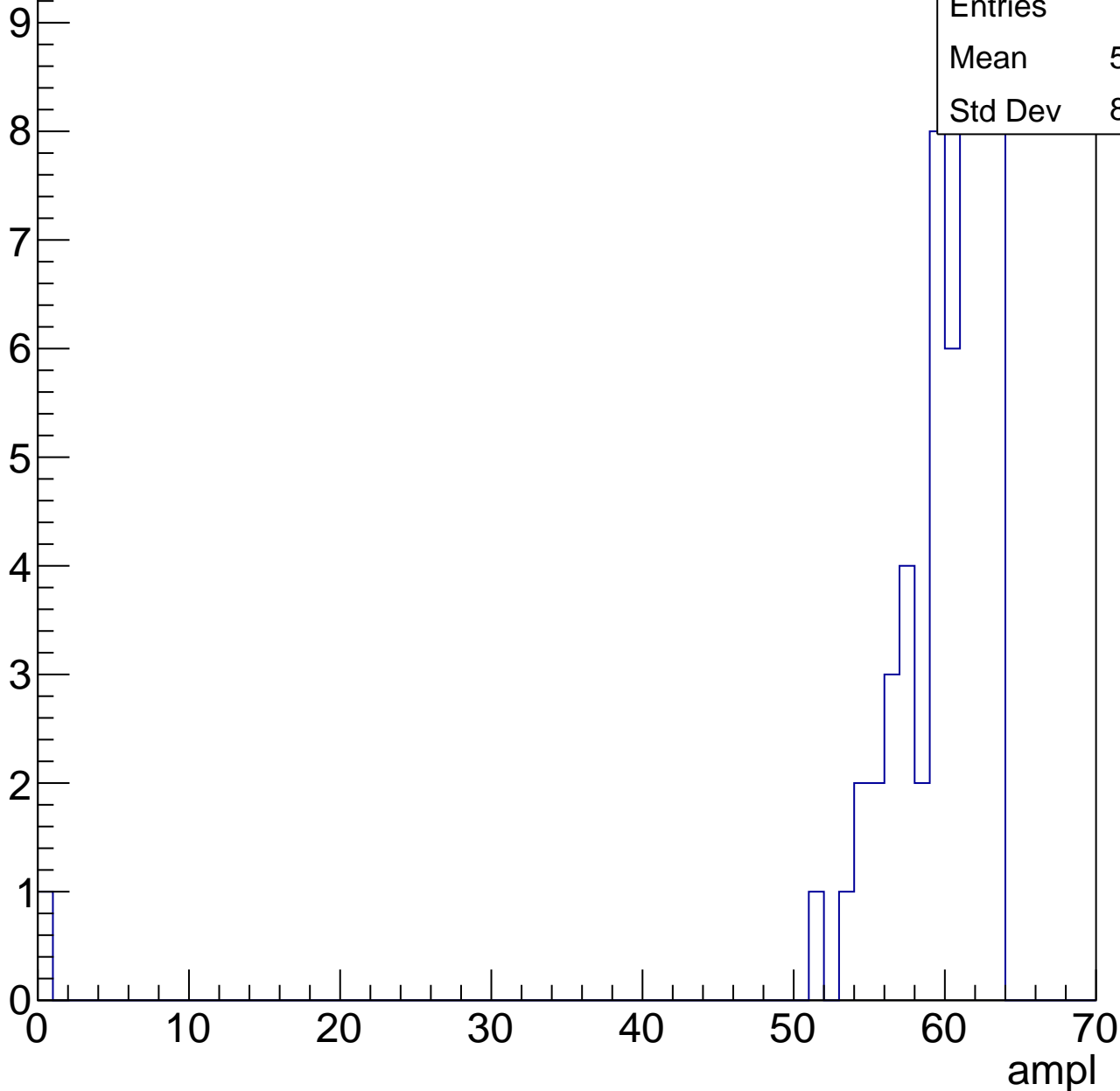


B1L102S, U18-ch21, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	58.47
Std Dev	8.464



B1L102S, U18-ch21, adc6

calib_packv5_042523_0143.root, FC#11, port A2

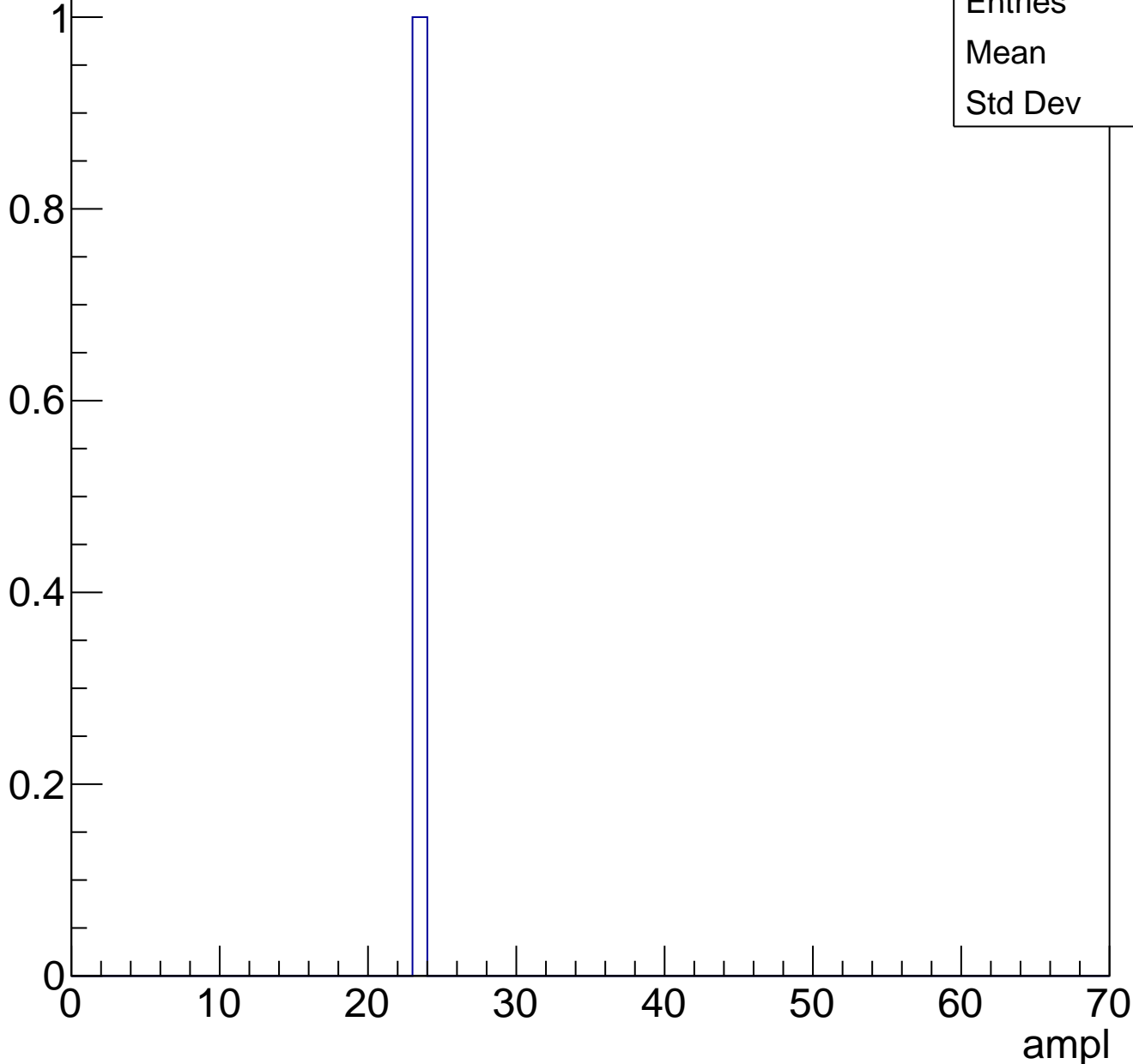
Entry



B1L102S, U18-ch21, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	23
Std Dev	0

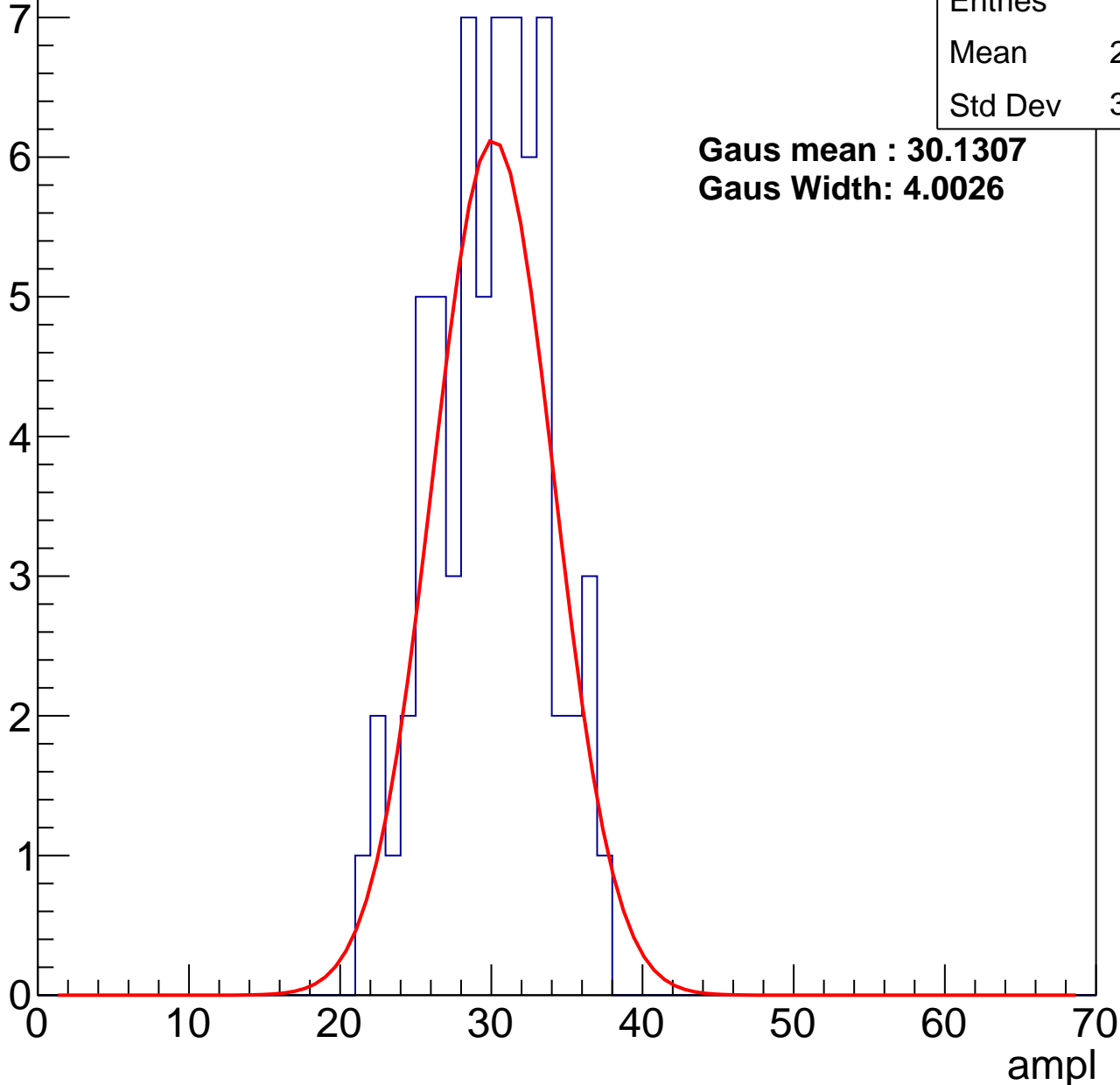
B1L102S, U18-ch22, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	29.48
Std Dev	3.714

Gaus mean : 30.1307
Gaus Width: 4.0026



B1L102S, U18-ch22, adc1

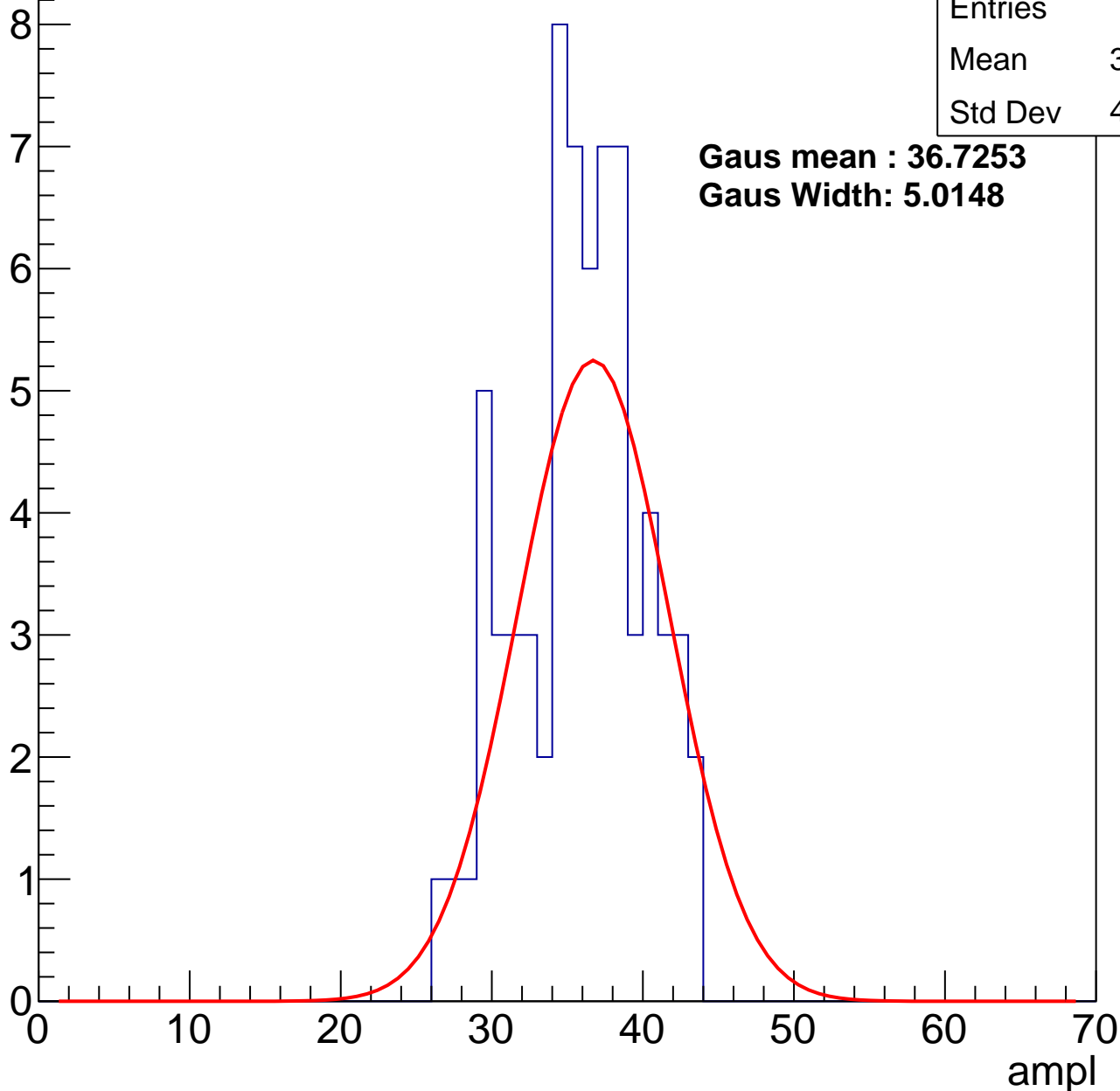
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.38
Std Dev	4.097

Gaus mean : 36.7253

Gaus Width: 5.0148



B1L102S, U18-ch22, adc2

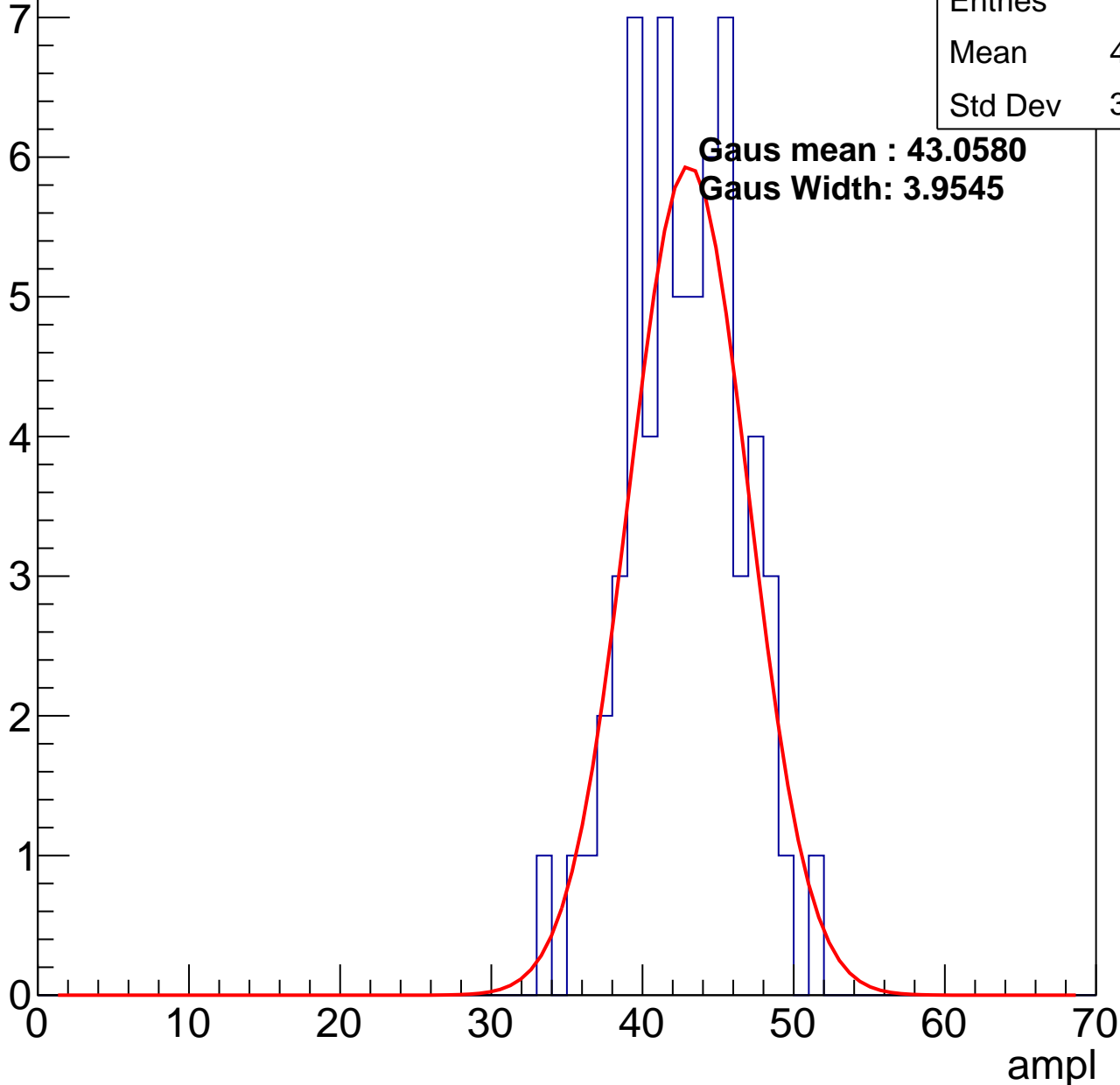
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	42.39
Std Dev	3.663

Gaus mean : 43.0580

Gaus Width: 3.9545

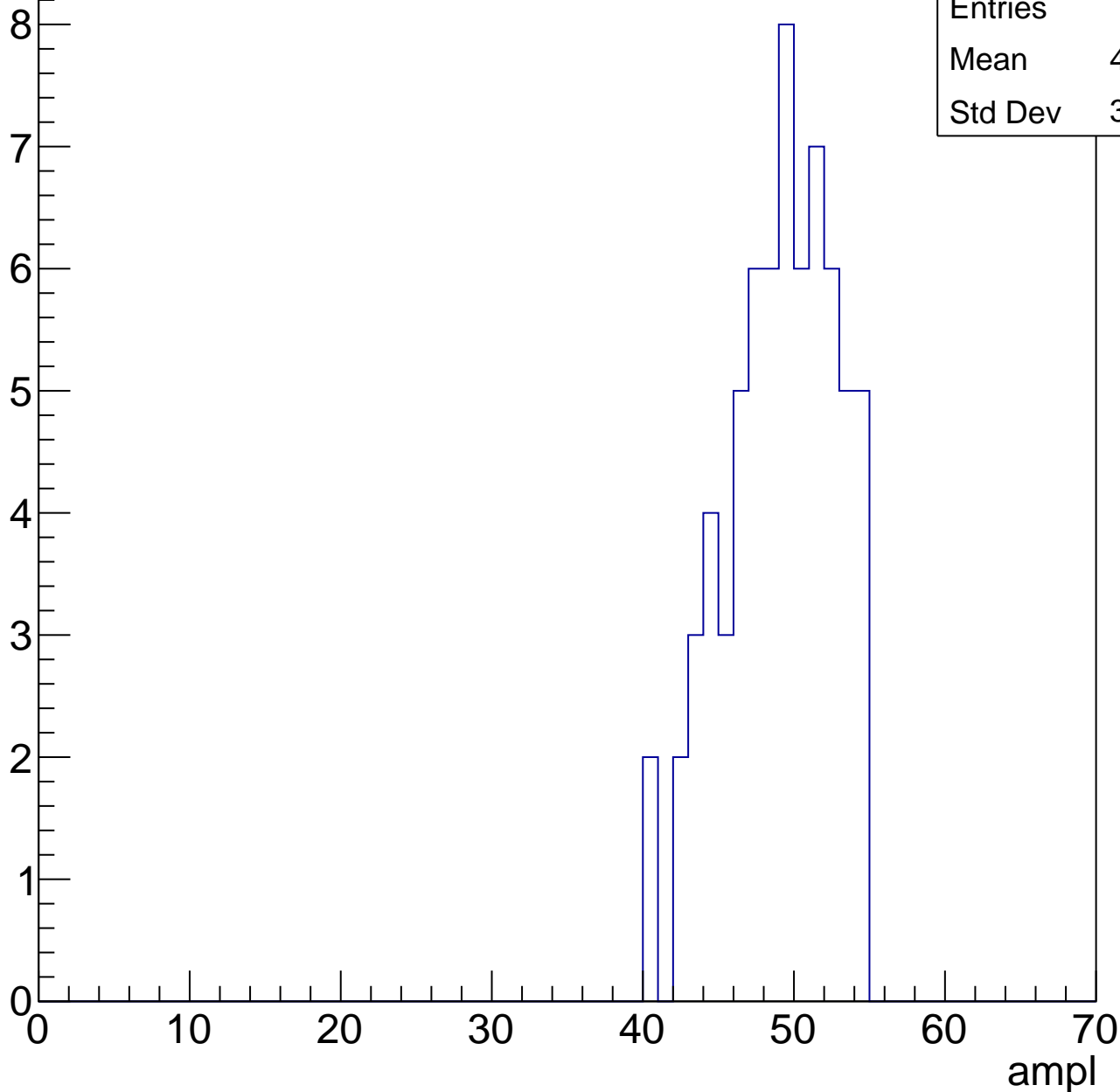


B1L102S, U18-ch22, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.53
Std Dev	3.575

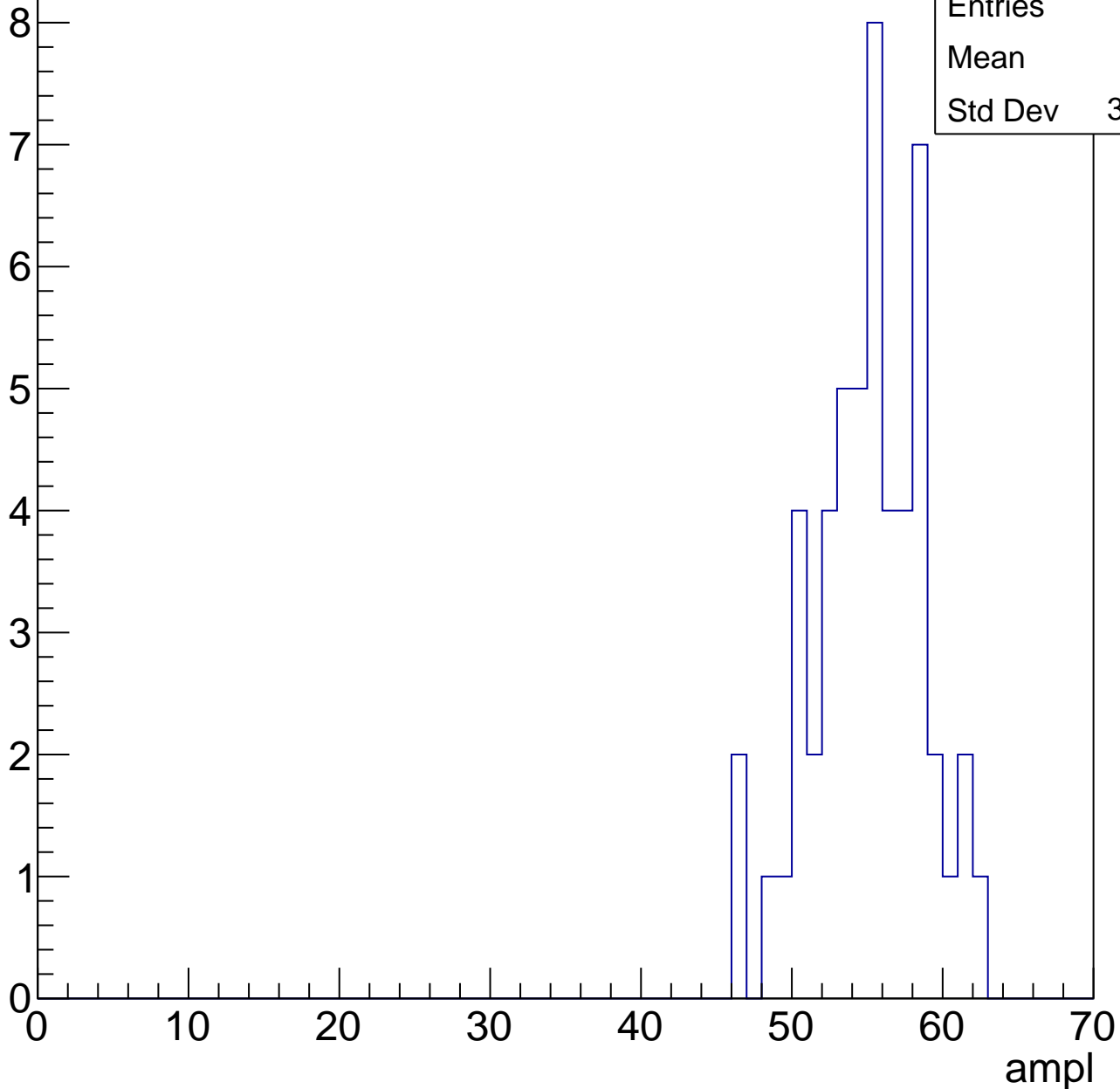


B1L102S, U18-ch22, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	54.6
Std Dev	3.615



B1L102S, U18-ch22, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.27
Std Dev	8.486

ampl

0

10

20

30

40

50

60

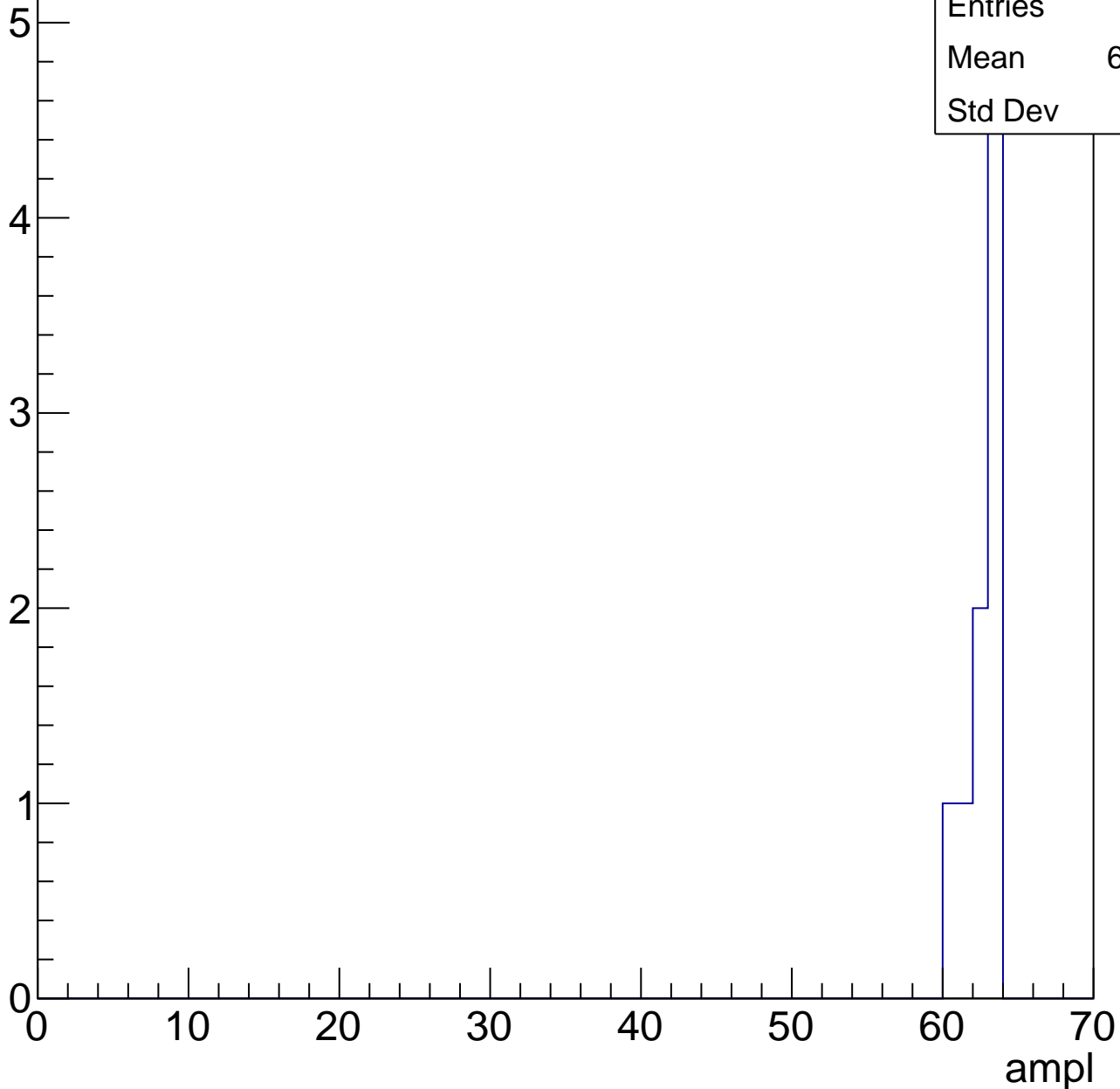
70

B1L102S, U18-ch22, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	9
Mean	62.22
Std Dev	1.03



B1L102S, U18-ch22, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch23, adc0

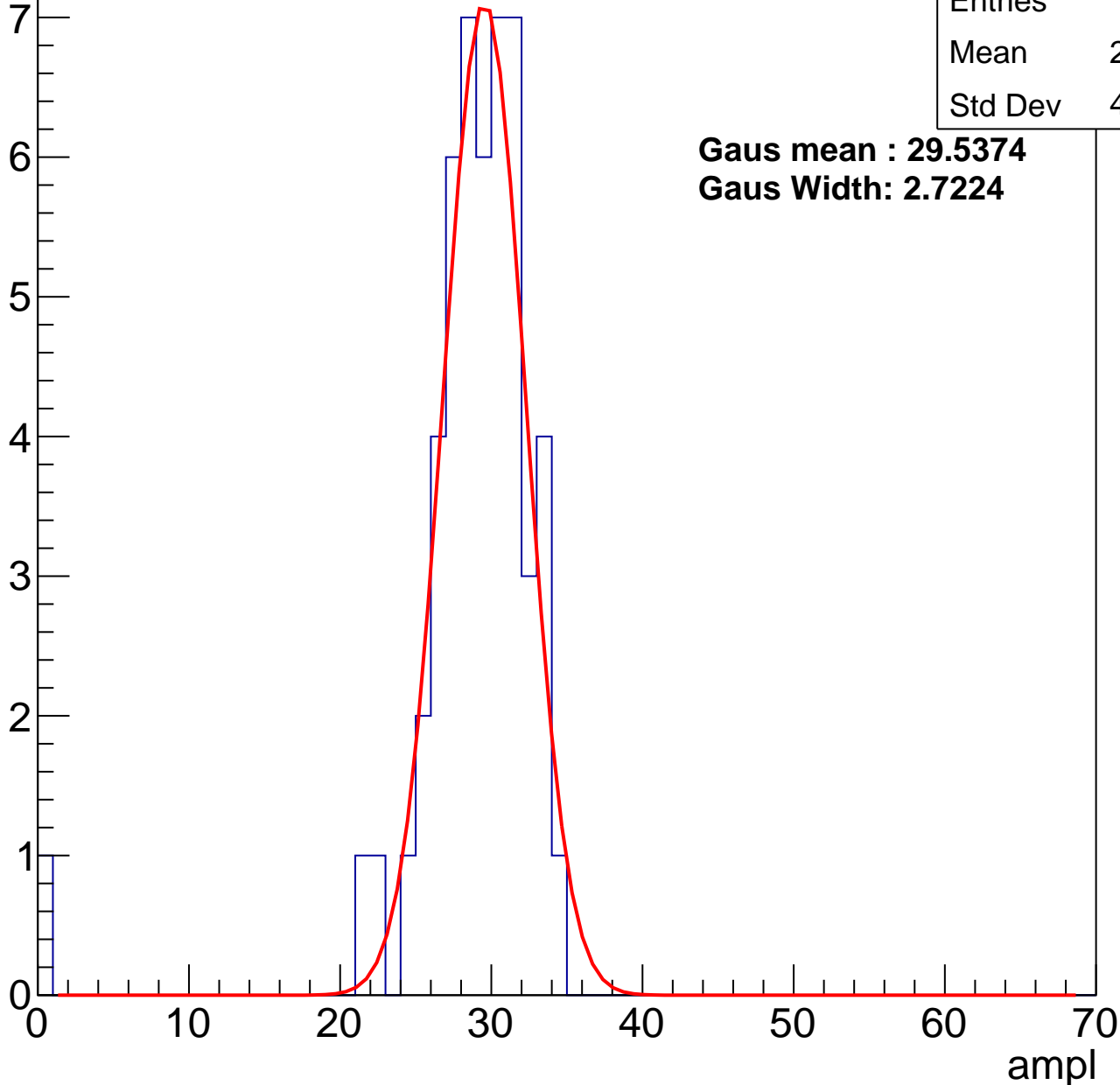
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	28.27
Std Dev	4.855

Gaus mean : 29.5374

Gaus Width: 2.7224



B1L102S, U18-ch23, adc1

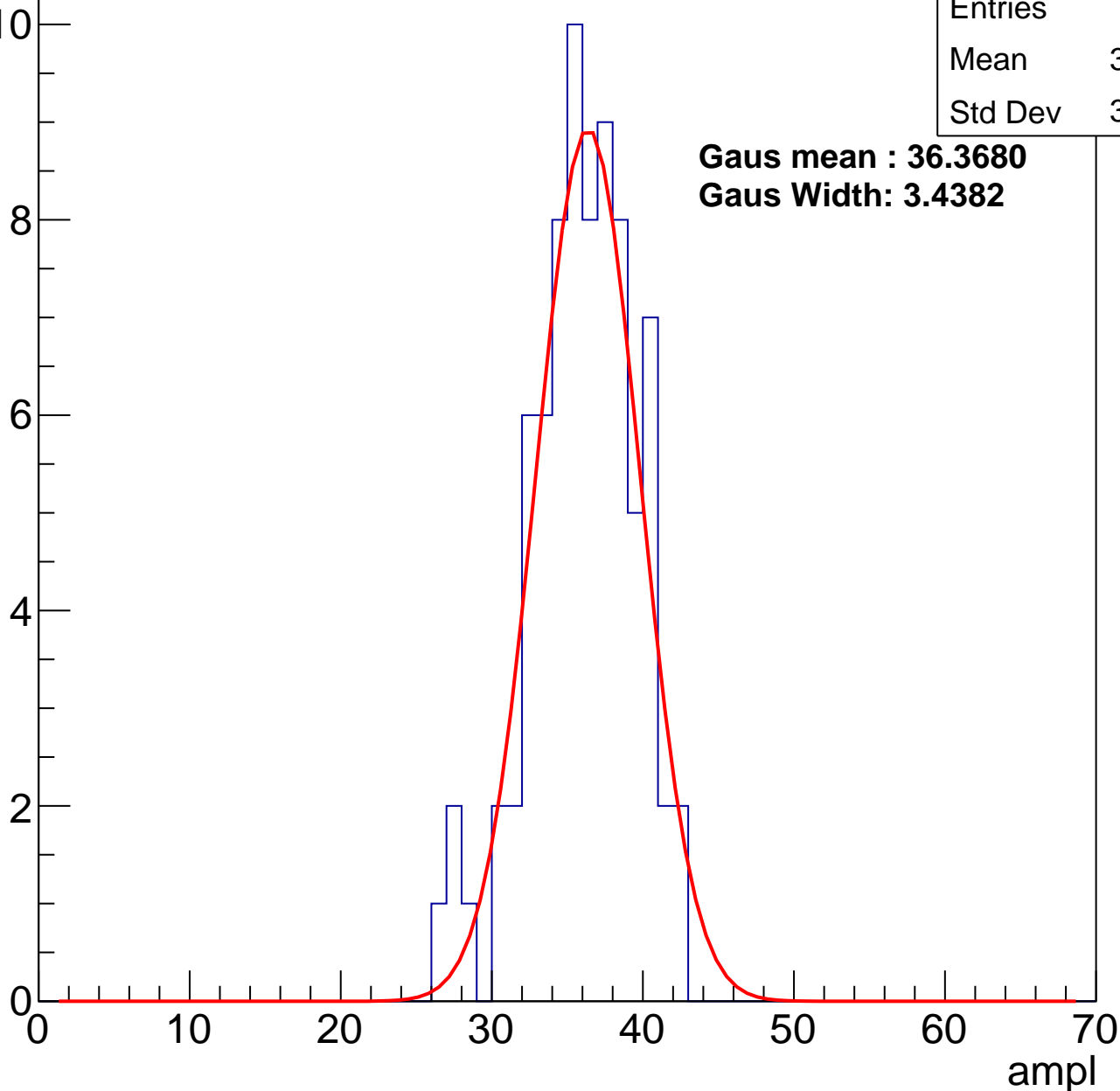
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	35.54
Std Dev	3.456

Gaus mean : 36.3680

Gaus Width: 3.4382



B1L102S, U18-ch23, adc2

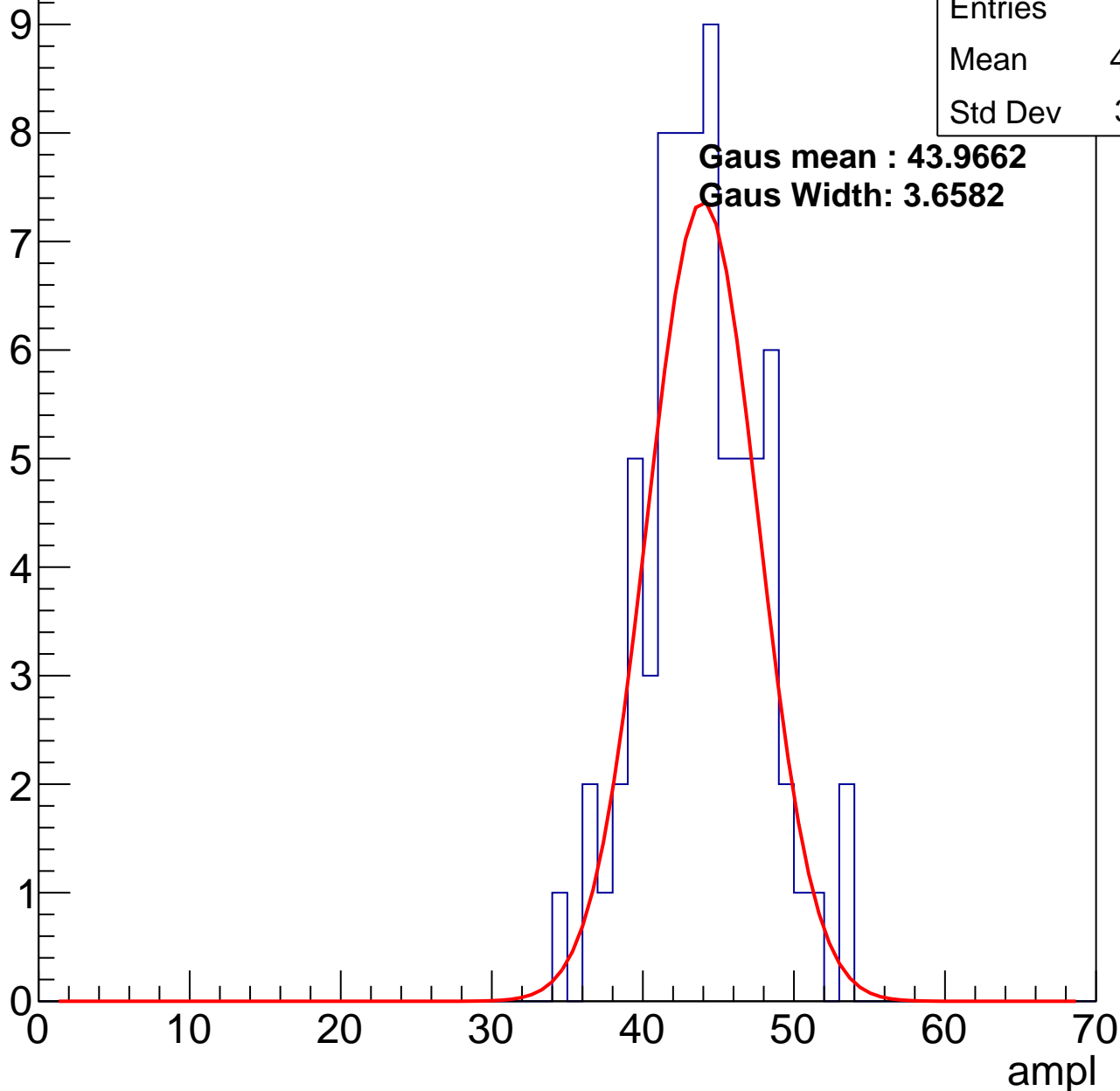
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	43.53
Std Dev	3.821

Gaus mean : 43.9662

Gaus Width: 3.6582

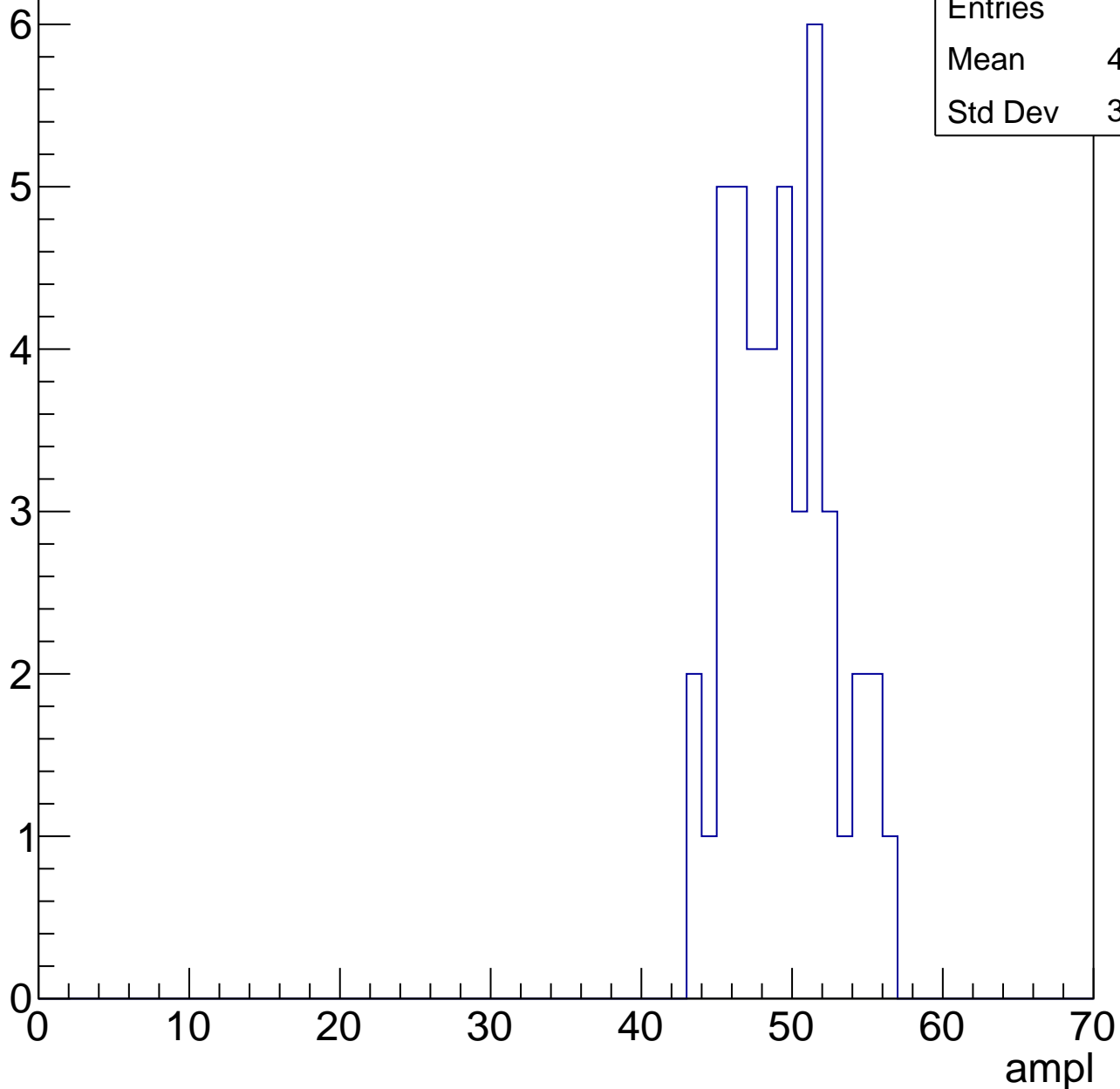


B1L102S, U18-ch23, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	48.84
Std Dev	3.309

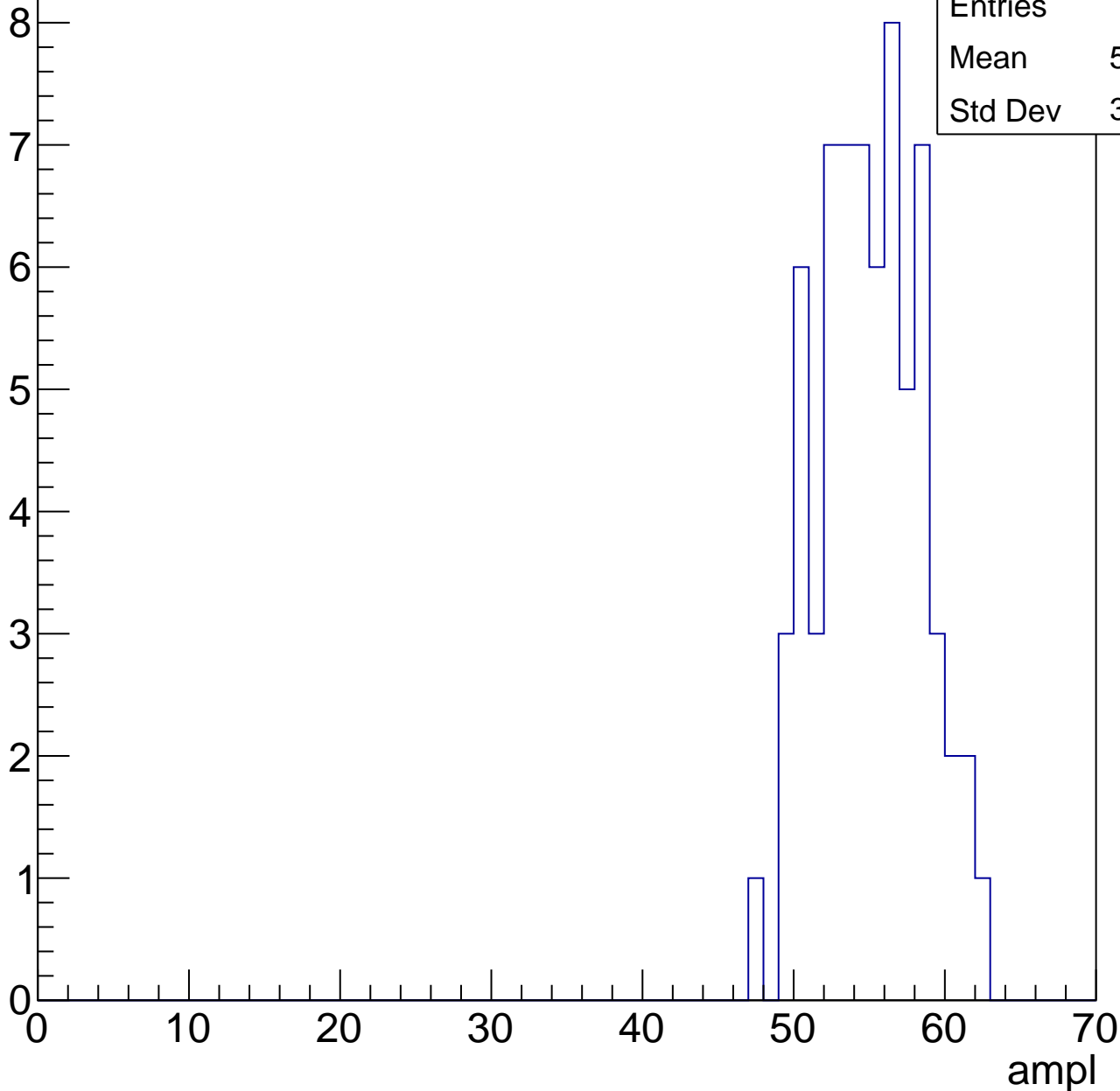


B1L102S, U18-ch23, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.56
Std Dev	3.354

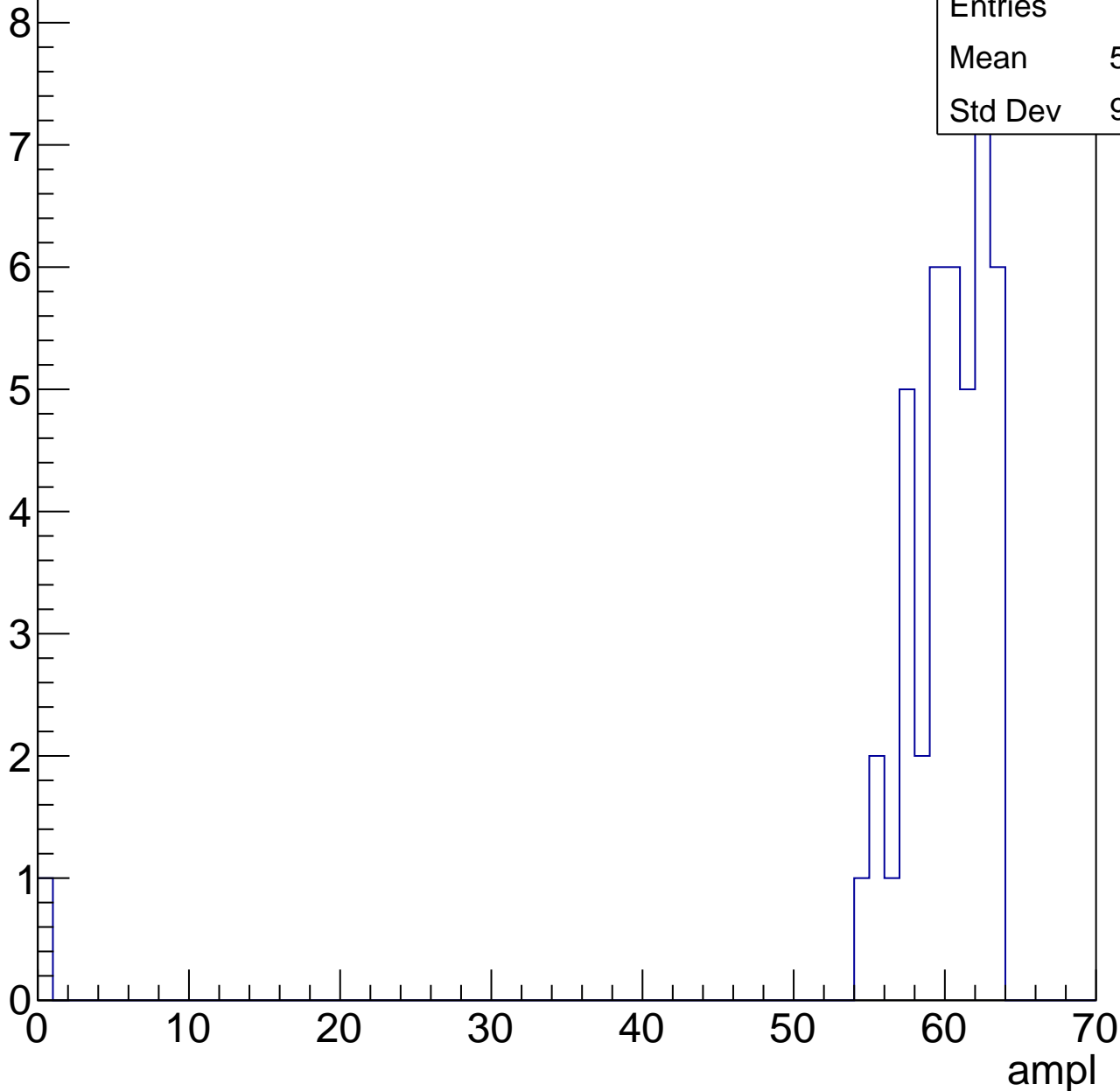


B1L102S, U18-ch23, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

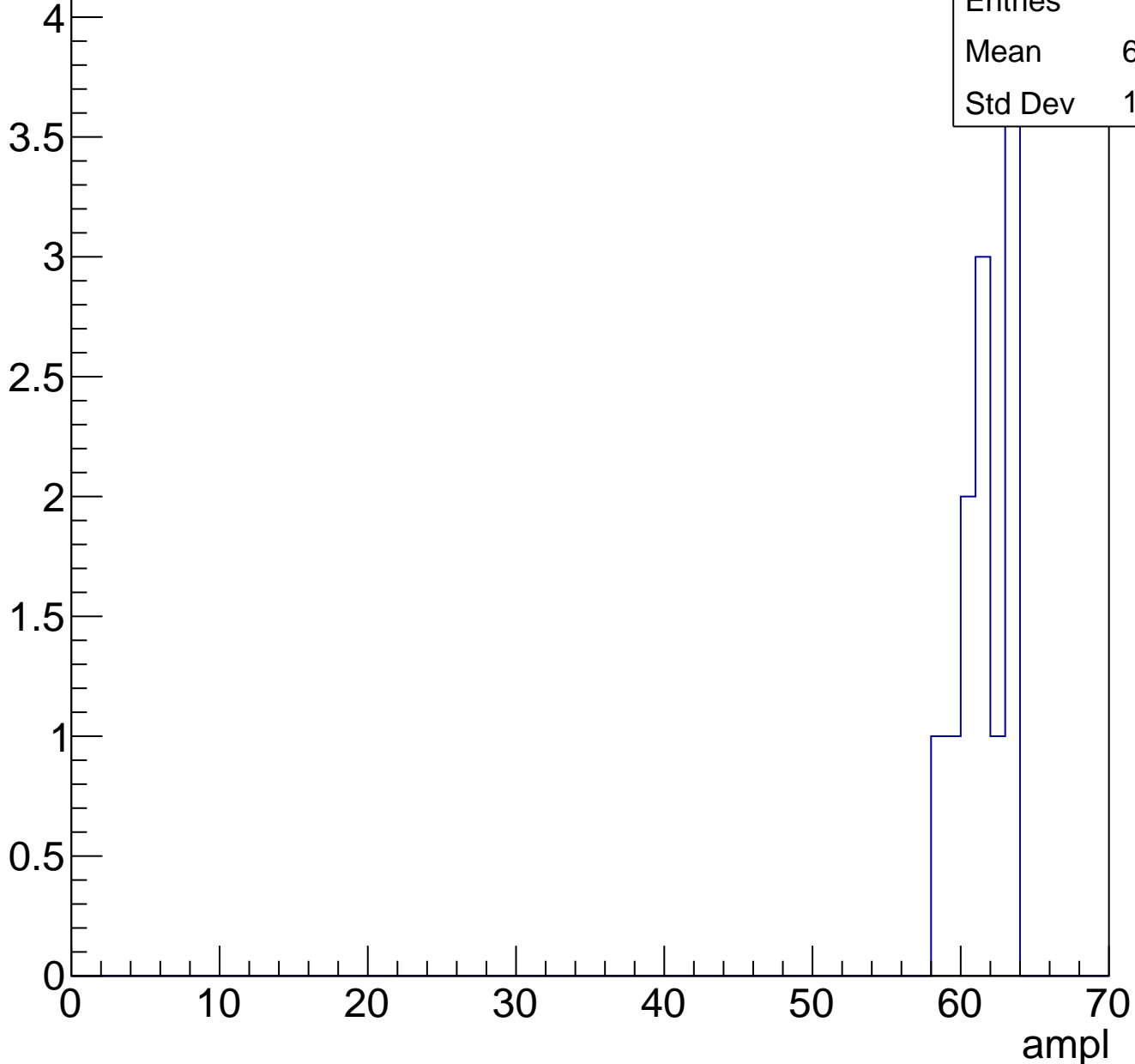
Entries	43
Mean	58.47
Std Dev	9.339



B1L102S, U18-ch23, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch23, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch24, adc0

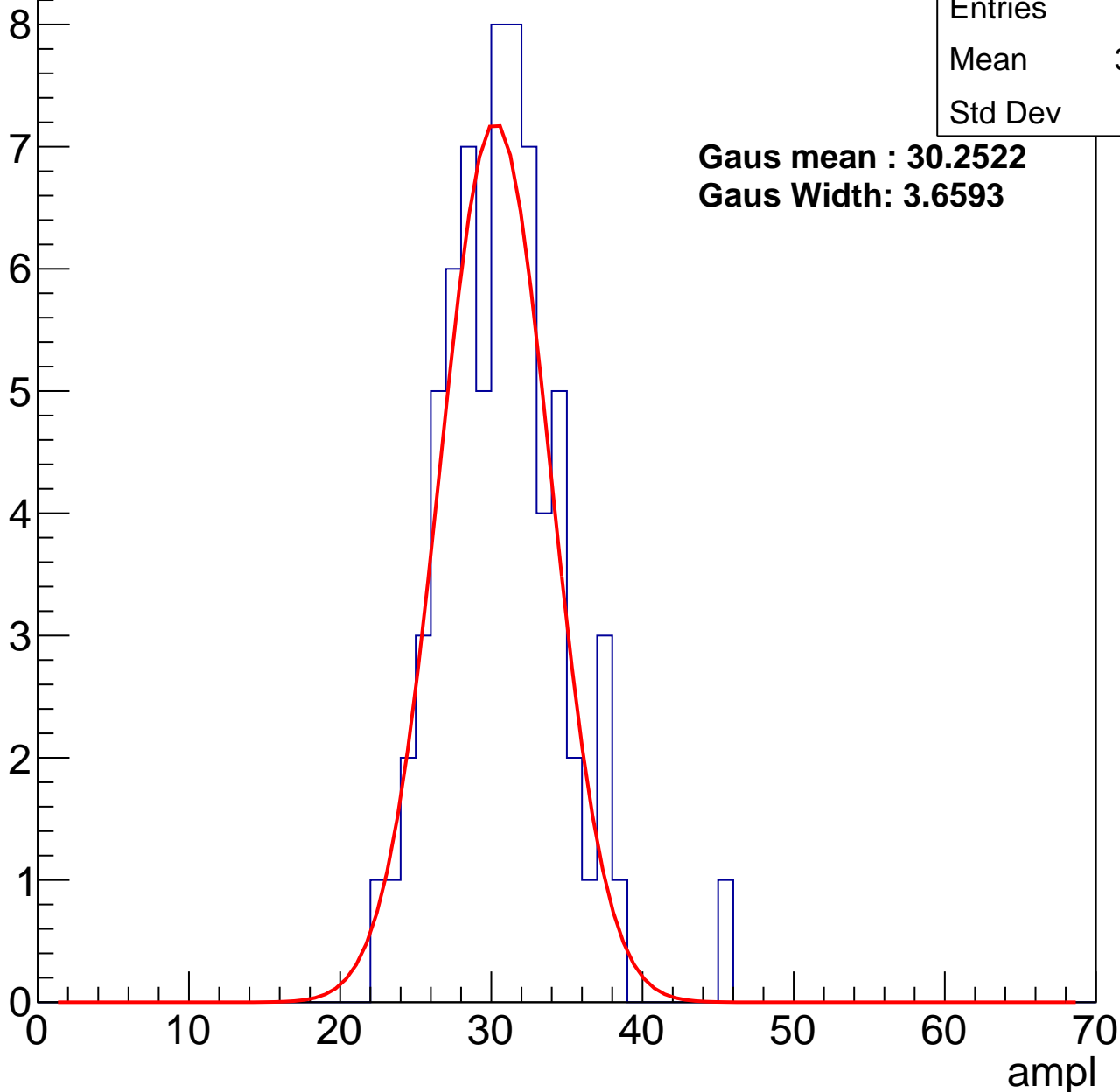
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	30.21
Std Dev	3.96

Gaus mean : 30.2522

Gaus Width: 3.6593



B1L102S, U18-ch24, adc1

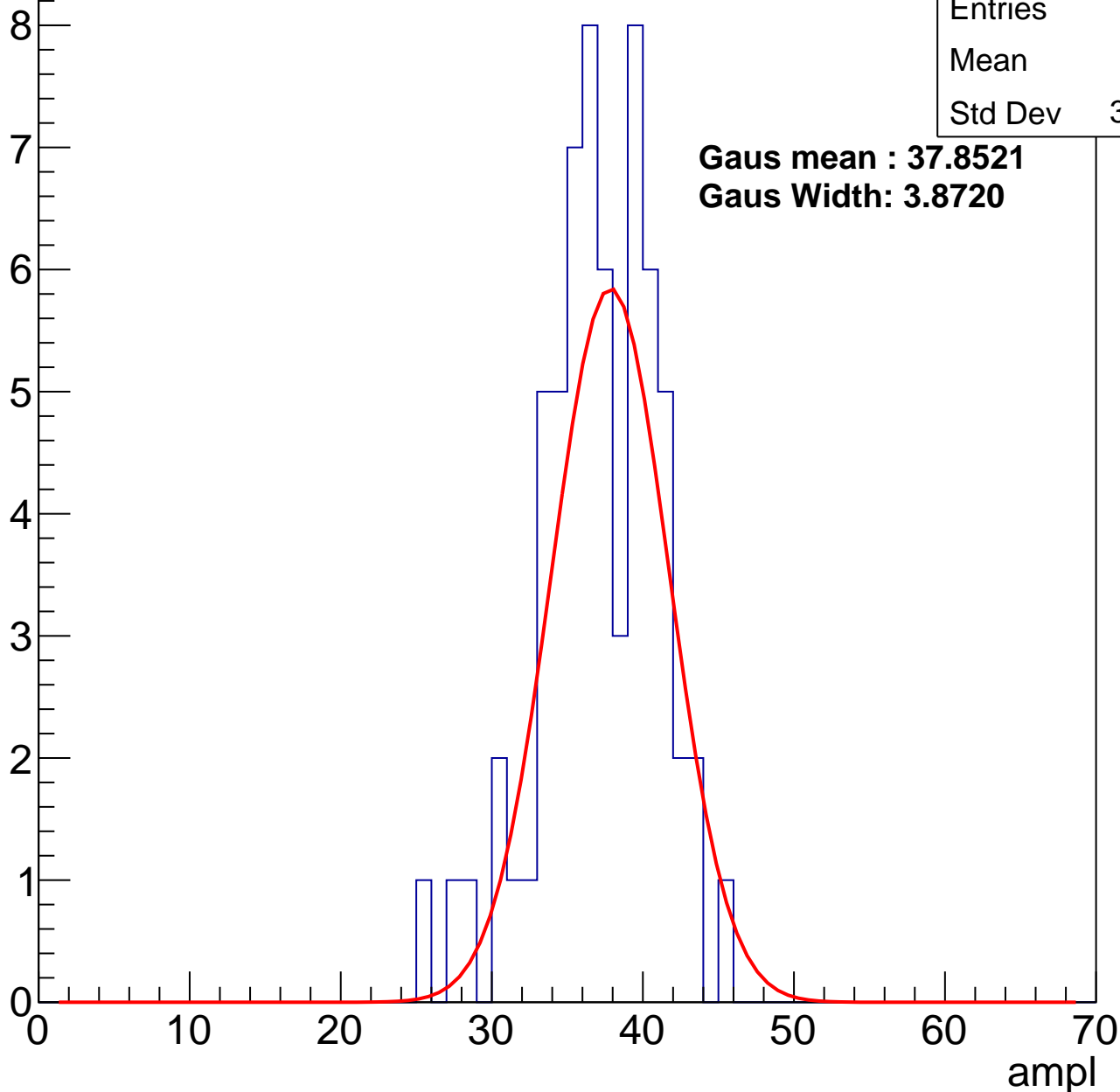
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	36.6
Std Dev	3.902

Gaus mean : 37.8521

Gaus Width: 3.8720



B1L102S, U18-ch24, adc2

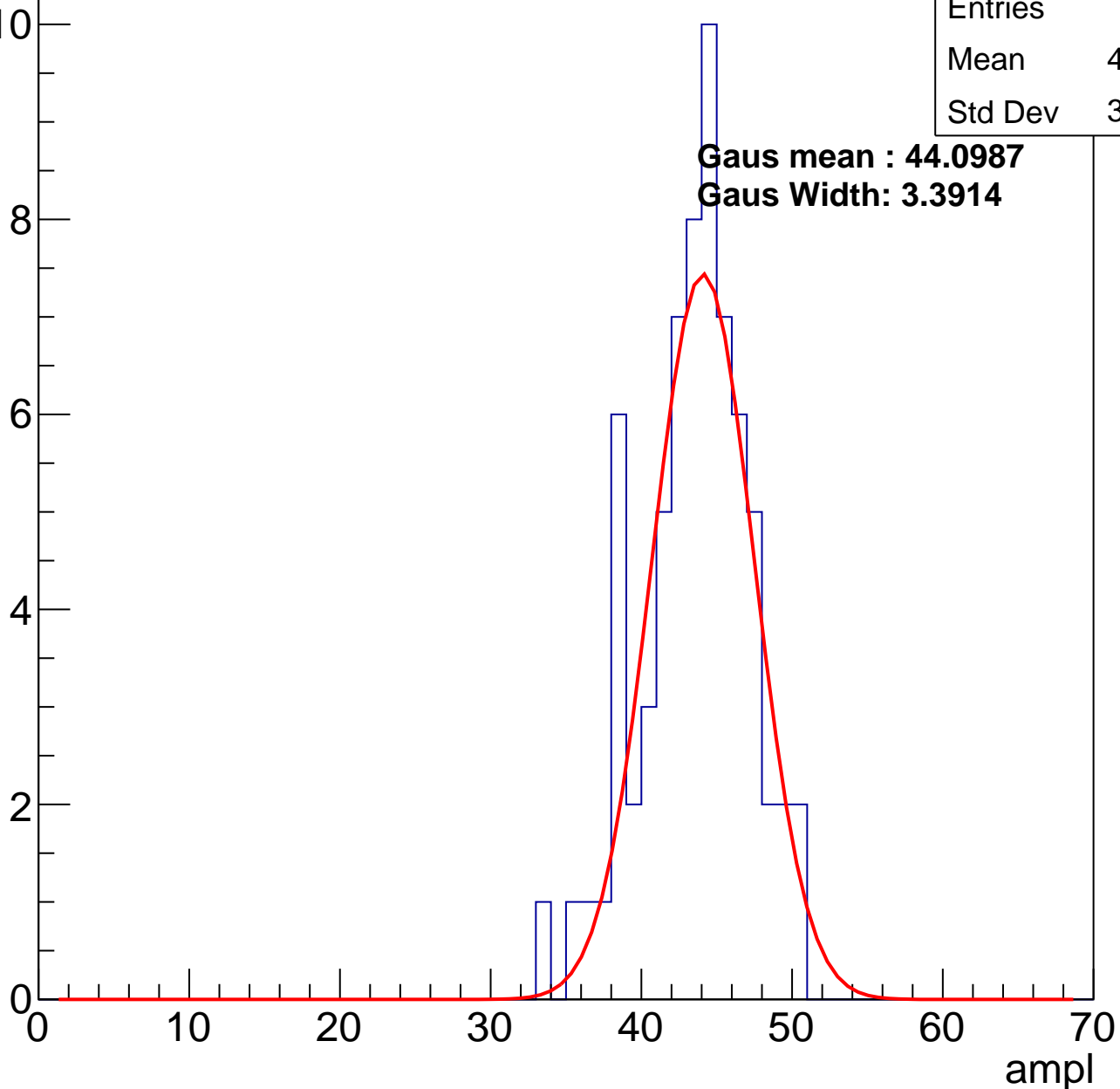
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	43.04
Std Dev	3.565

Gaus mean : 44.0987

Gaus Width: 3.3914

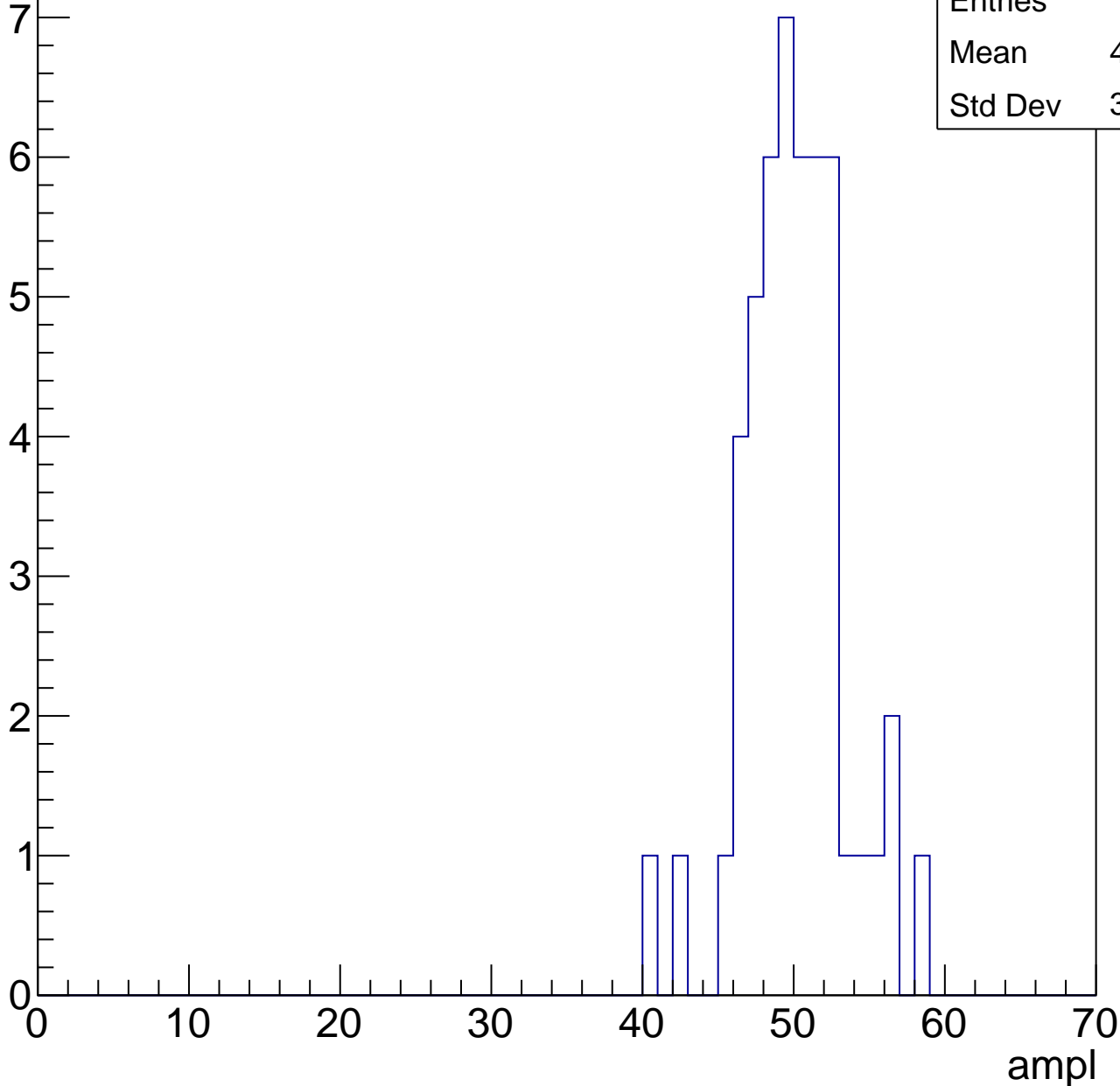


B1L102S, U18-ch24, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	49.53
Std Dev	3.302

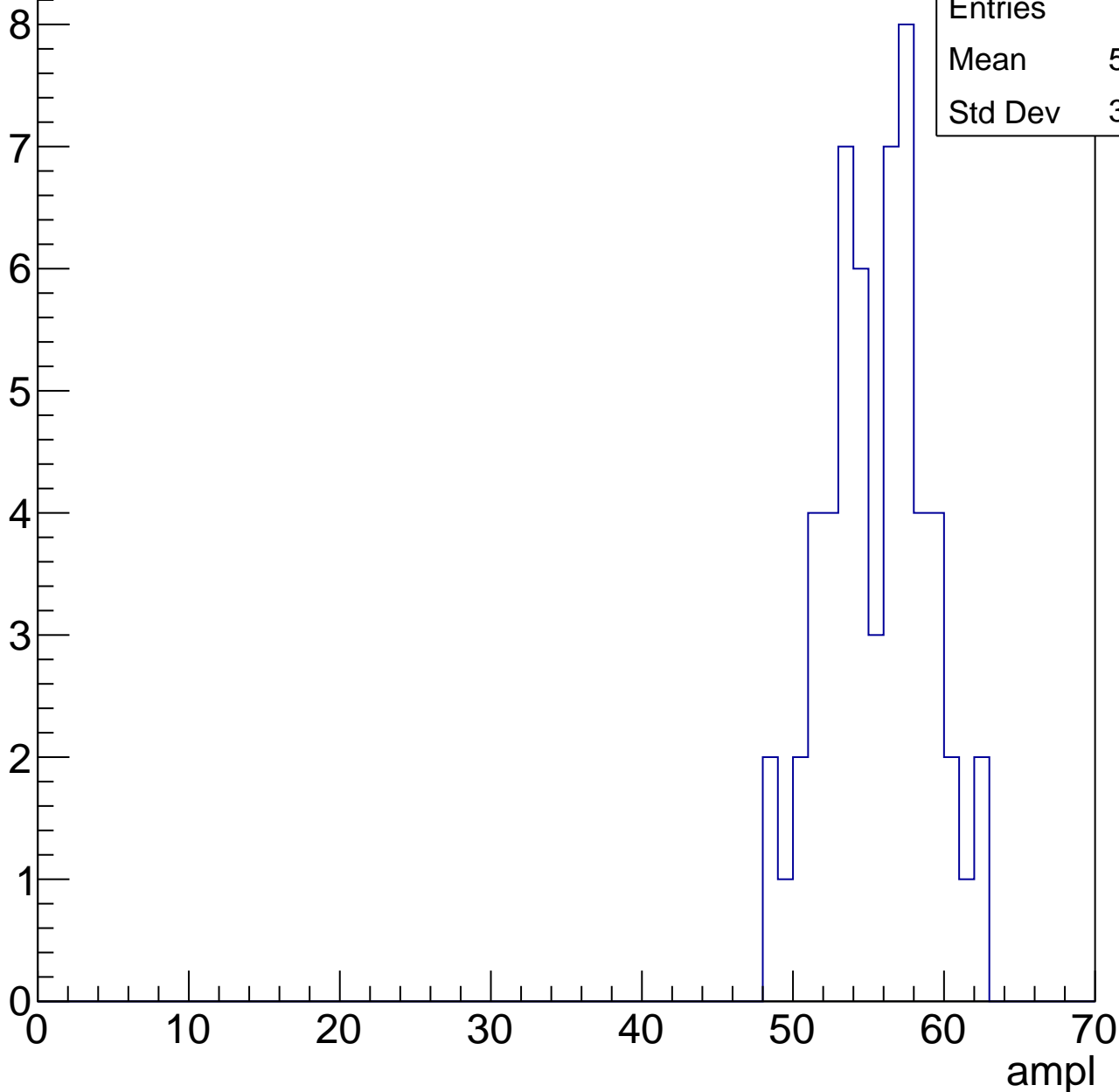


B1L102S, U18-ch24, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

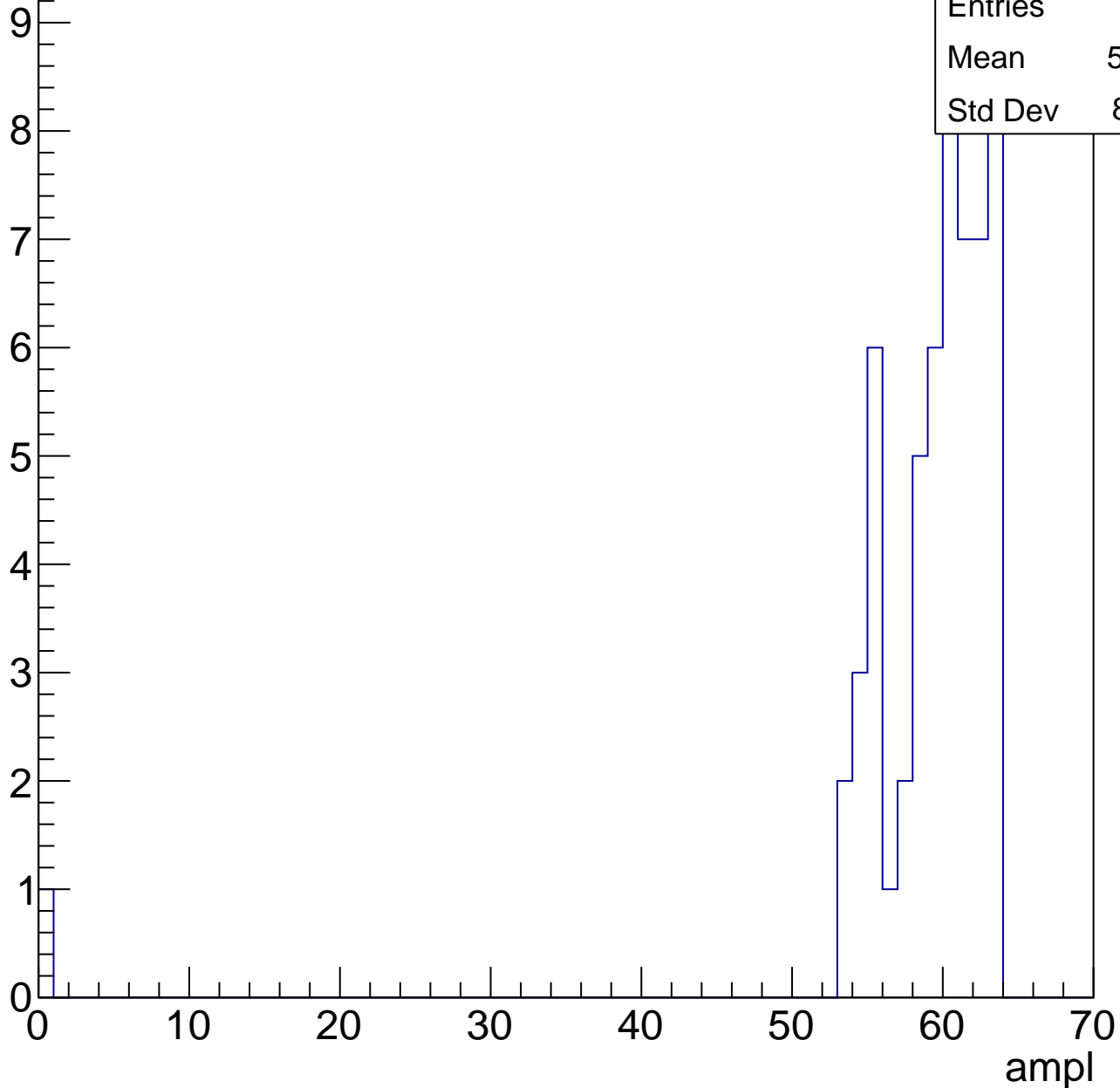
Entries	57
Mean	55.05
Std Dev	3.353



B1L102S, U18-ch24, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch24, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L102S, U18-ch24, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch25, adc0

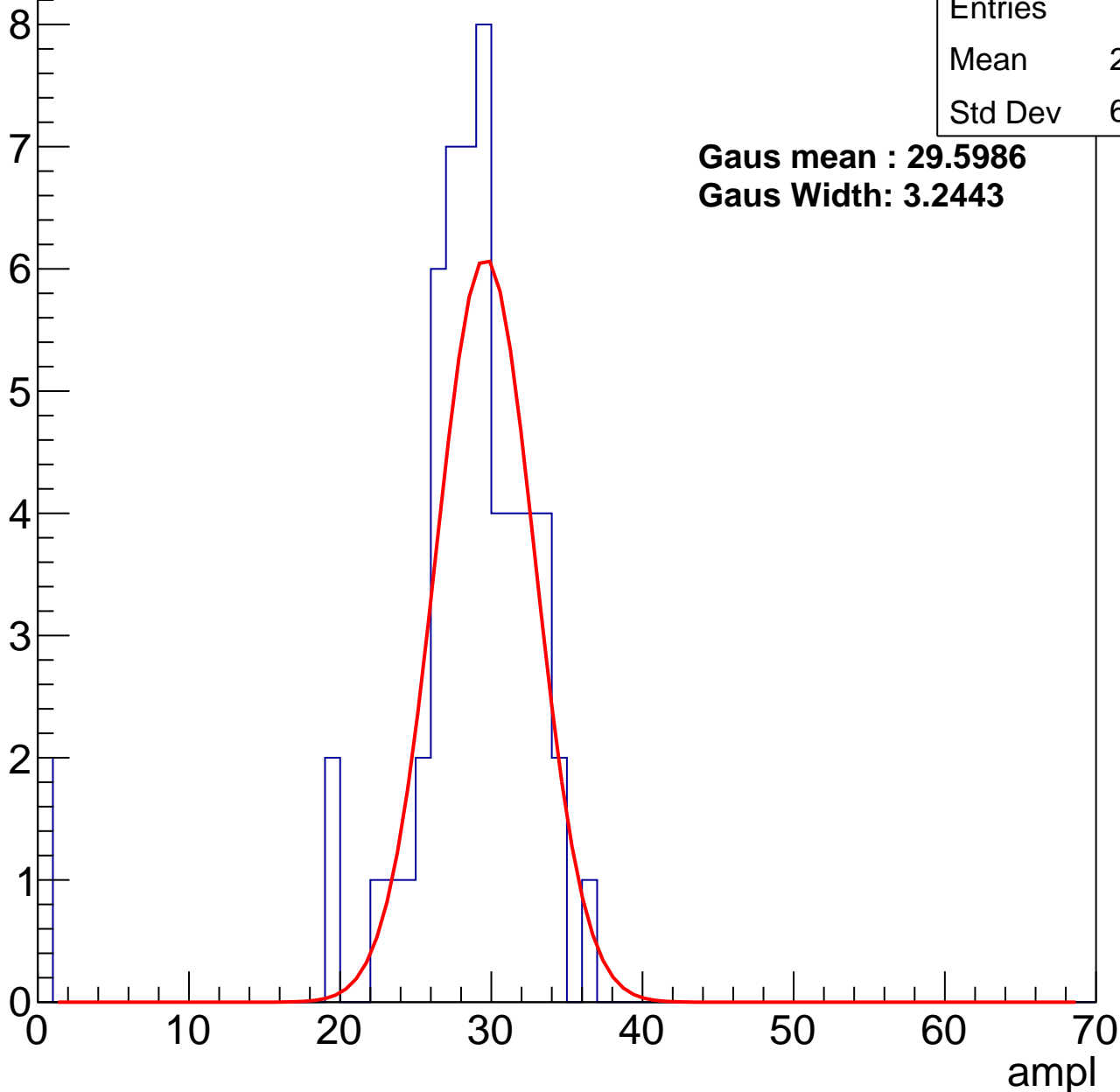
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	27.46
Std Dev	6.268

Gaus mean : 29.5986

Gaus Width: 3.2443



B1L102S, U18-ch25, adc1

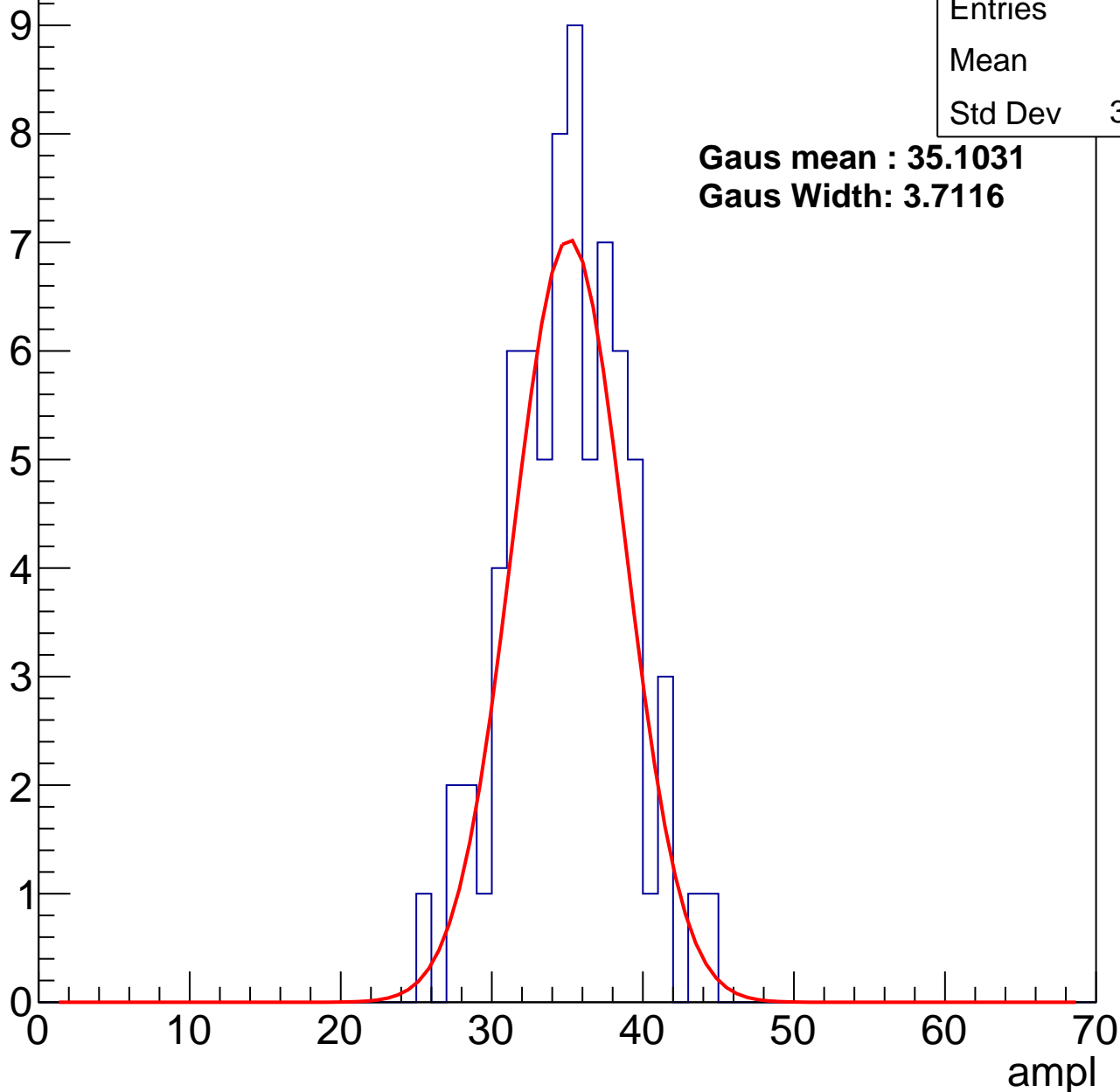
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.6
Std Dev	3.835

Gaus mean : 35.1031

Gaus Width: 3.7116



B1L102S, U18-ch25, adc2

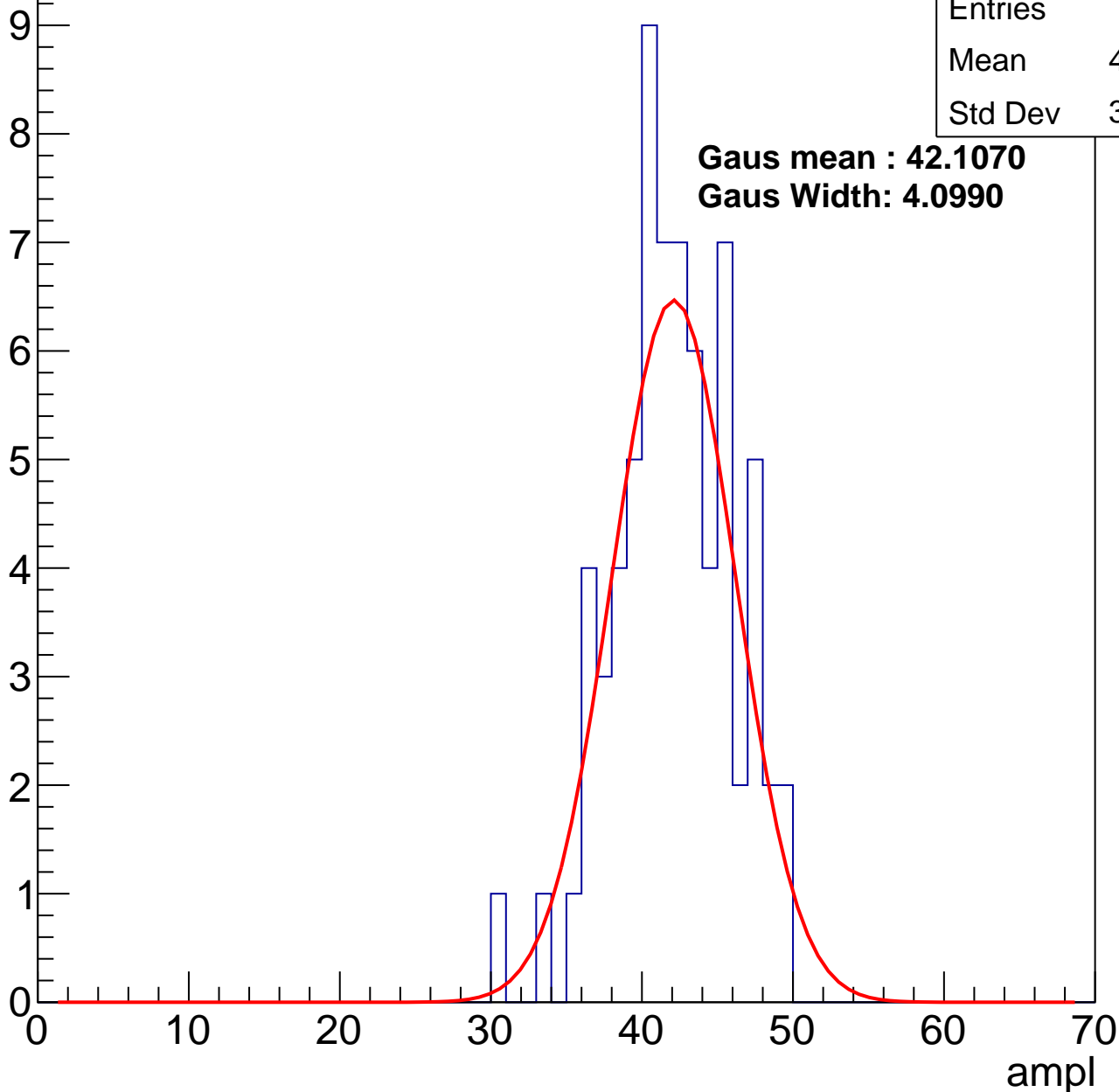
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	41.59
Std Dev	3.864

Gaus mean : 42.1070

Gaus Width: 4.0990

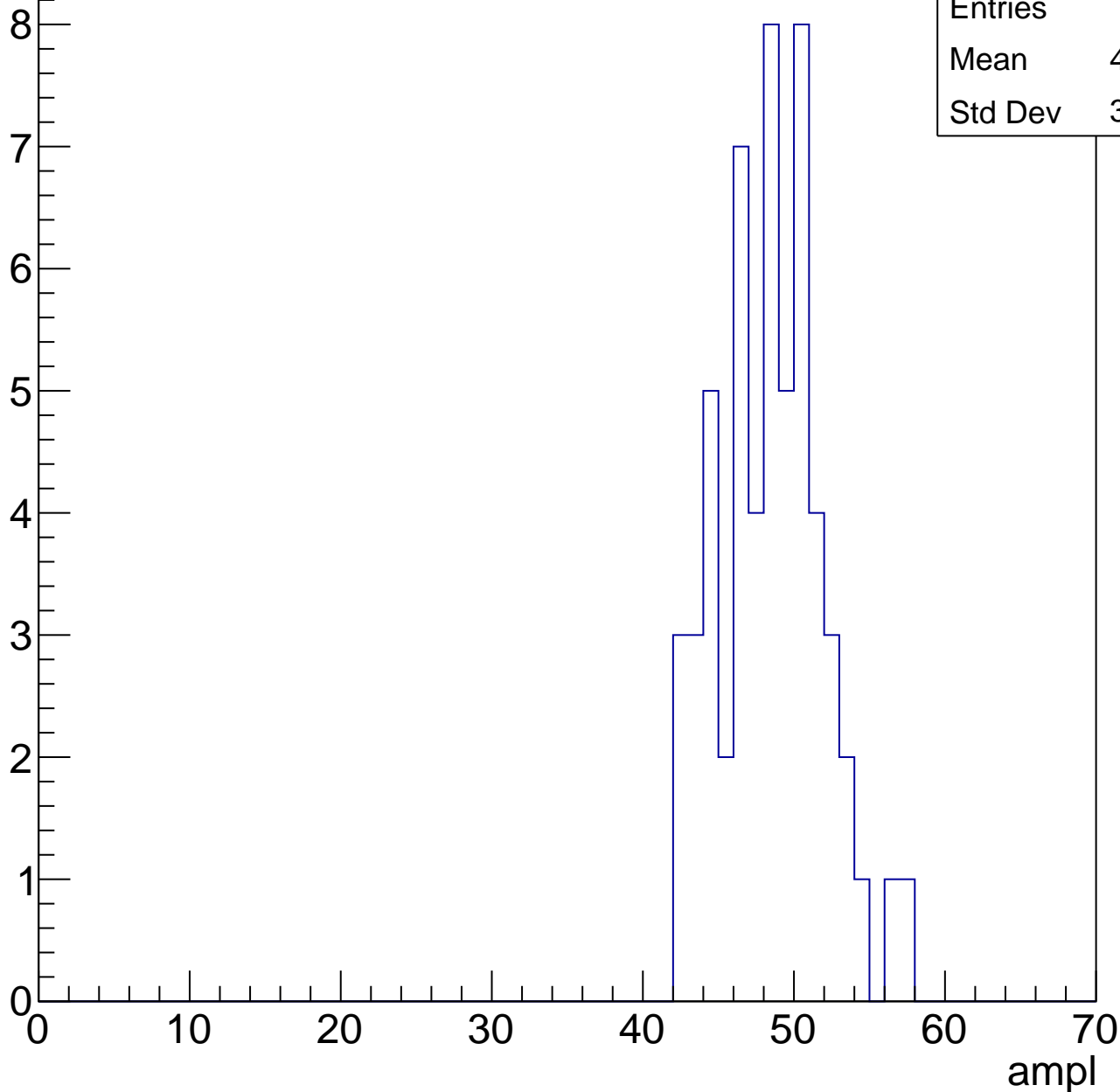


B1L102S, U18-ch25, adc3

calib_packv5_042523_0143.root, FC#11, port A2

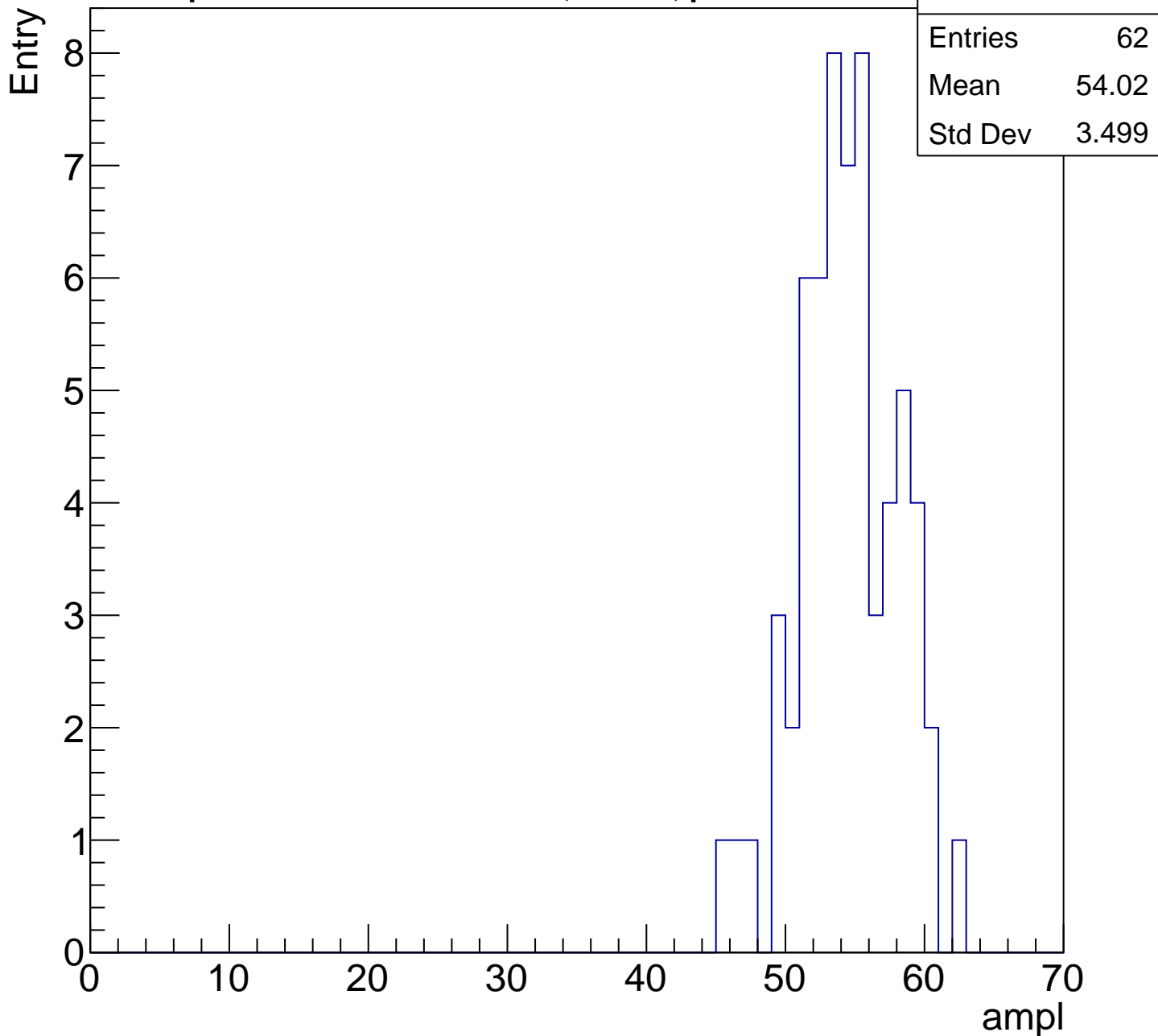
Entry

Entries	57
Mean	48.02
Std Dev	3.416



B1L102S, U18-ch25, adc4

calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U18-ch25, adc5

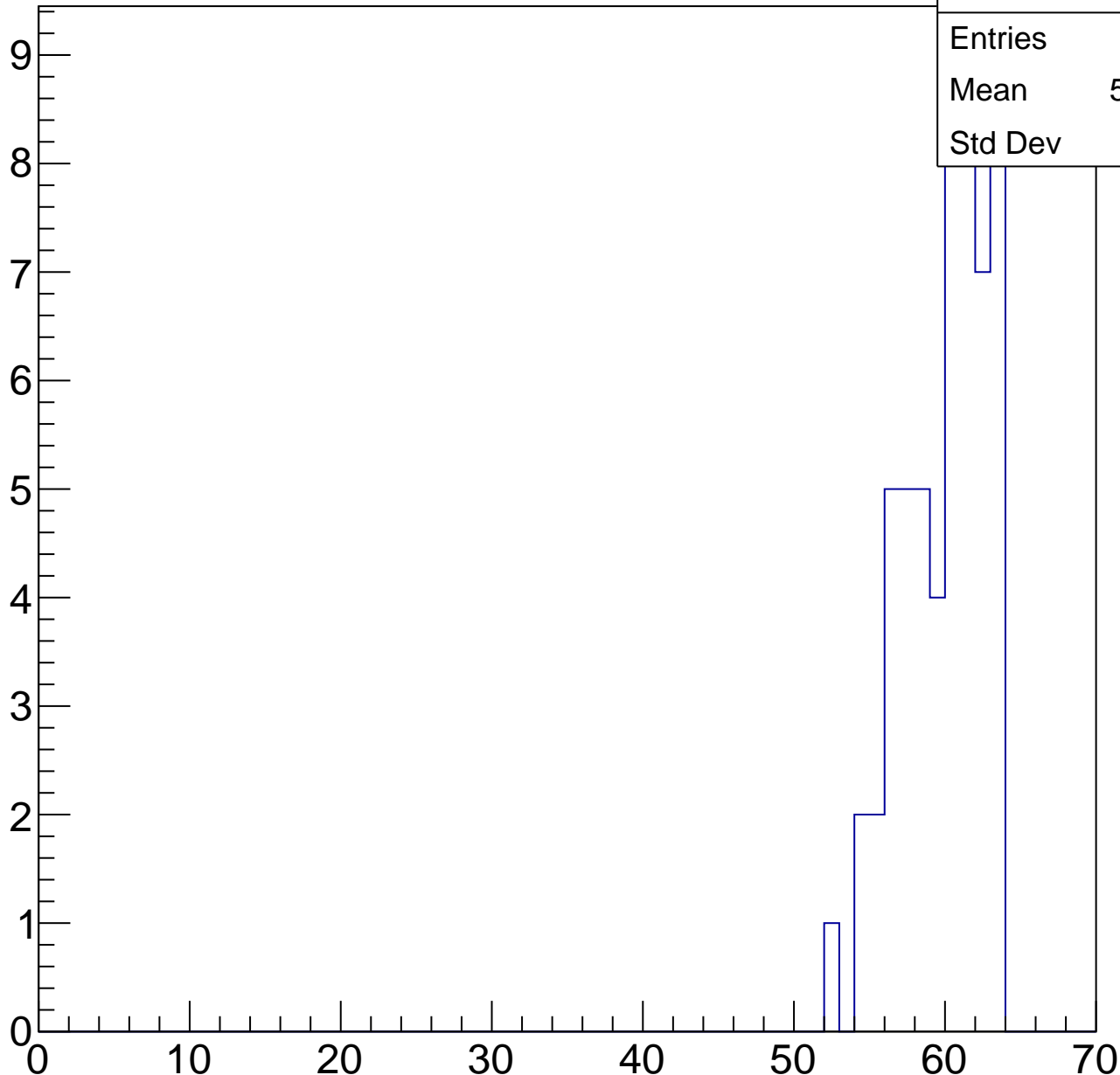
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.46
Std Dev	2.79

ampl



B1L102S, U18-ch25, adc6

calib_packv5_042523_0143.root, FC#11, port A2

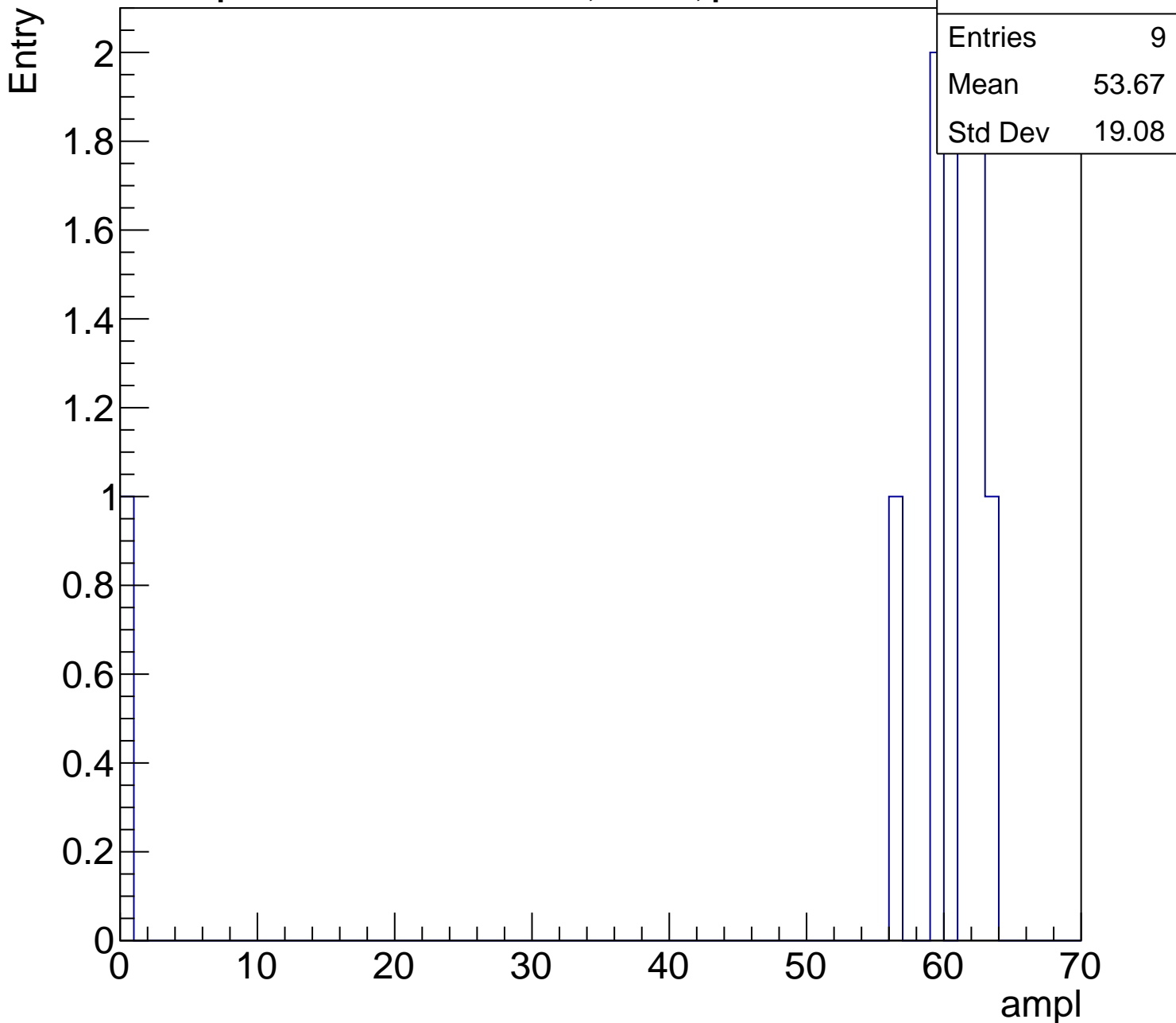
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.67
Std Dev	19.08

0 10 20 30 40 50 60 70

ampl



B1L102S, U18-ch25, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch26, adc0

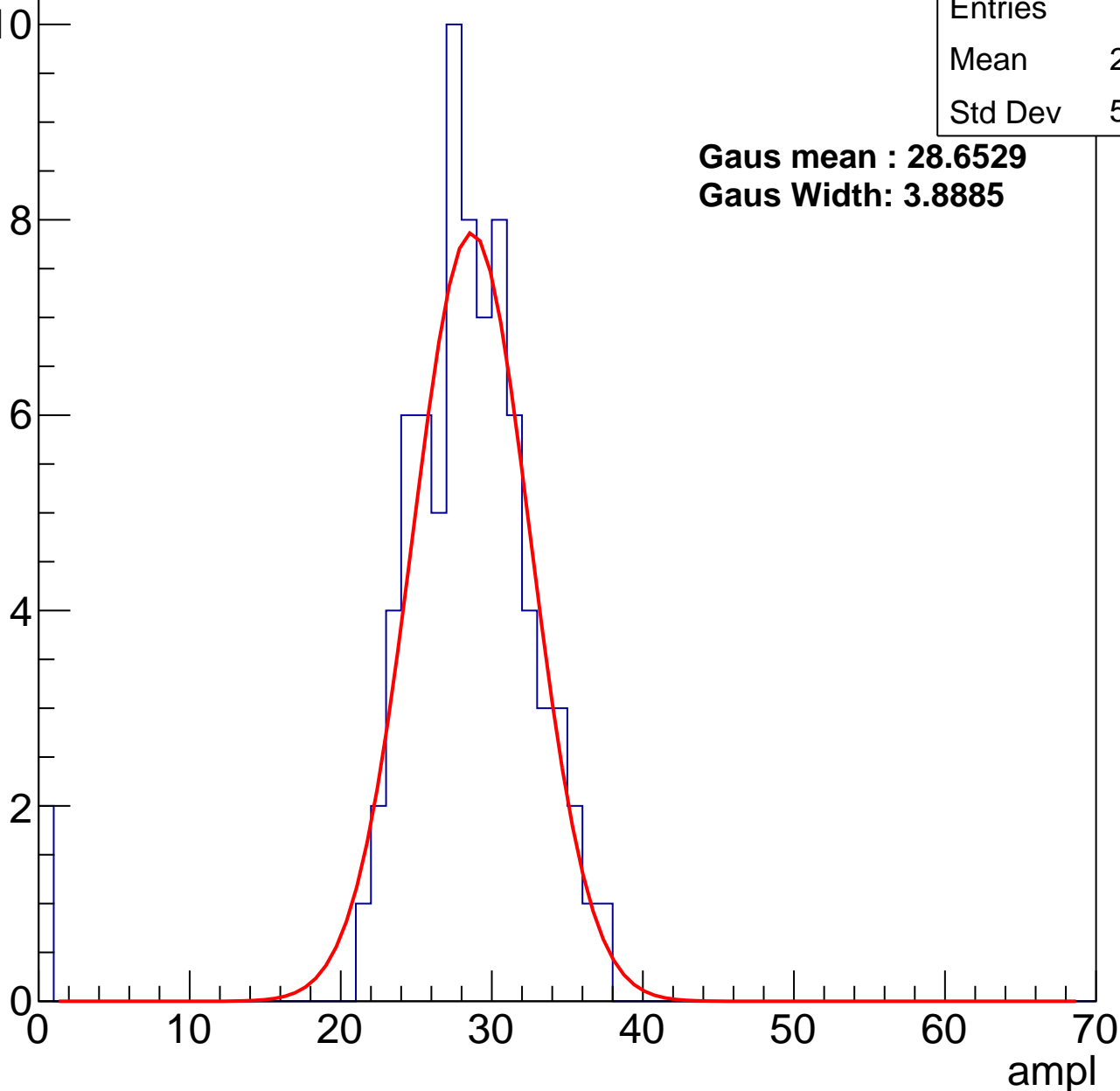
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	27.54
Std Dev	5.665

Gaus mean : 28.6529

Gaus Width: 3.8885



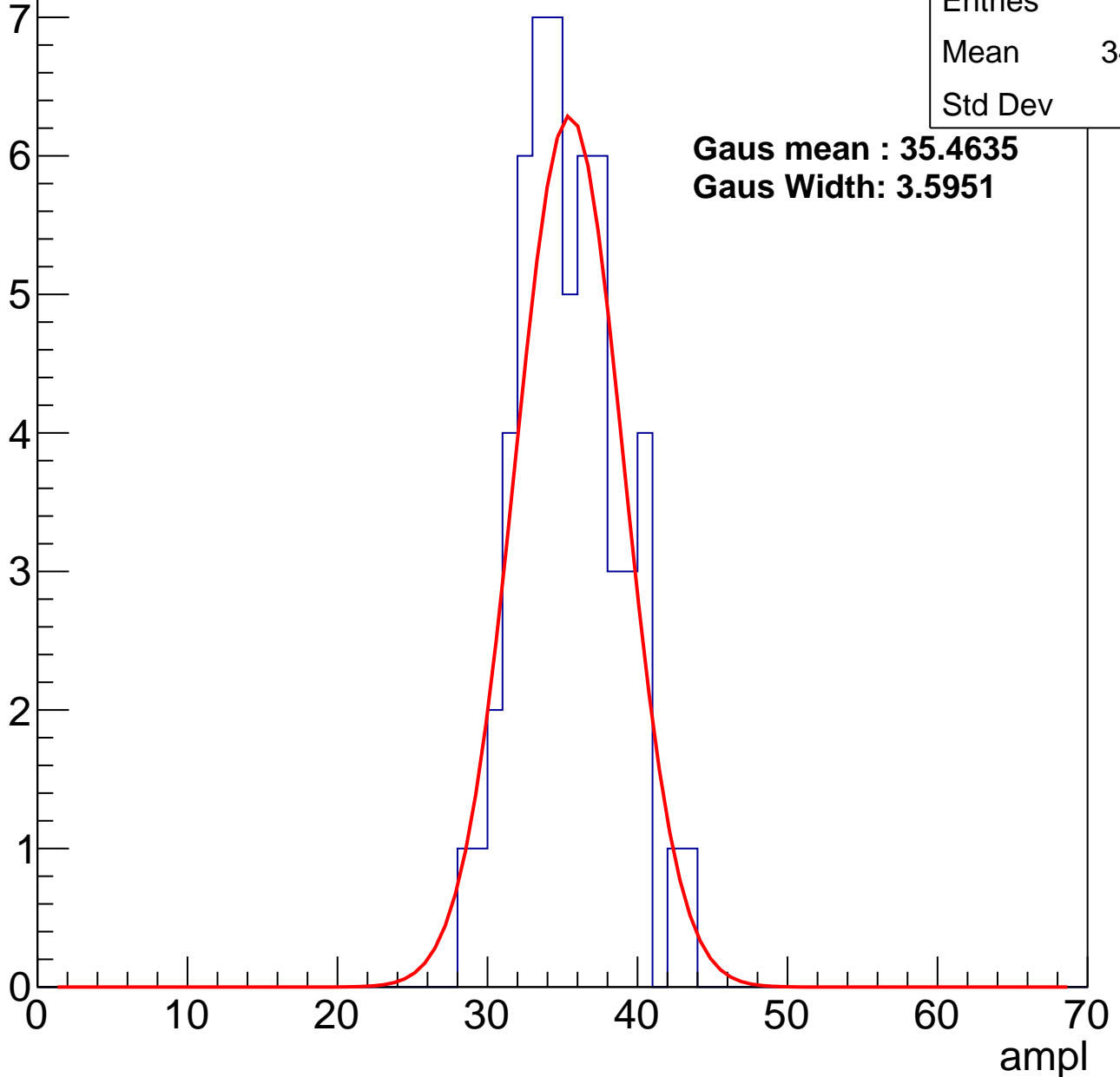
B1L102S, U18-ch26, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	34.93
Std Dev	3.26

Gaus mean : 35.4635
Gaus Width: 3.5951



B1L102S, U18-ch26, adc2

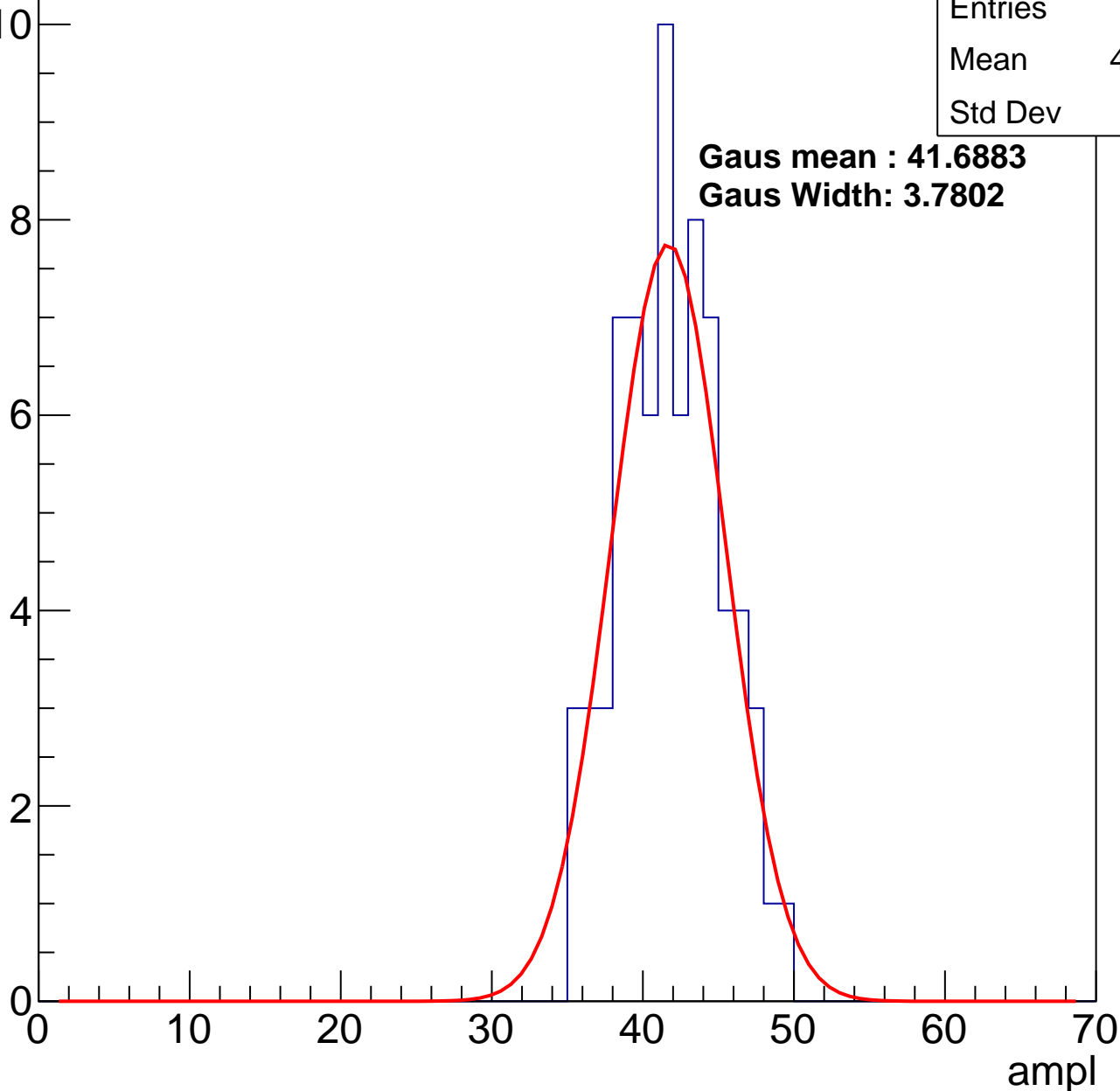
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.36
Std Dev	3.32

Gaus mean : 41.6883

Gaus Width: 3.7802

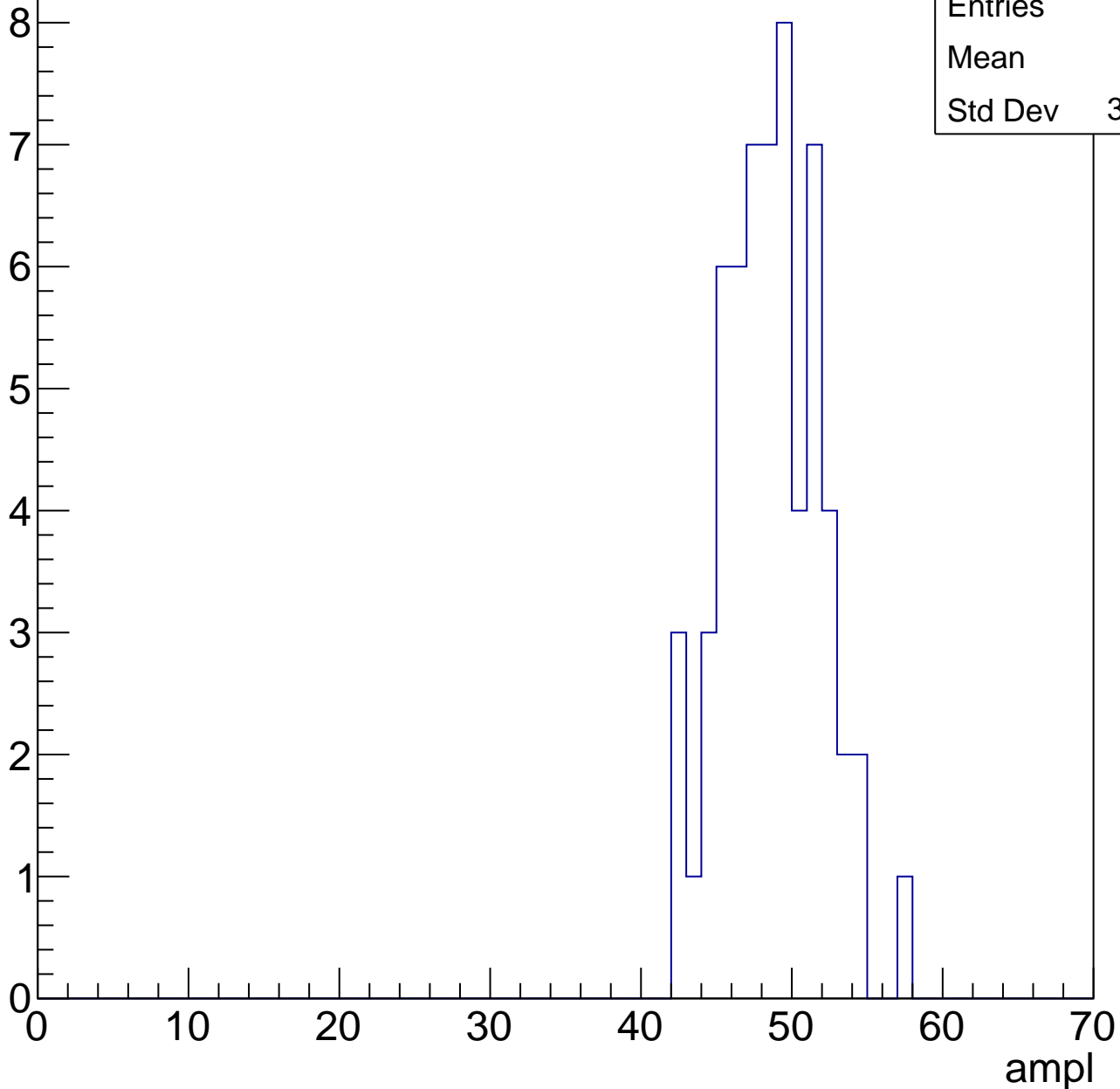


B1L102S, U18-ch26, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.2
Std Dev	3.182

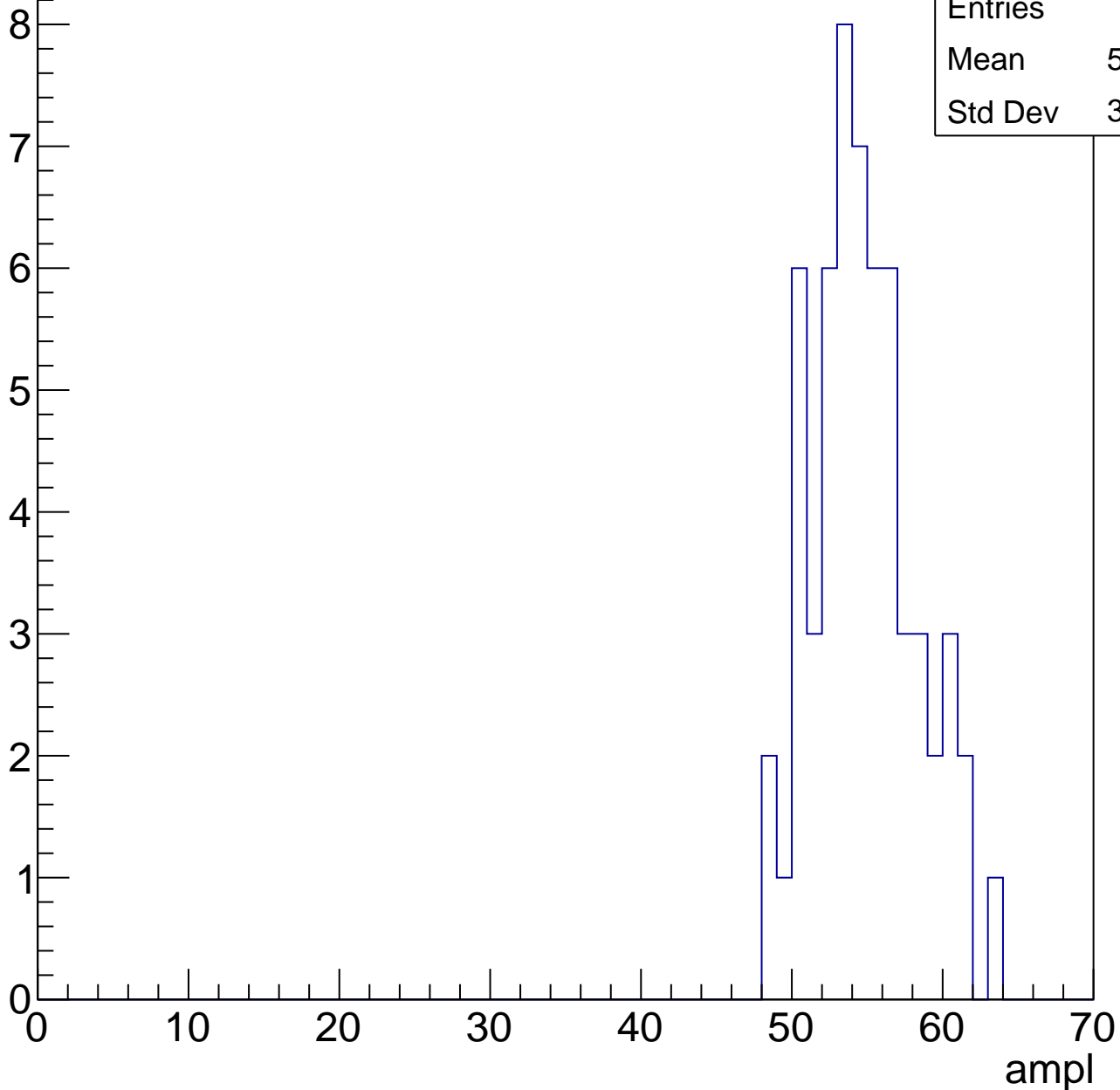


B1L102S, U18-ch26, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	54.34
Std Dev	3.413

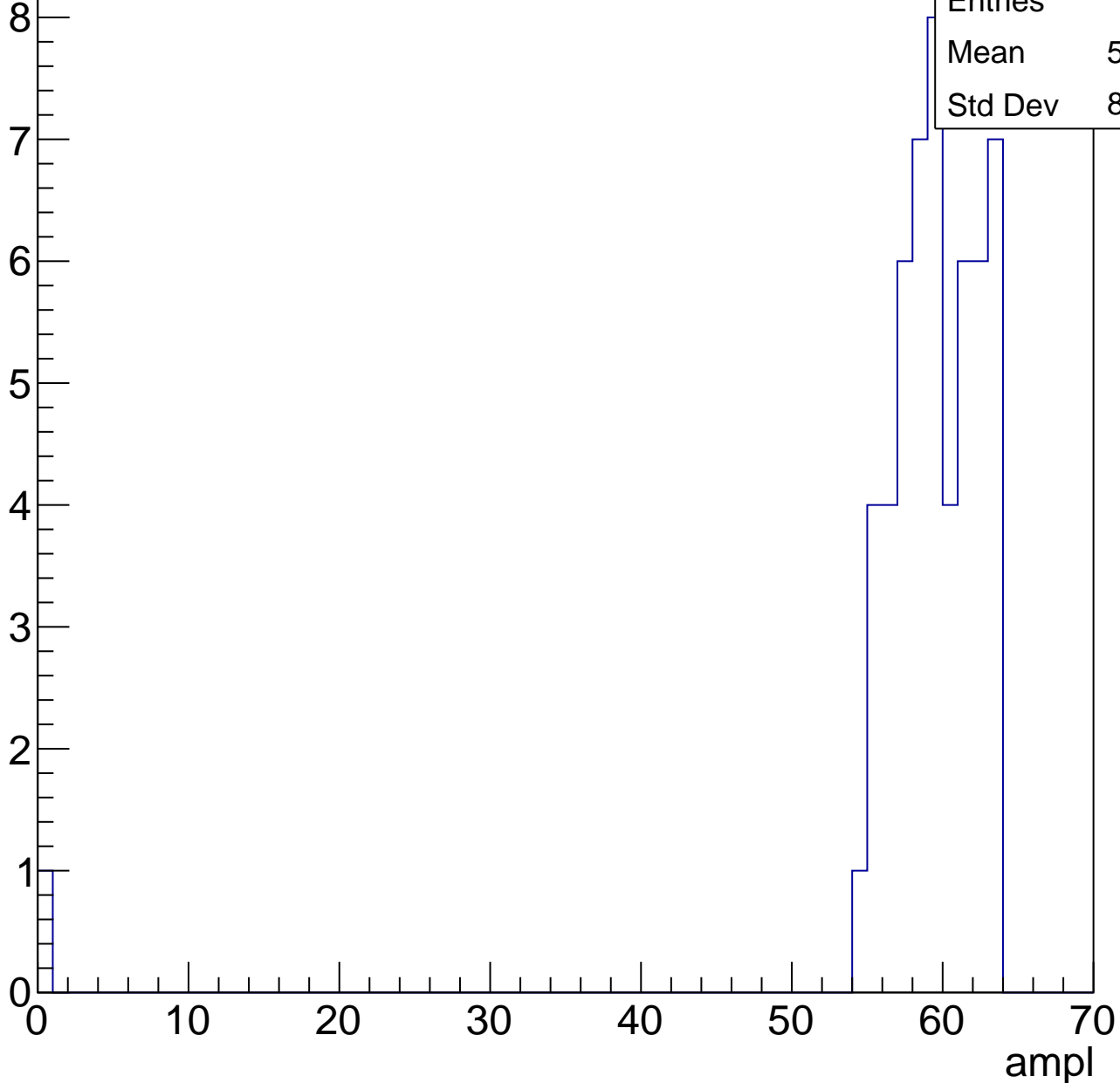


B1L102S, U18-ch26, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

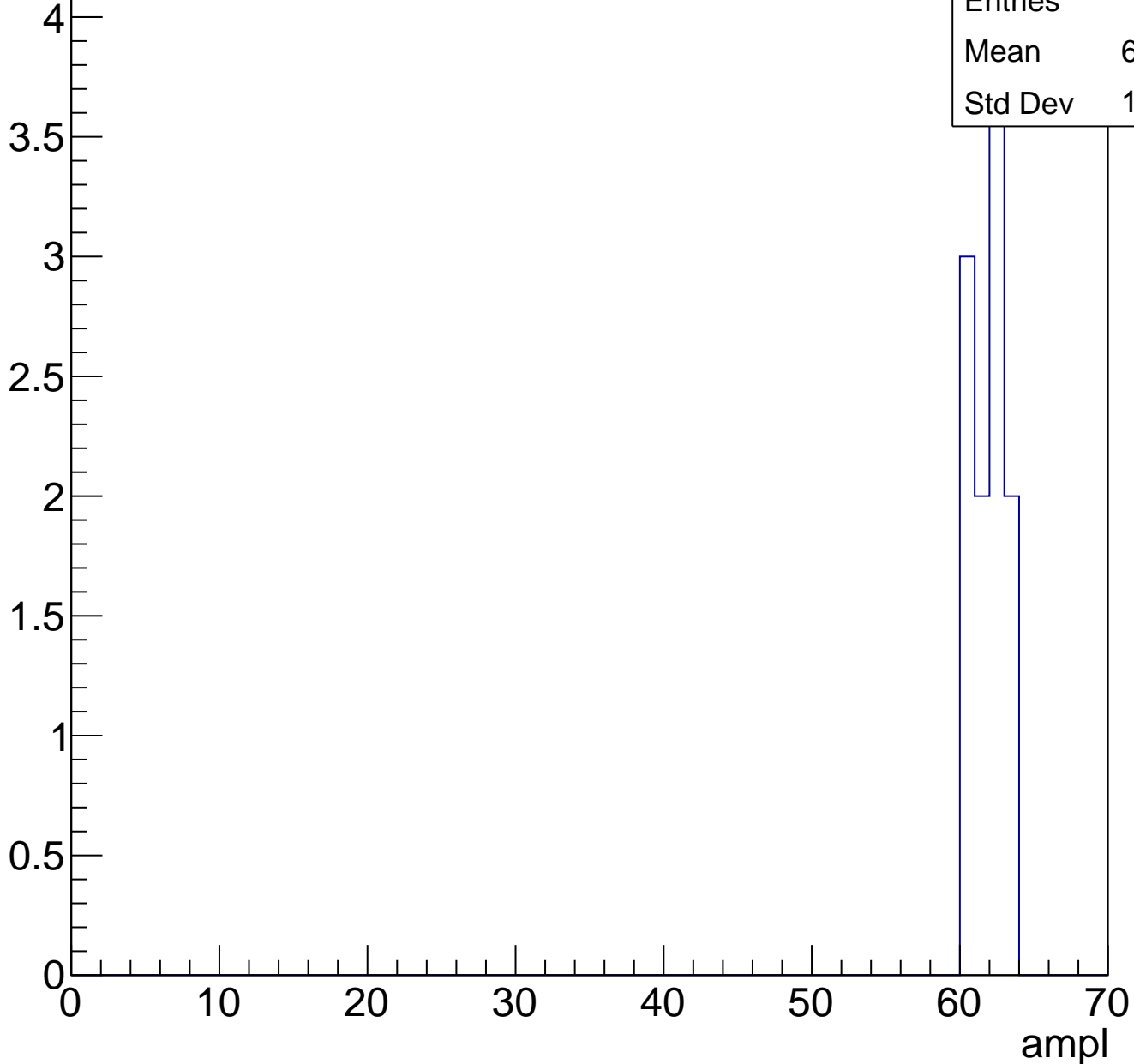
Entries	54
Mean	58.09
Std Dev	8.374



B1L102S, U18-ch26, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch26, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch27, adc0

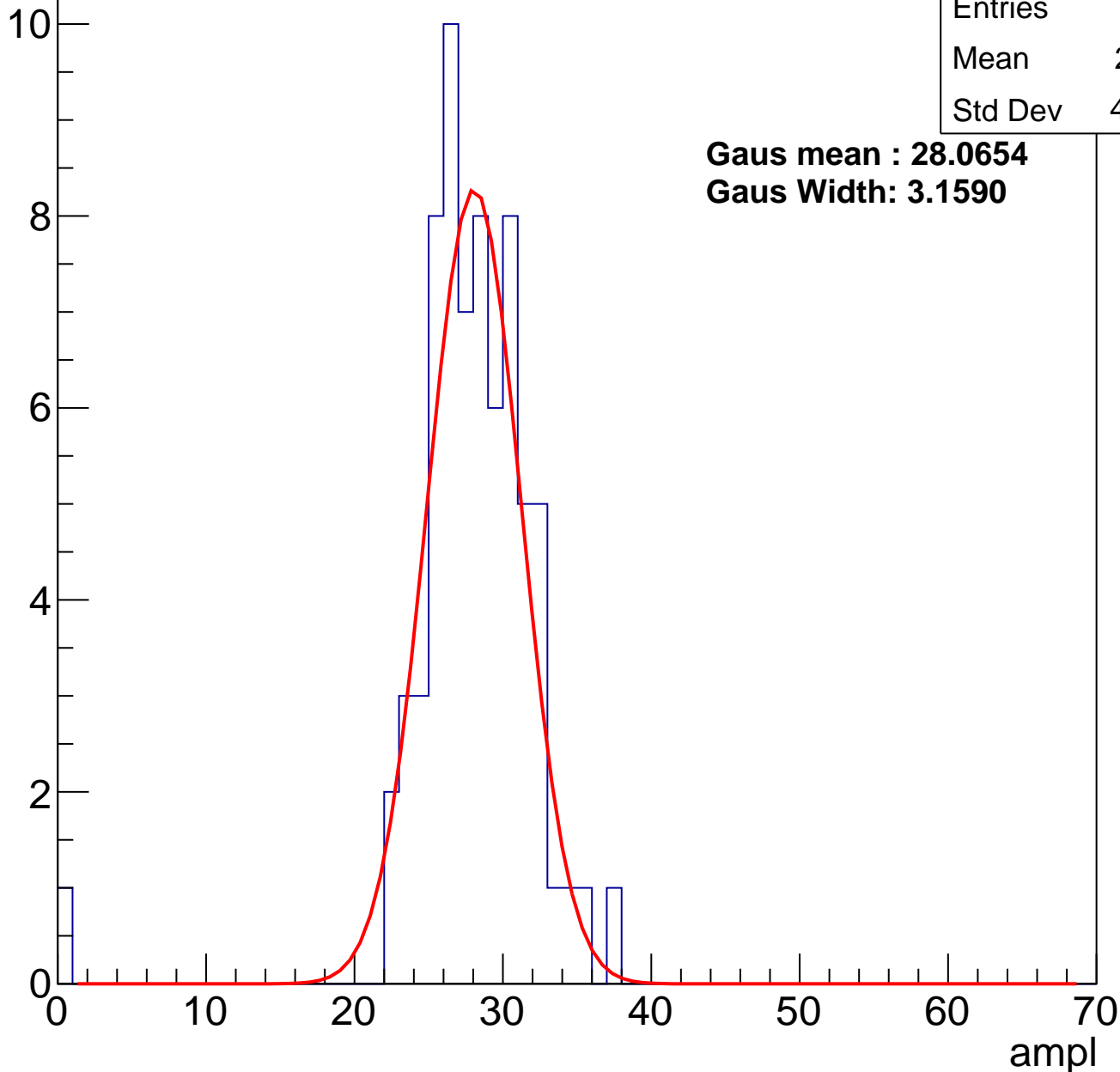
calib_packv5_042523_0143.root, FC#11, port A2

Entries	70
Mean	27.51
Std Dev	4.528

Gaus mean : 28.0654

Gaus Width: 3.1590

Entry



B1L102S, U18-ch27, adc1

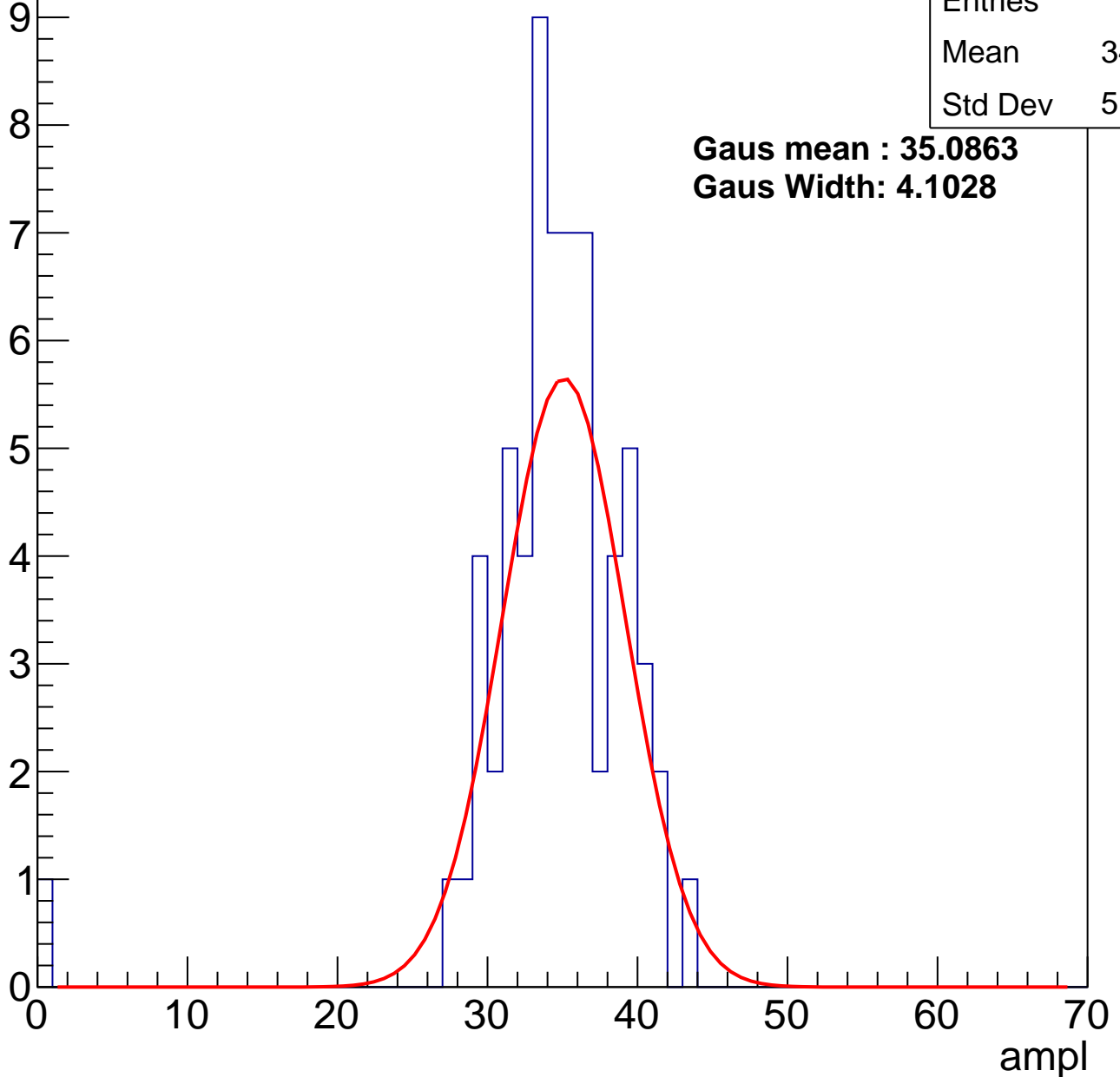
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.03
Std Dev	5.508

Gaus mean : 35.0863

Gaus Width: 4.1028



B1L102S, U18-ch27, adc2

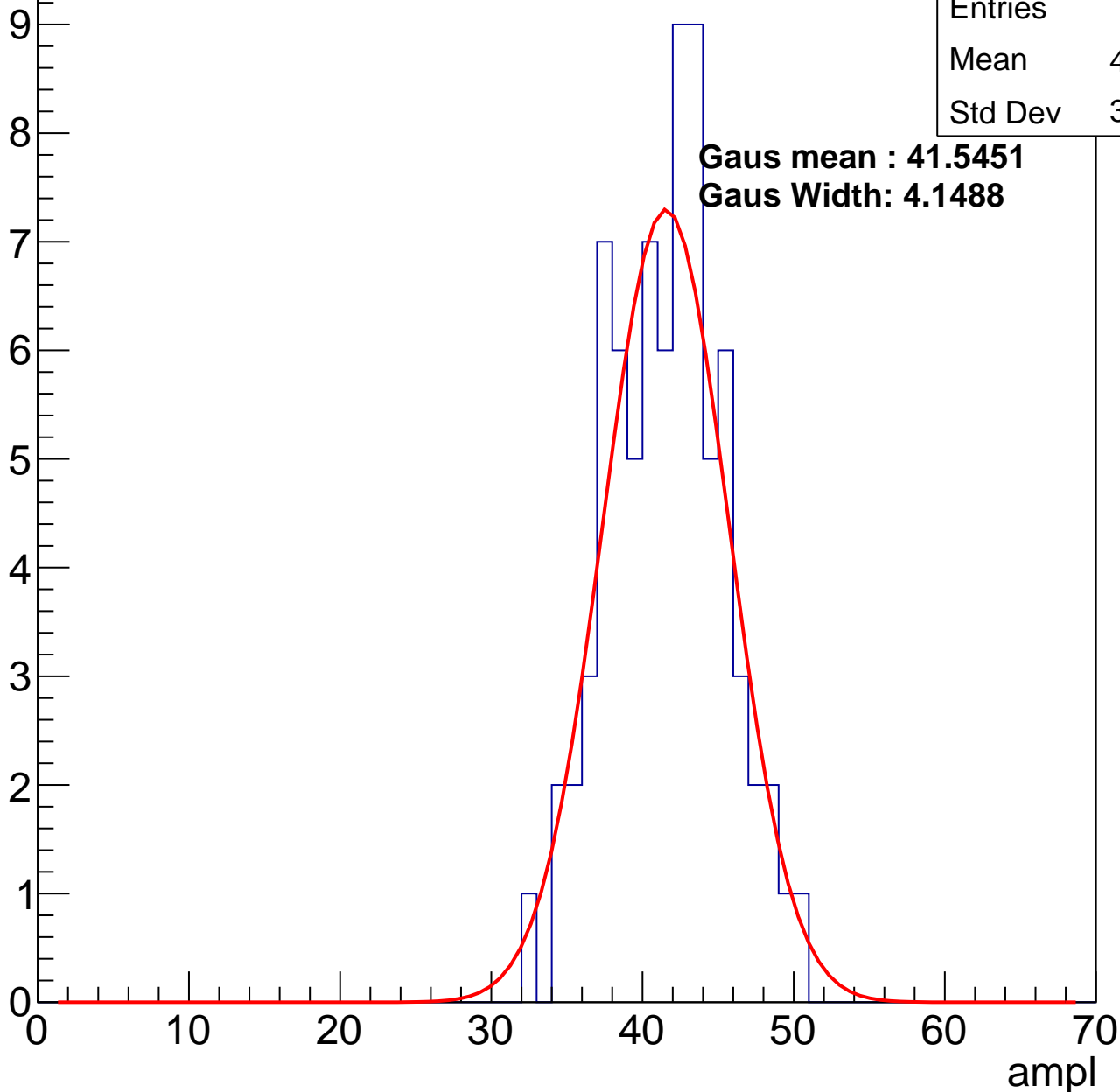
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	41.14
Std Dev	3.768

Gaus mean : 41.5451

Gaus Width: 4.1488

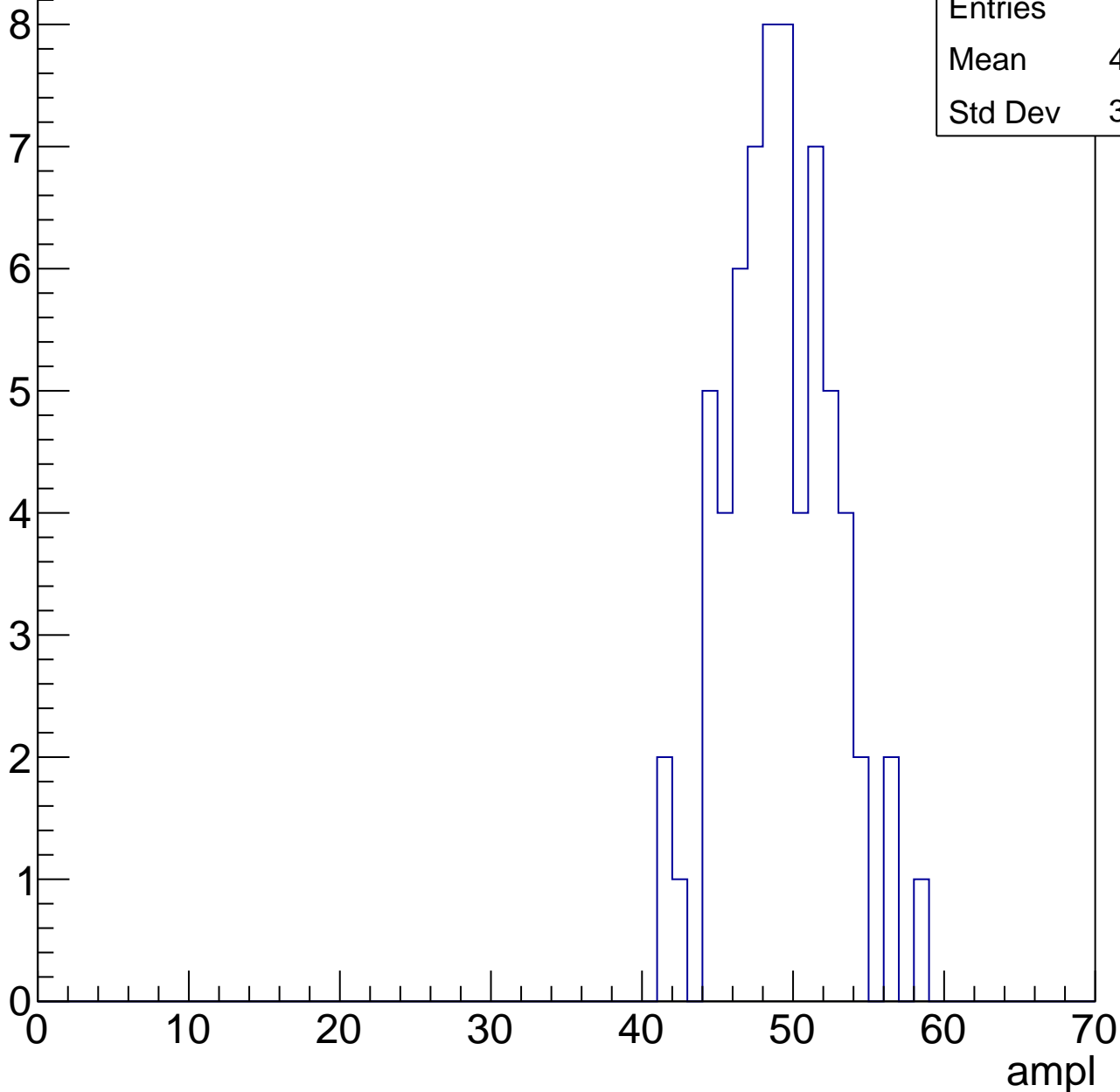


B1L102S, U18-ch27, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.67
Std Dev	3.522

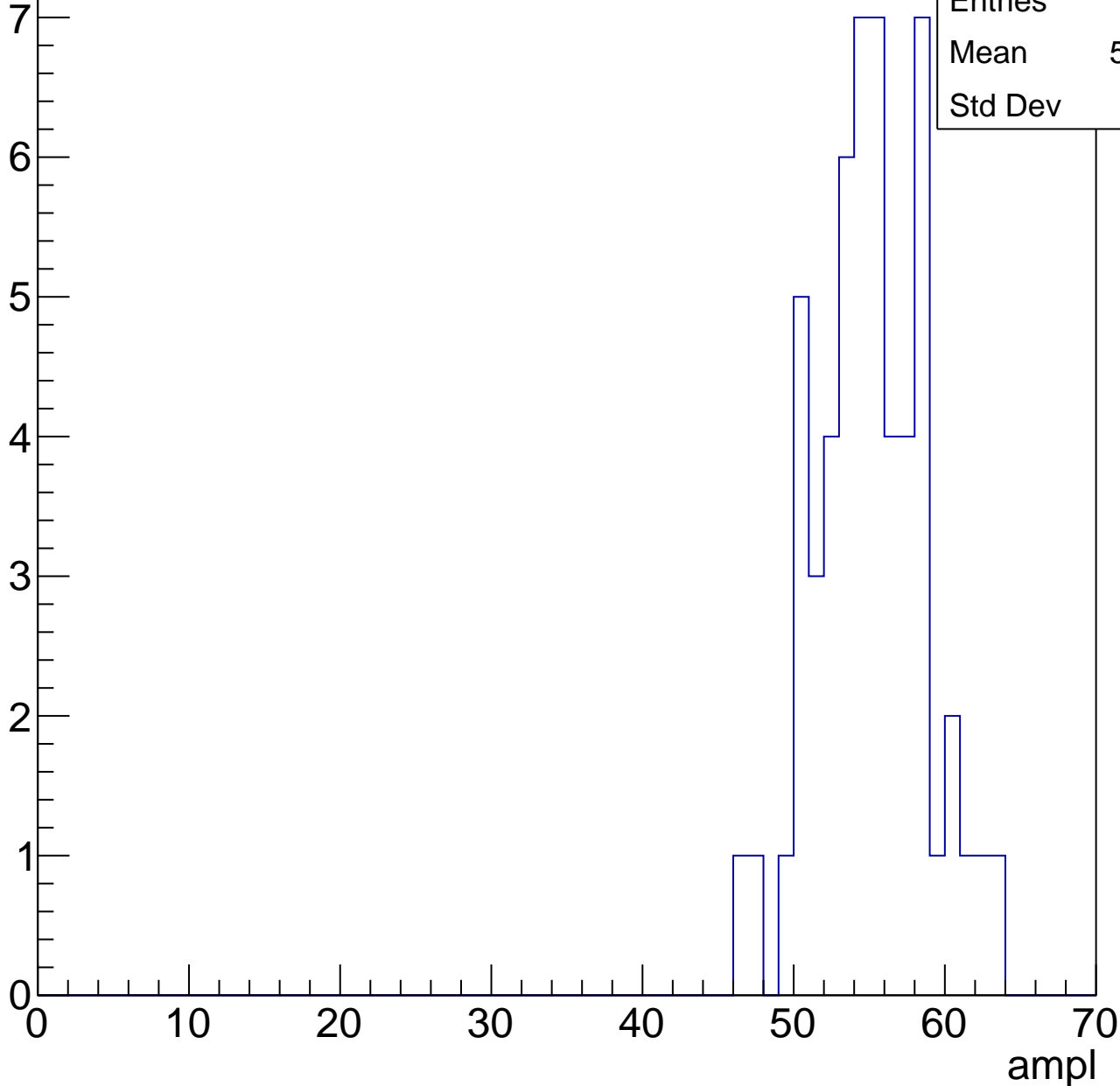


B1L102S, U18-ch27, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.59
Std Dev	3.56

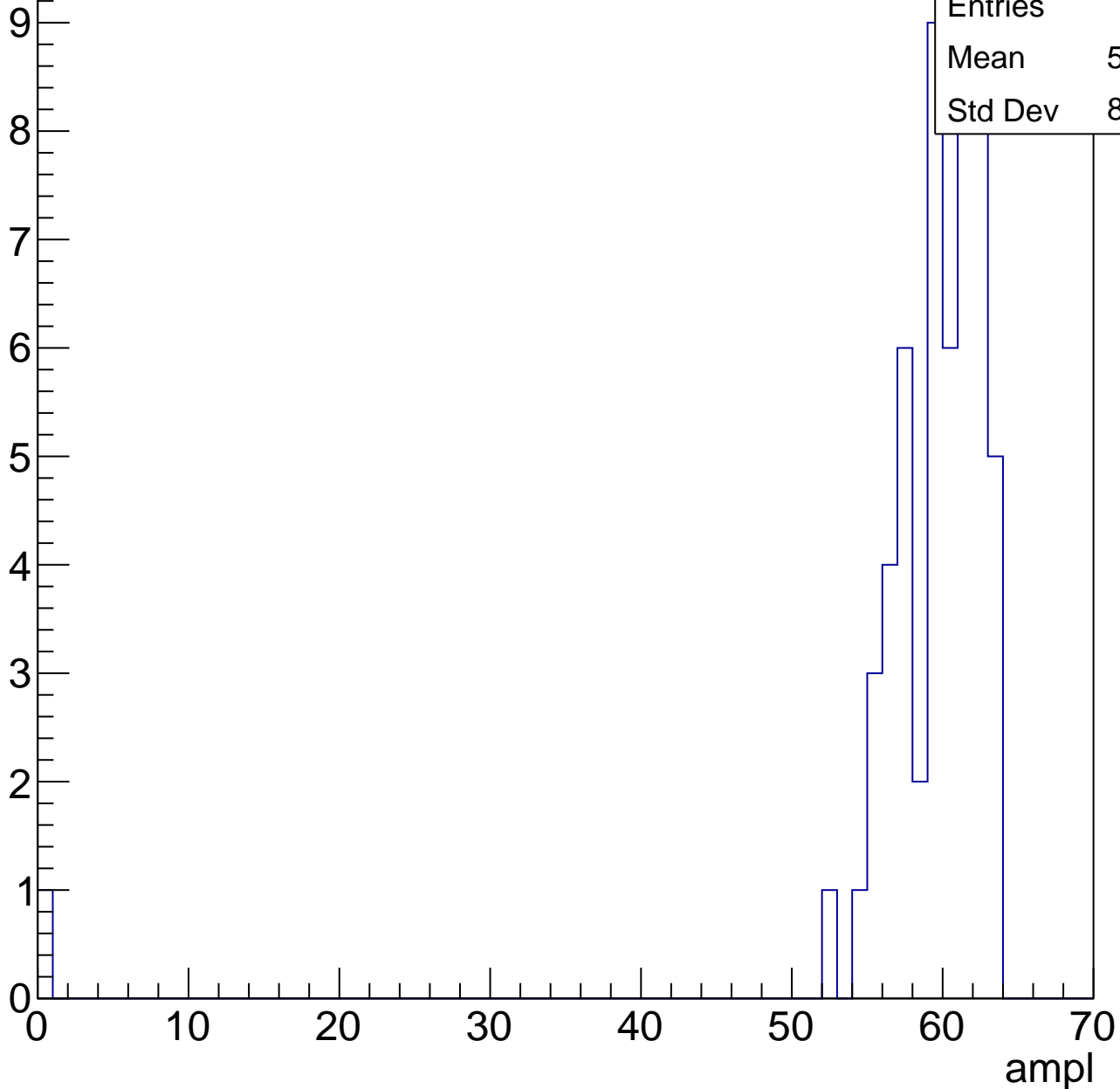


B1L102S, U18-ch27, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

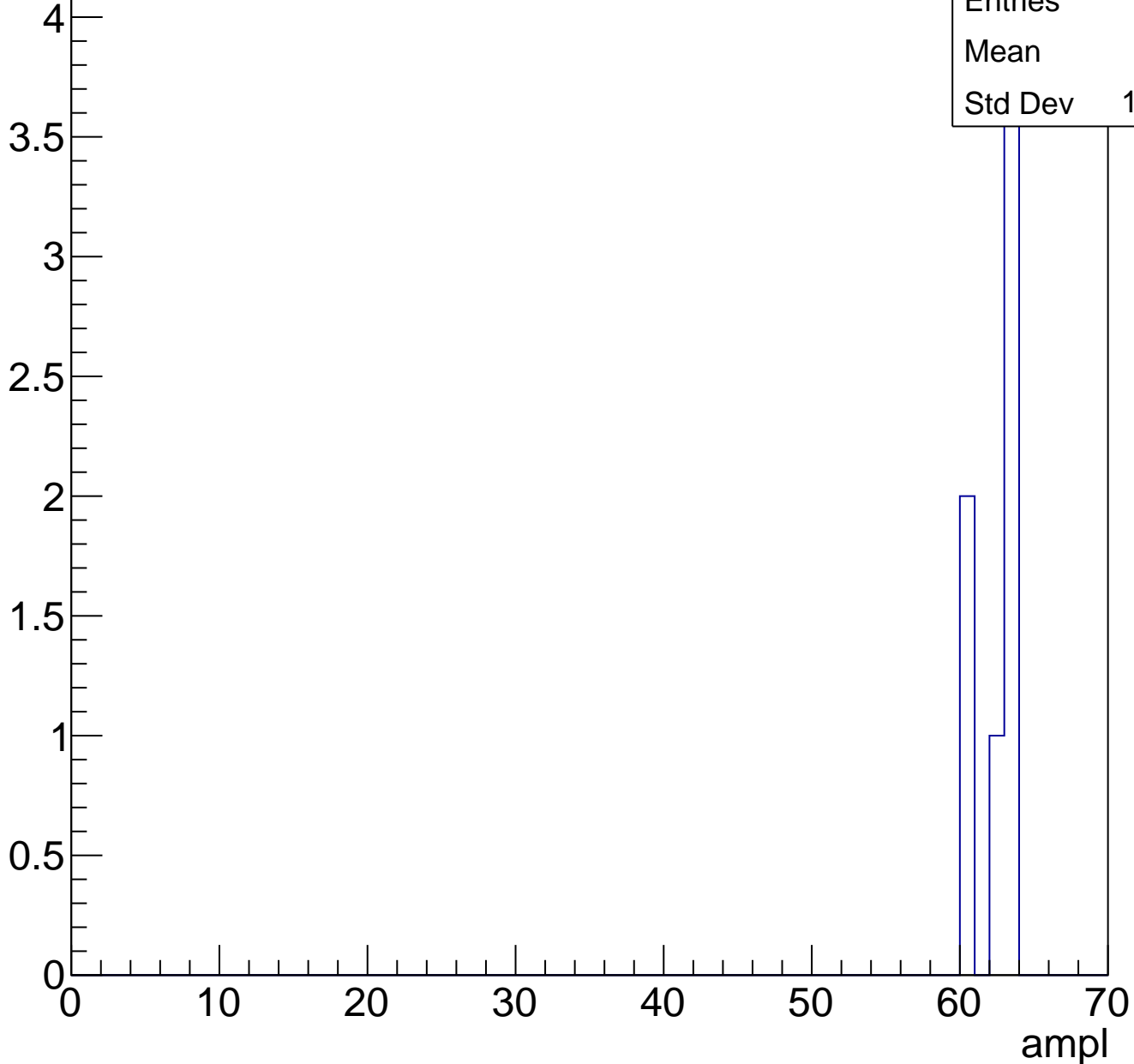
Entries	55
Mean	58.25
Std Dev	8.343



B1L102S, U18-ch27, adc6

calib_packv5_042523_0143.root, FC#11, port A2

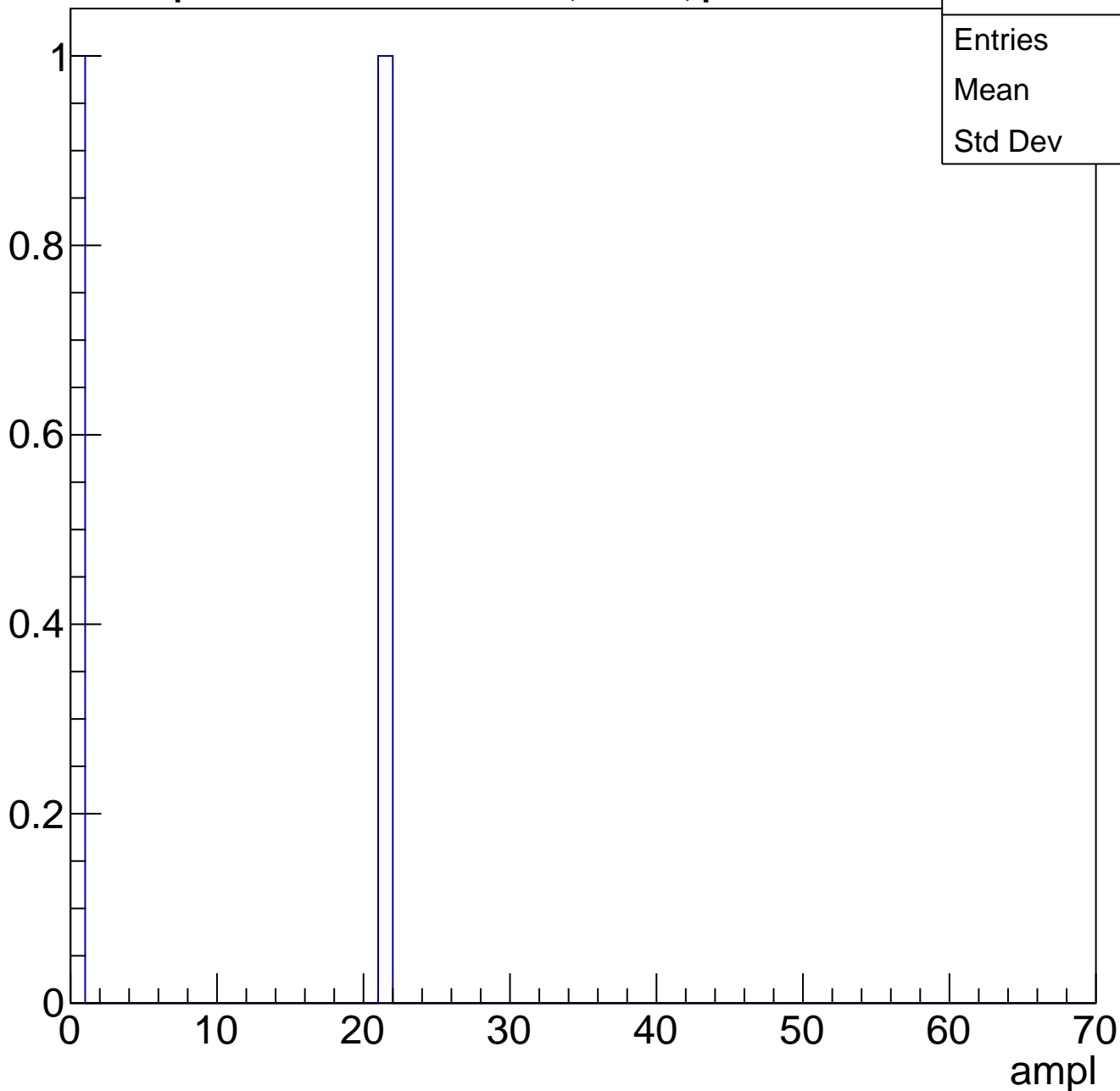
Entry



B1L102S, U18-ch27, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch28, adc0

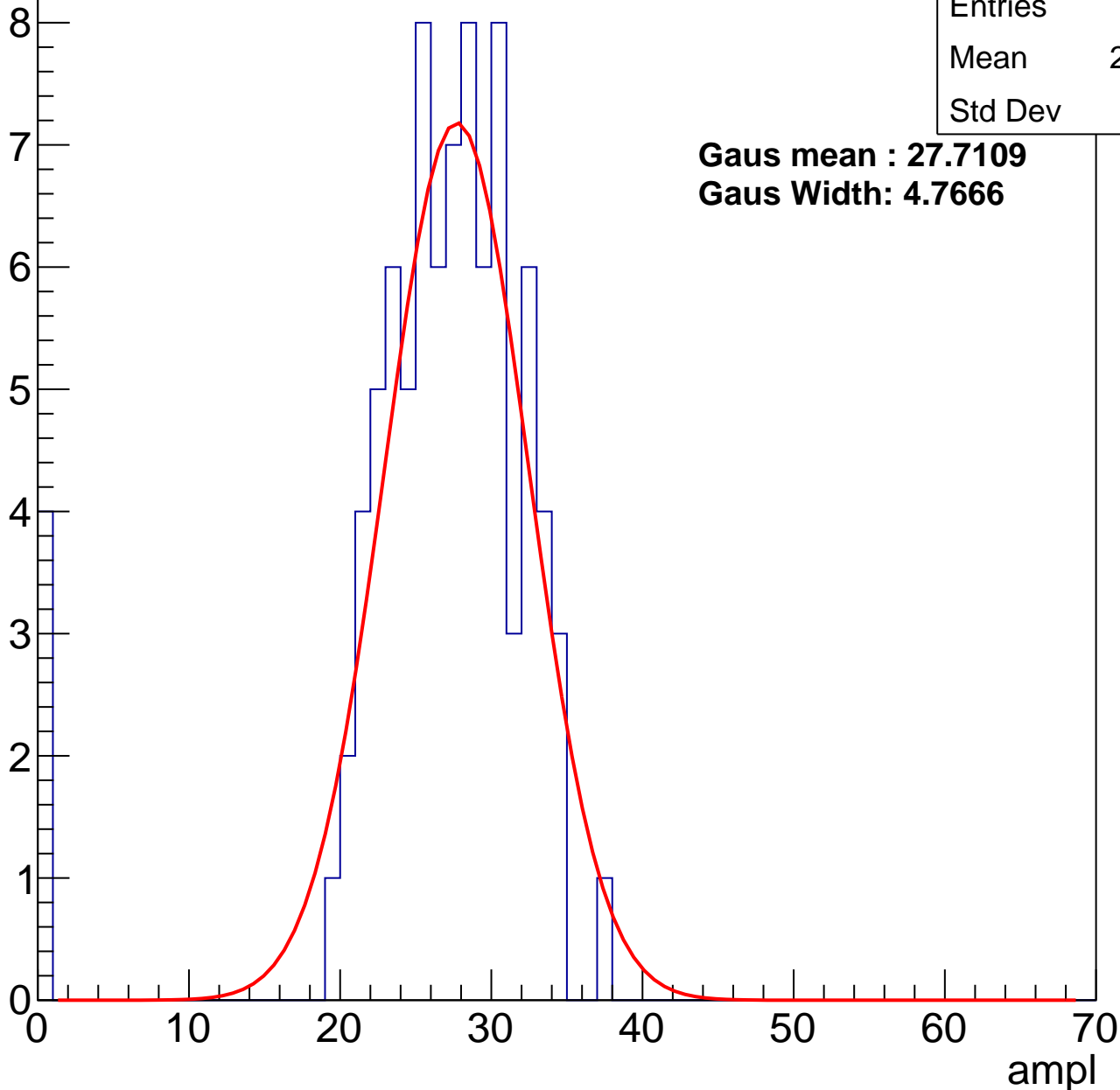
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	25.86
Std Dev	6.87

Gaus mean : 27.7109

Gaus Width: 4.7666



B1L102S, U18-ch28, adc1

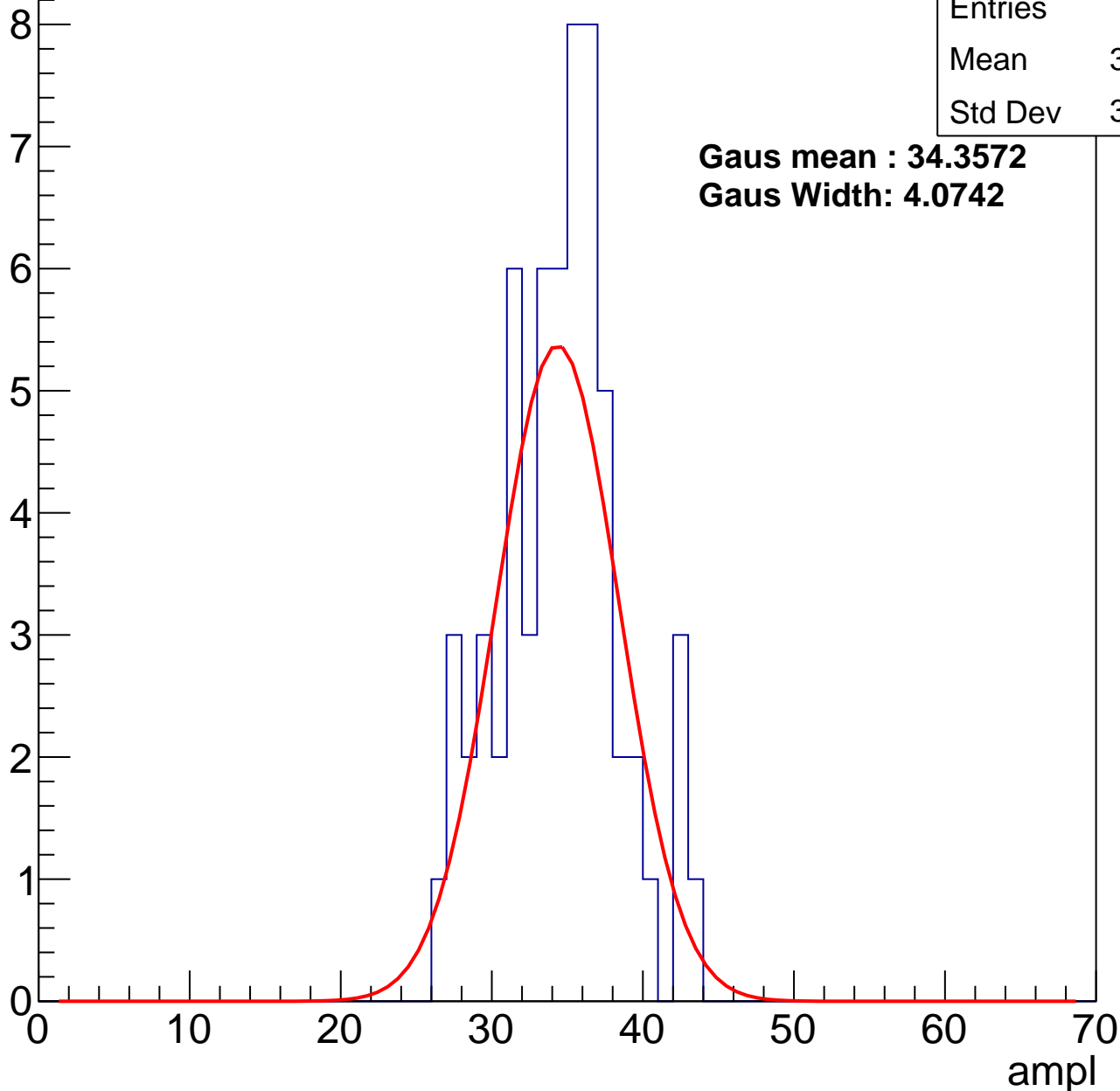
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	34.03
Std Dev	3.889

Gaus mean : 34.3572

Gaus Width: 4.0742



B1L102S, U18-ch28, adc2

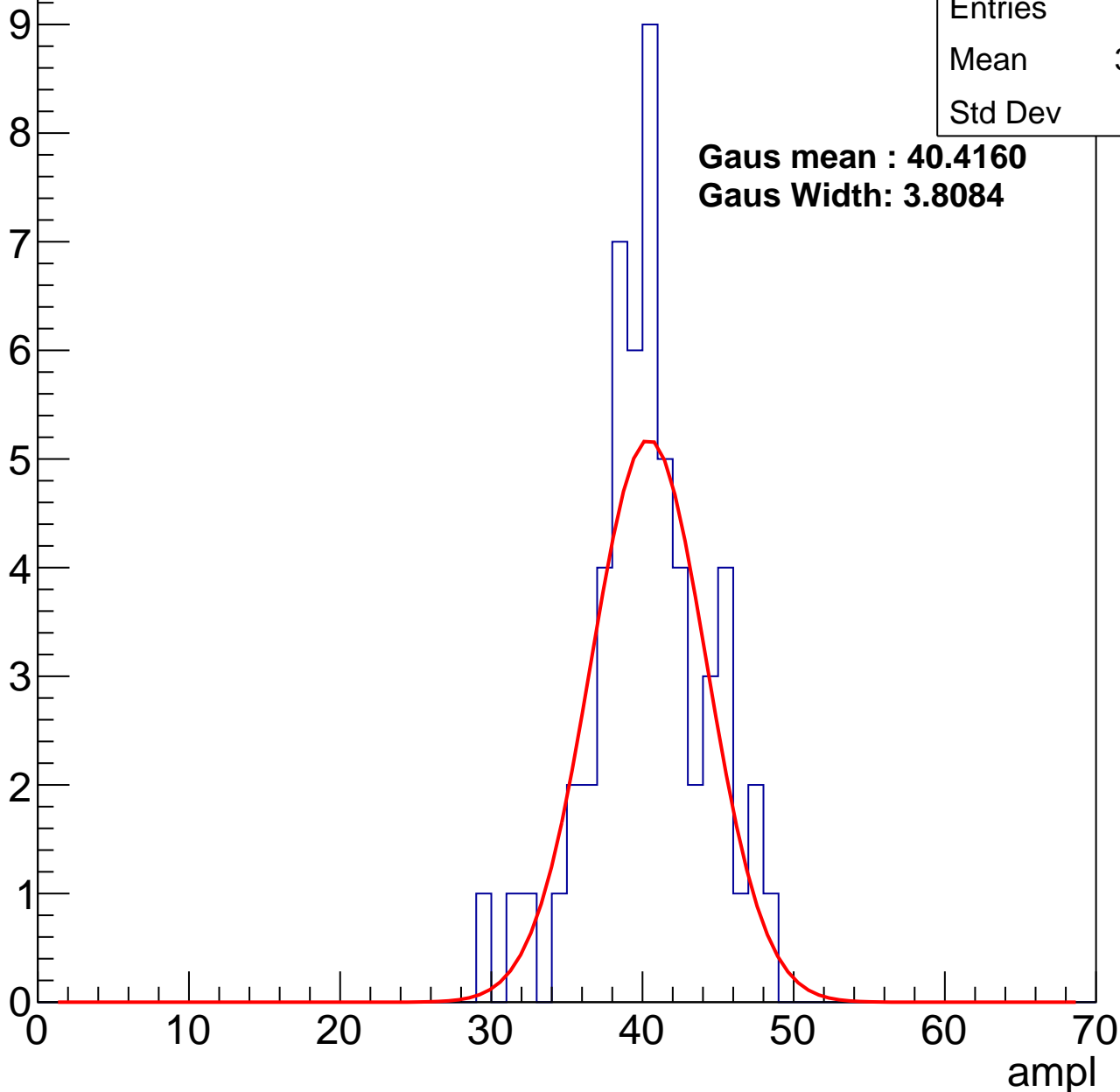
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	39.91
Std Dev	3.87

Gaus mean : 40.4160

Gaus Width: 3.8084

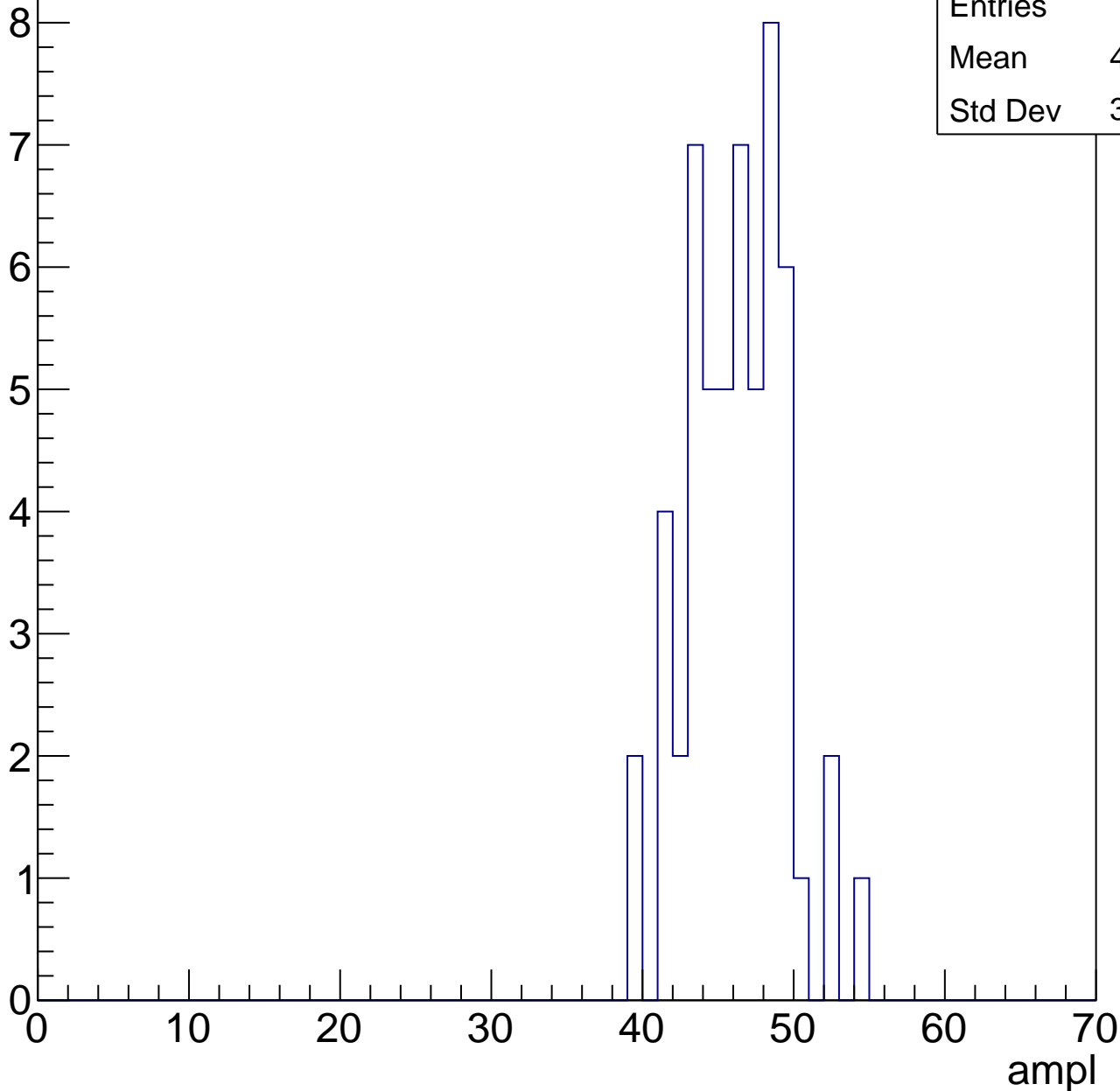


B1L102S, U18-ch28, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	45.73
Std Dev	3.182

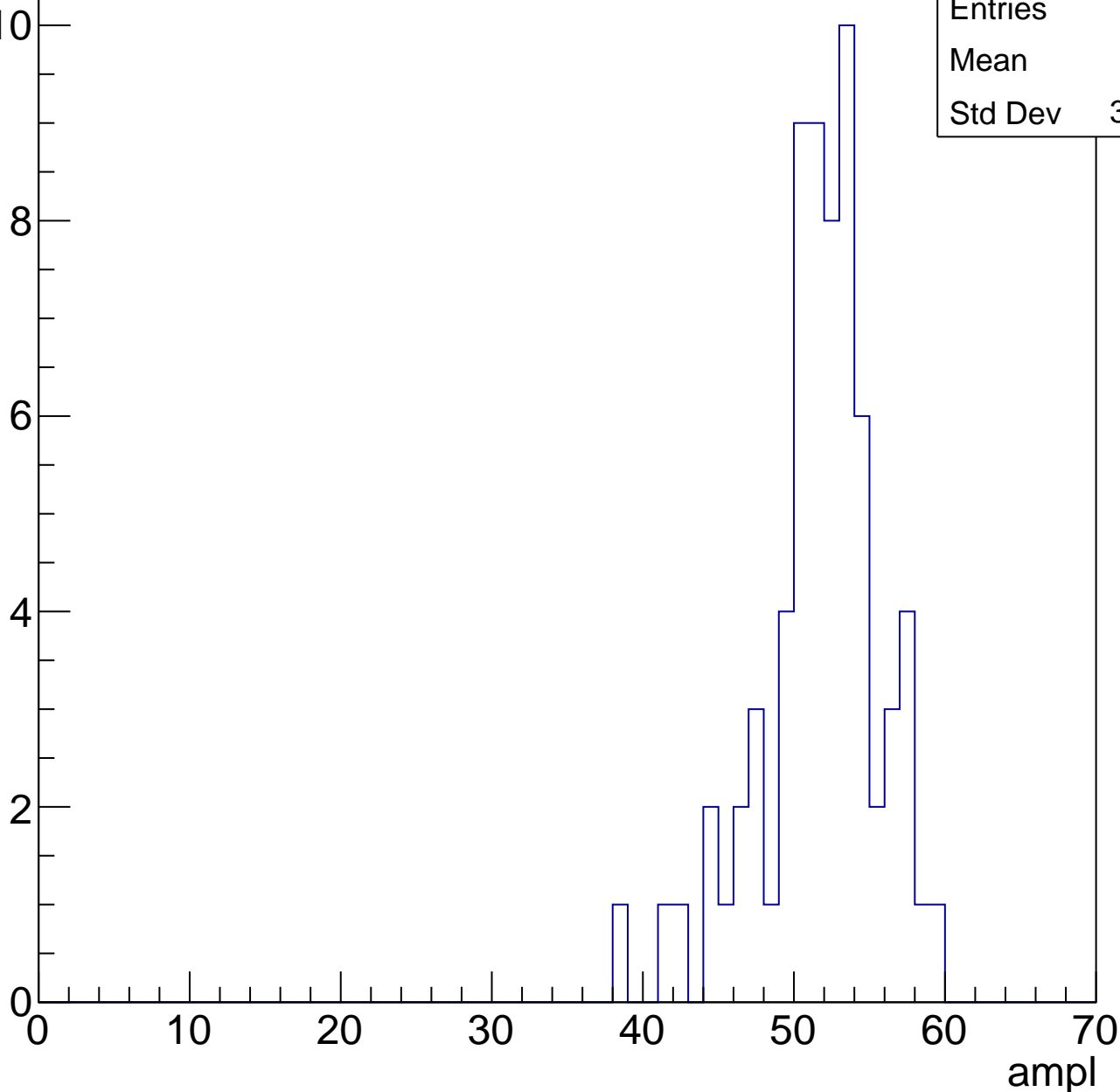


B1L102S, U18-ch28, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	51.2
Std Dev	3.955

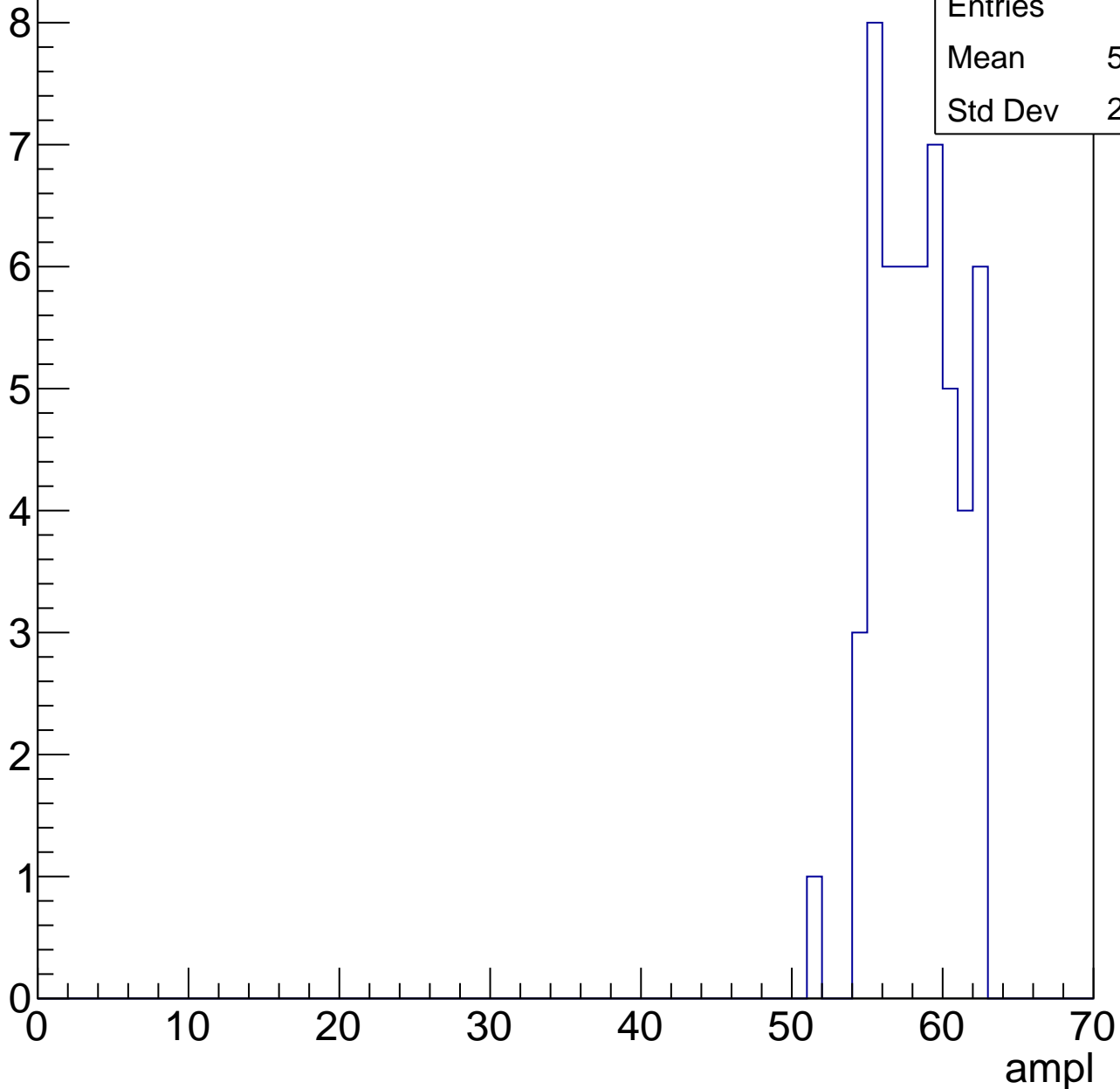


B1L102S, U18-ch28, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.85
Std Dev	2.619

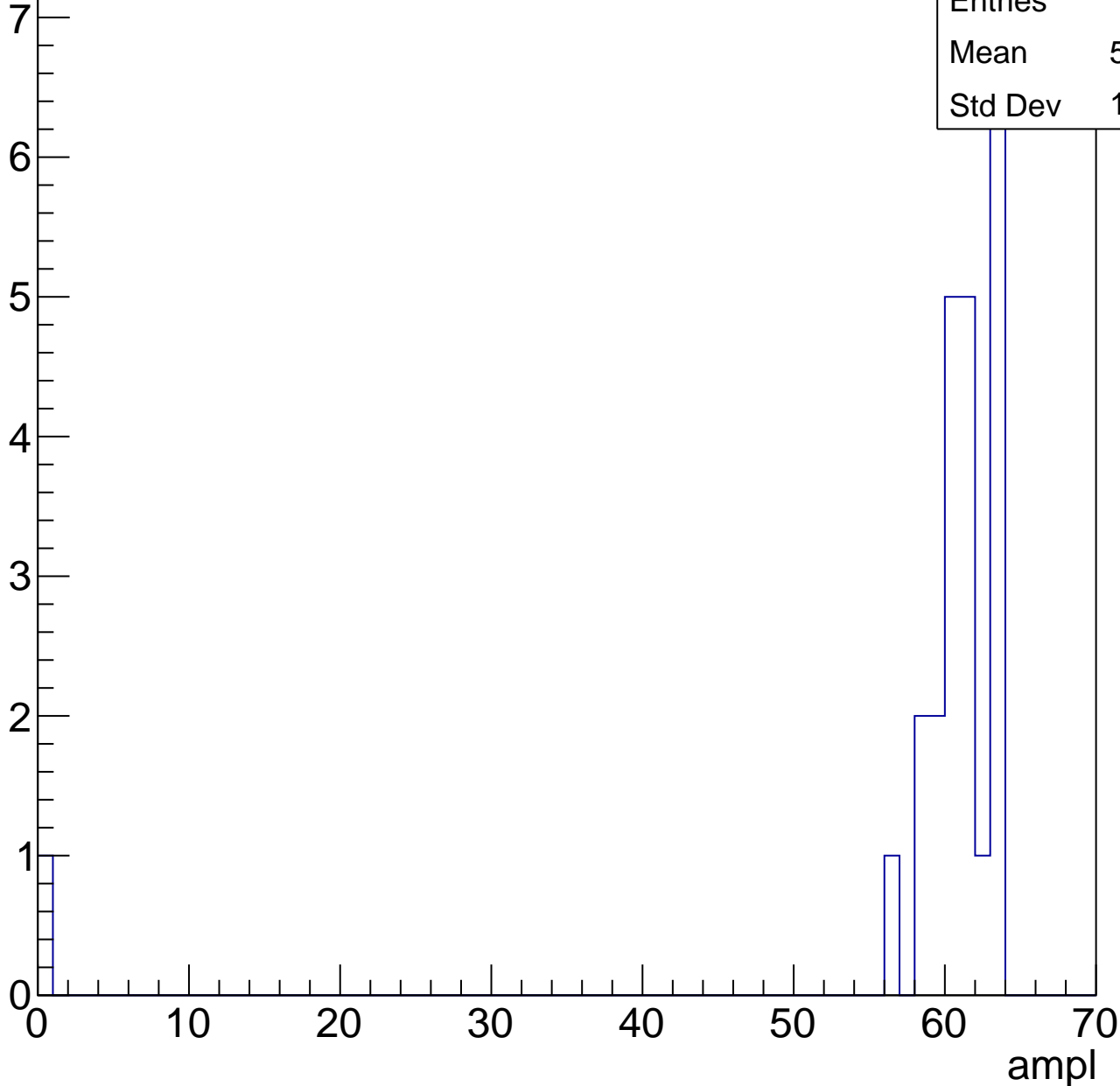


B1L102S, U18-ch28, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

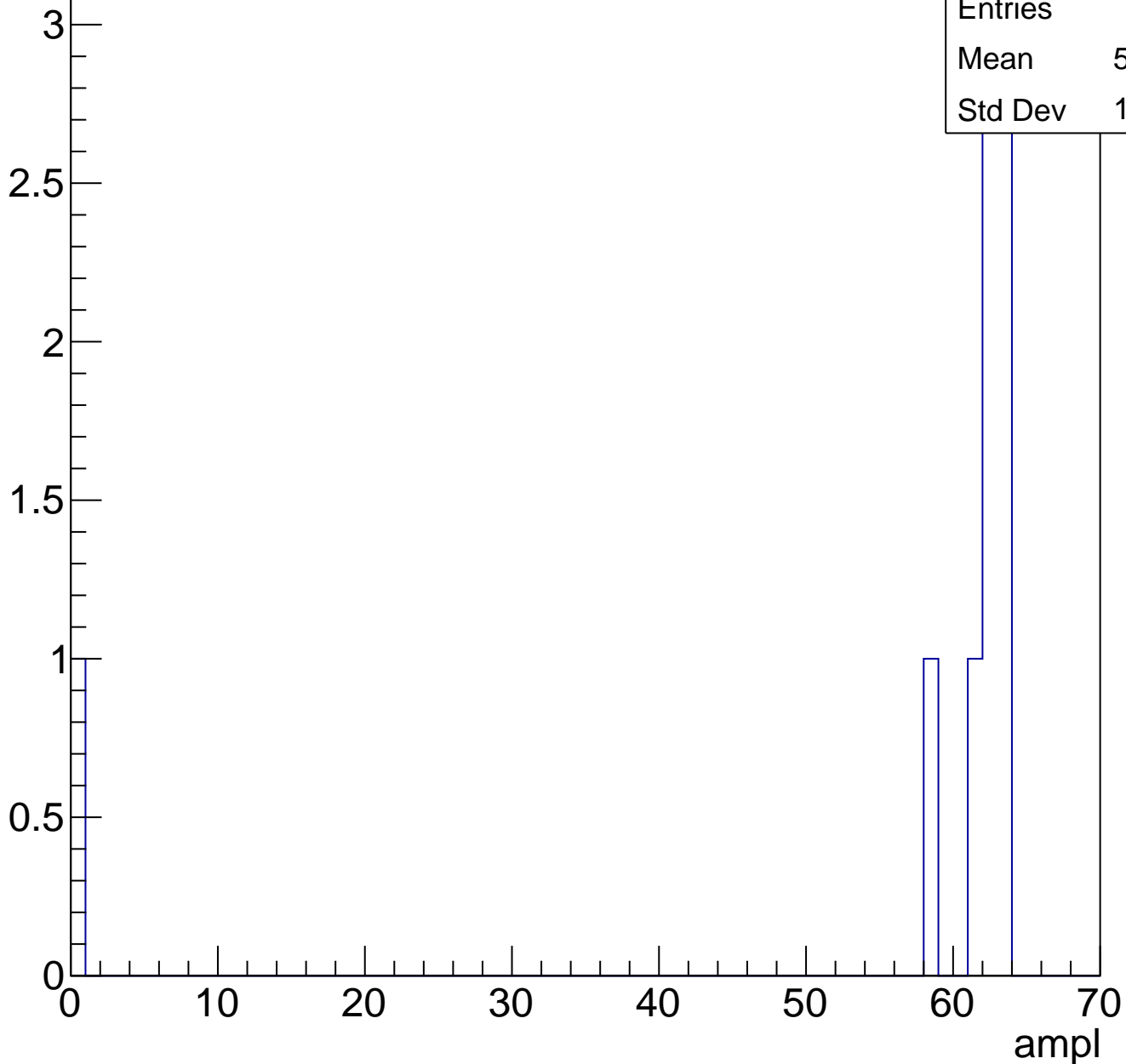
Entries	24
Mean	58.25
Std Dev	12.29



B1L102S, U18-ch28, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch29, adc0

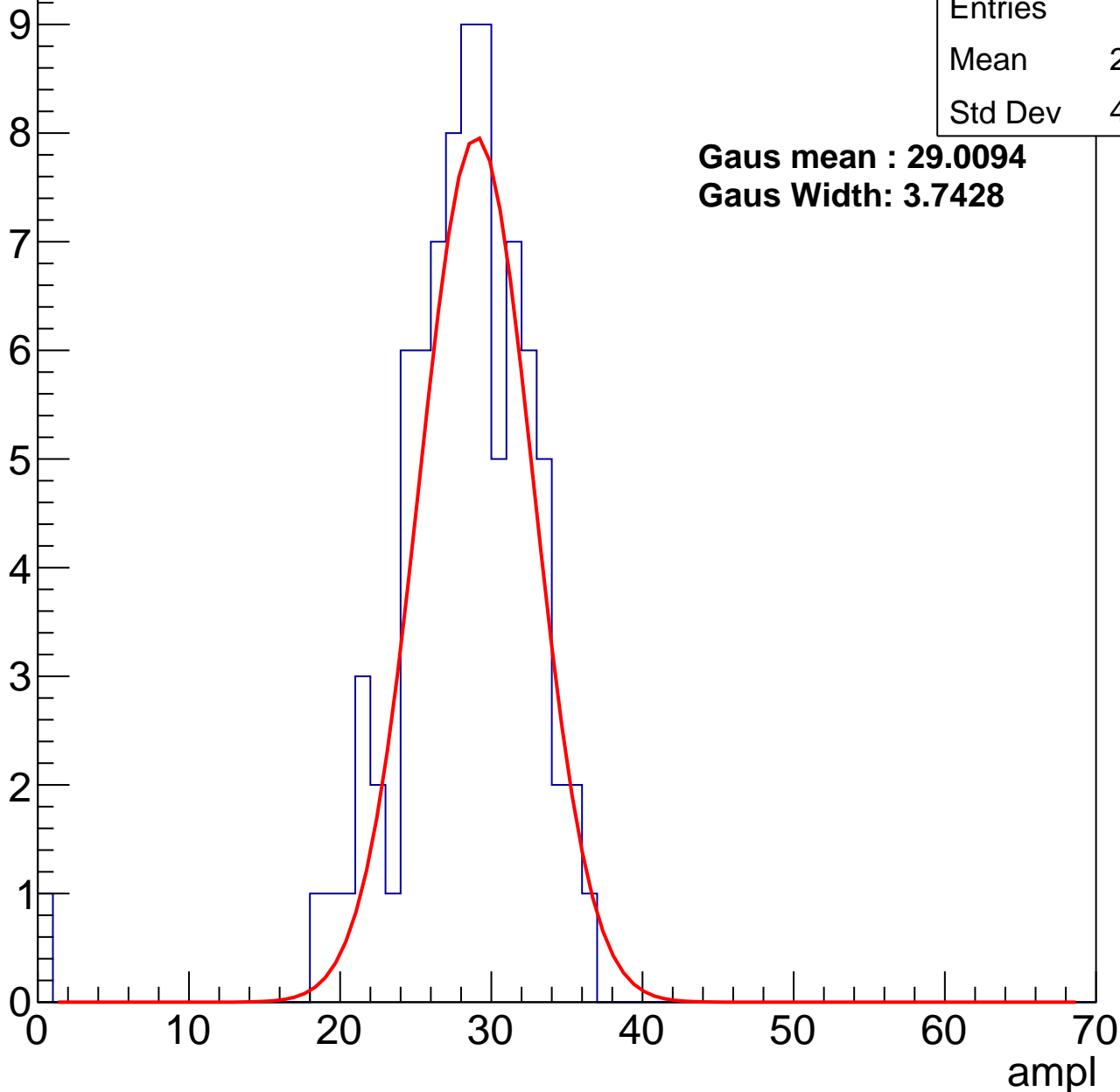
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.59
Std Dev	4.899

Gaus mean : 29.0094

Gaus Width: 3.7428



B1L102S, U18-ch29, adc1

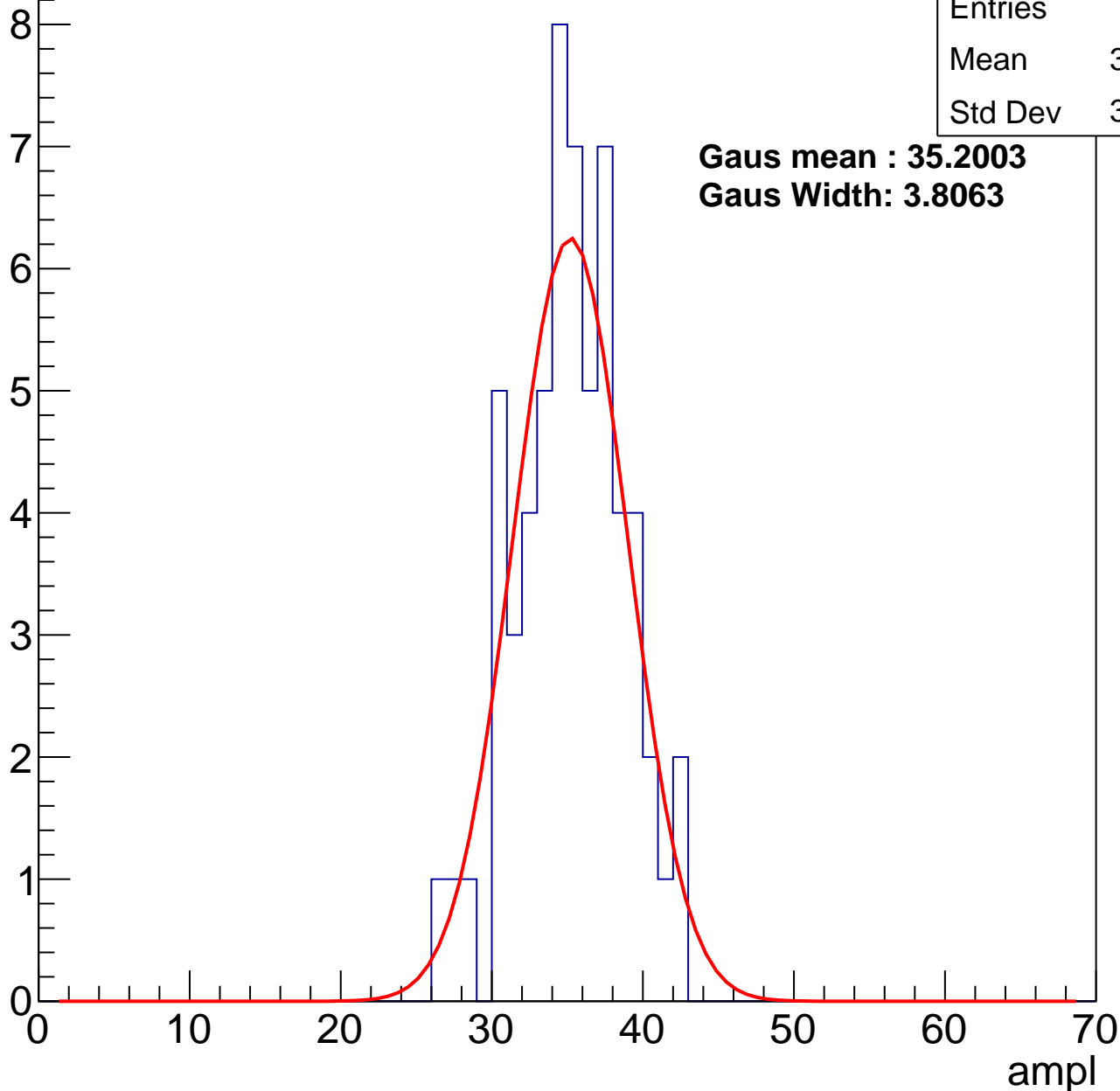
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	34.77
Std Dev	3.523

Gaus mean : 35.2003

Gaus Width: 3.8063



B1L102S, U18-ch29, adc2

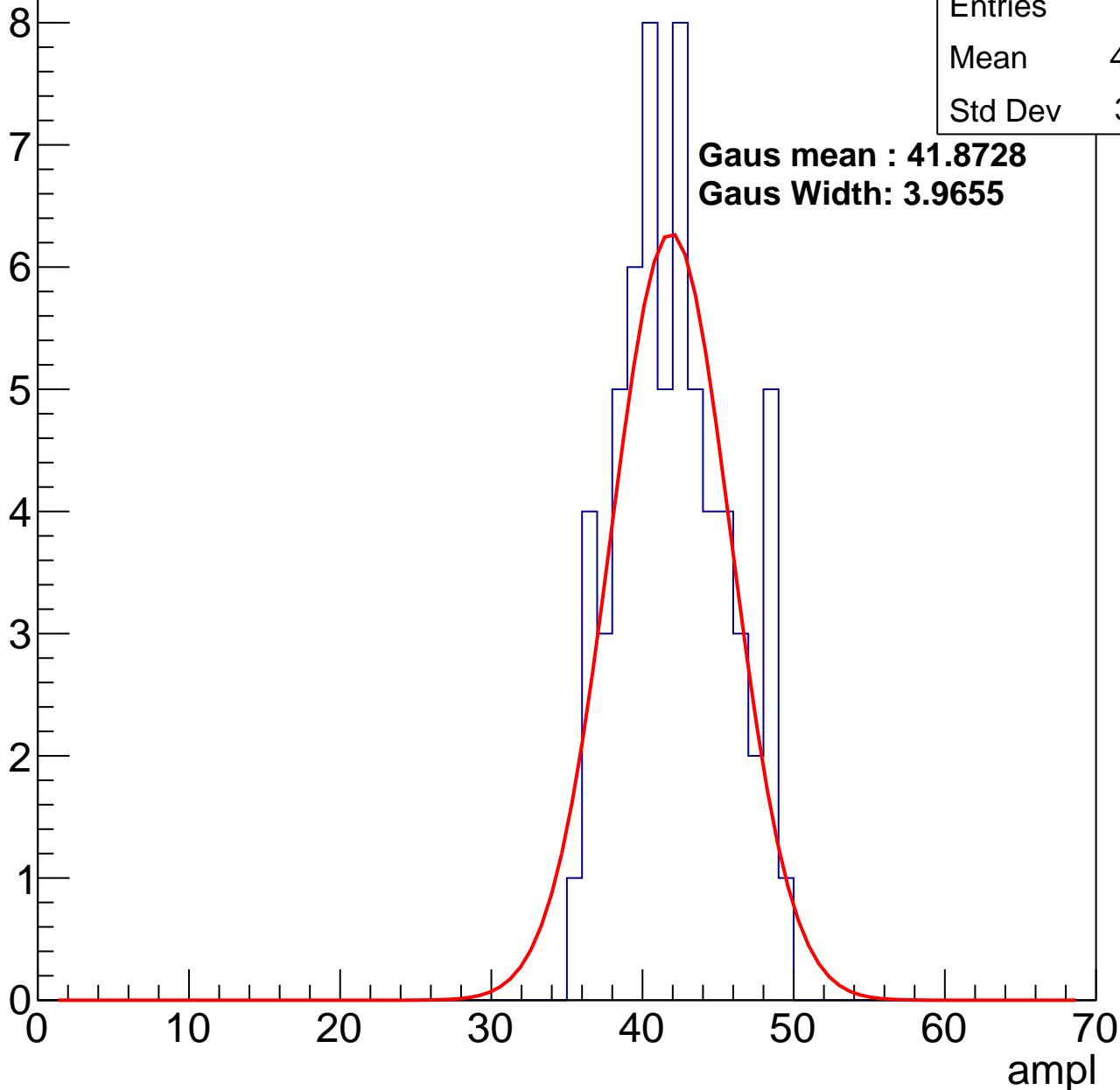
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.67
Std Dev	3.571

Gaus mean : 41.8728

Gaus Width: 3.9655

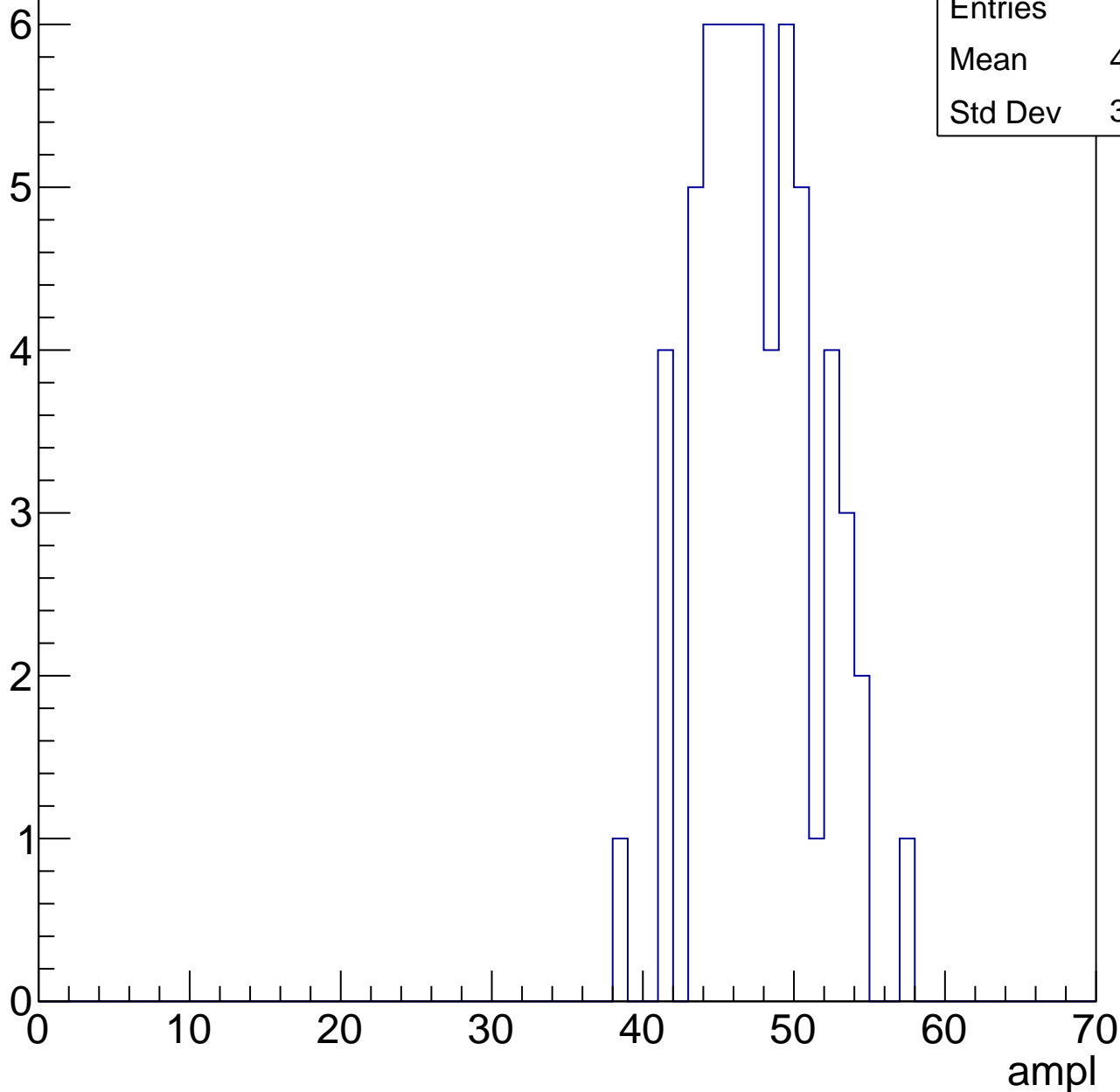


B1L102S, U18-ch29, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	47.13
Std Dev	3.836

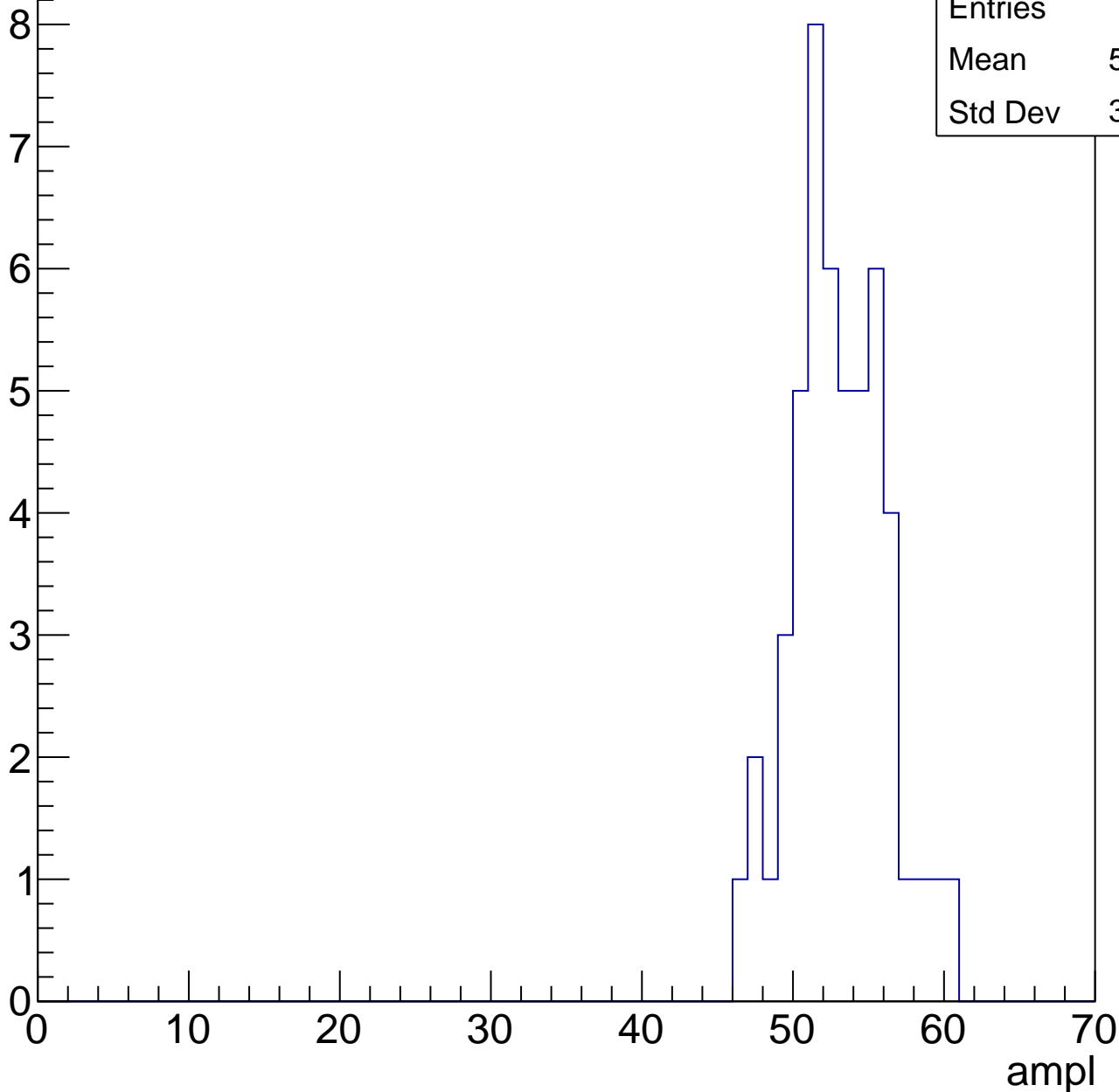


B1L102S, U18-ch29, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	52.56
Std Dev	3.028



B1L102S, U18-ch29, adc5

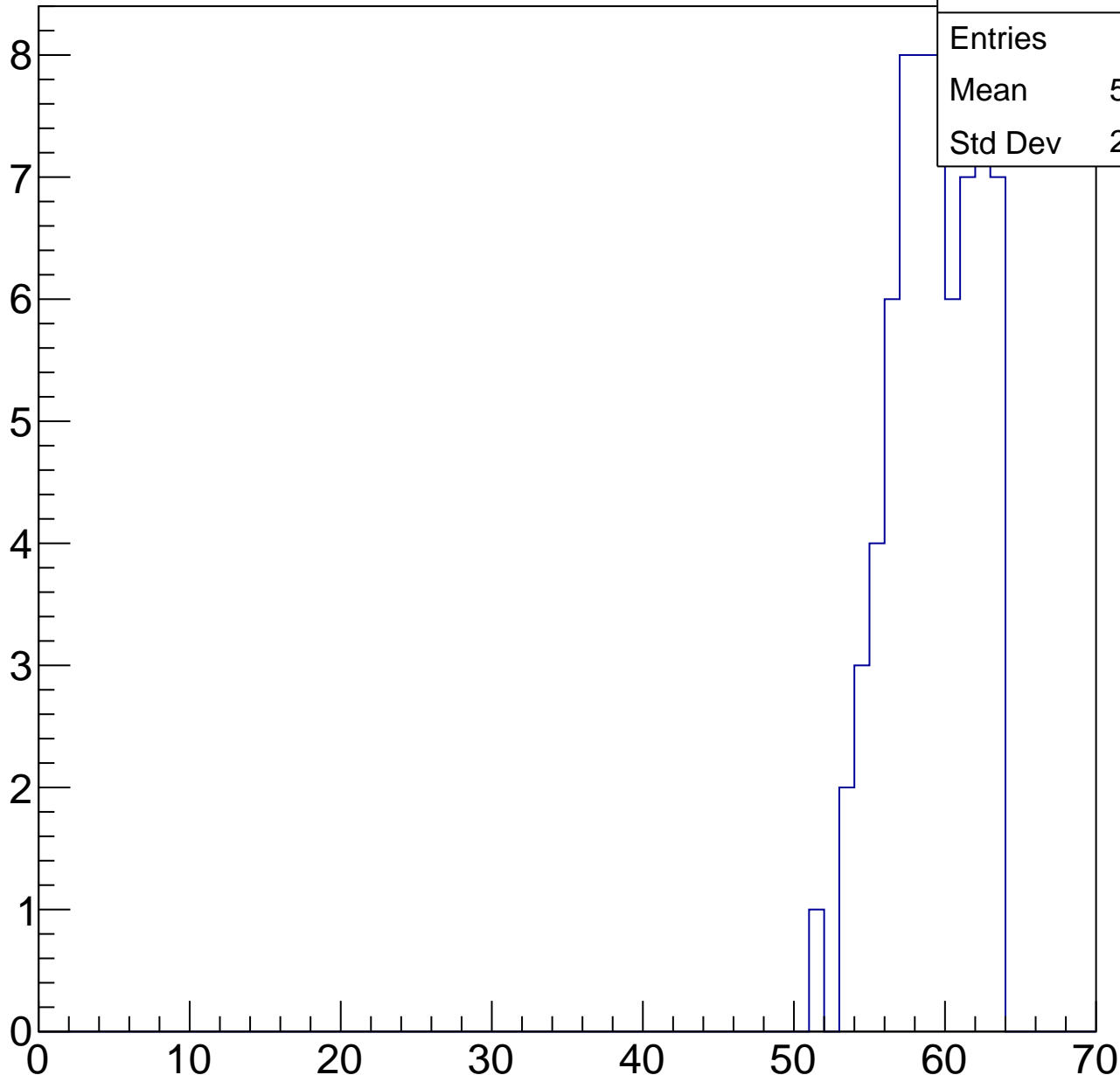
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	68
Mean	58.69
Std Dev	2.922

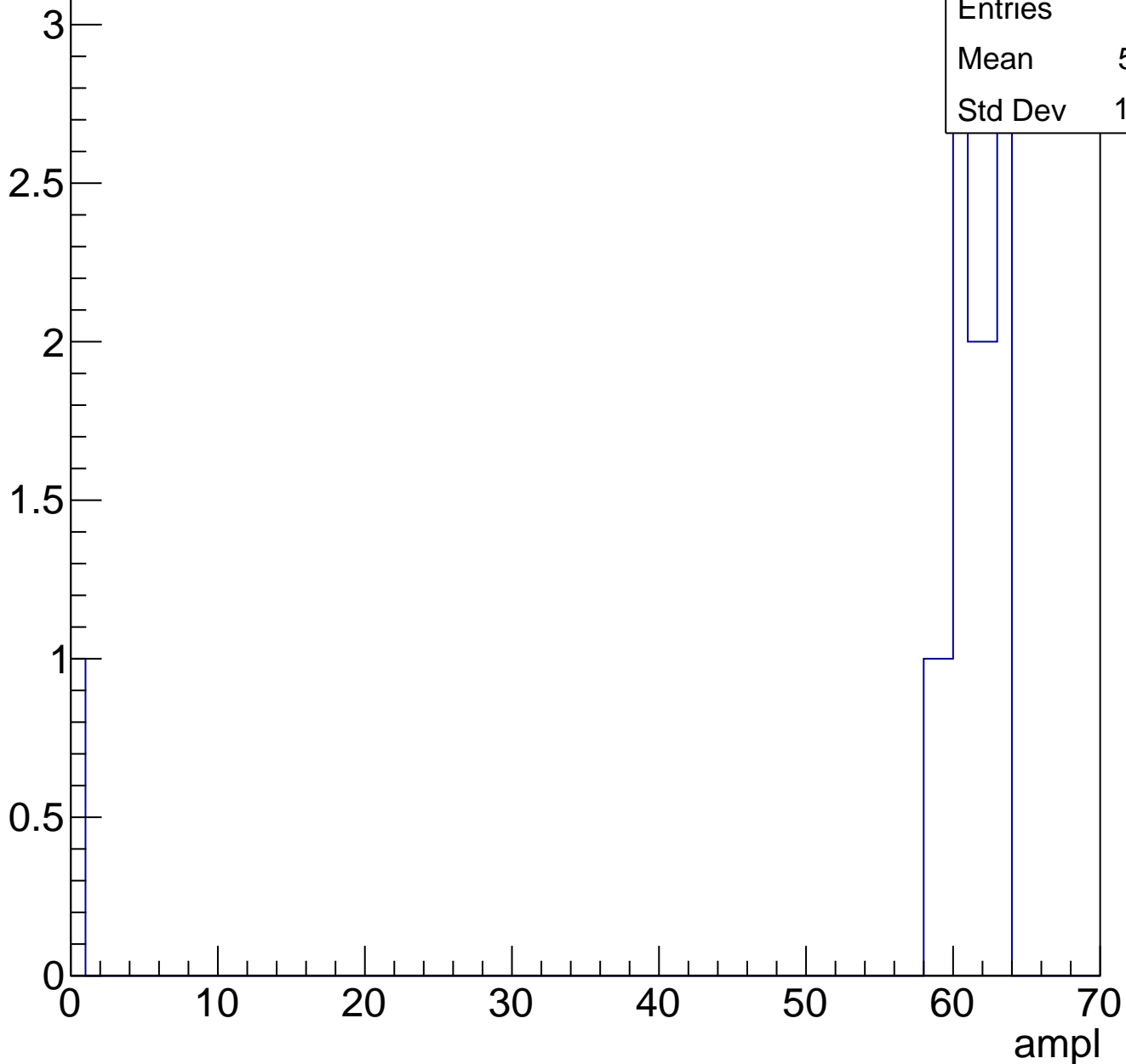
ampl



B1L102S, U18-ch29, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch29, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch30, adc0

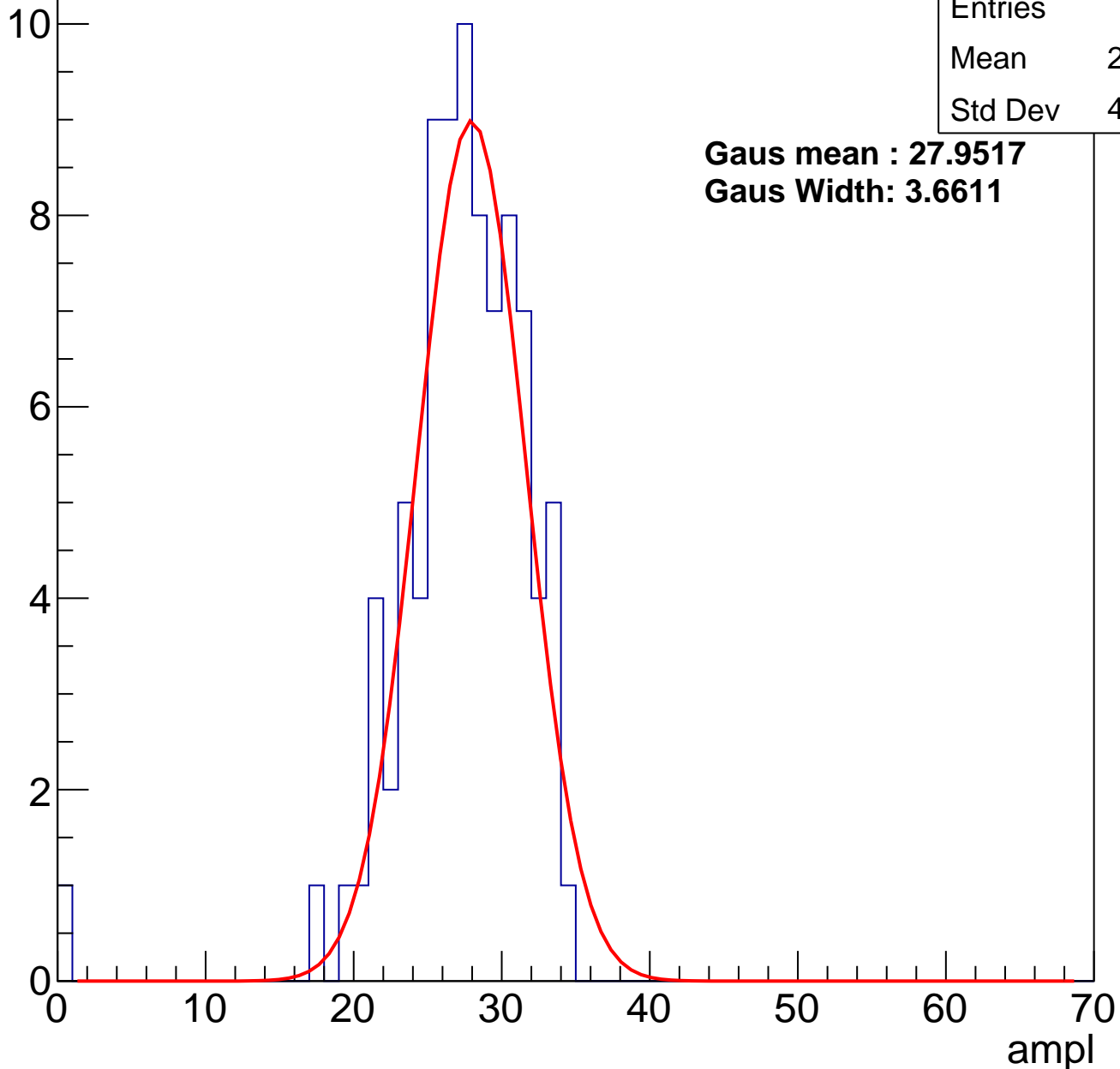
calib_packv5_042523_0143.root, FC#11, port A2

Entries	87
Mean	26.84
Std Dev	4.604

Gaus mean : 27.9517

Gaus Width: 3.6611

Entry



B1L102S, U18-ch30, adc1

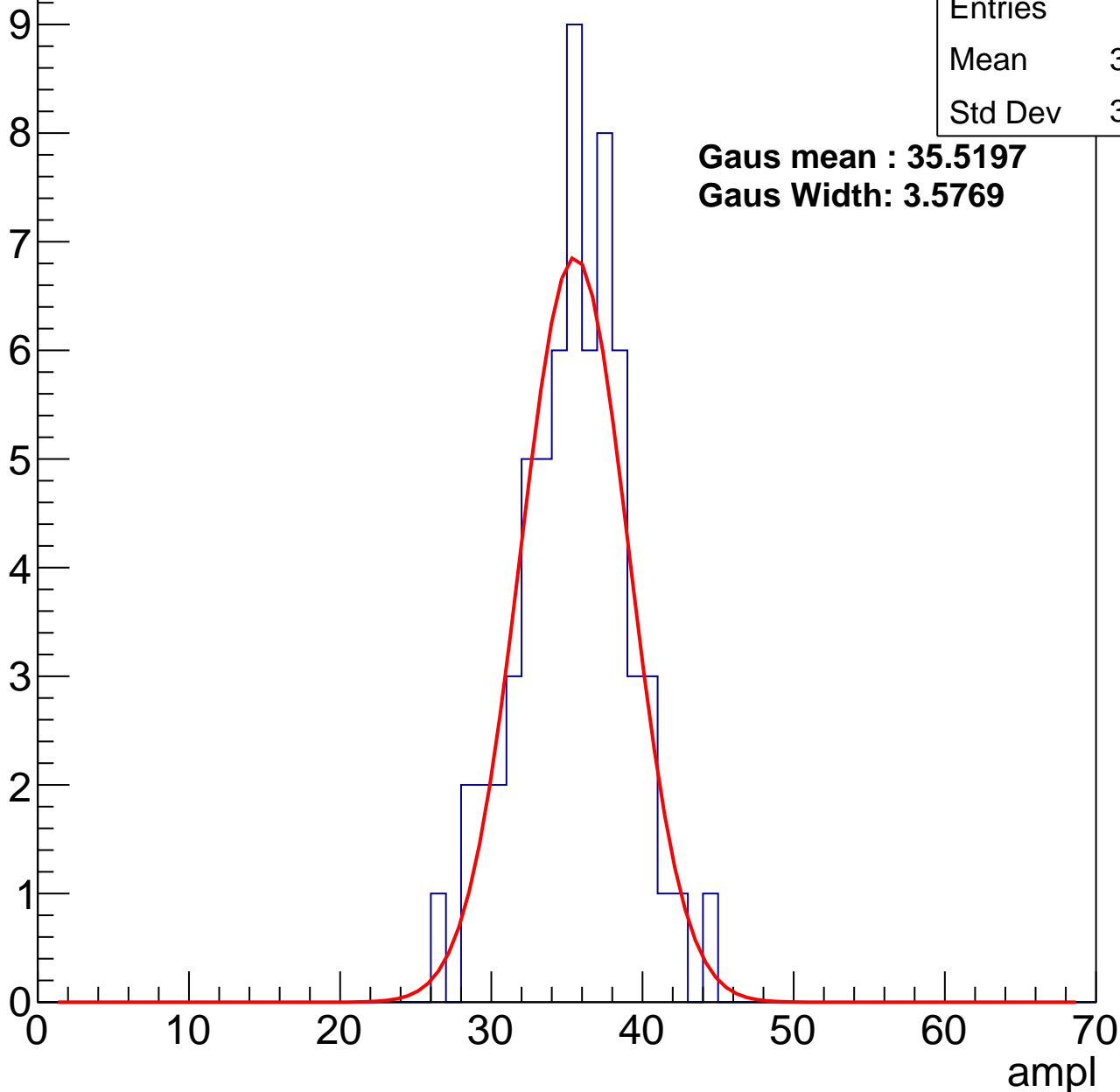
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.02
Std Dev	3.538

Gaus mean : 35.5197

Gaus Width: 3.5769



B1L102S, U18-ch30, adc2

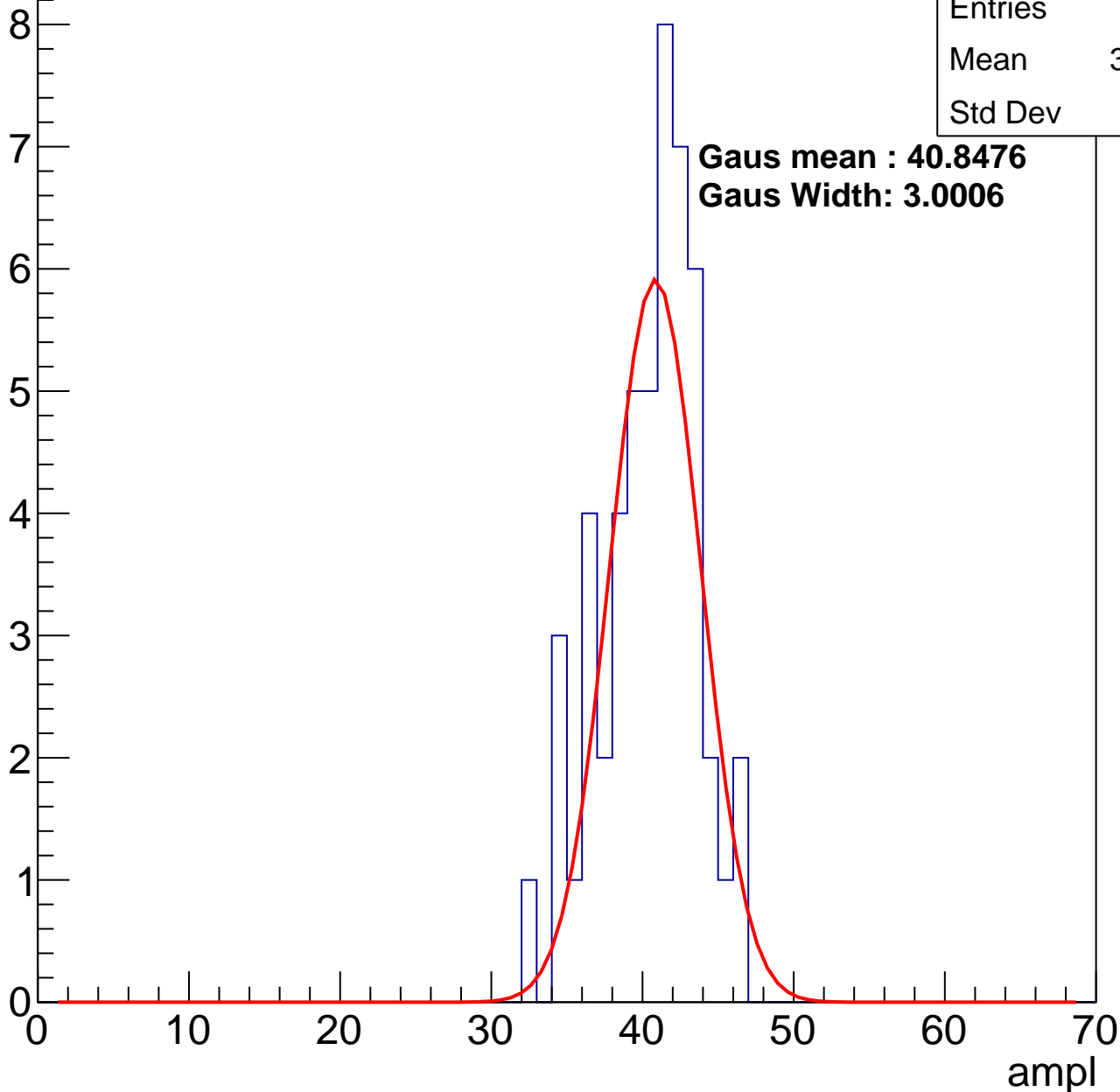
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	39.98
Std Dev	3.19

Gaus mean : 40.8476

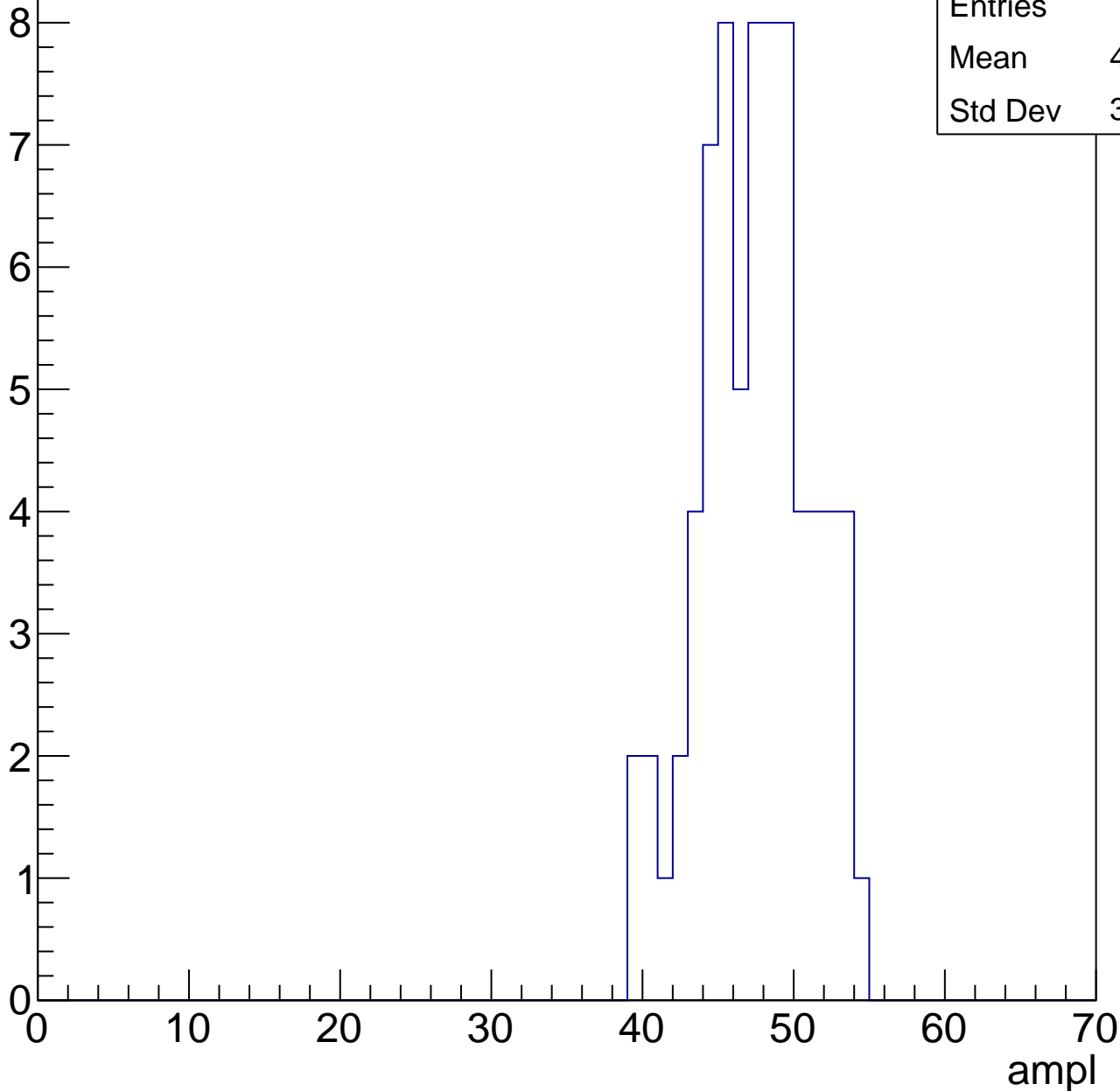
Gaus Width: 3.0006



B1L102S, U18-ch30, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

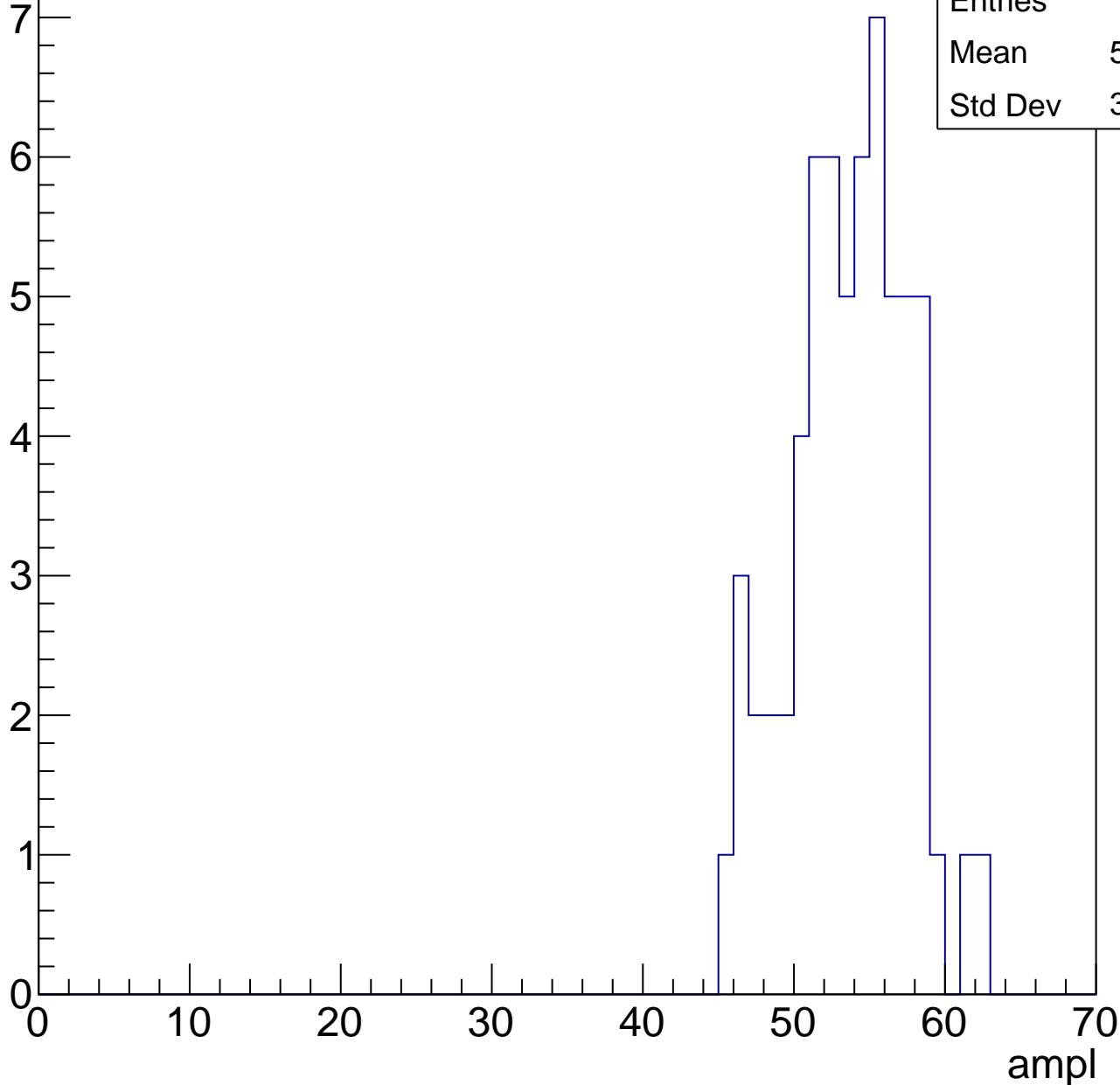


Entries	72
Mean	46.99
Std Dev	3.553

B1L102S, U18-ch30, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



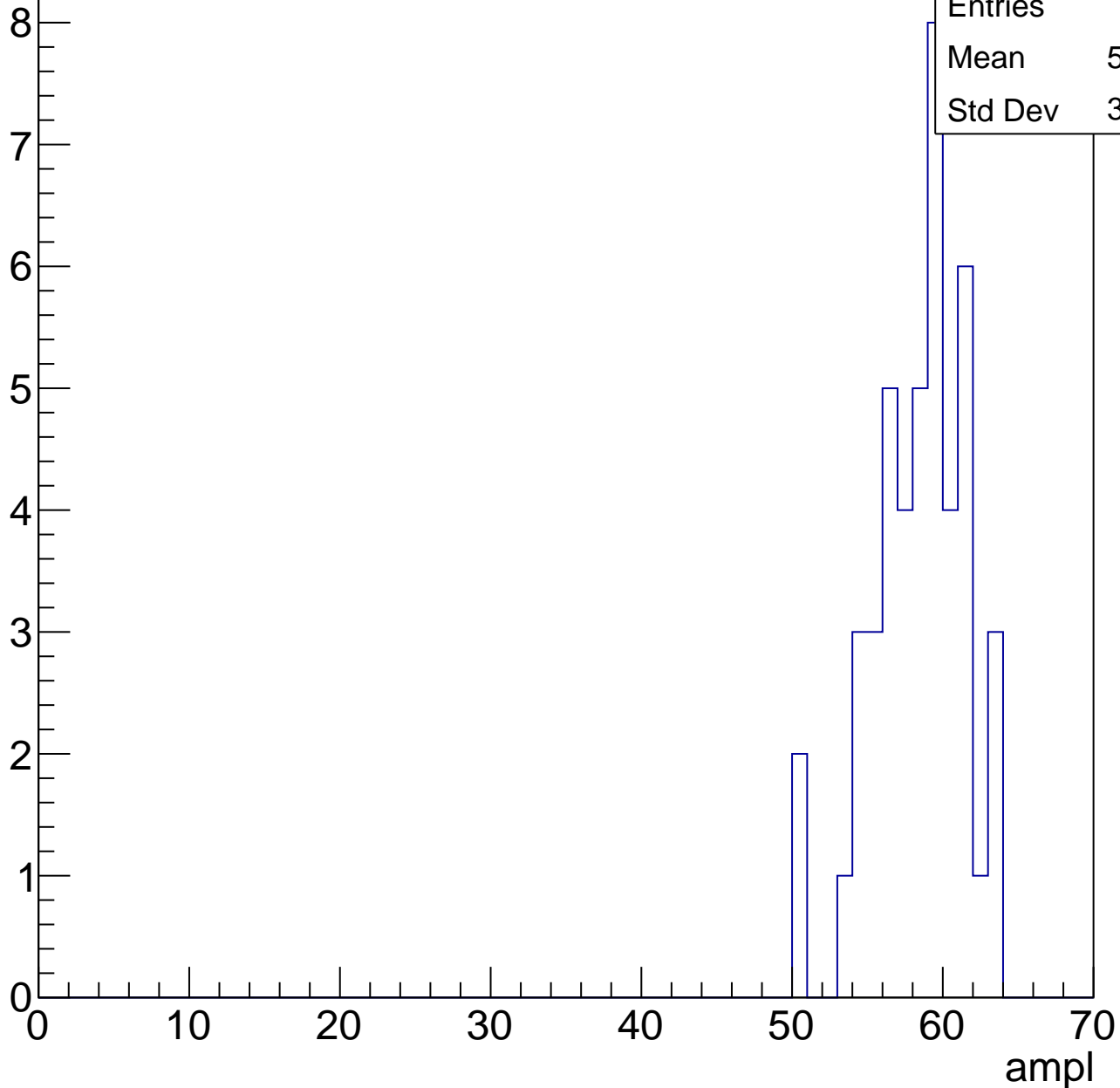
Entries	62
Mean	53.23
Std Dev	3.795

B1L102S, U18-ch30, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

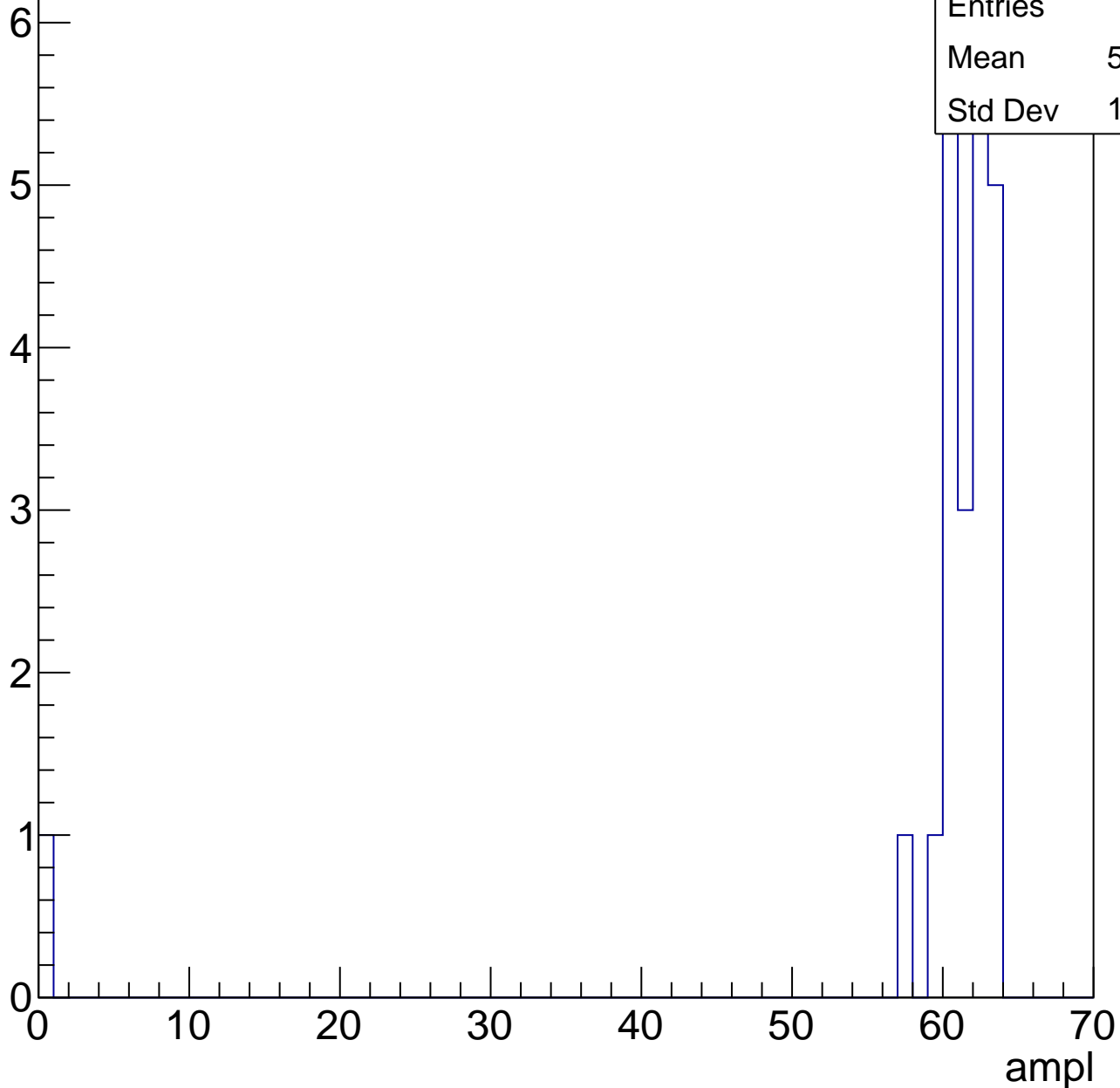
Entries	45
Mean	57.93
Std Dev	3.058



B1L102S, U18-ch30, adc6

calib_packv5_042523_0143.root, FC#11, port A2

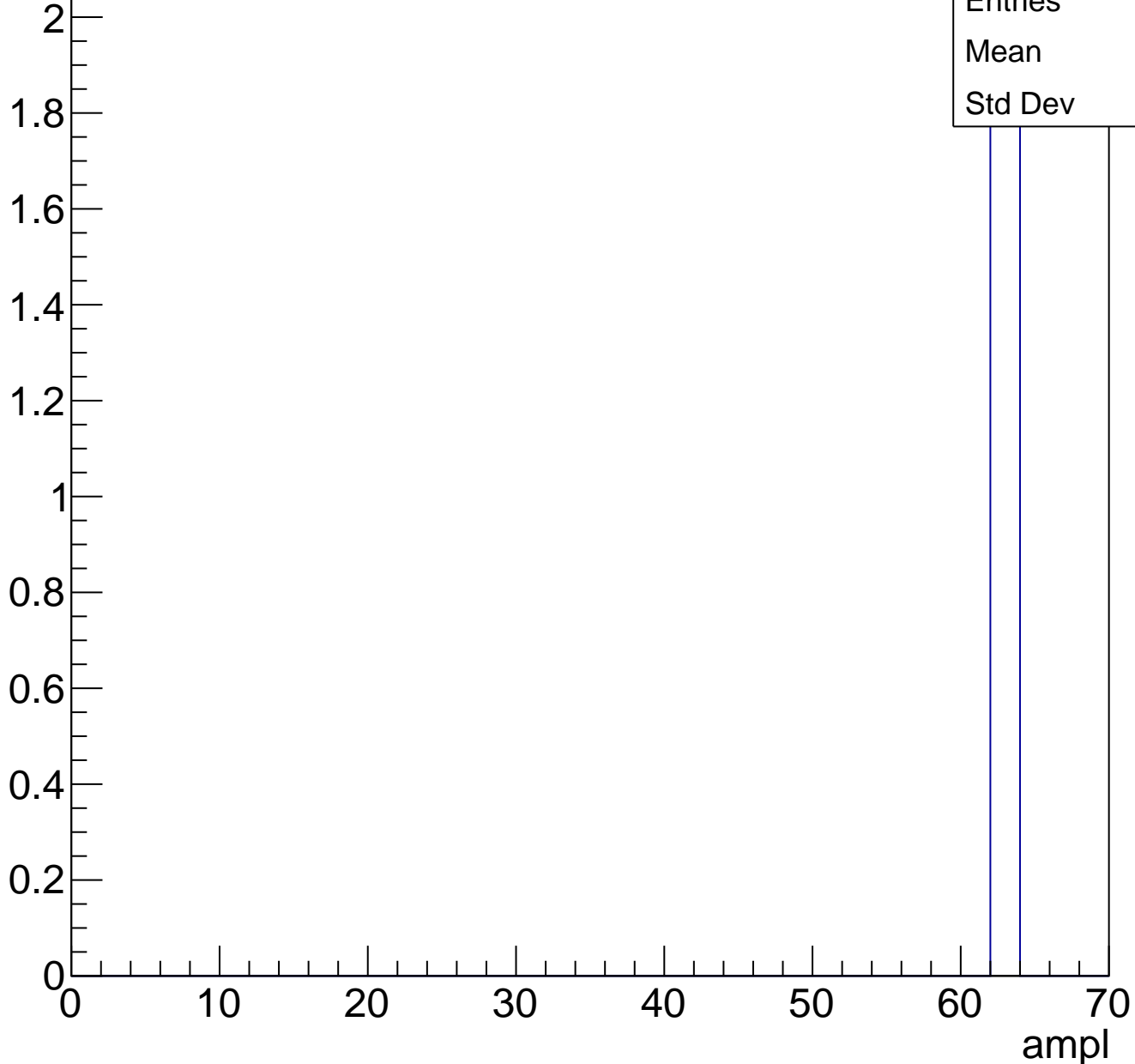
Entry



B1L102S, U18-ch30, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch31, adc0

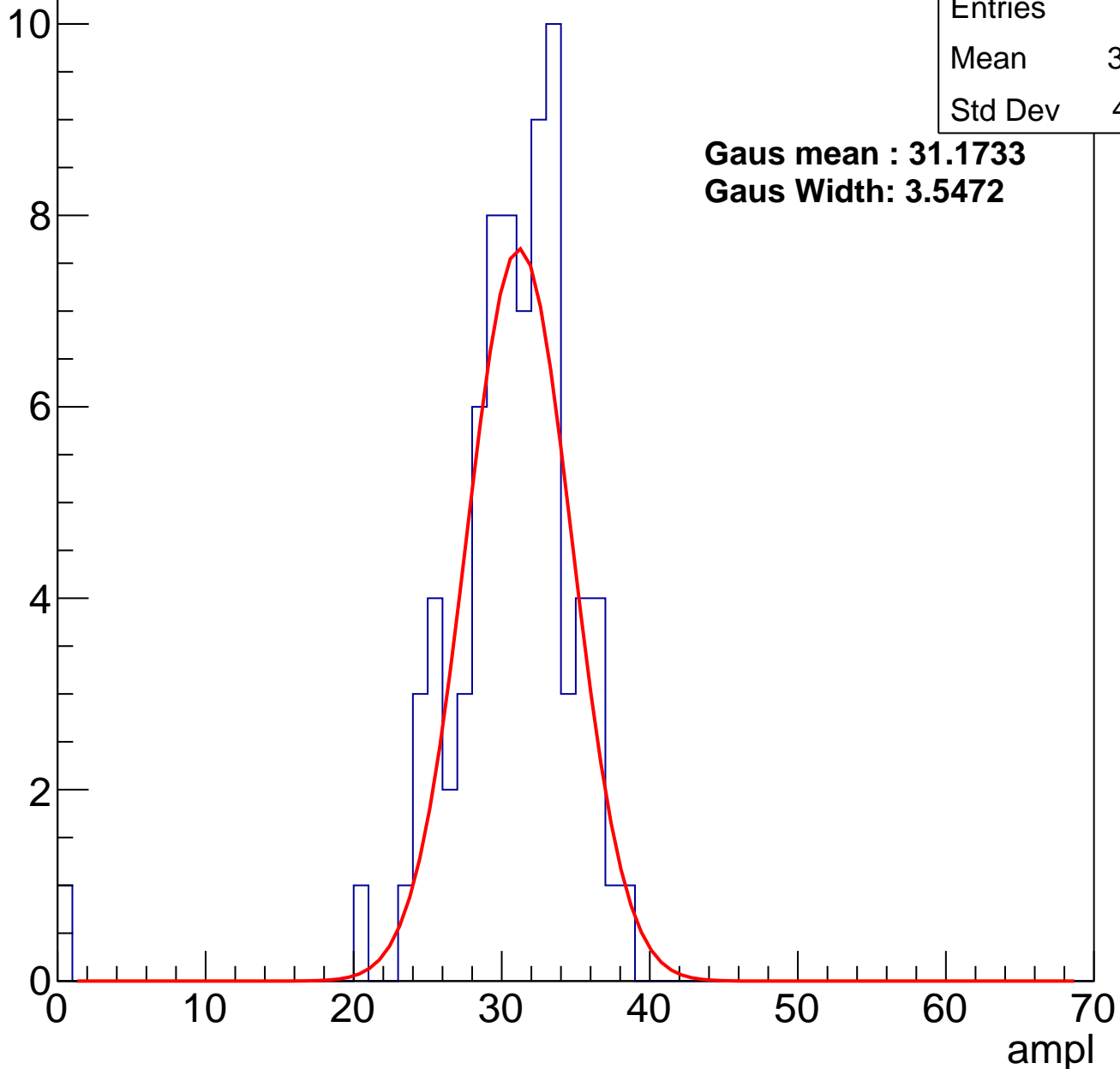
calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	30.05
Std Dev	4.981

Gaus mean : 31.1733

Gaus Width: 3.5472

Entry



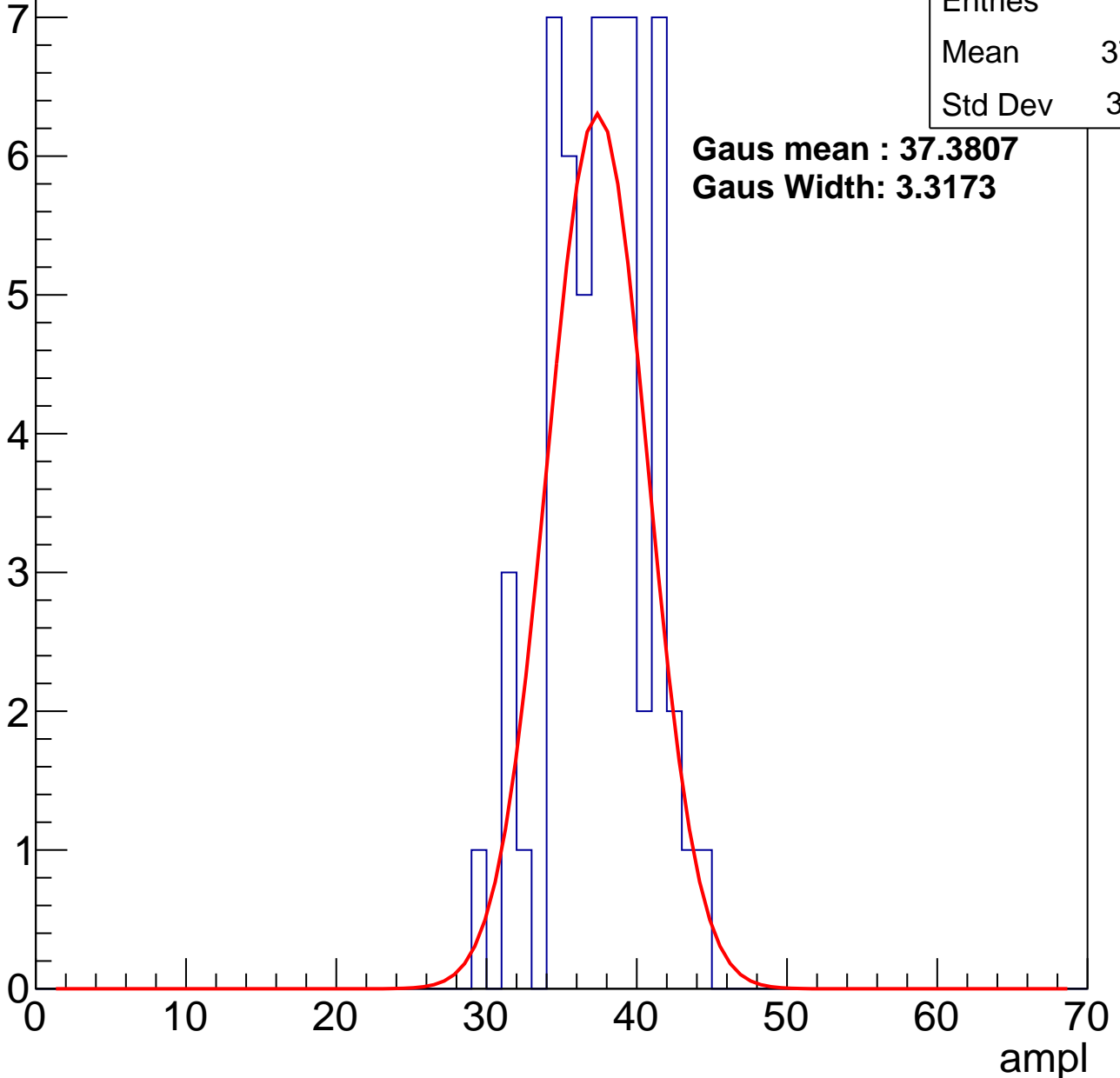
B1L102S, U18-ch31, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	37.16
Std Dev	3.211

Gaus mean : 37.3807
Gaus Width: 3.3173



B1L102S, U18-ch31, adc2

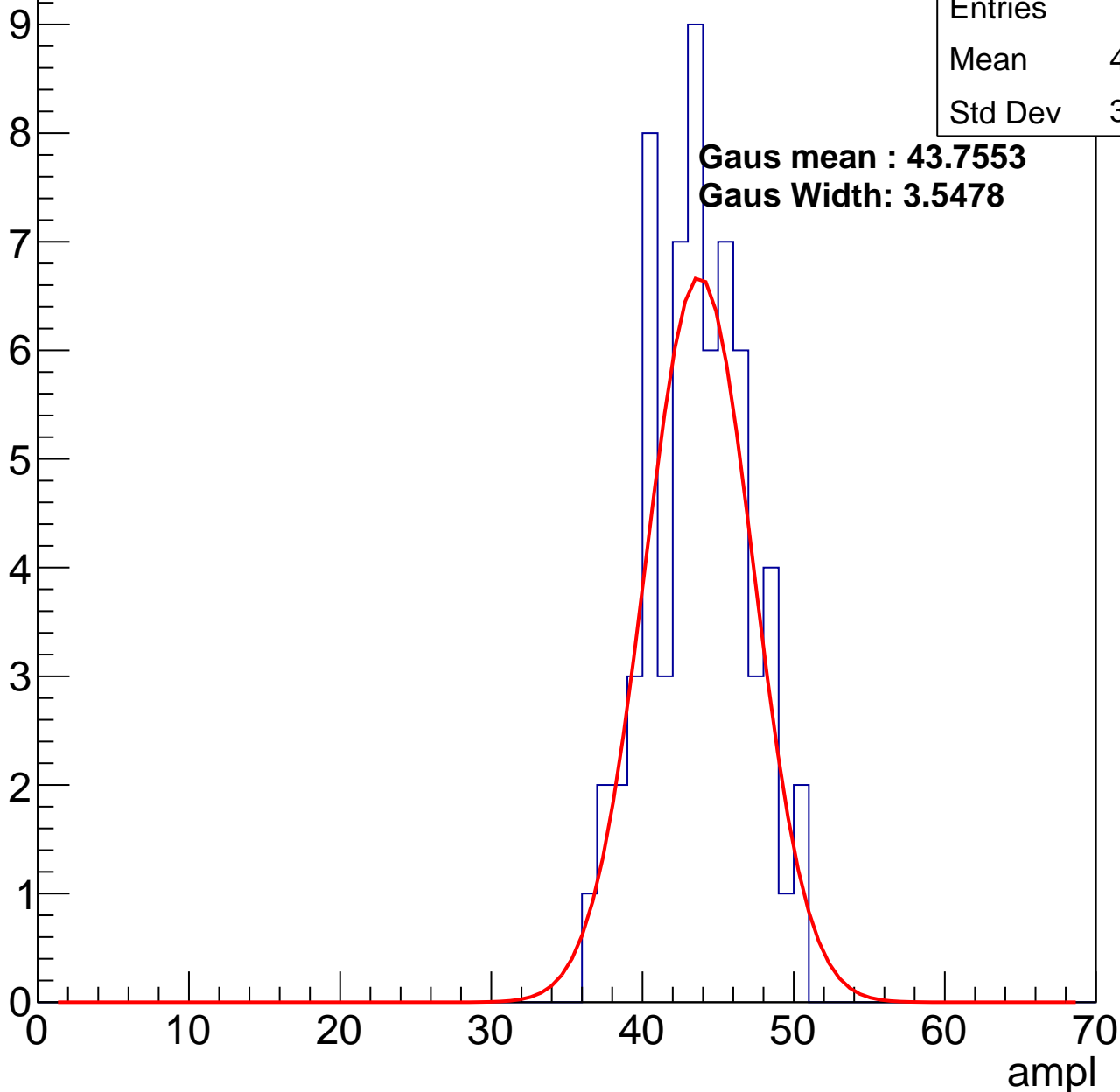
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.19
Std Dev	3.254

Gaus mean : 43.7553

Gaus Width: 3.5478

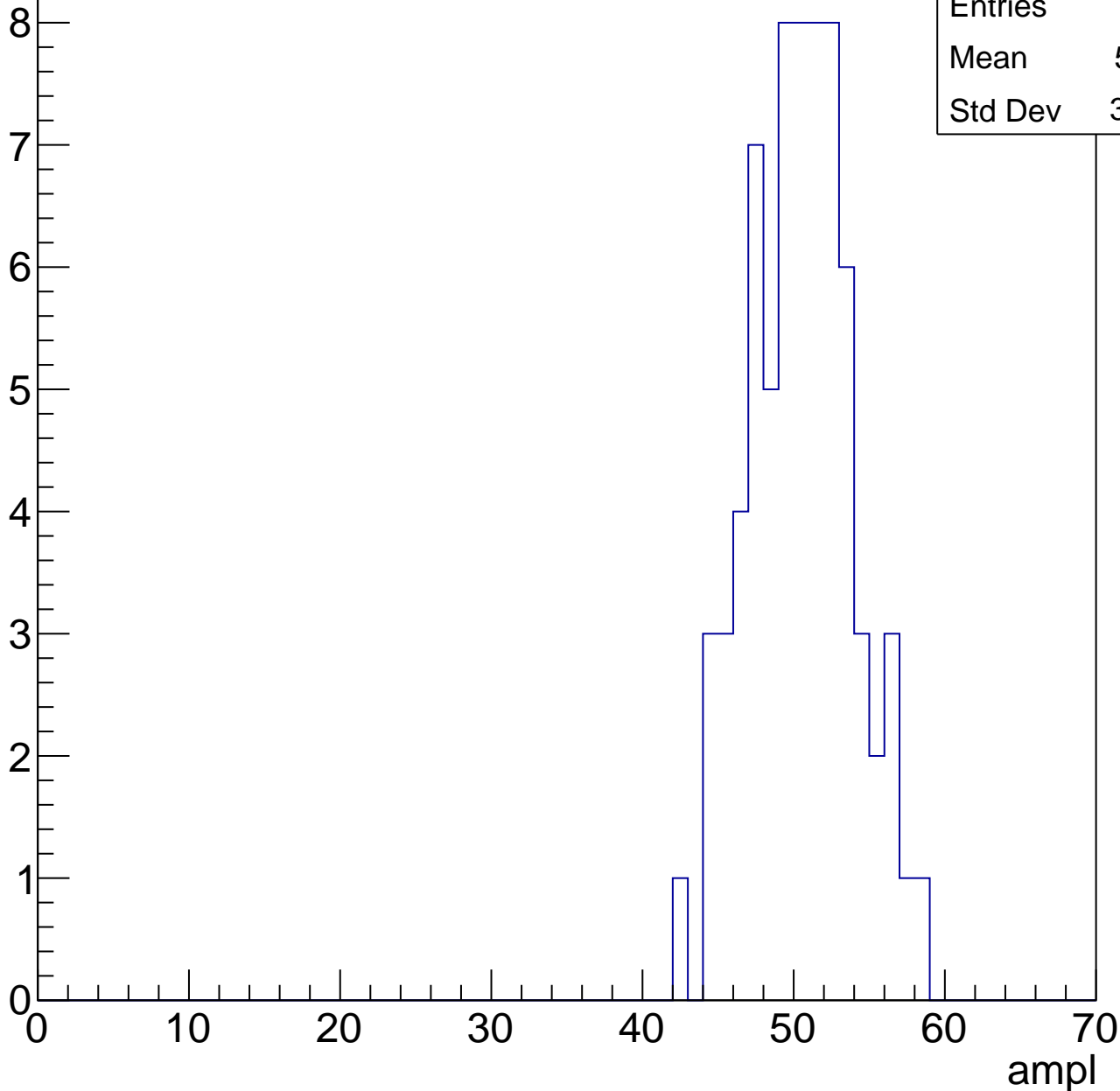


B1L102S, U18-ch31, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	50.01
Std Dev	3.388

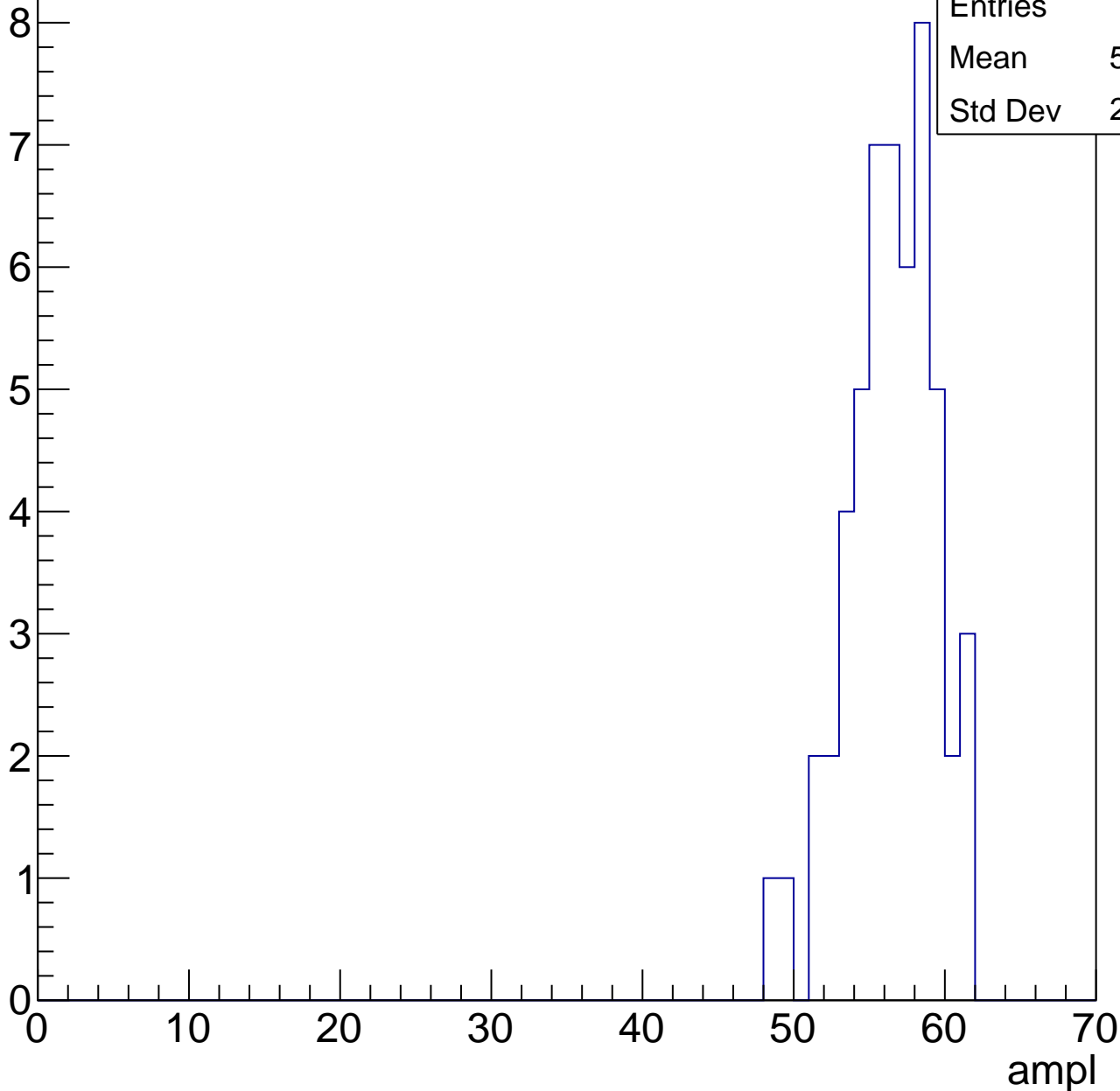


B1L102S, U18-ch31, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.96
Std Dev	2.907

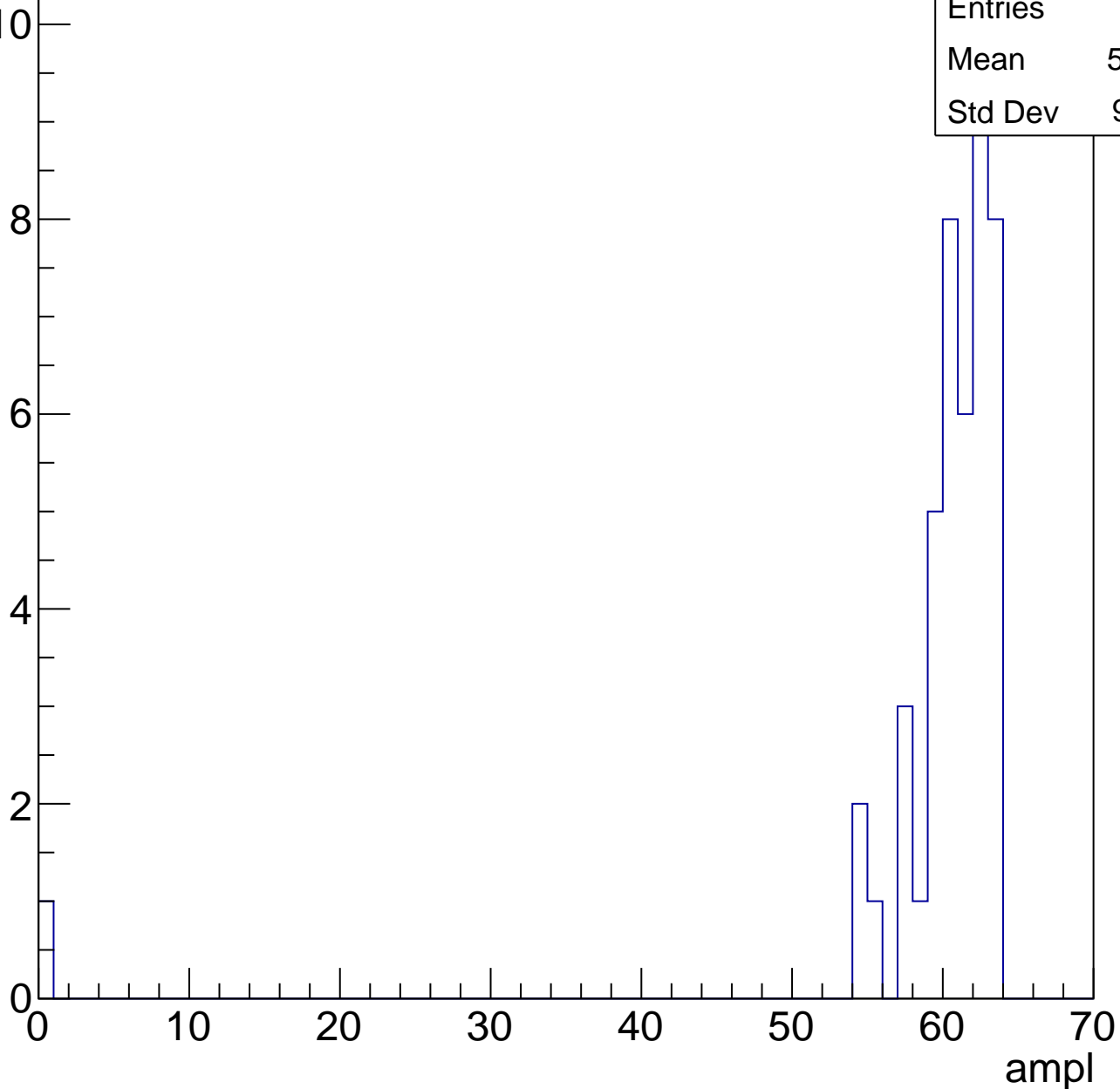


B1L102S, U18-ch31, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.04
Std Dev	9.201



B1L102S, U18-ch31, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch31, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch32, adc0

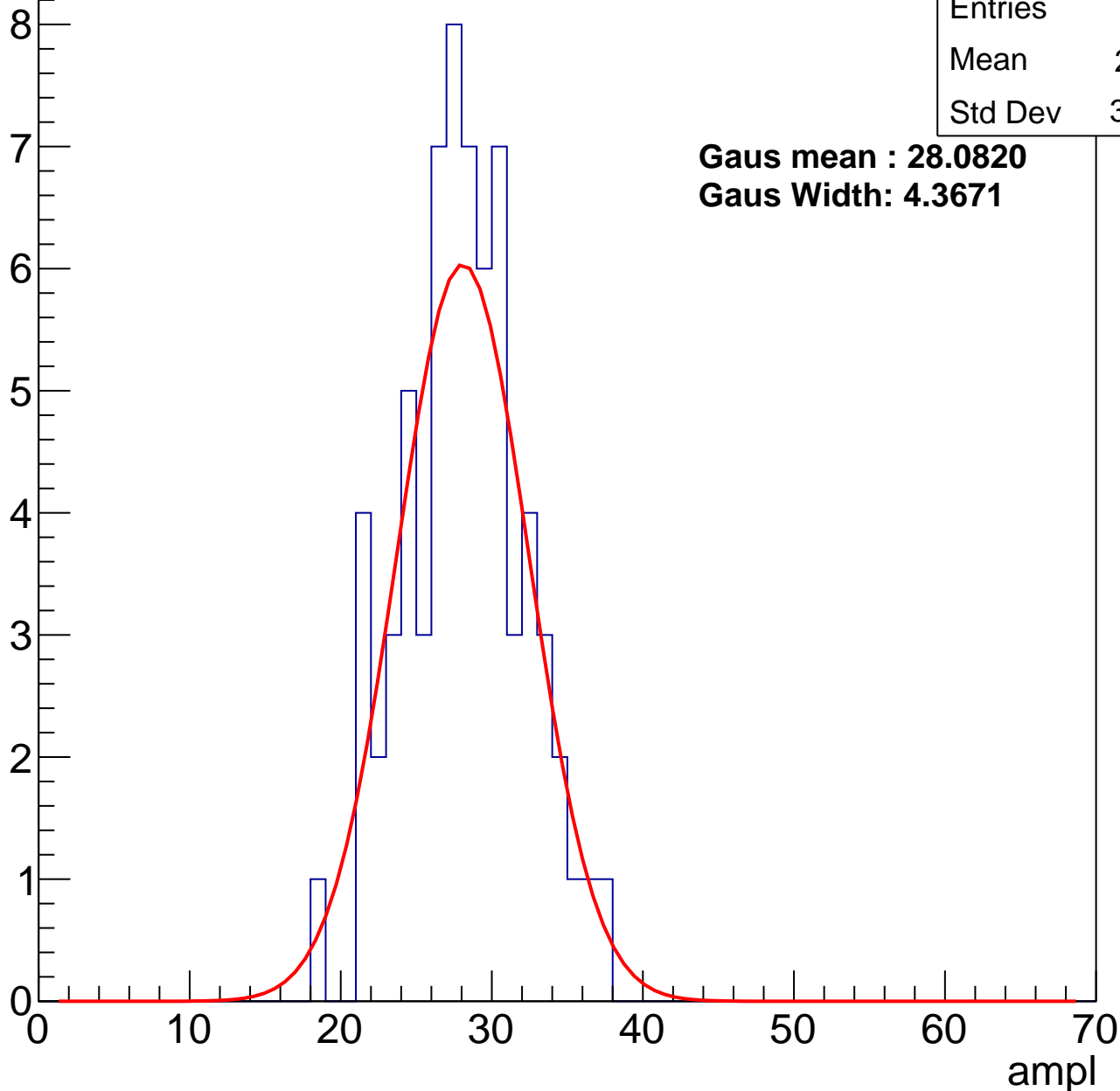
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	27.71
Std Dev	3.922

Gaus mean : 28.0820

Gaus Width: 4.3671



B1L102S, U18-ch32, adc1

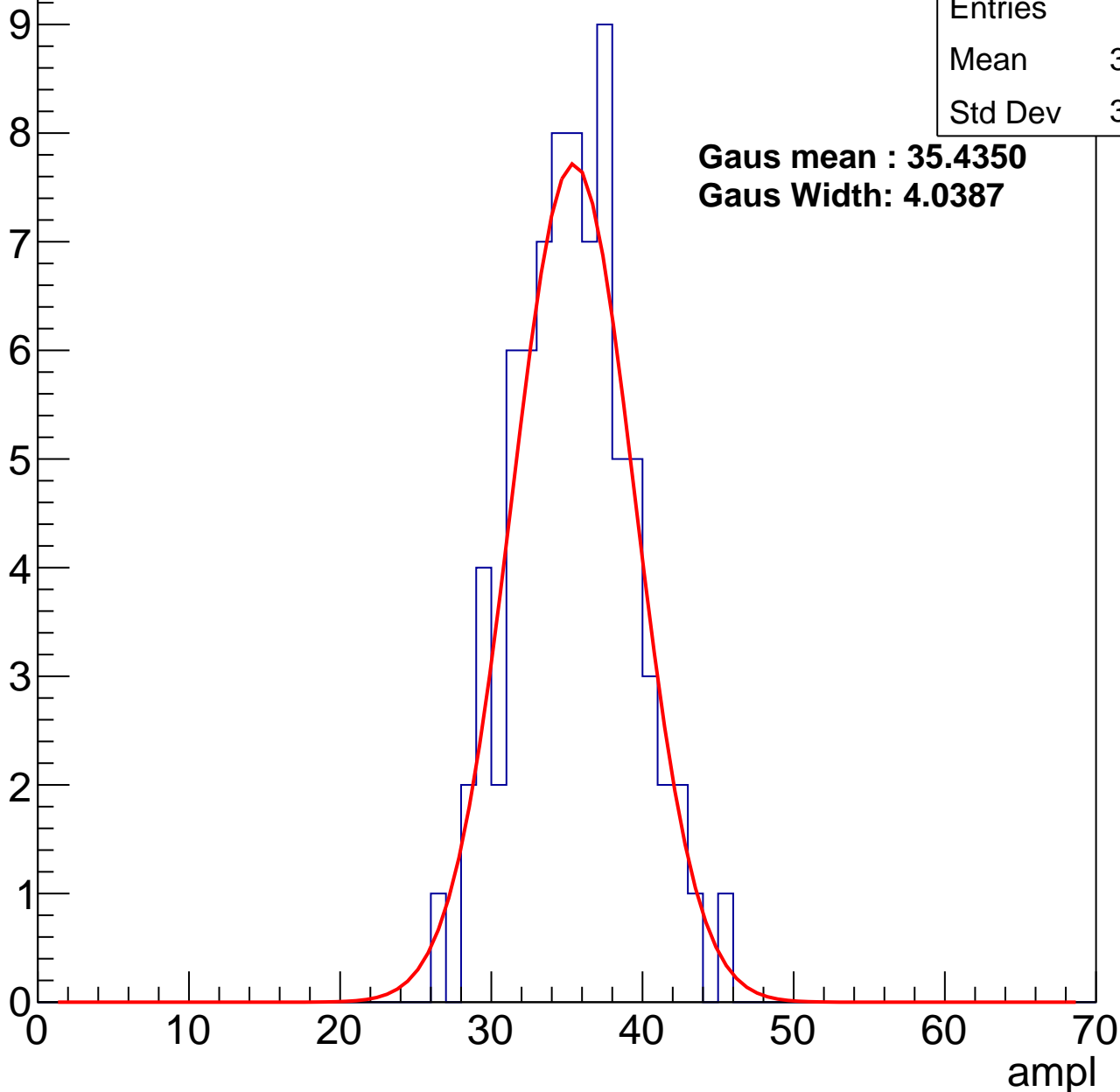
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	34.97
Std Dev	3.792

Gaus mean : 35.4350

Gaus Width: 4.0387



B1L102S, U18-ch32, adc2

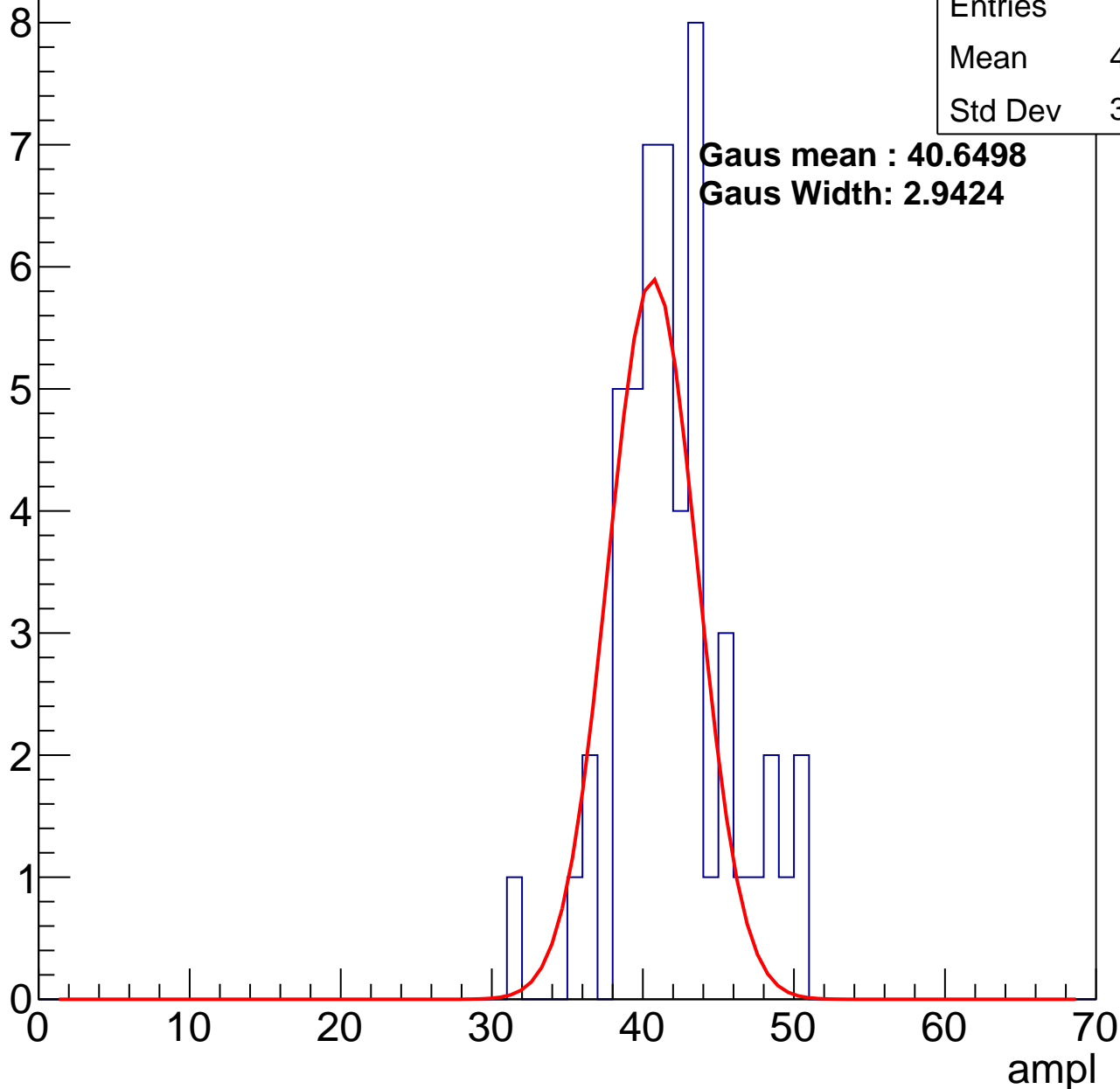
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	41.55
Std Dev	3.759

Gaus mean : 40.6498

Gaus Width: 2.9424

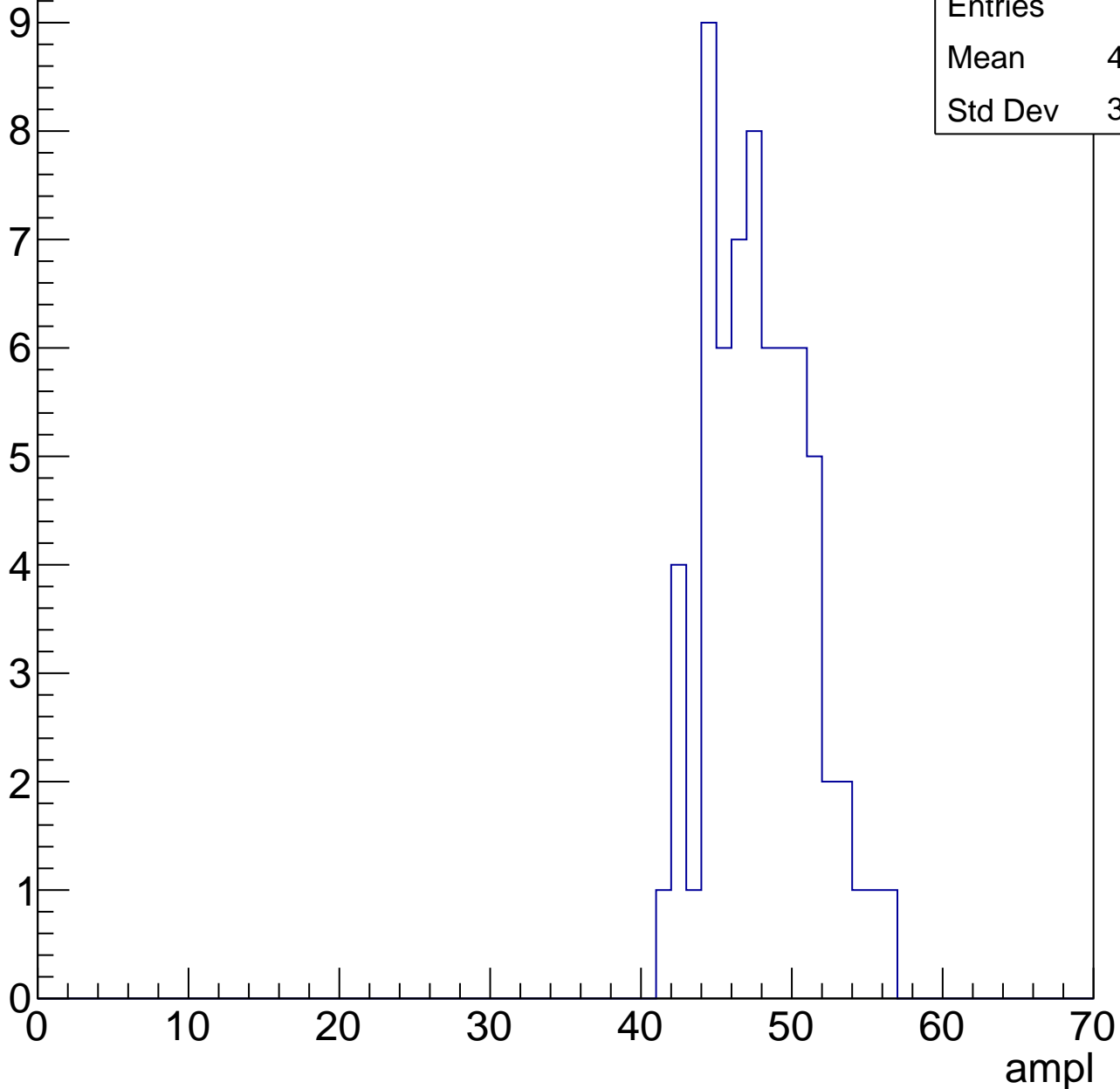


B1L102S, U18-ch32, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.39
Std Dev	3.334

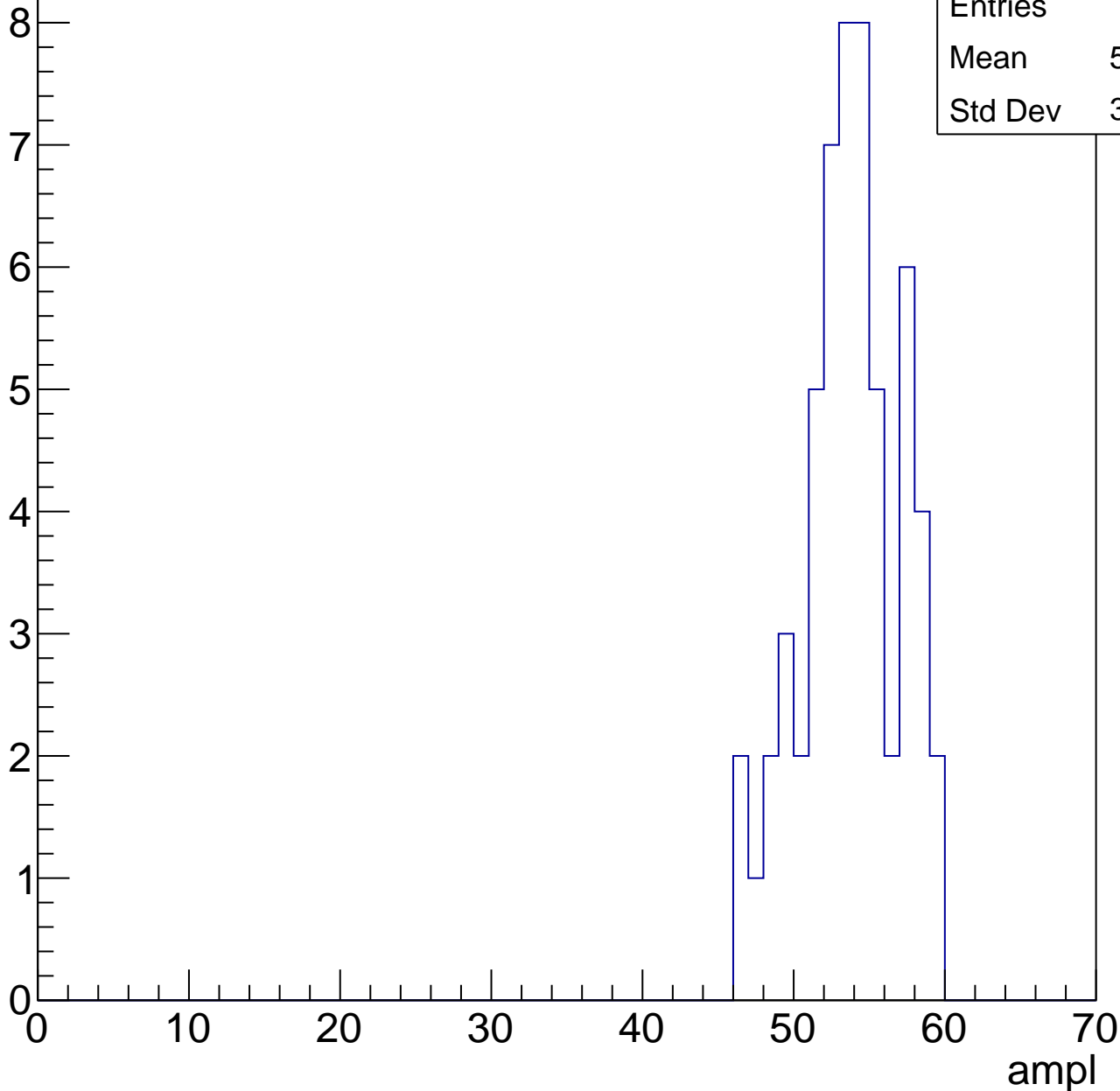


B1L102S, U18-ch32, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.26
Std Dev	3.209



B1L102S, U18-ch32, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	49
Mean	58.71
Std Dev	2.711

10

20

30

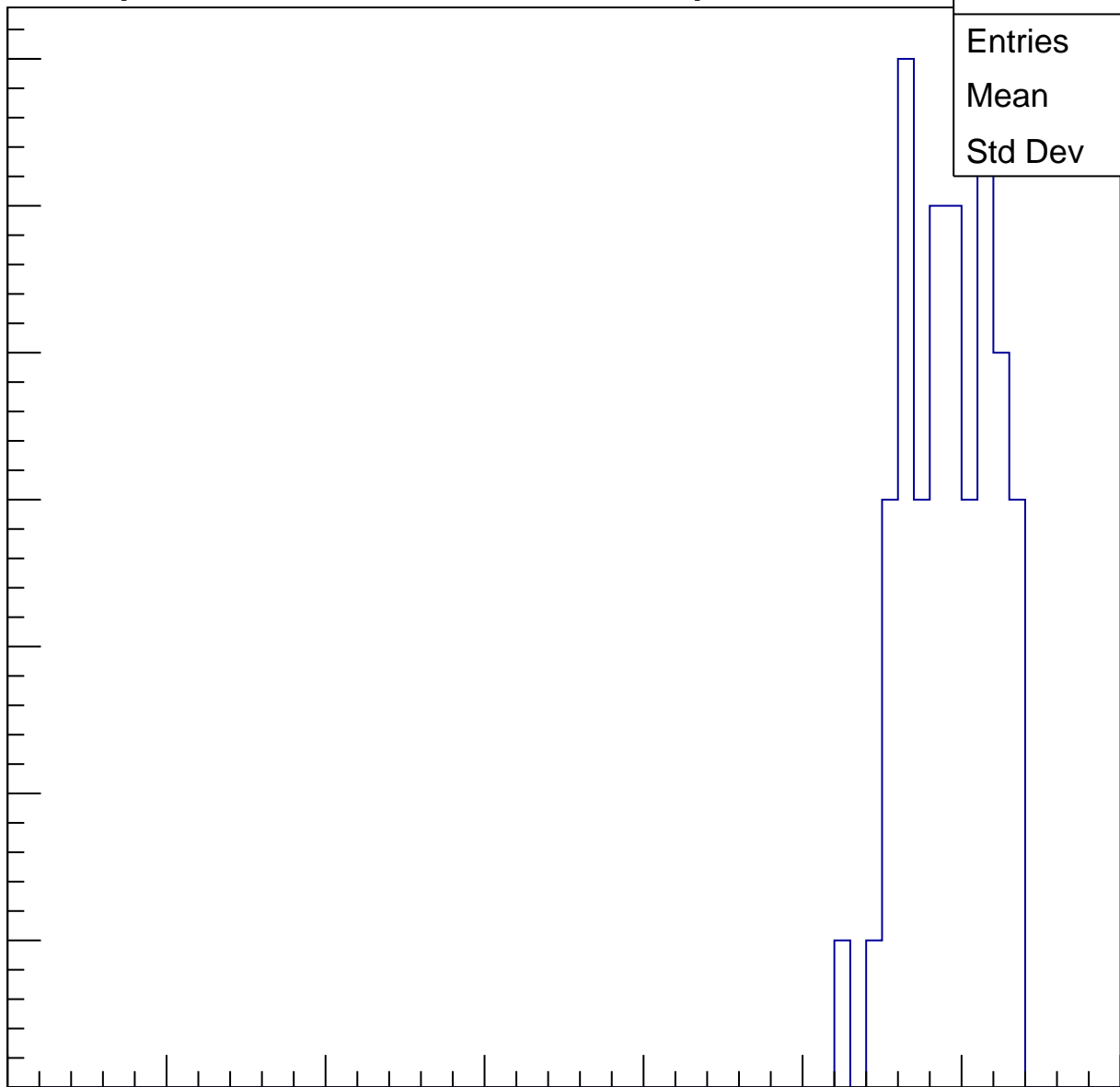
40

50

60

70

ampl

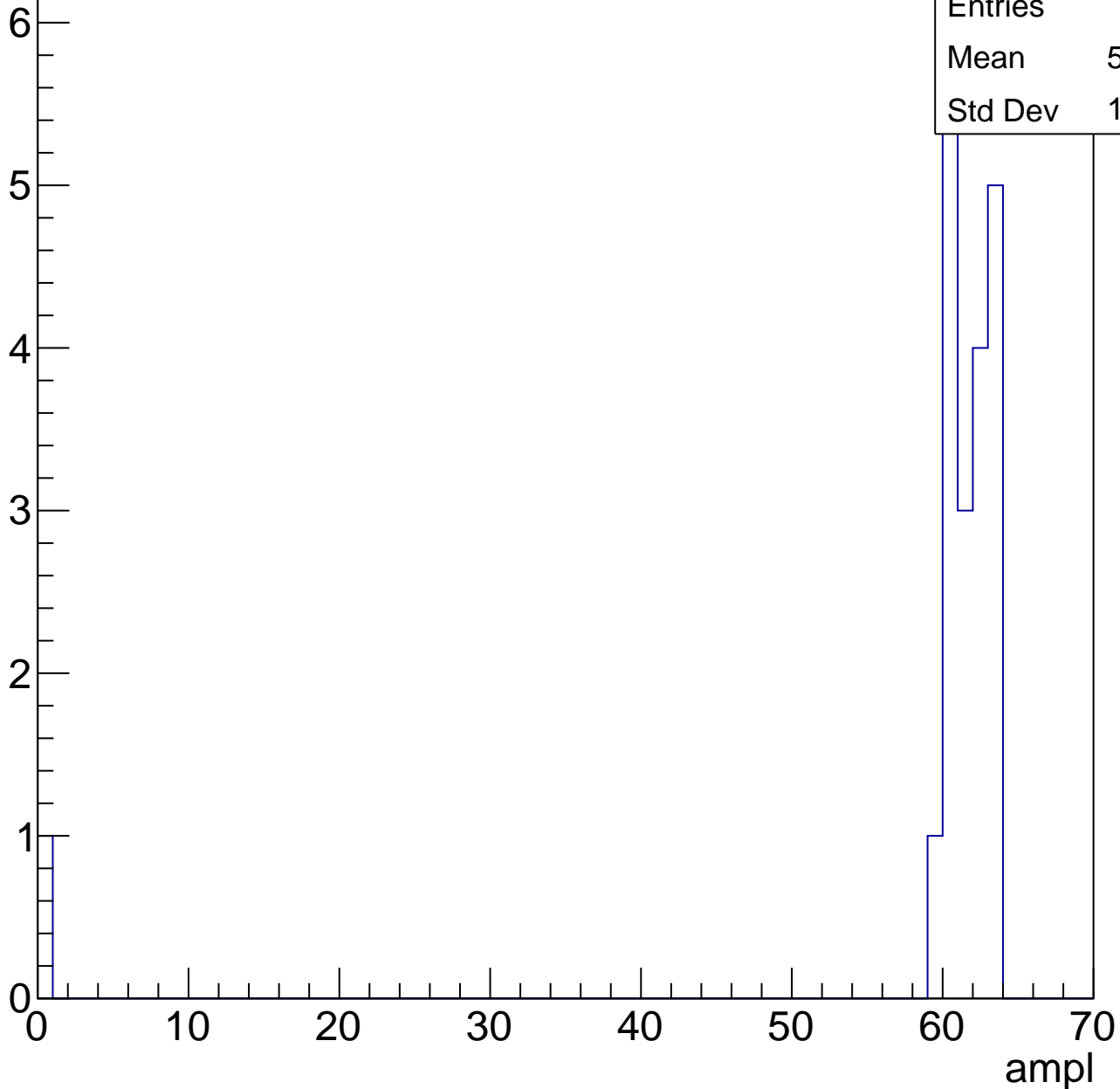


B1L102S, U18-ch32, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

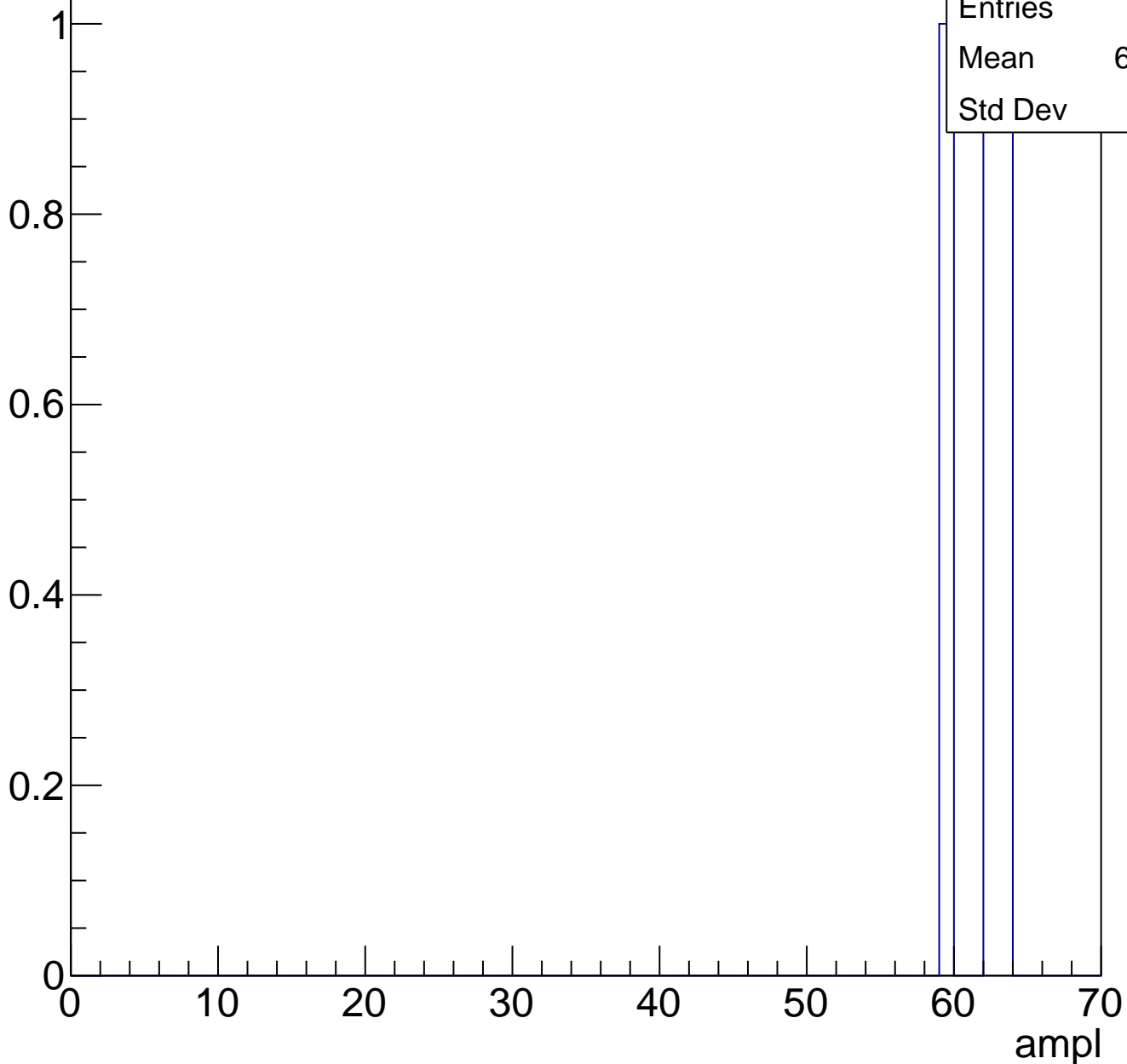
Entries	20
Mean	58.25
Std Dev	13.42



B1L102S, U18-ch32, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch33, adc0

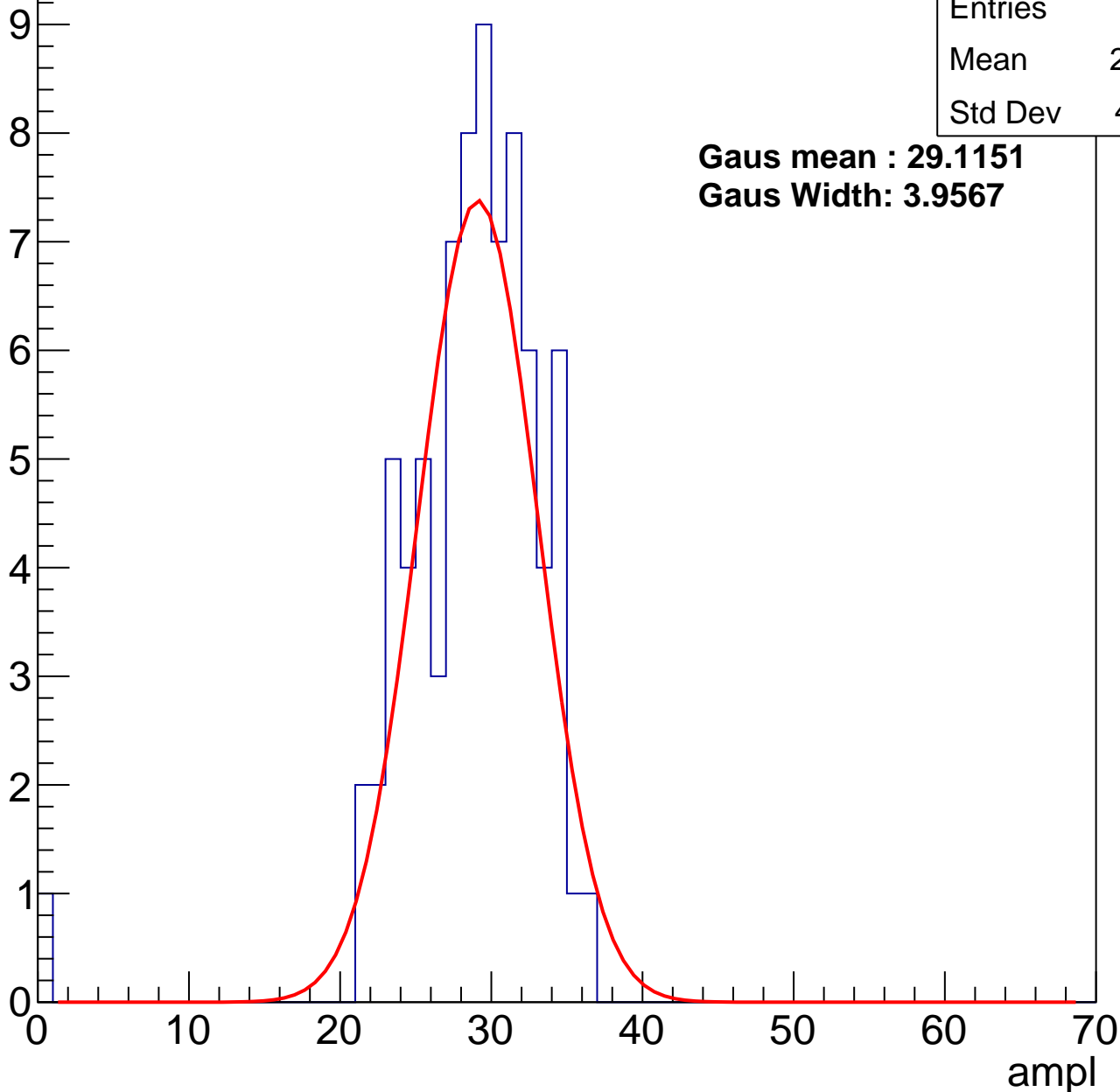
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.24
Std Dev	4.821

Gaus mean : 29.1151

Gaus Width: 3.9567



B1L102S, U18-ch33, adc1

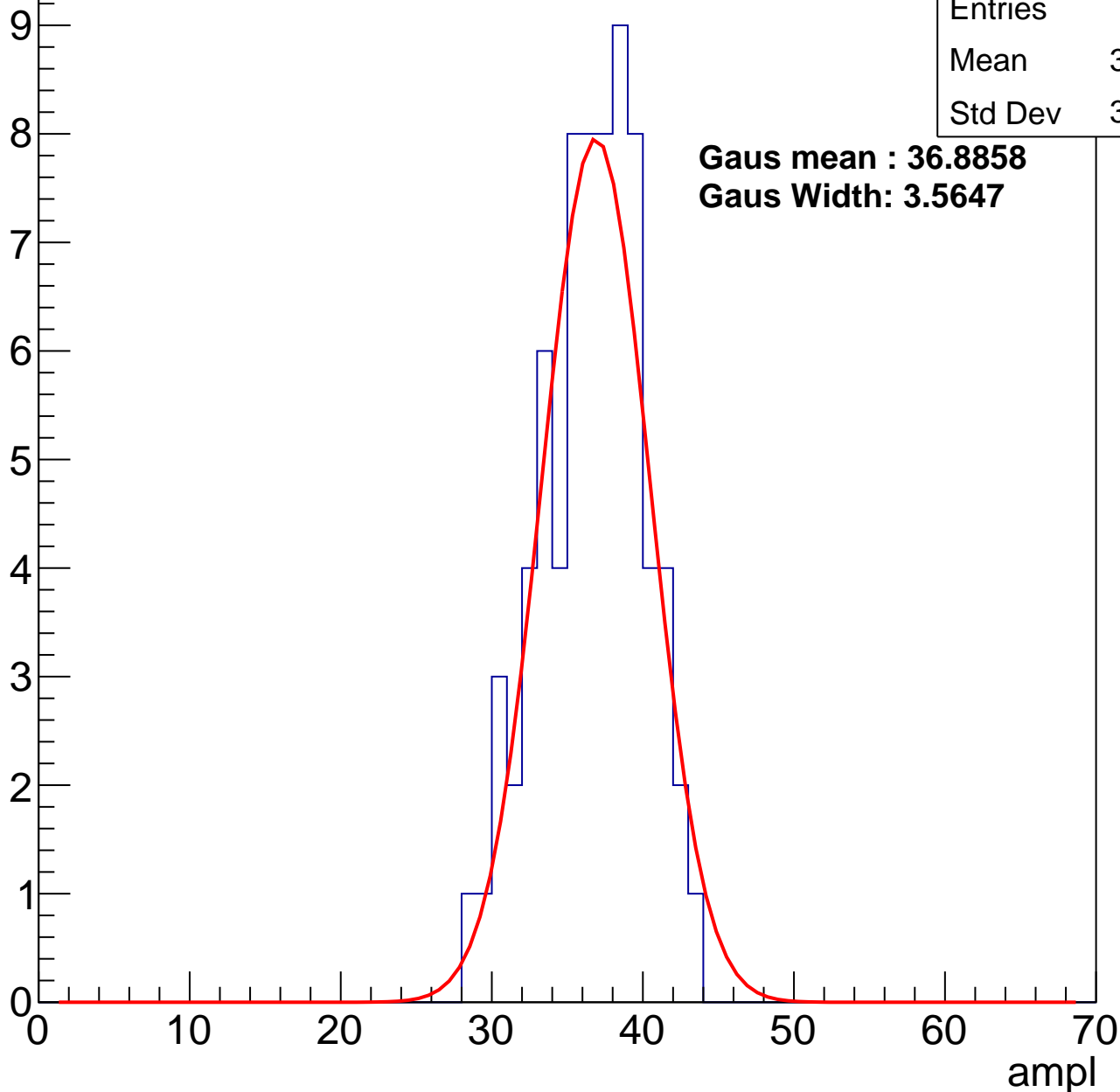
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.16
Std Dev	3.335

Gaus mean : 36.8858

Gaus Width: 3.5647



B1L102S, U18-ch33, adc2

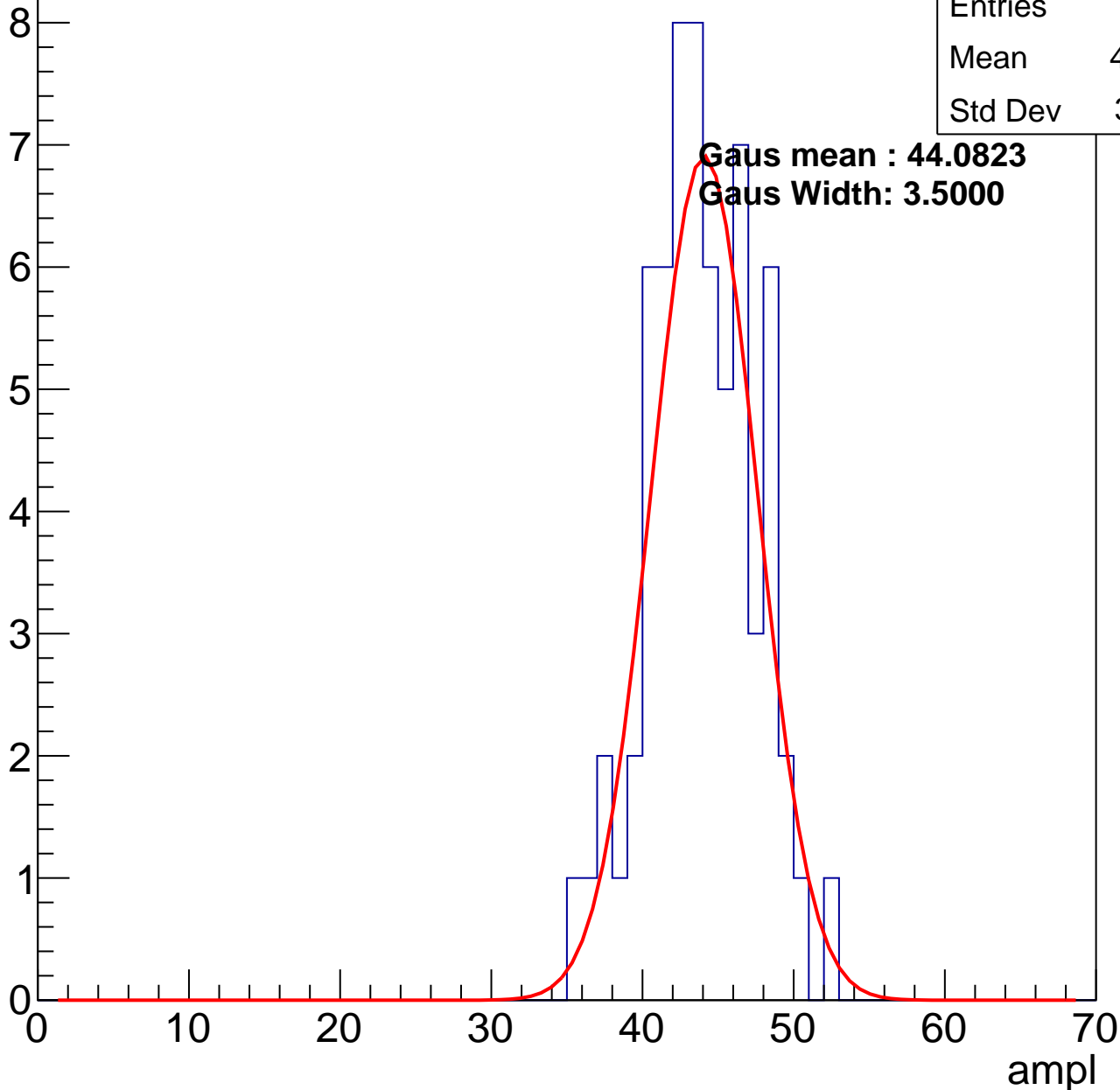
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	43.44
Std Dev	3.491

Gaus mean : 44.0823

Gaus Width: 3.5000

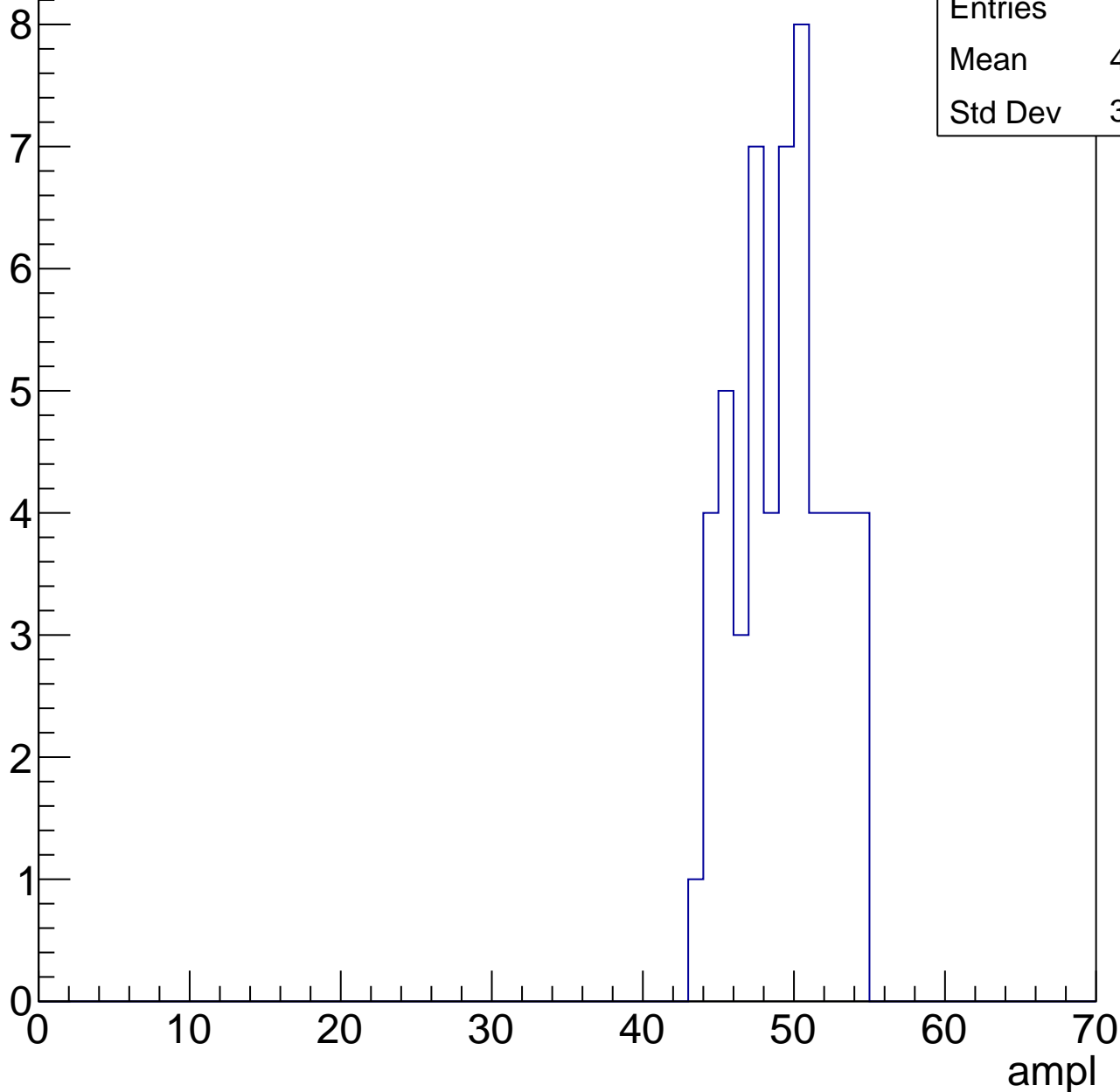


B1L102S, U18-ch33, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.84
Std Dev	3.008

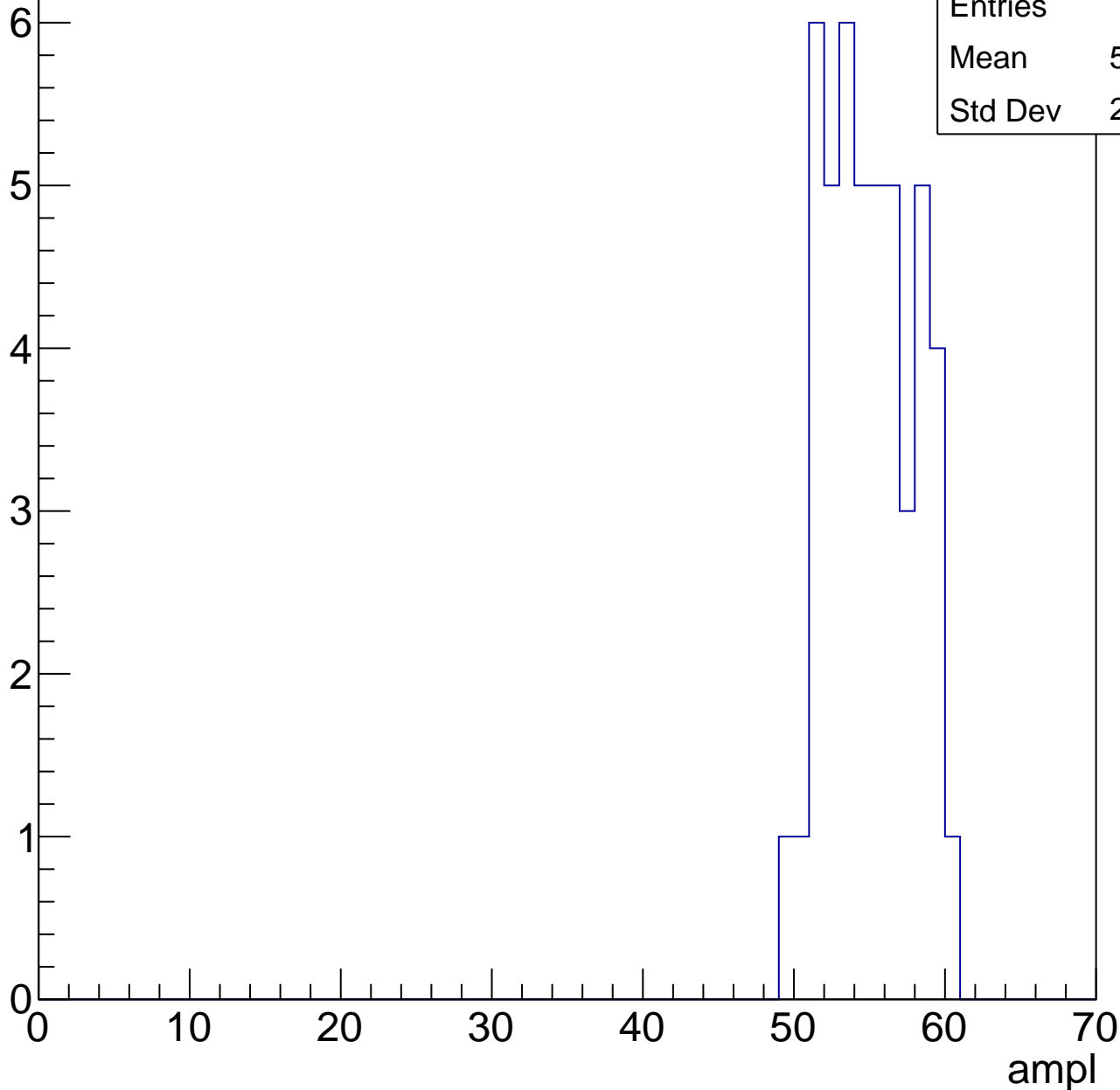


B1L102S, U18-ch33, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

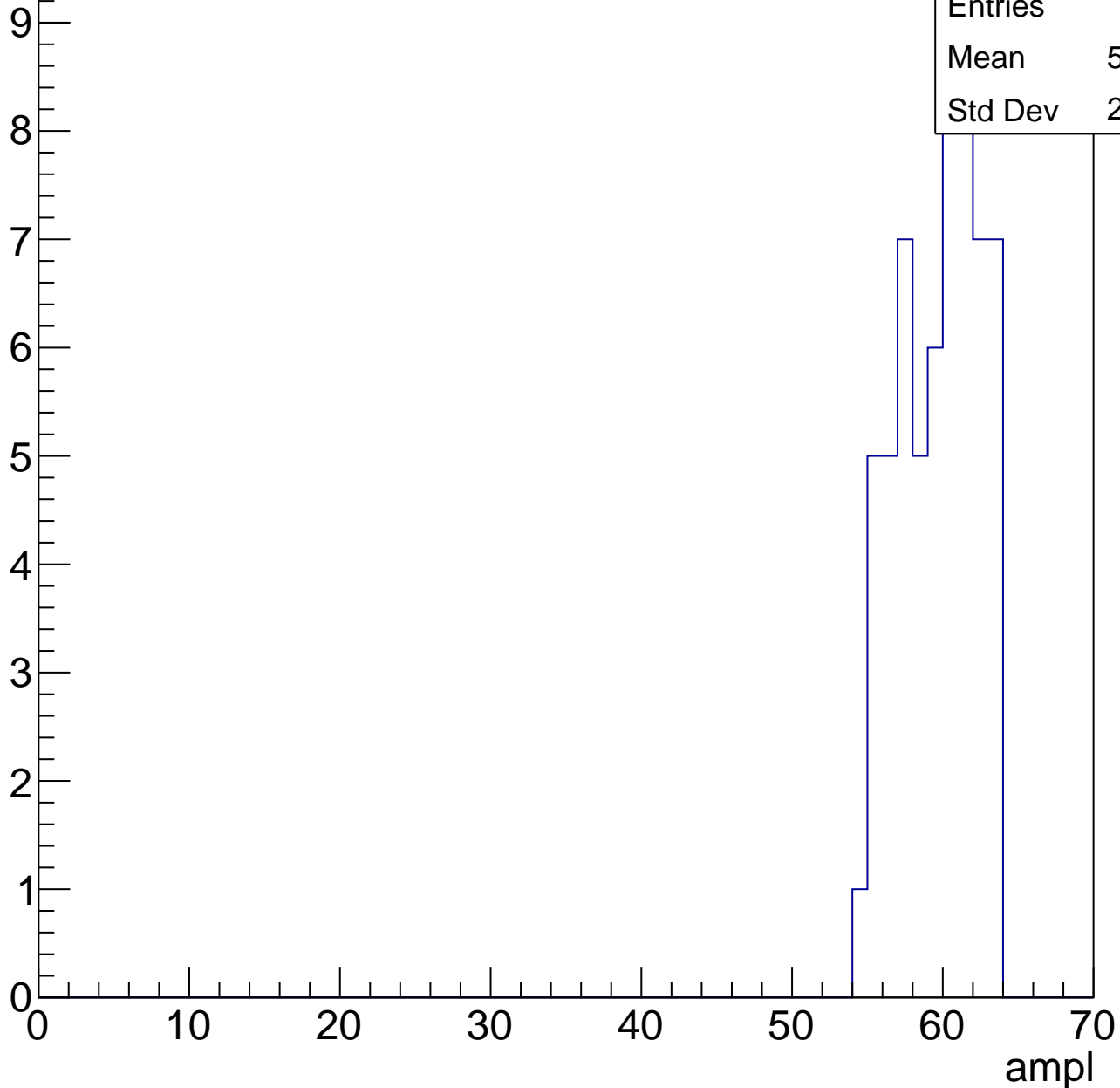
Entries	47
Mean	54.57
Std Dev	2.819



B1L102S, U18-ch33, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch33, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	55.22
Std Dev	19.54

B1L102S, U18-ch33, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch34, adc0

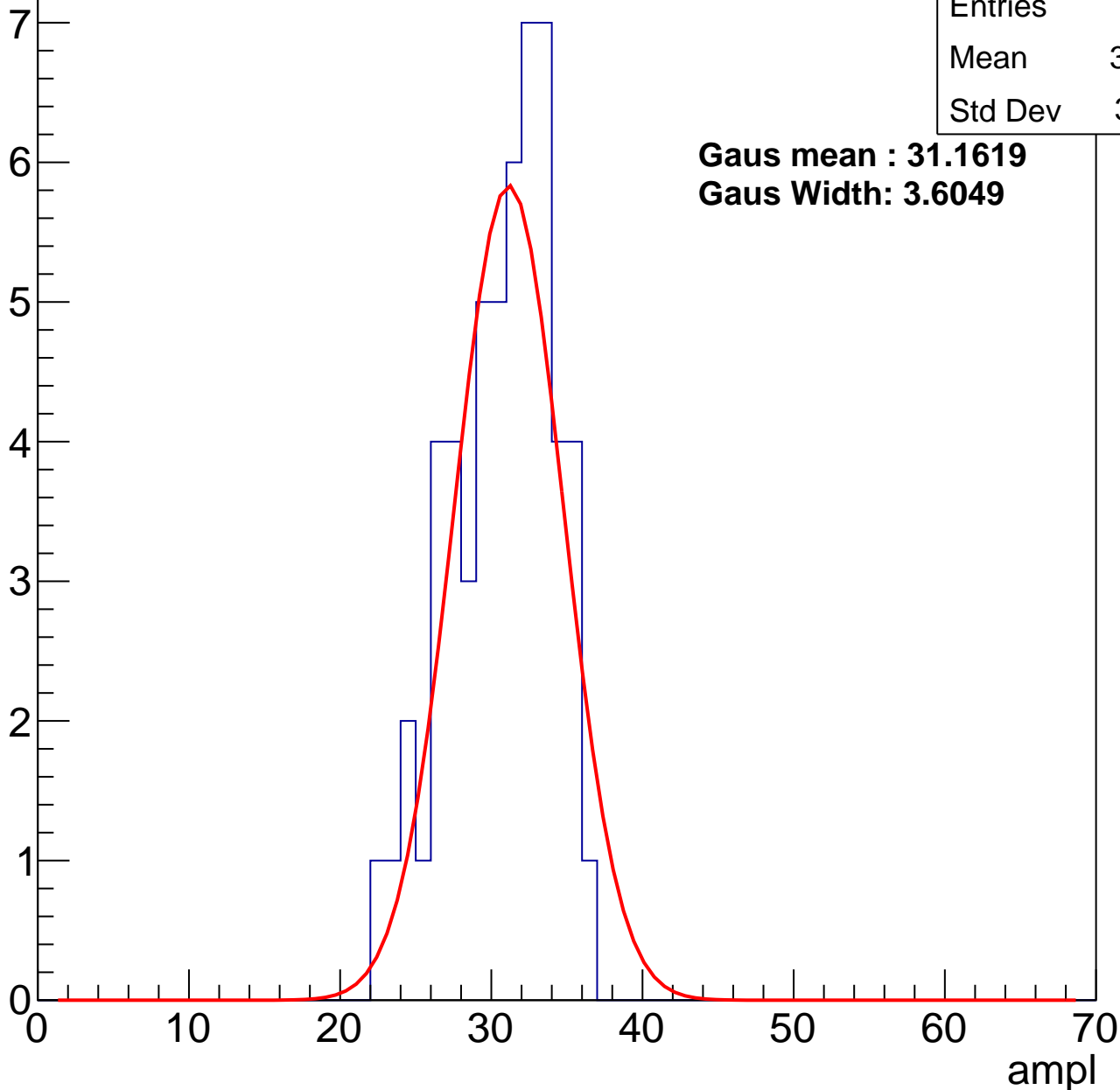
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	30.22
Std Dev	3.361

Gaus mean : 31.1619

Gaus Width: 3.6049



B1L102S, U18-ch34, adc1

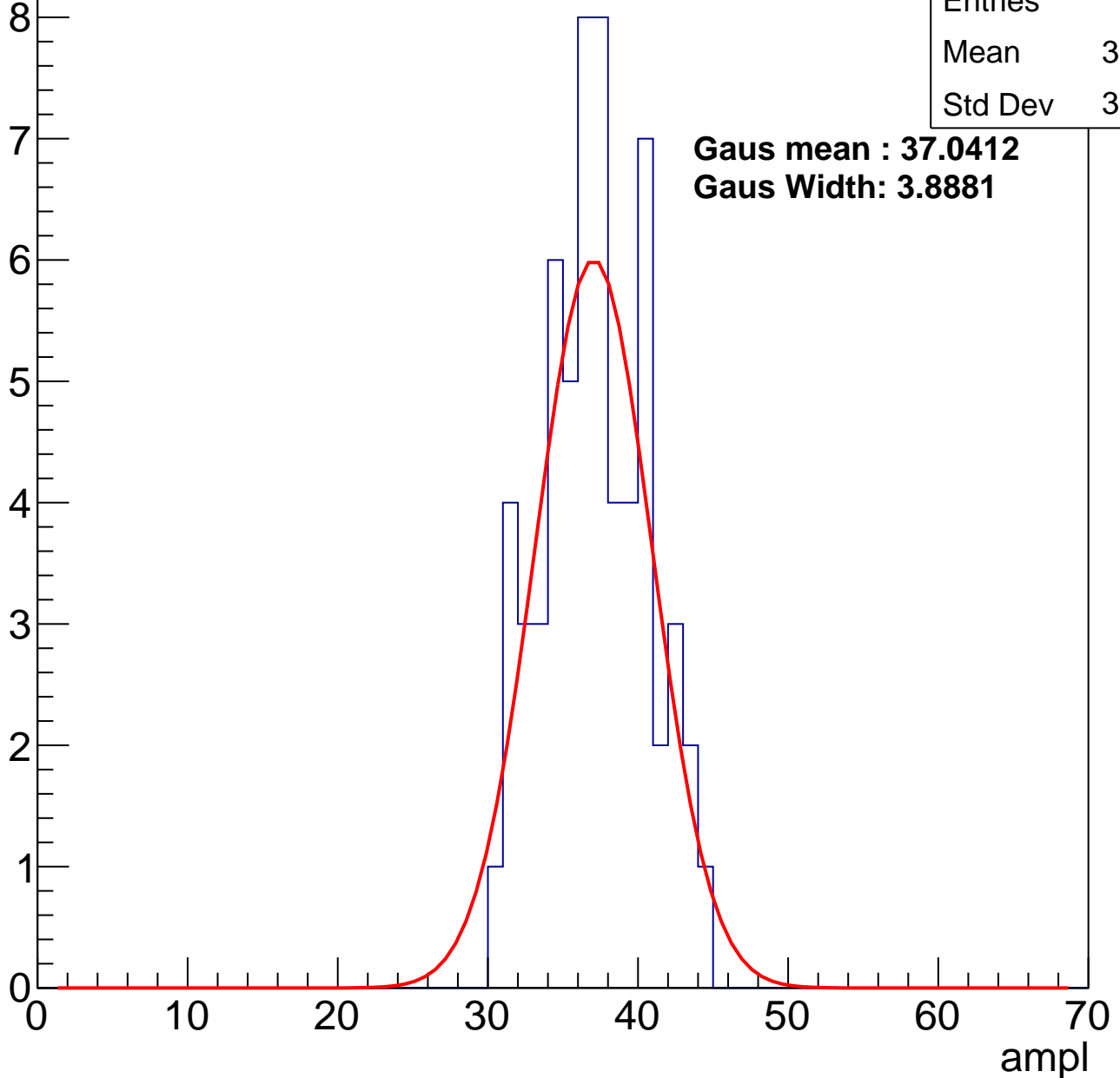
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.69
Std Dev	3.395

Gaus mean : 37.0412

Gaus Width: 3.8881



B1L102S, U18-ch34, adc2

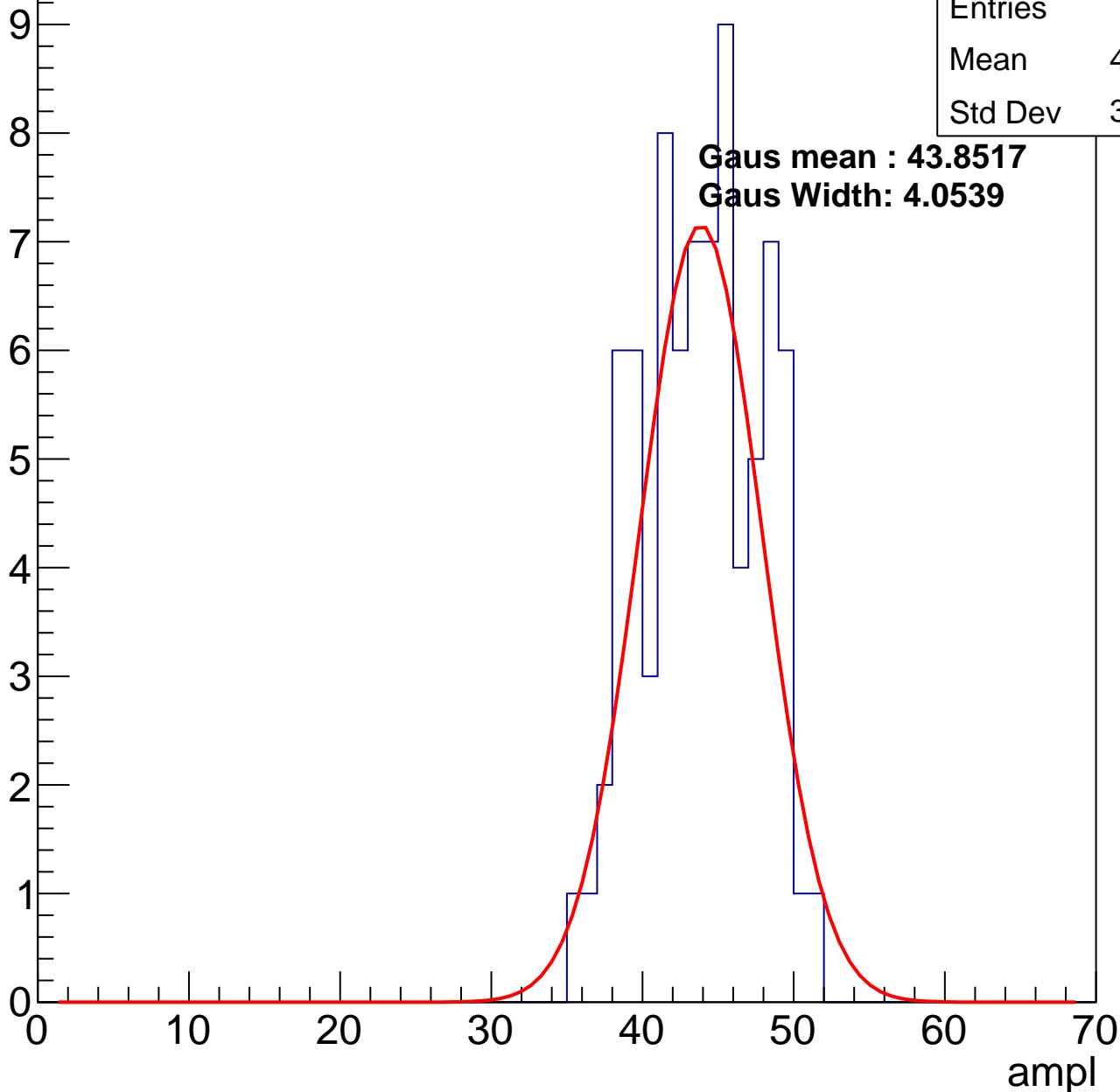
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	43.39
Std Dev	3.786

Gaus mean : 43.8517

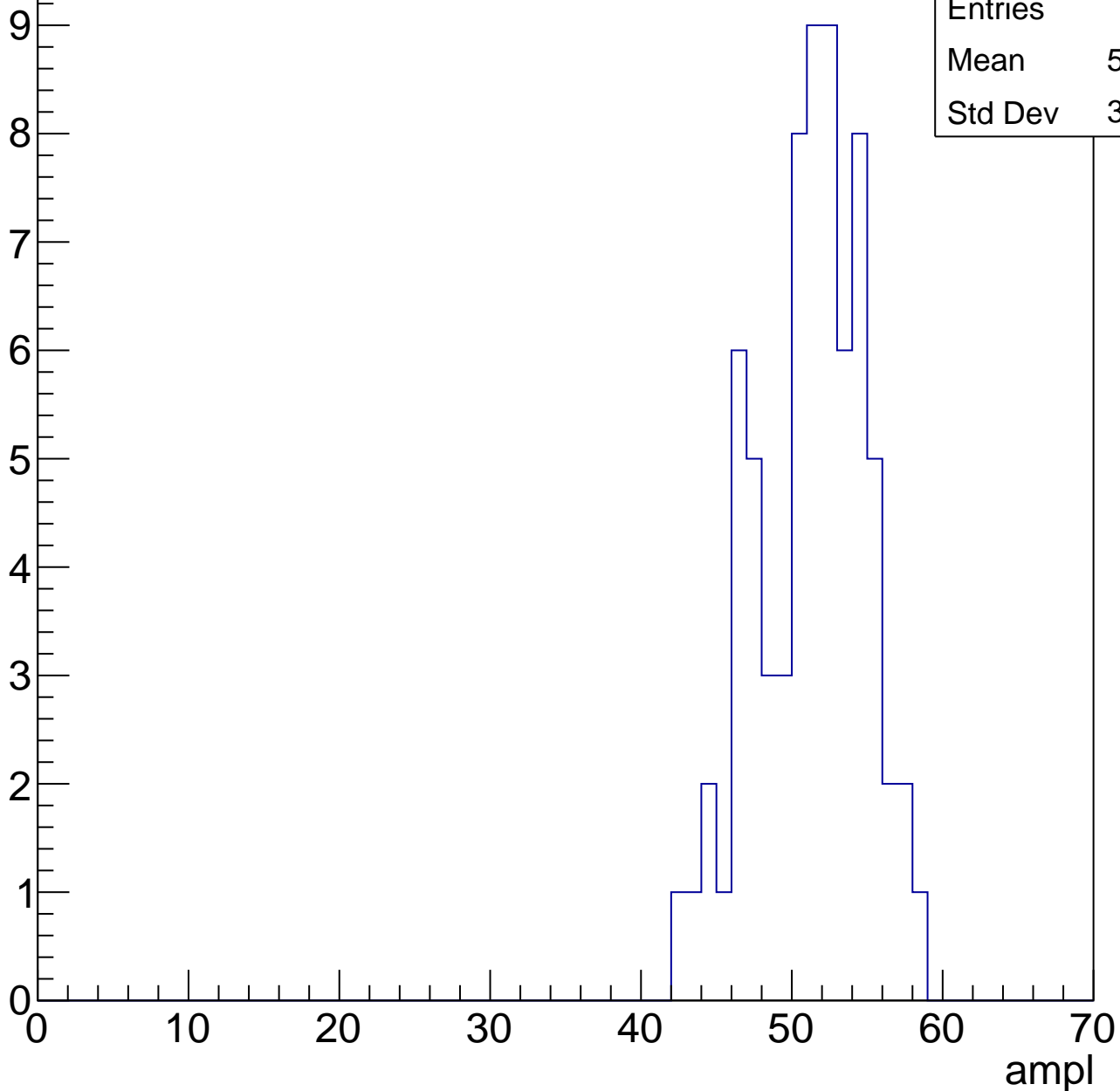
Gaus Width: 4.0539



B1L102S, U18-ch34, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

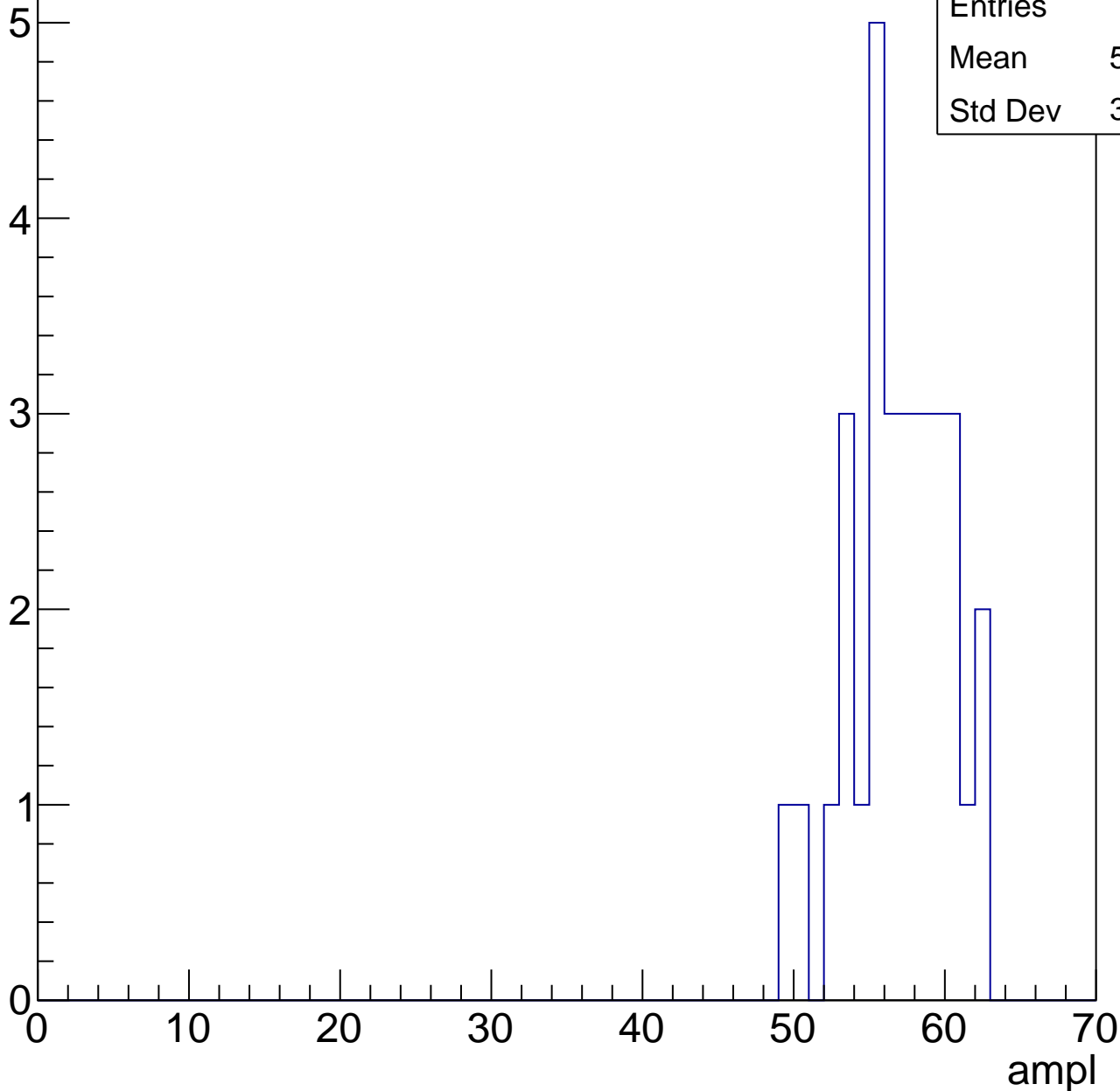


B1L102S, U18-ch34, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	30
Mean	56.47
Std Dev	3.253

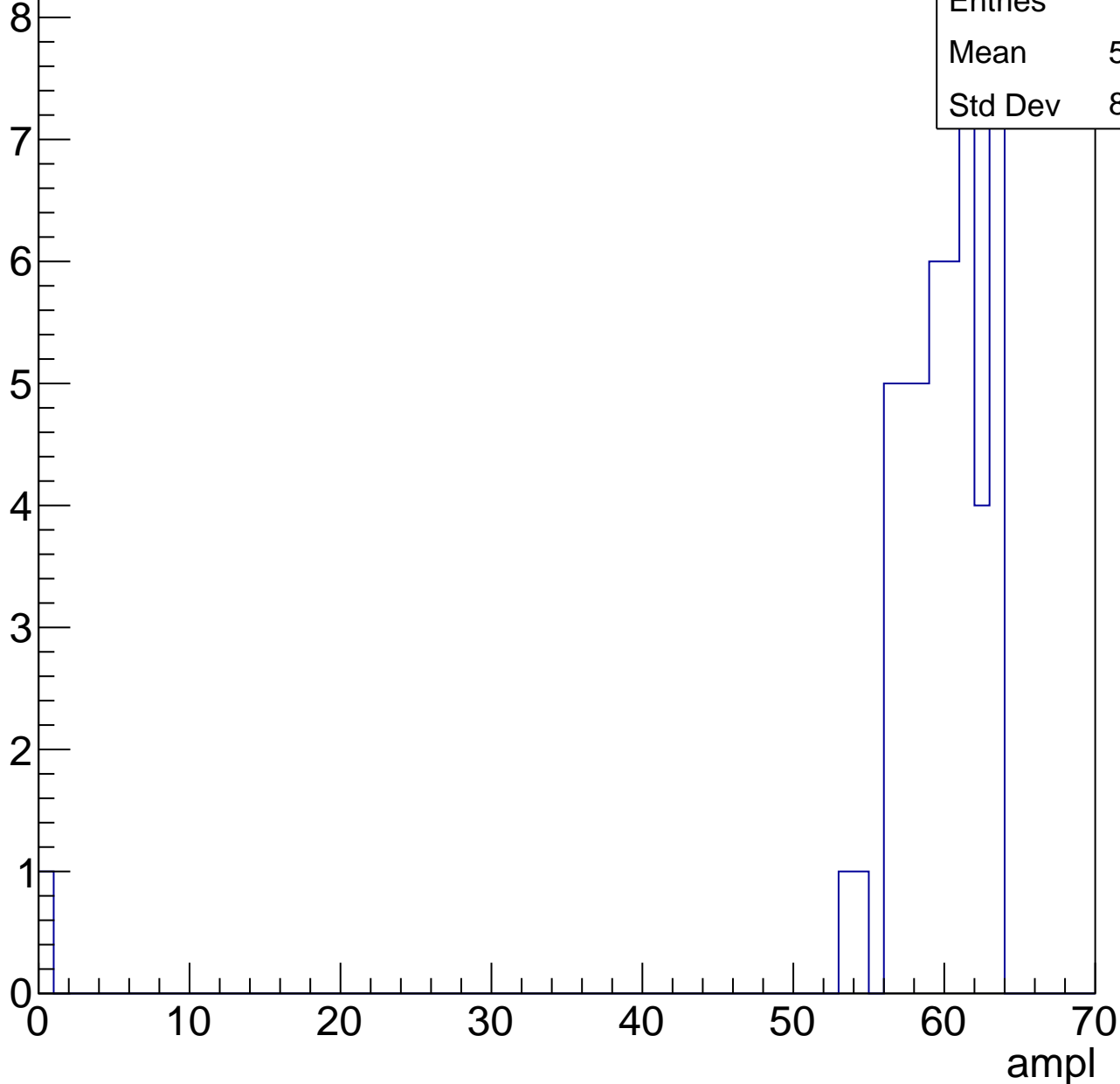


B1L102S, U18-ch34, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

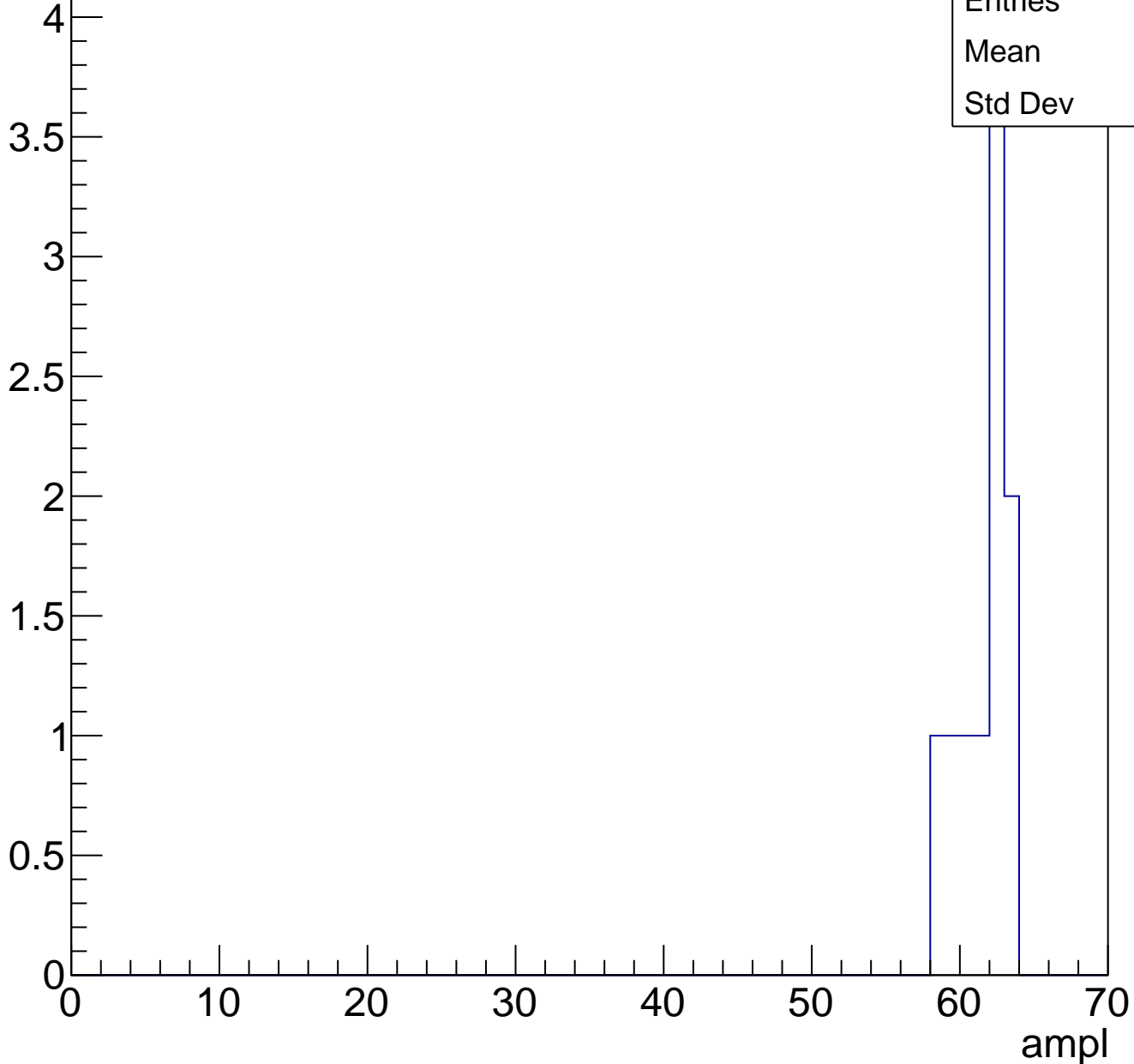
Entries	50
Mean	58.32
Std Dev	8.707



B1L102S, U18-ch34, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	10
Mean	61.2
Std Dev	1.6

B1L102S, U18-ch34, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch35, adc0

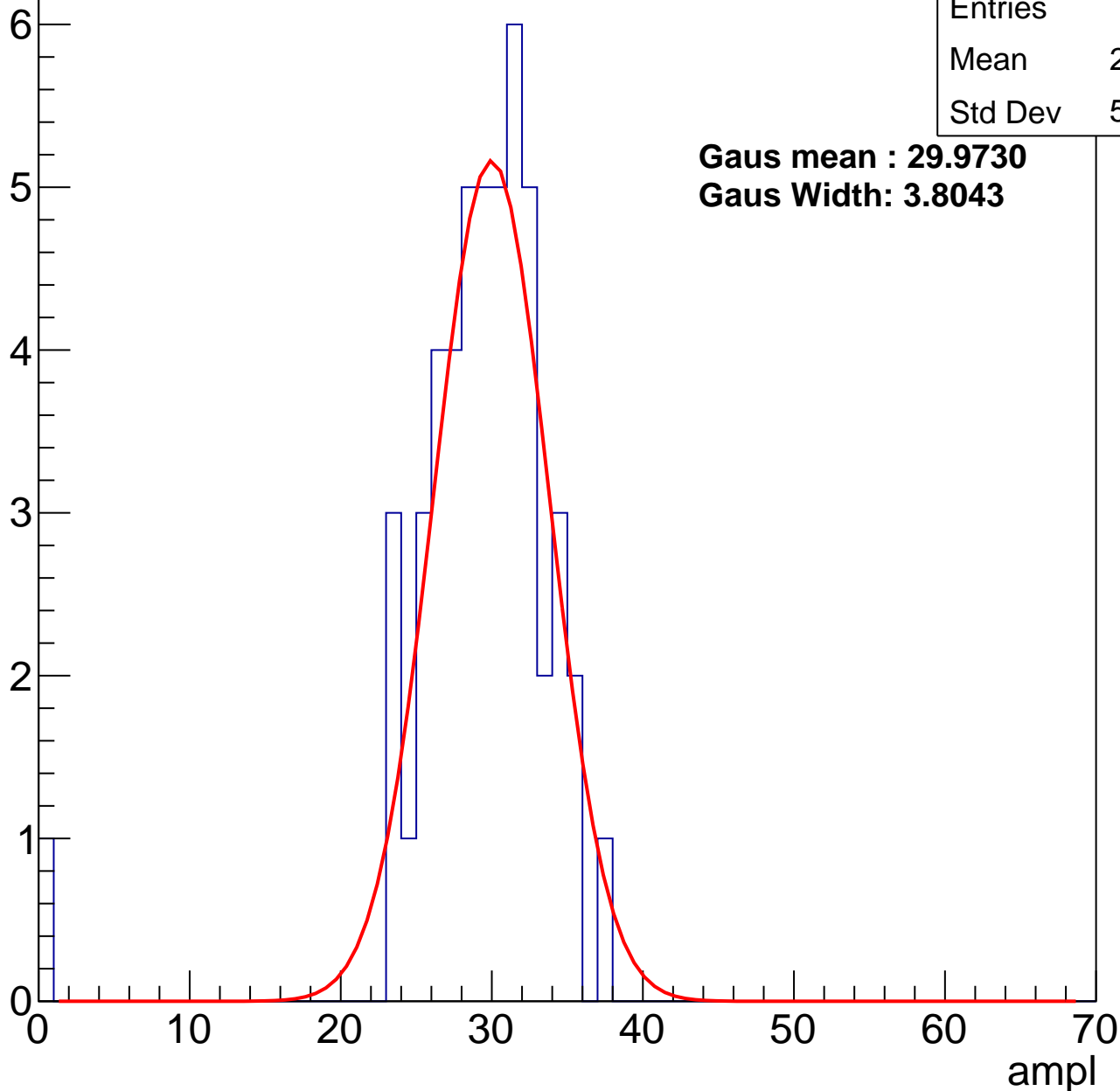
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	28.72
Std Dev	5.277

Gaus mean : 29.9730

Gaus Width: 3.8043



B1L102S, U18-ch35, adc1

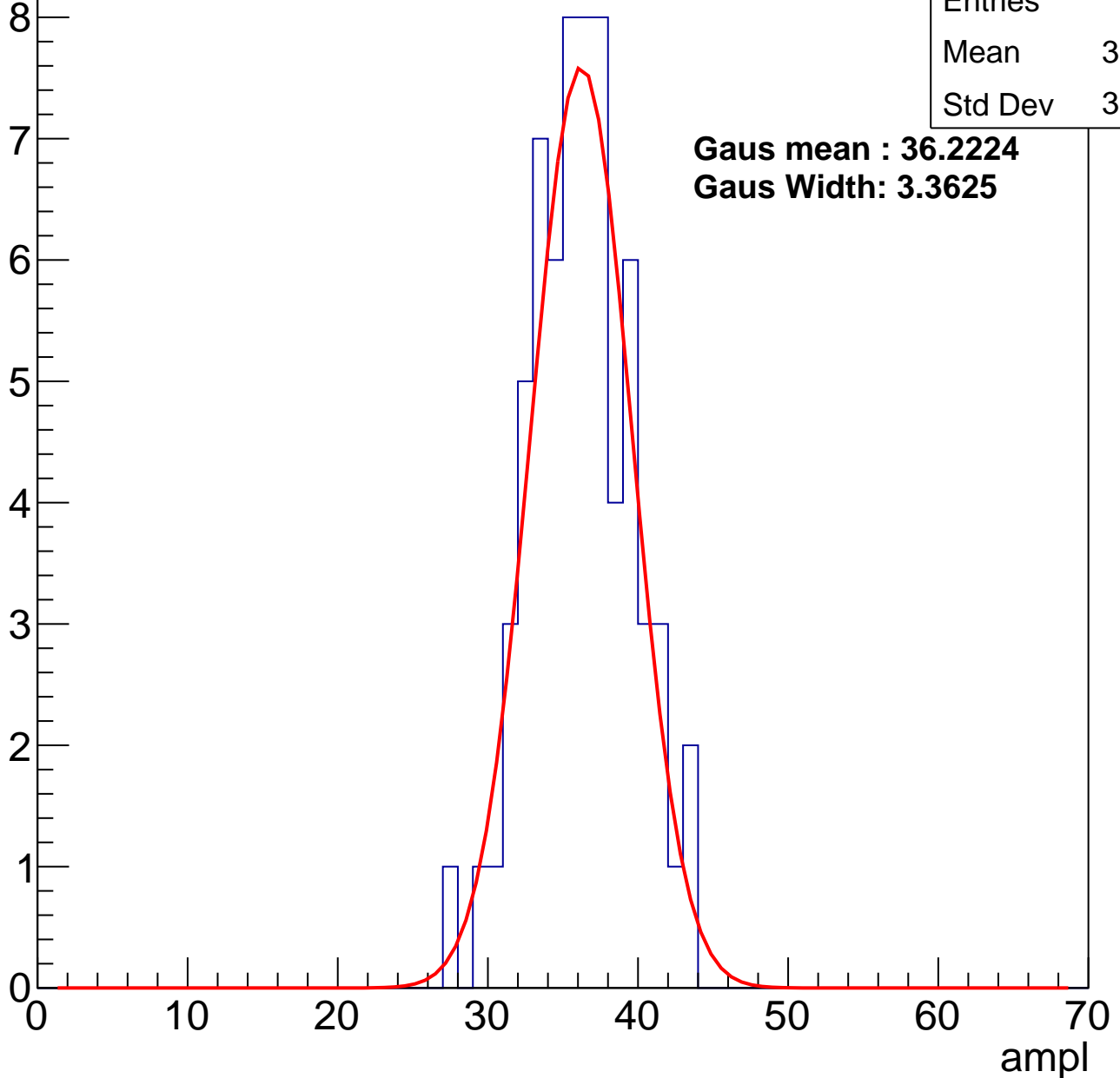
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.75
Std Dev	3.338

Gaus mean : 36.2224

Gaus Width: 3.3625



B1L102S, U18-ch35, adc2

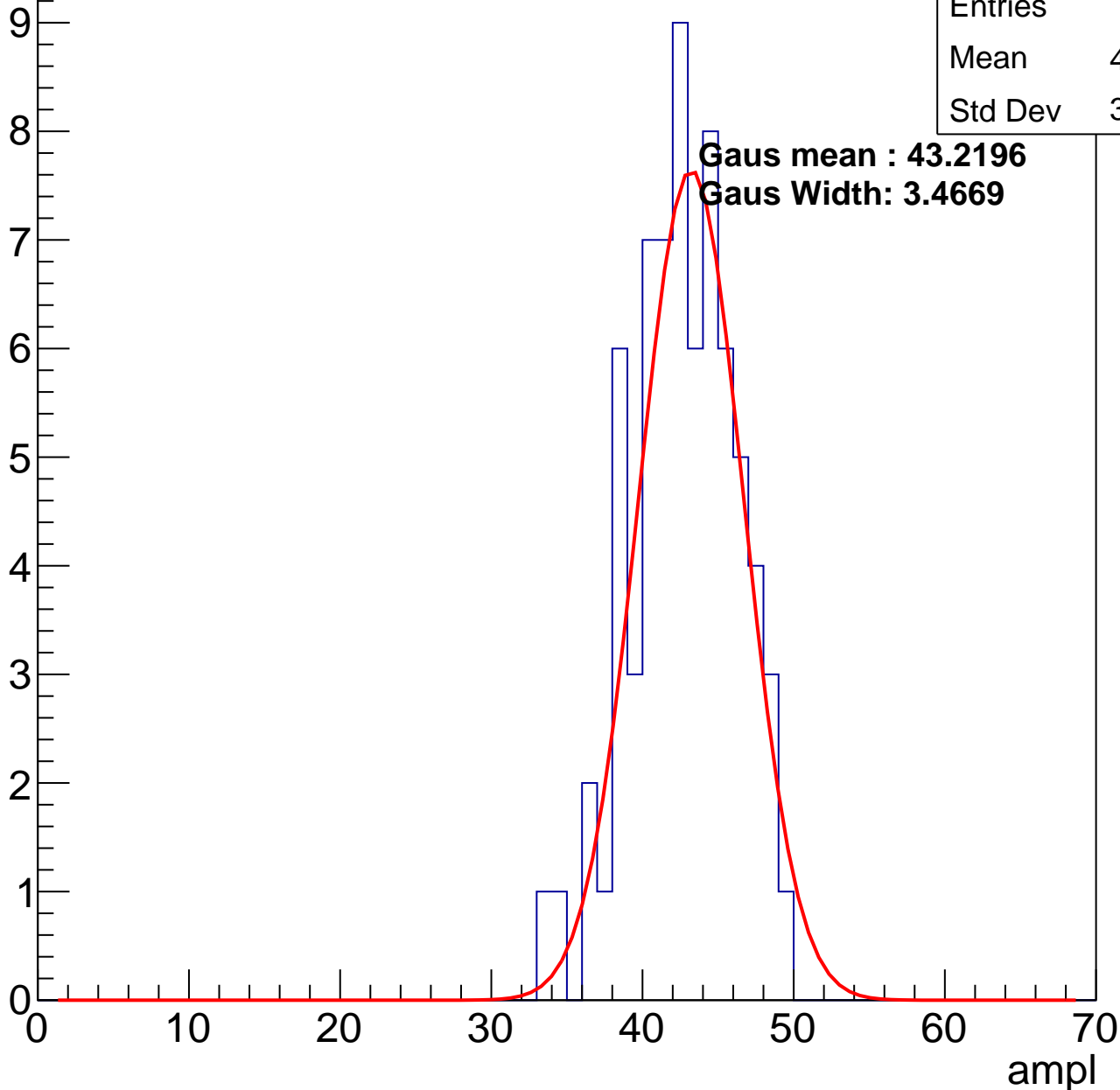
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	42.24
Std Dev	3.433

Gaus mean : 43.2196

Gaus Width: 3.4669

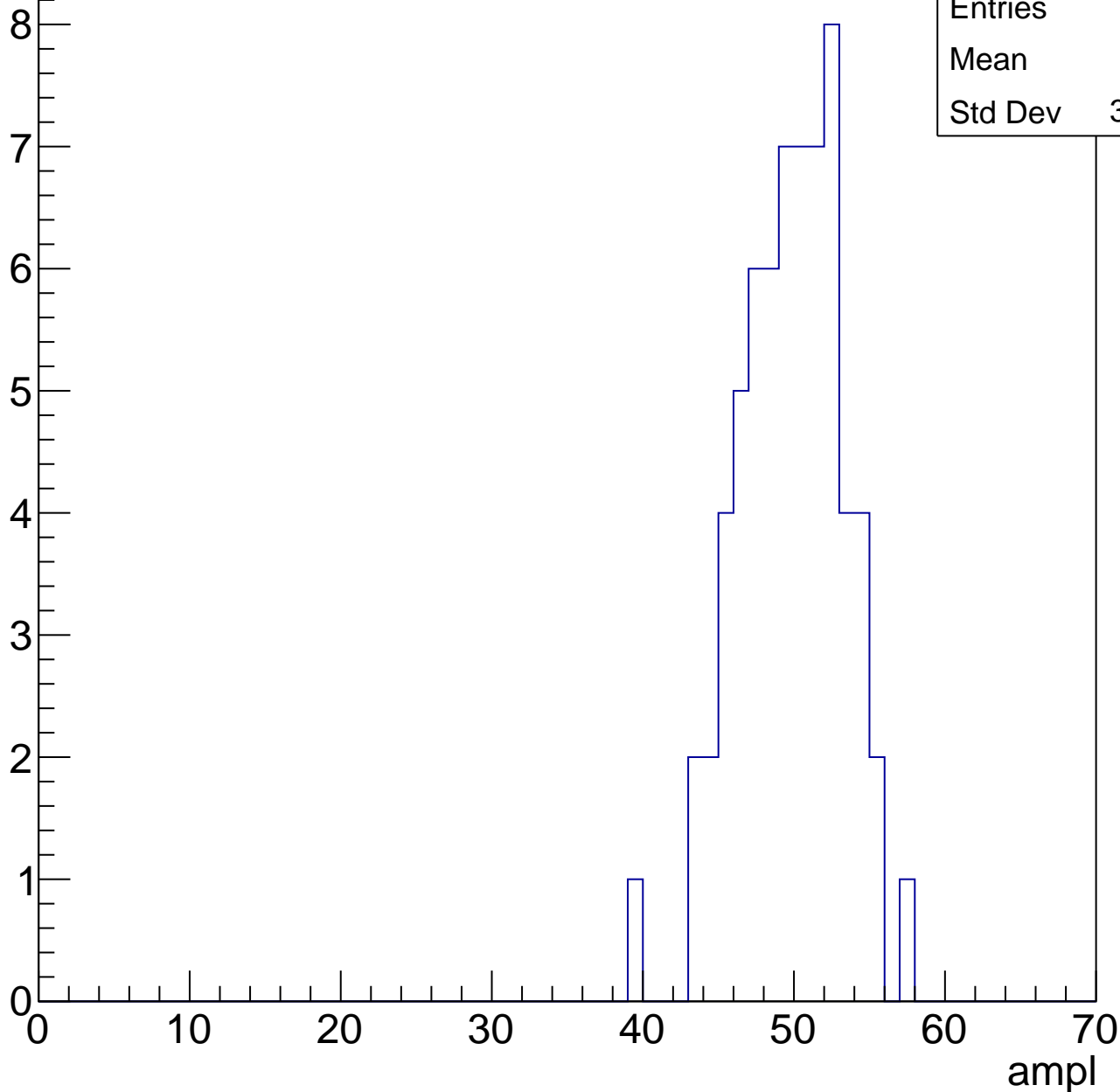


B1L102S, U18-ch35, adc3

calib_packv5_042523_0143.root, FC#11, port A2

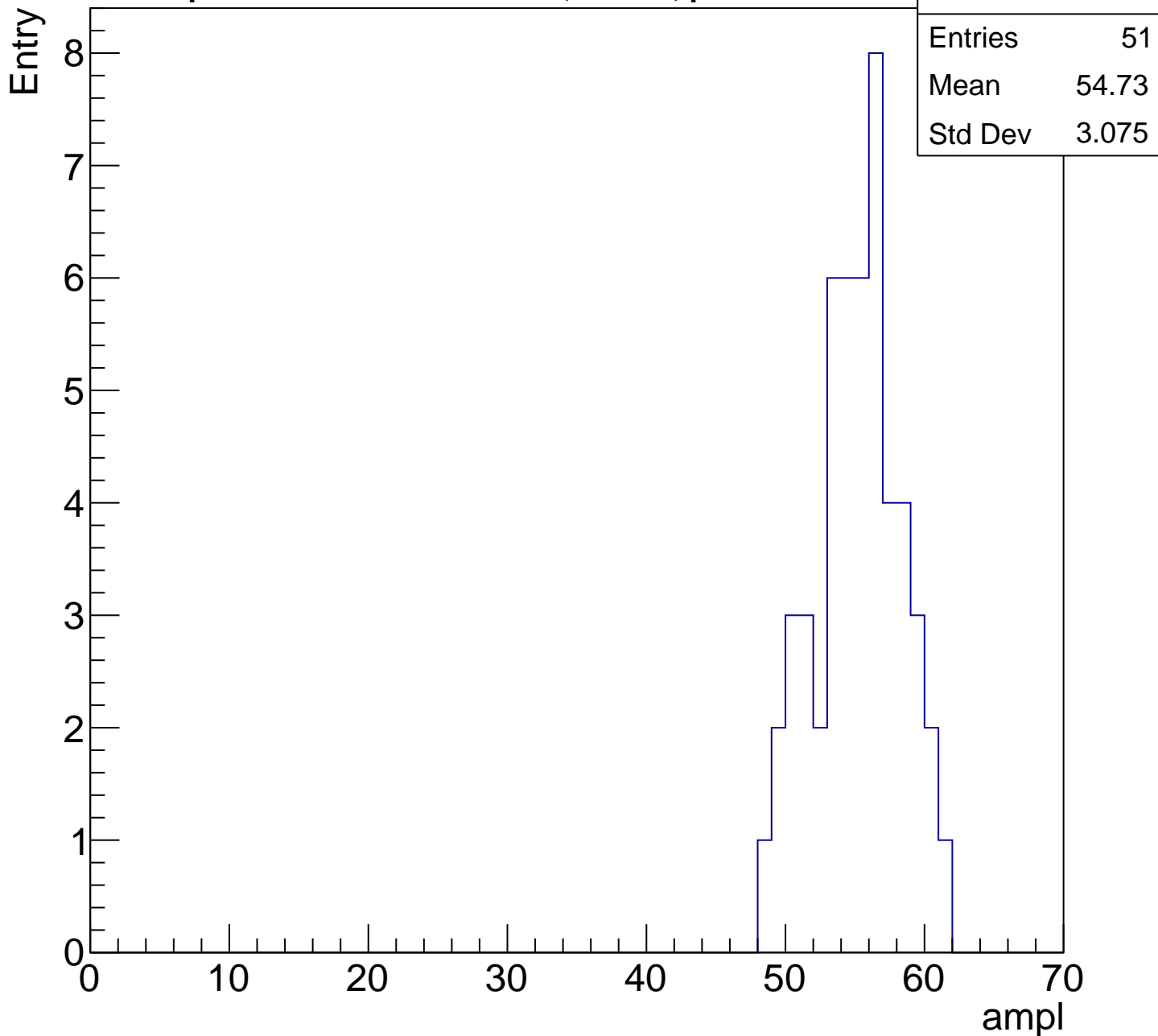
Entry

Entries	66
Mean	49.3
Std Dev	3.398



B1L102S, U18-ch35, adc4

calib_packv5_042523_0143.root, FC#11, port A2

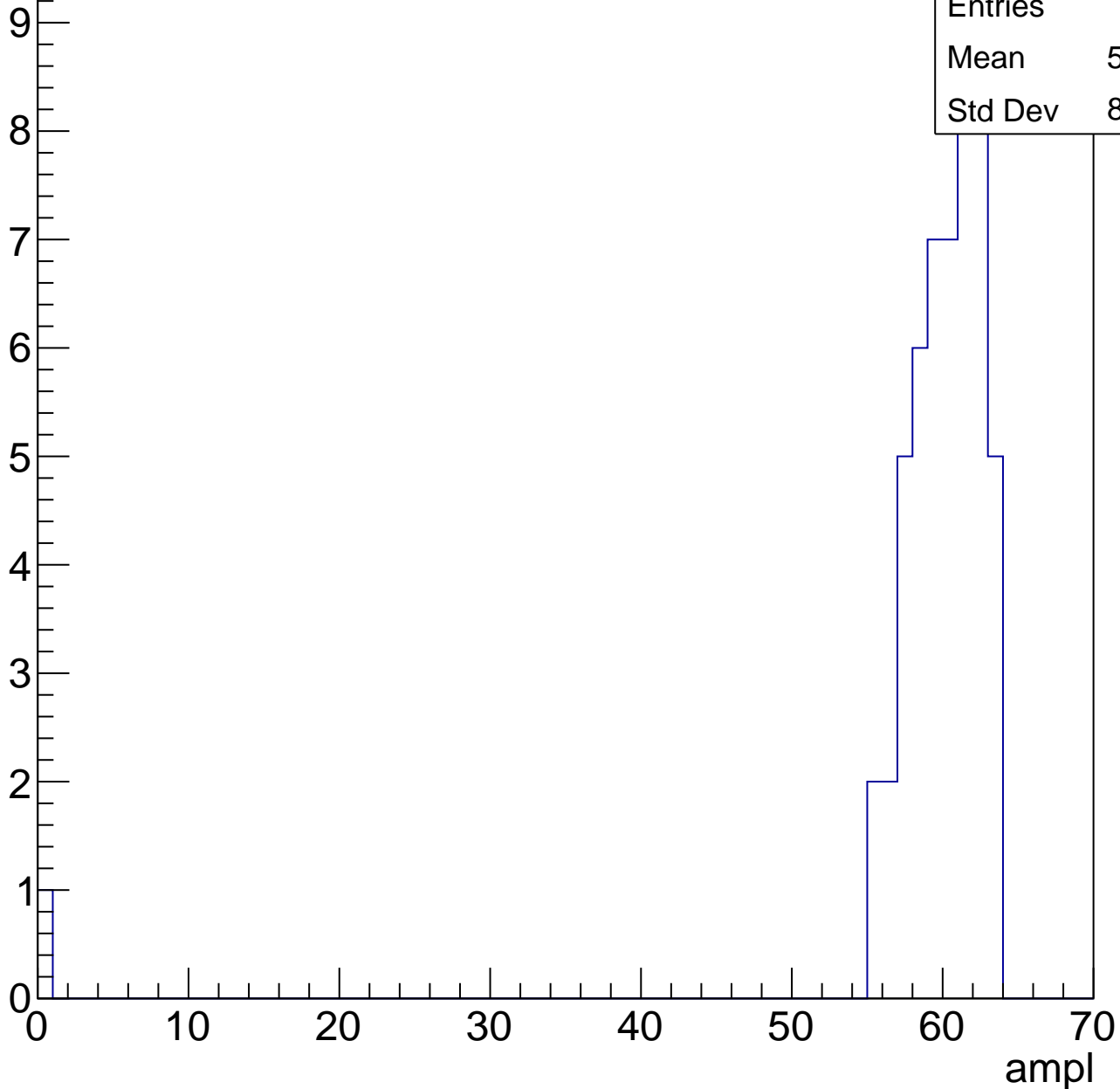


B1L102S, U18-ch35, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.63
Std Dev	8.492

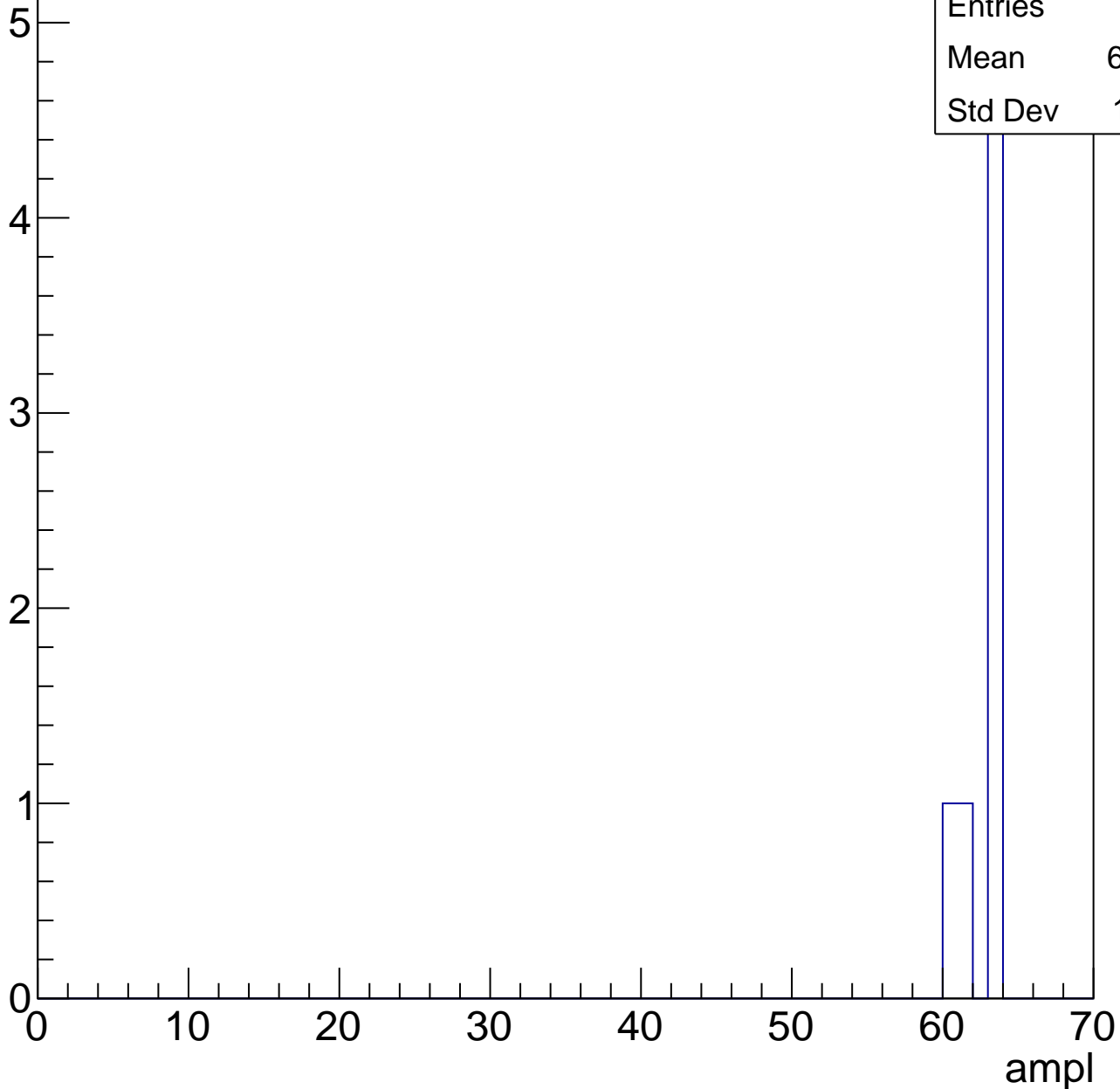


B1L102S, U18-ch35, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	7
Mean	62.29
Std Dev	1.161



B1L102S, U18-ch35, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch36, adc0

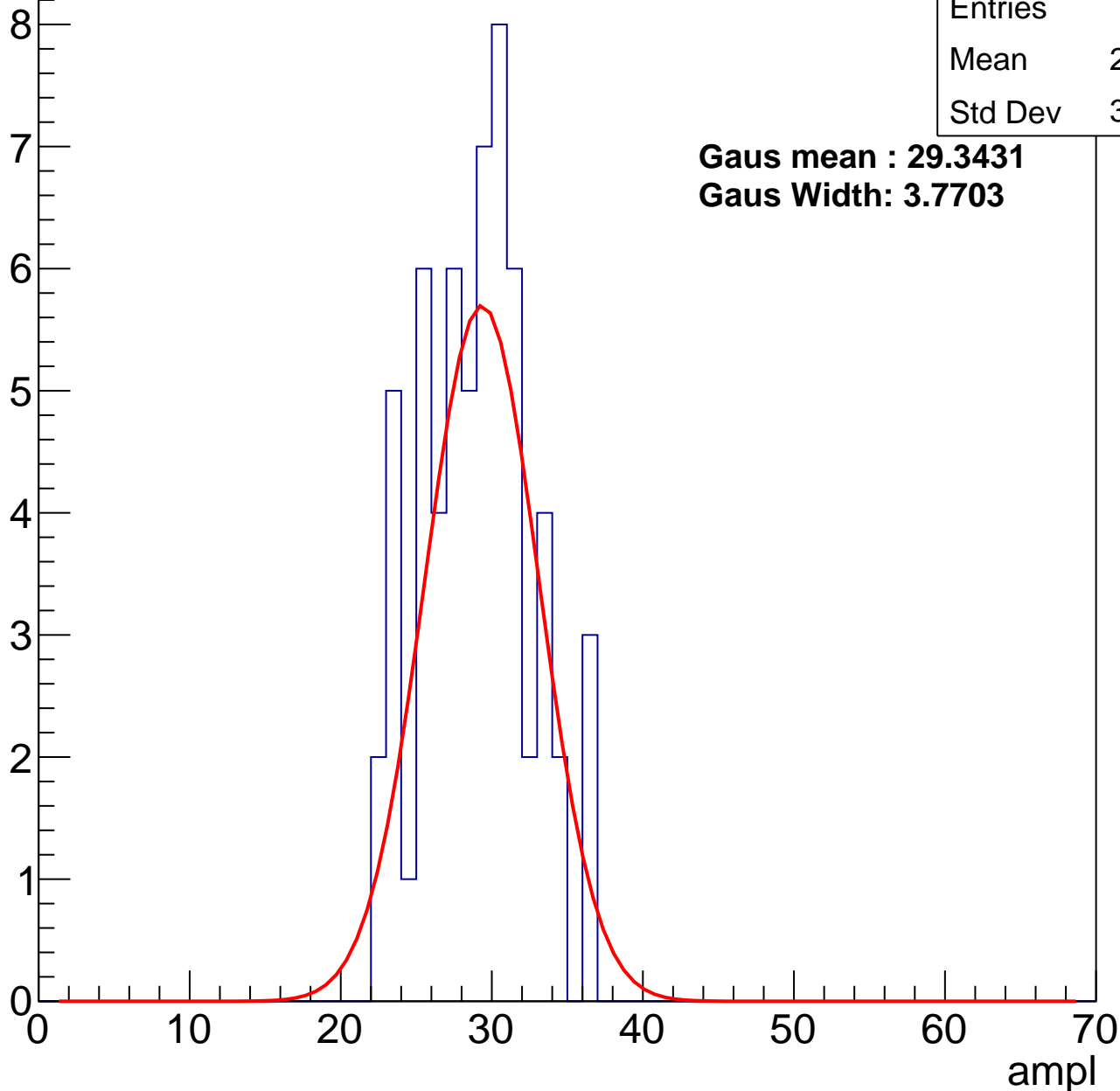
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	28.52
Std Dev	3.533

Gaus mean : 29.3431

Gaus Width: 3.7703



B1L102S, U18-ch36, adc1

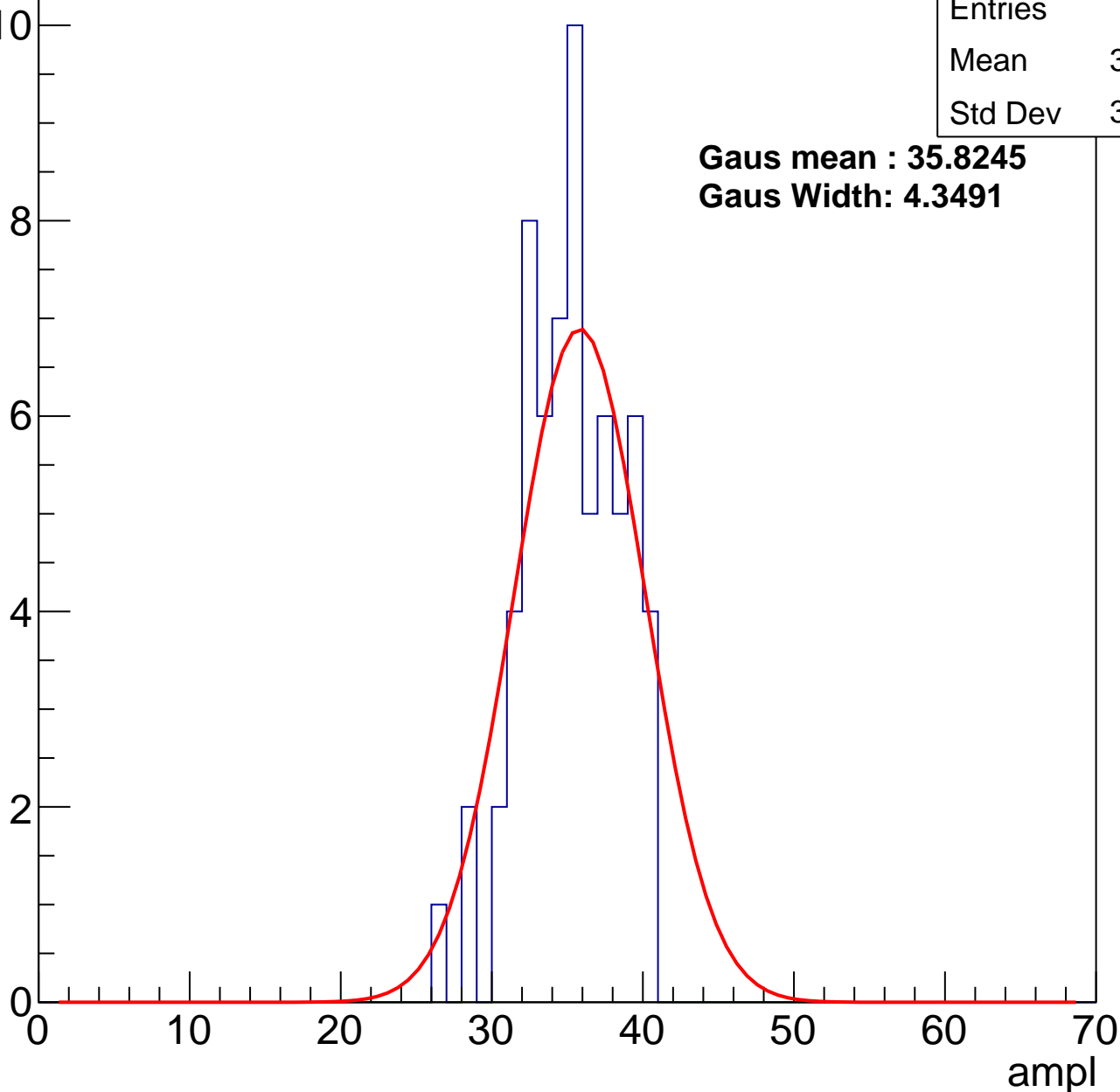
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.76
Std Dev	3.167

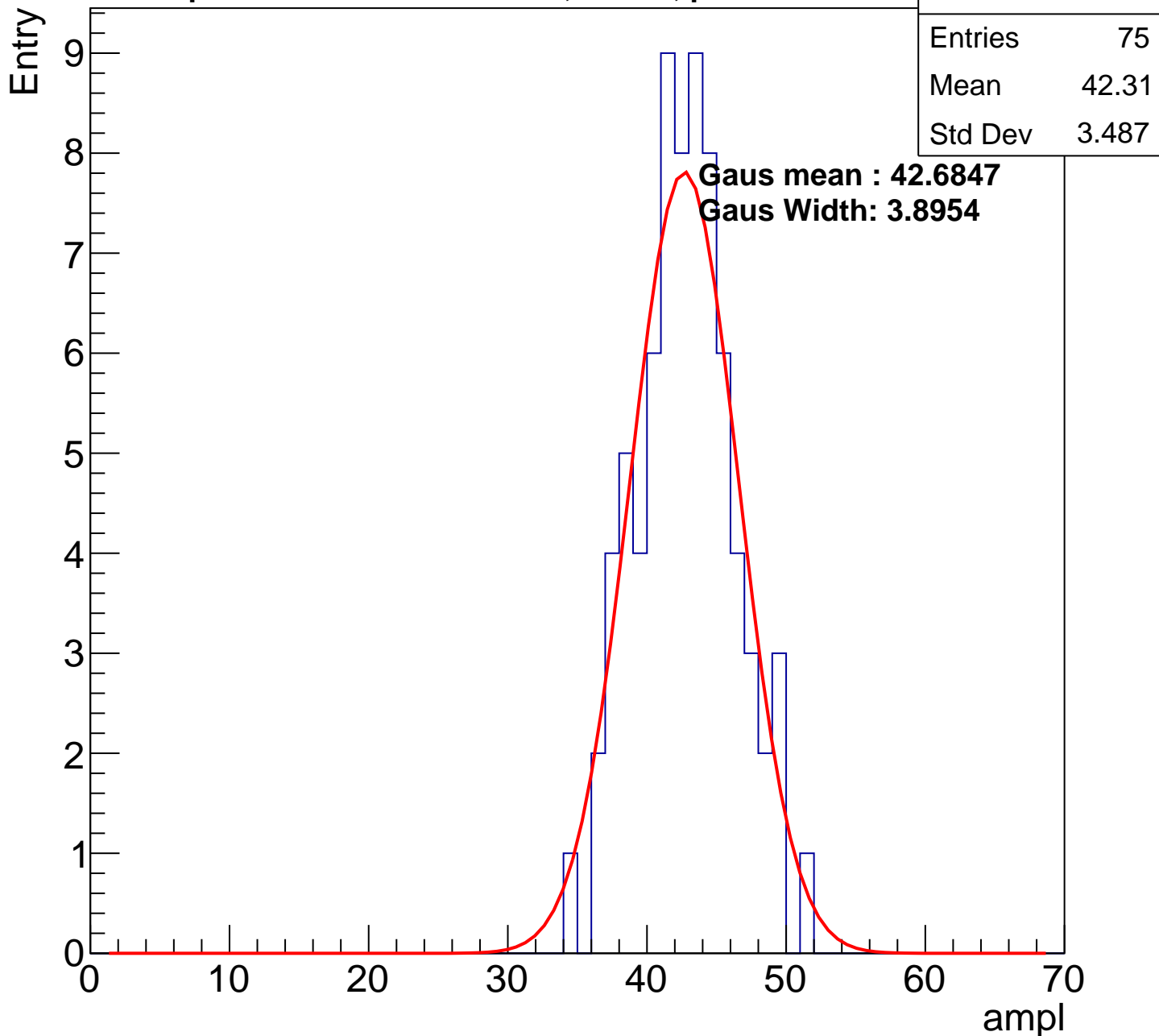
Gaus mean : 35.8245

Gaus Width: 4.3491



B1L102S, U18-ch36, adc2

calib_packv5_042523_0143.root, FC#11, port A2

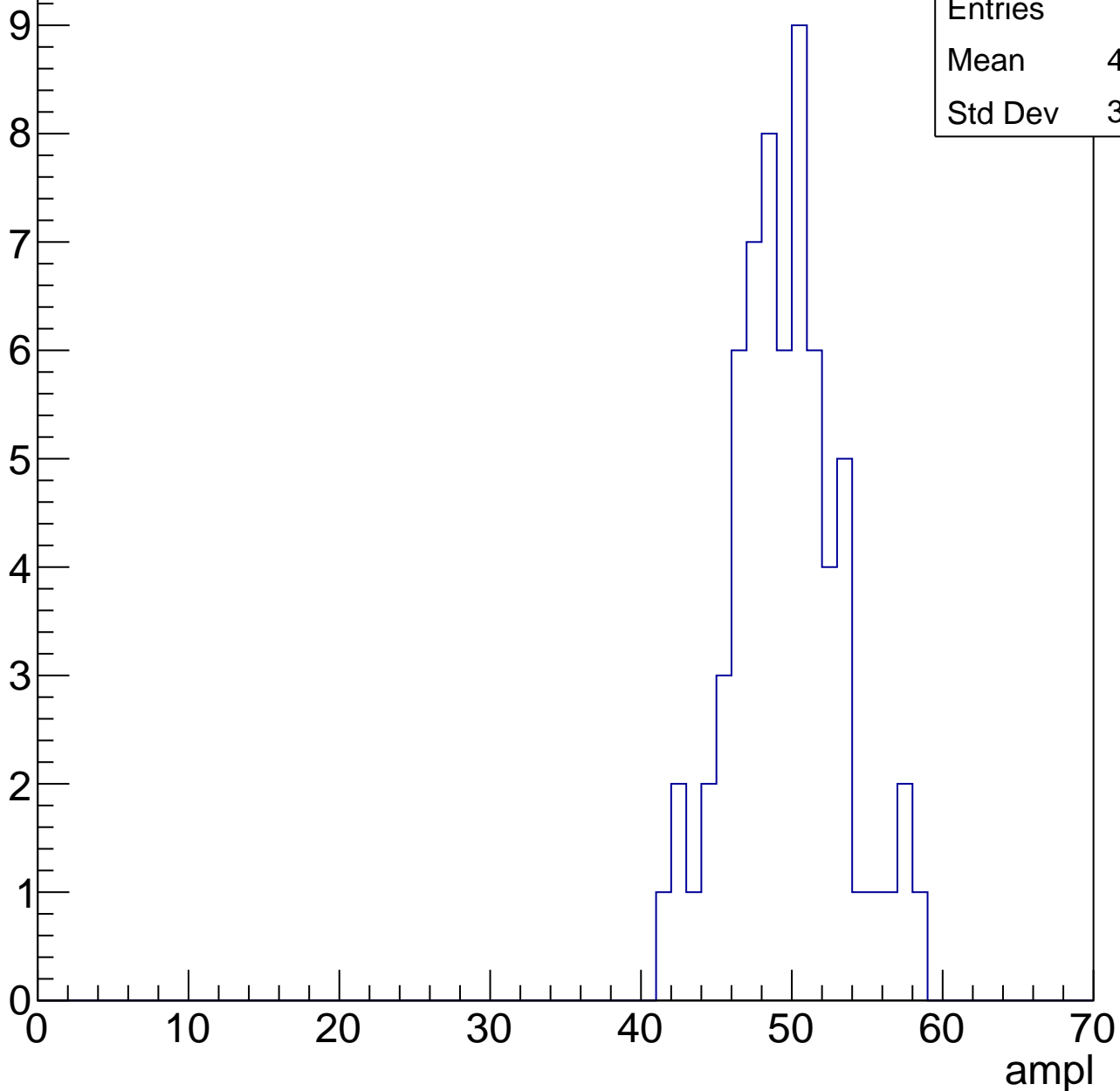


B1L102S, U18-ch36, adc3

calib_packv5_042523_0143.root, FC#11, port A2

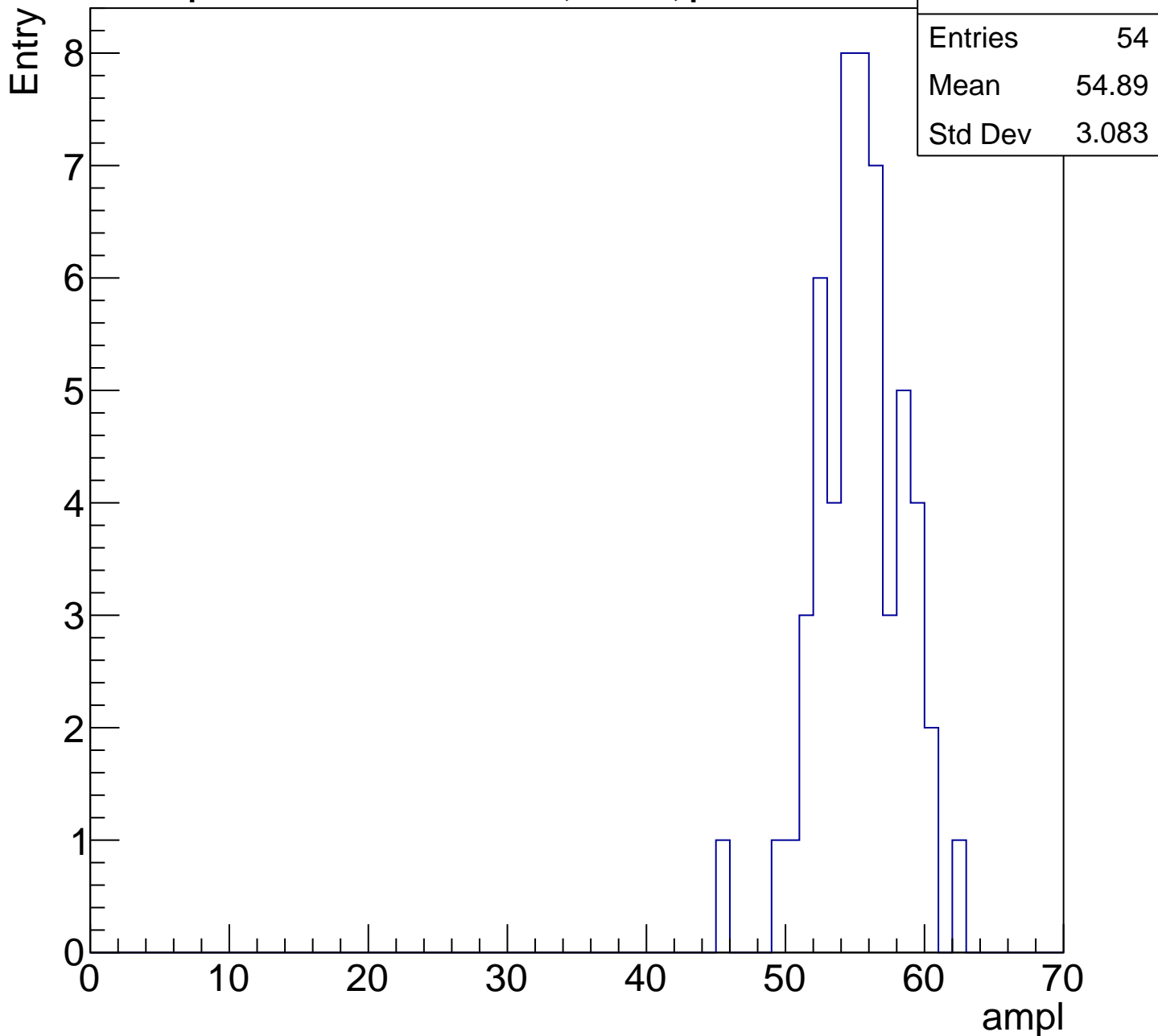
Entry

Entries	66
Mean	49.09
Std Dev	3.596



B1L102S, U18-ch36, adc4

calib_packv5_042523_0143.root, FC#11, port A2

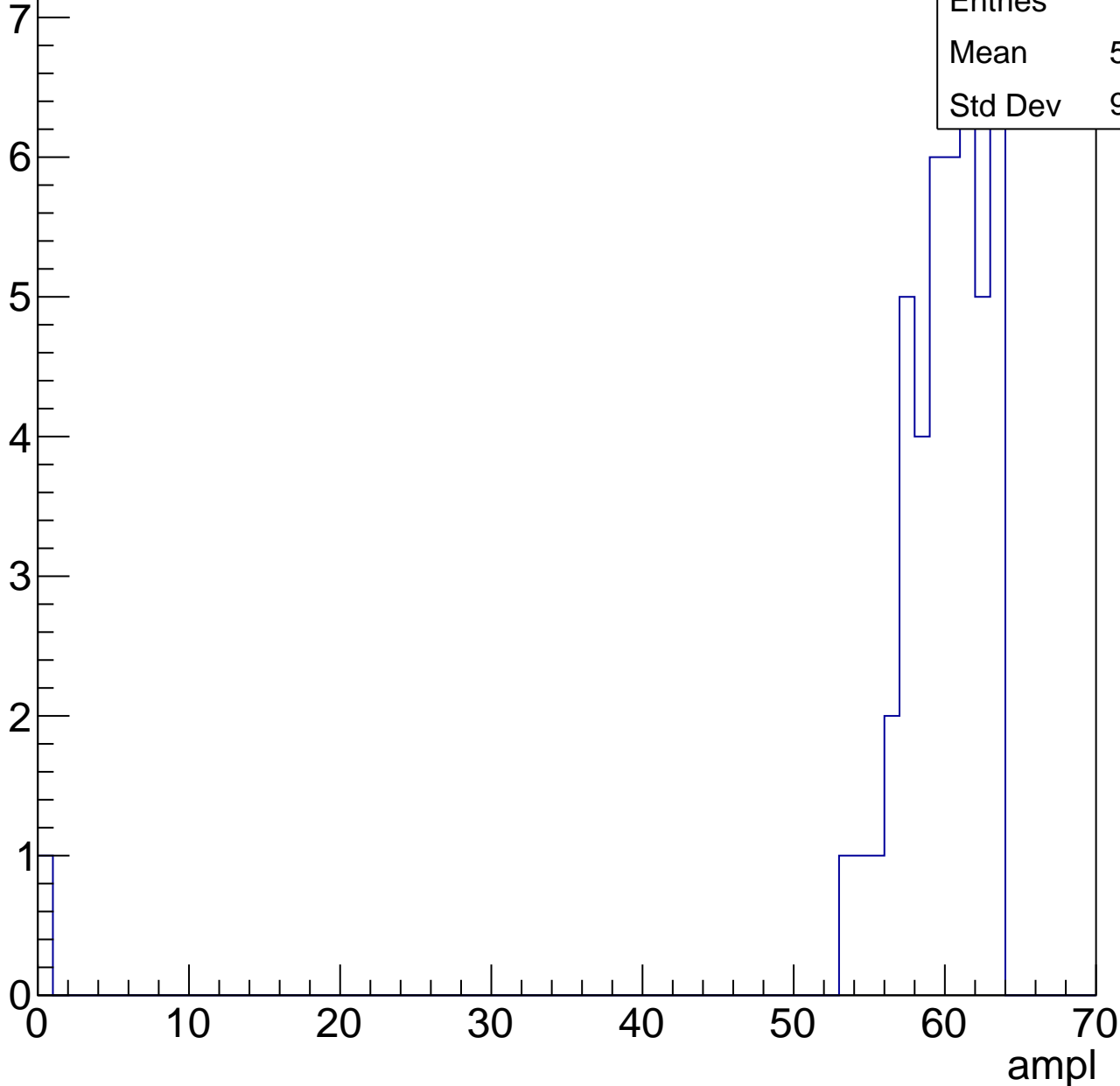


B1L102S, U18-ch36, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

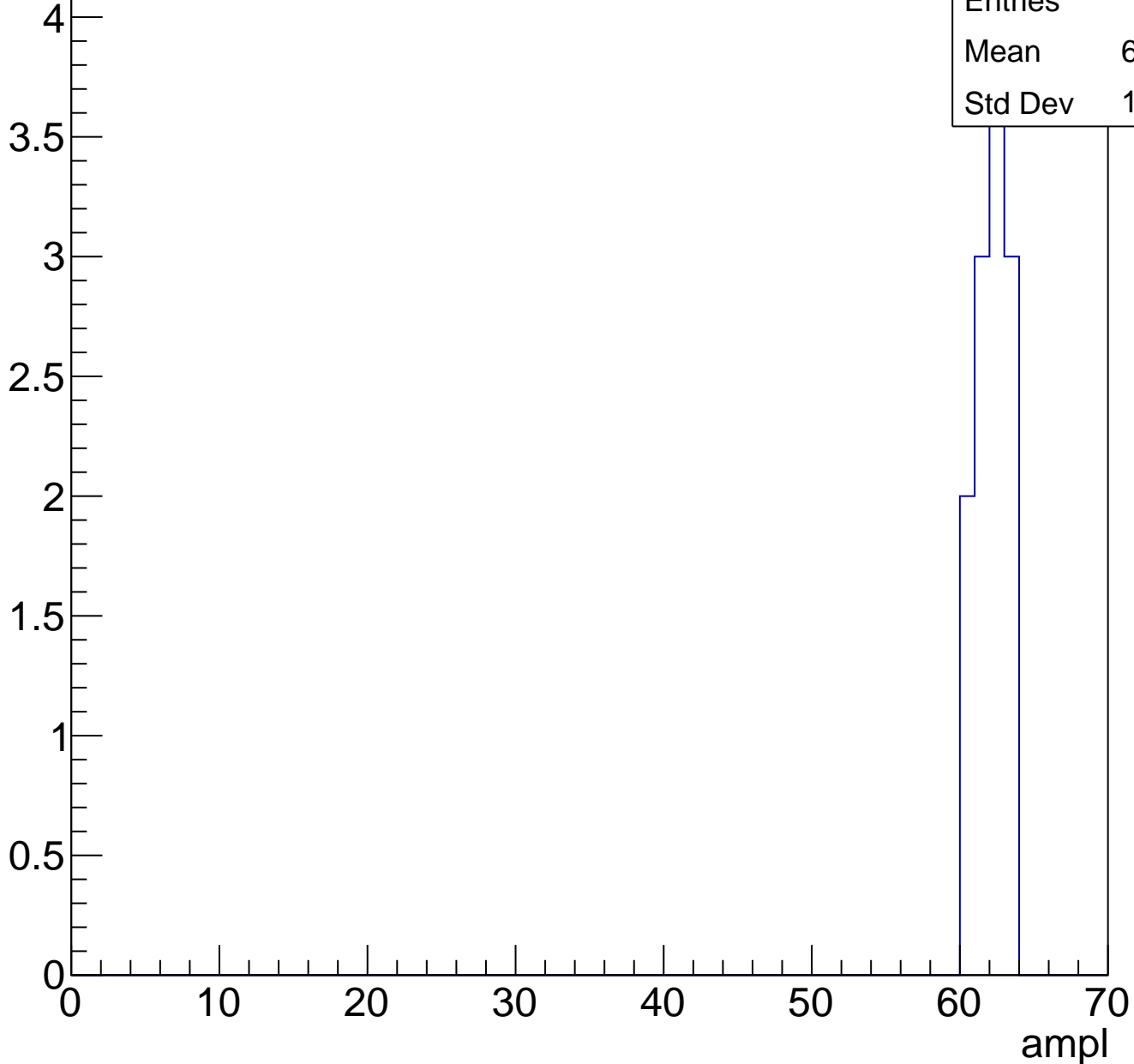
Entries	46
Mean	58.33
Std Dev	9.053



B1L102S, U18-ch36, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch36, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L102S, U18-ch37, adc0

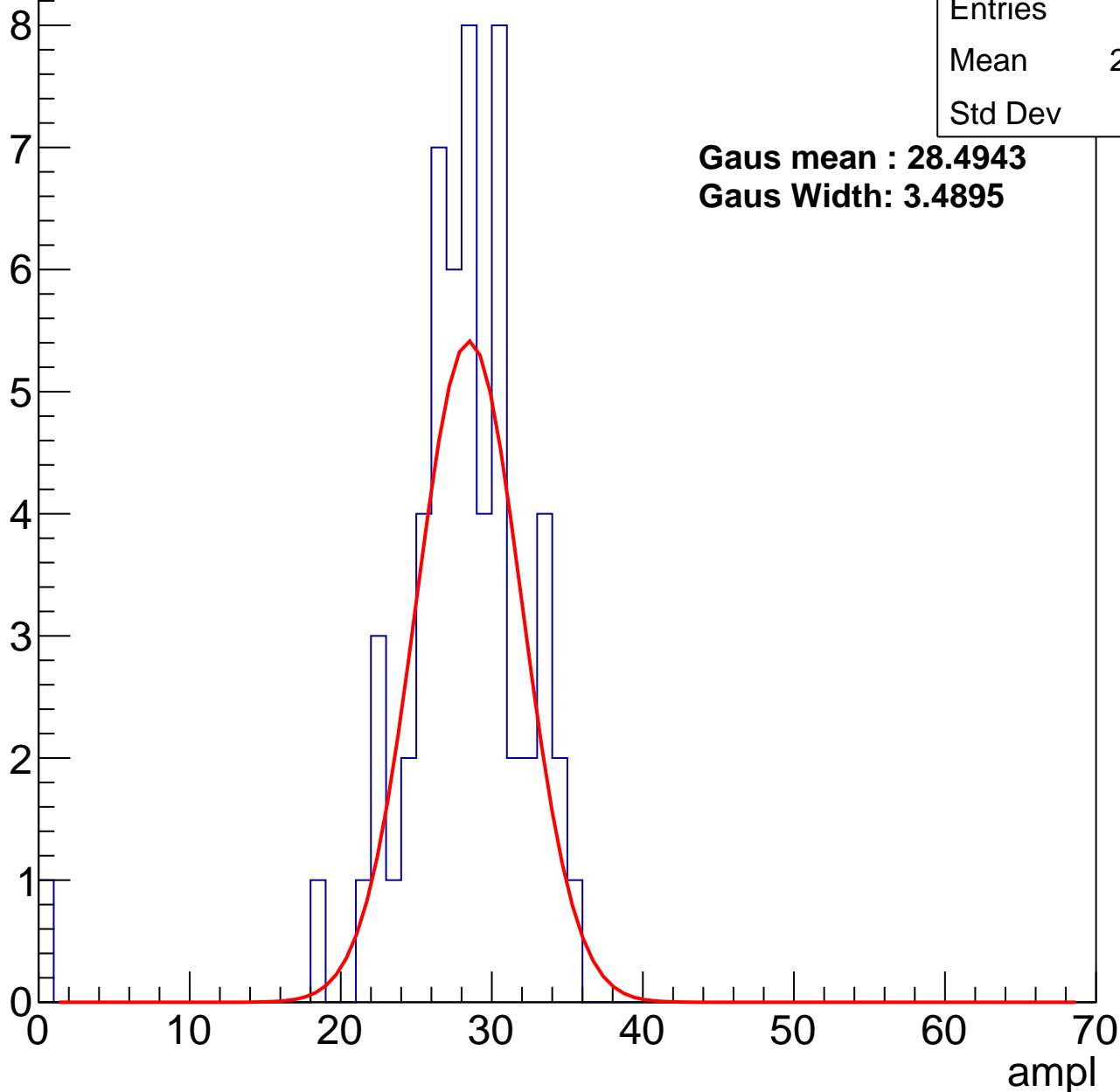
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.39
Std Dev	5.05

Gaus mean : 28.4943

Gaus Width: 3.4895



B1L102S, U18-ch37, adc1

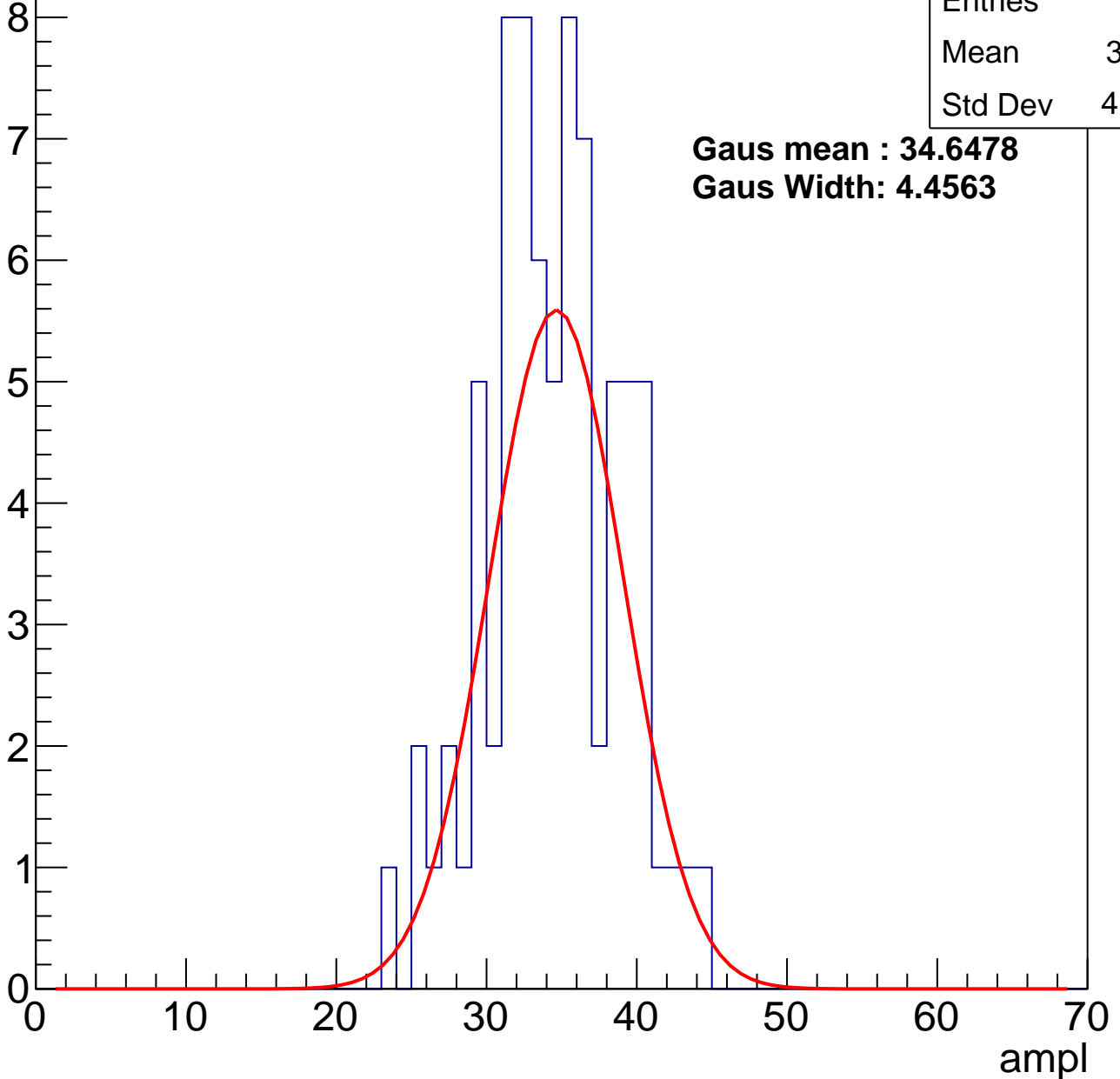
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.01
Std Dev	4.386

Gaus mean : 34.6478

Gaus Width: 4.4563



B1L102S, U18-ch37, adc2

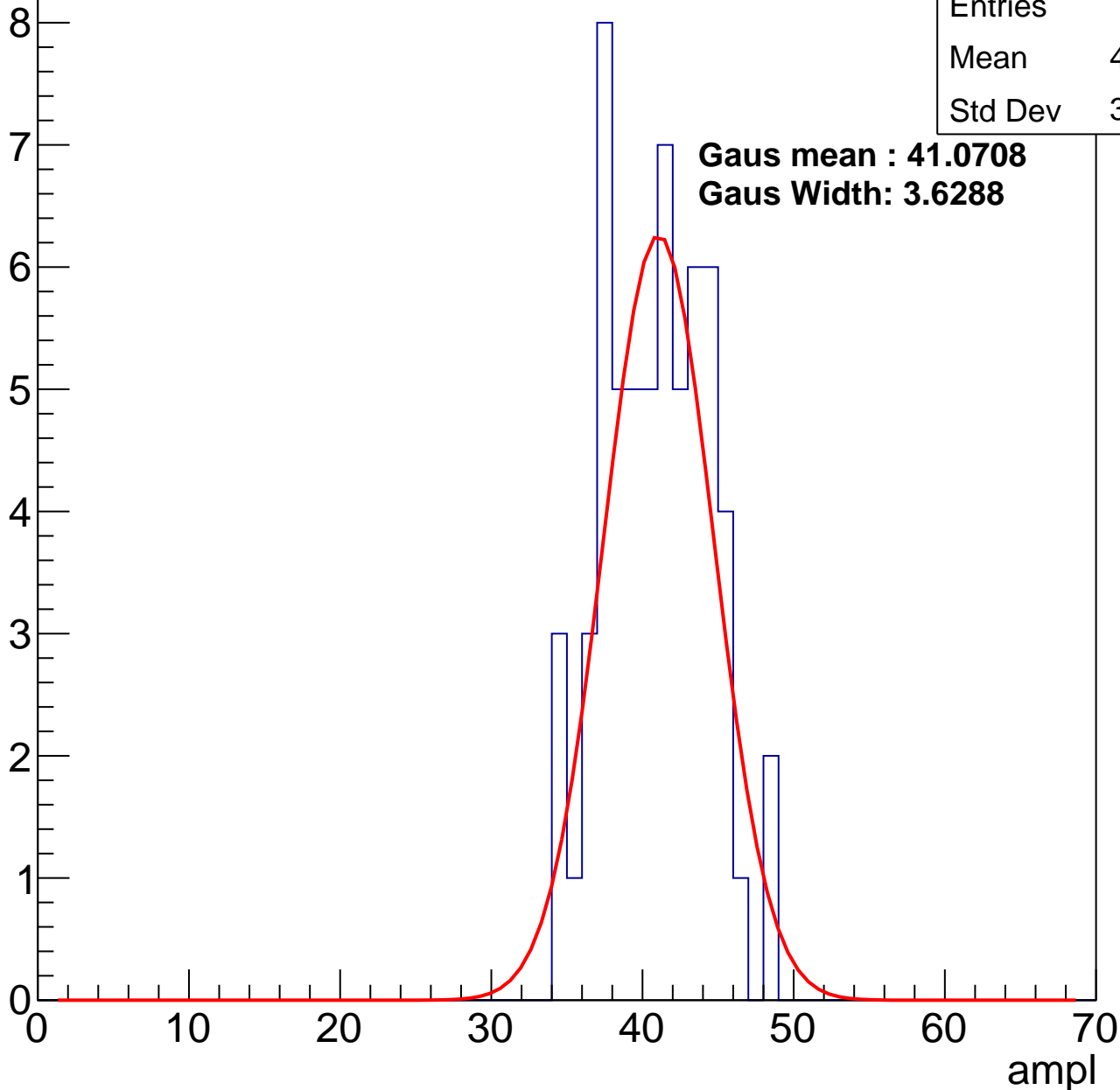
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	40.44
Std Dev	3.405

Gaus mean : 41.0708

Gaus Width: 3.6288

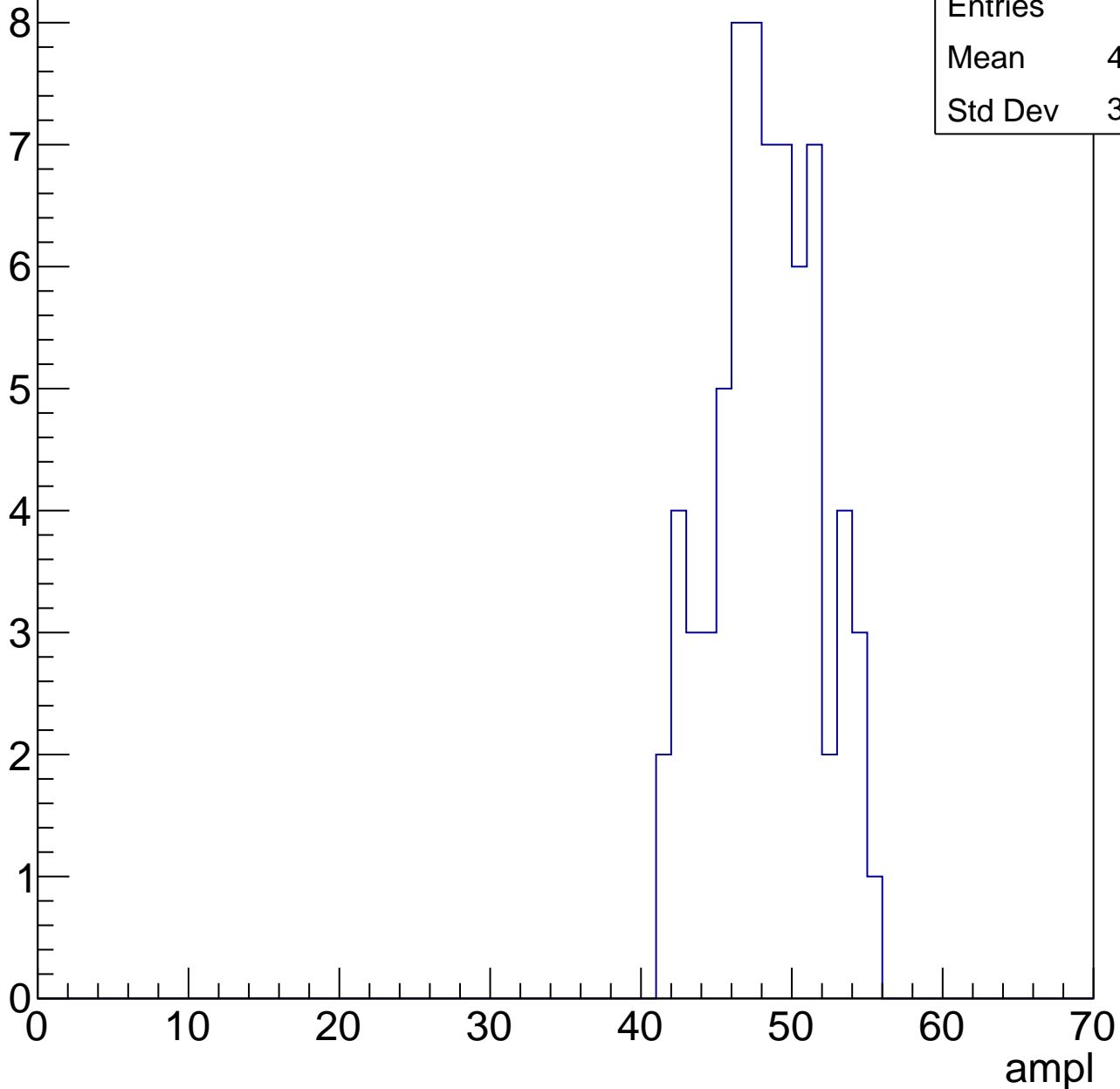


B1L102S, U18-ch37, adc3

calib_packv5_042523_0143.root, FC#11, port A2

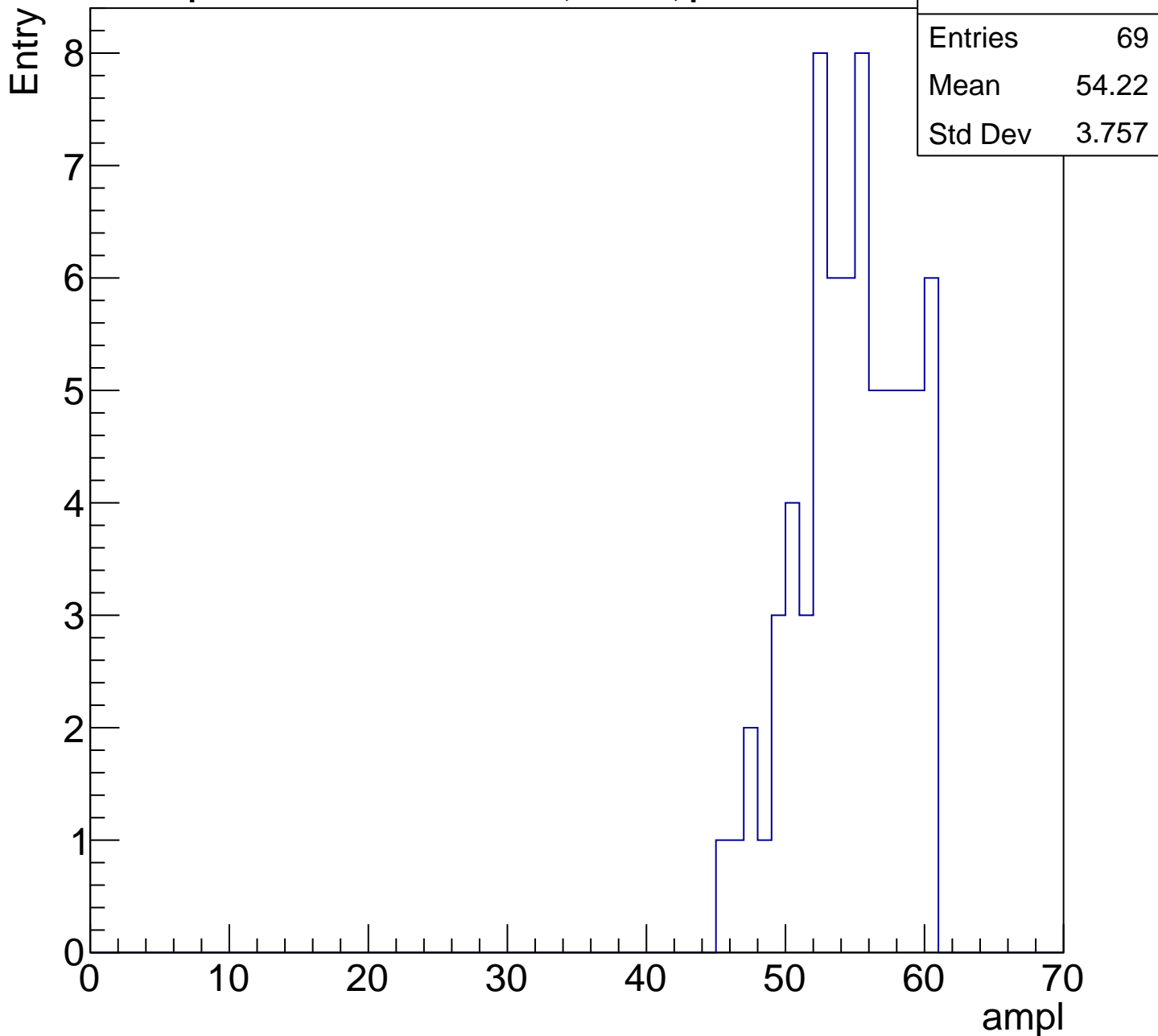
Entry

Entries	70
Mean	47.84
Std Dev	3.446



B1L102S, U18-ch37, adc4

calib_packv5_042523_0143.root, FC#11, port A2

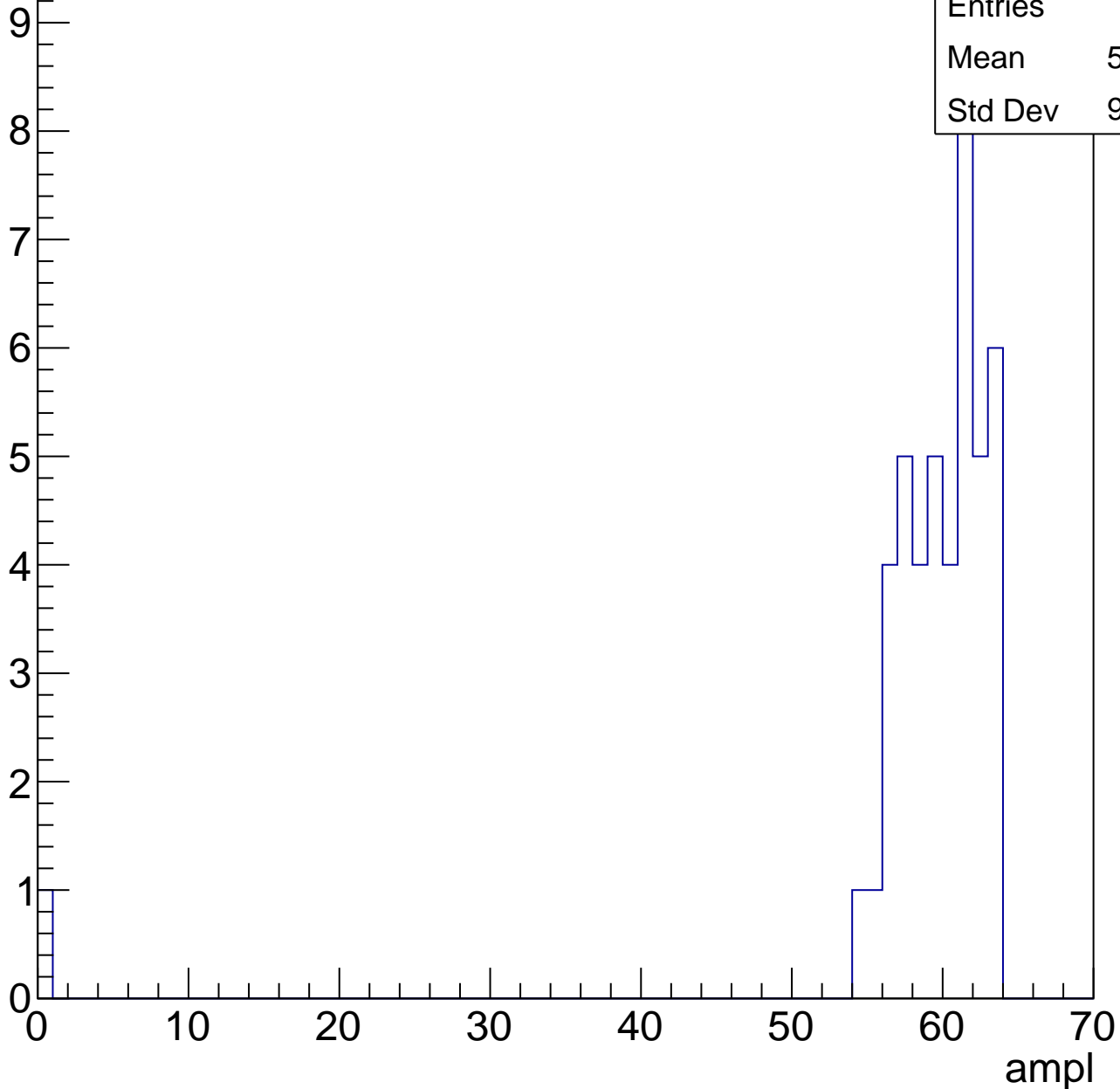


B1L102S, U18-ch37, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	58.27
Std Dev	9.115

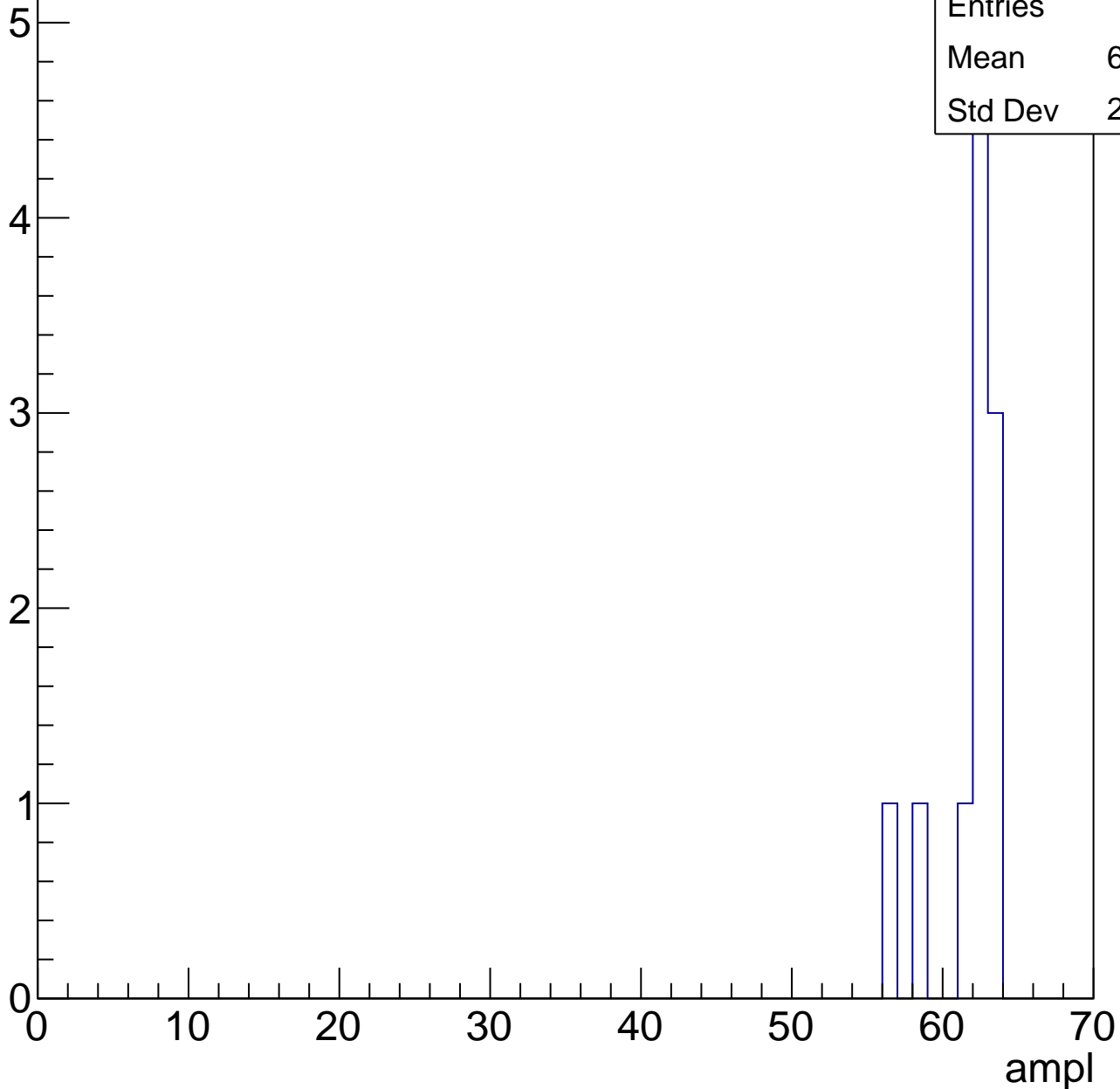


B1L102S, U18-ch37, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	61.27
Std Dev	2.136



B1L102S, U18-ch37, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch38, adc0

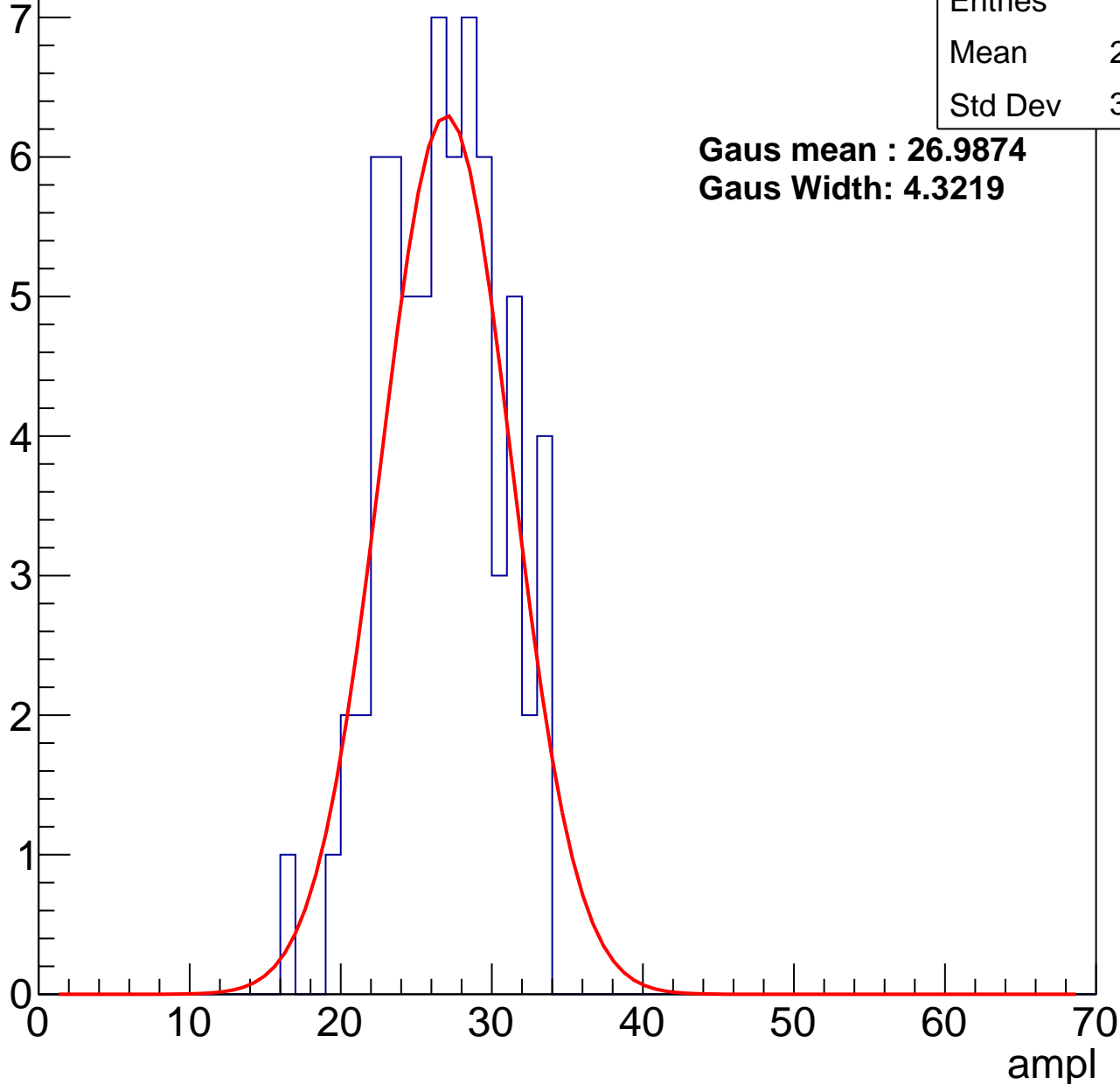
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	26.28
Std Dev	3.776

Gaus mean : 26.9874

Gaus Width: 4.3219



B1L102S, U18-ch38, adc1

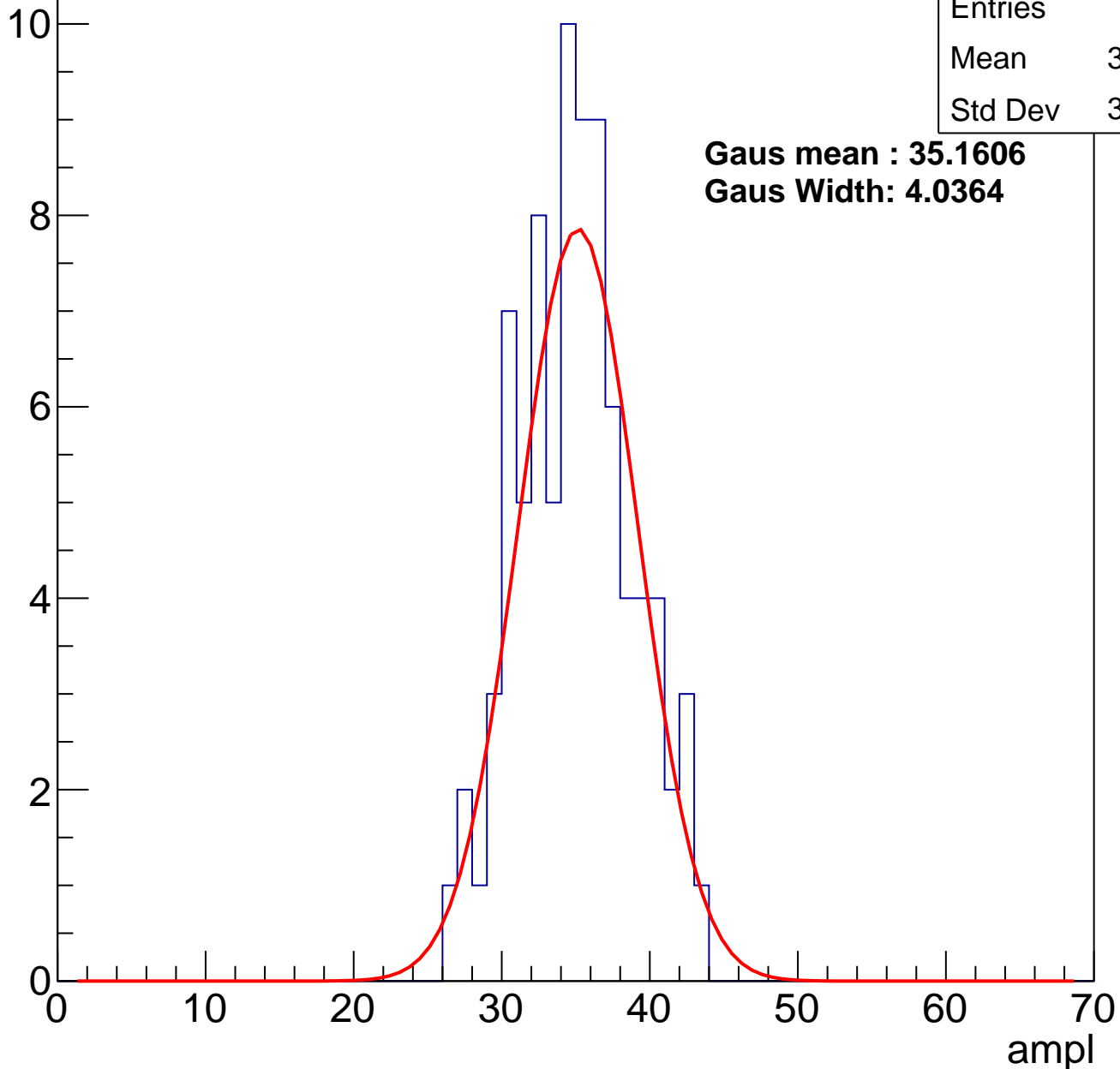
calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	34.54
Std Dev	3.812

Gaus mean : 35.1606

Gaus Width: 4.0364

Entry

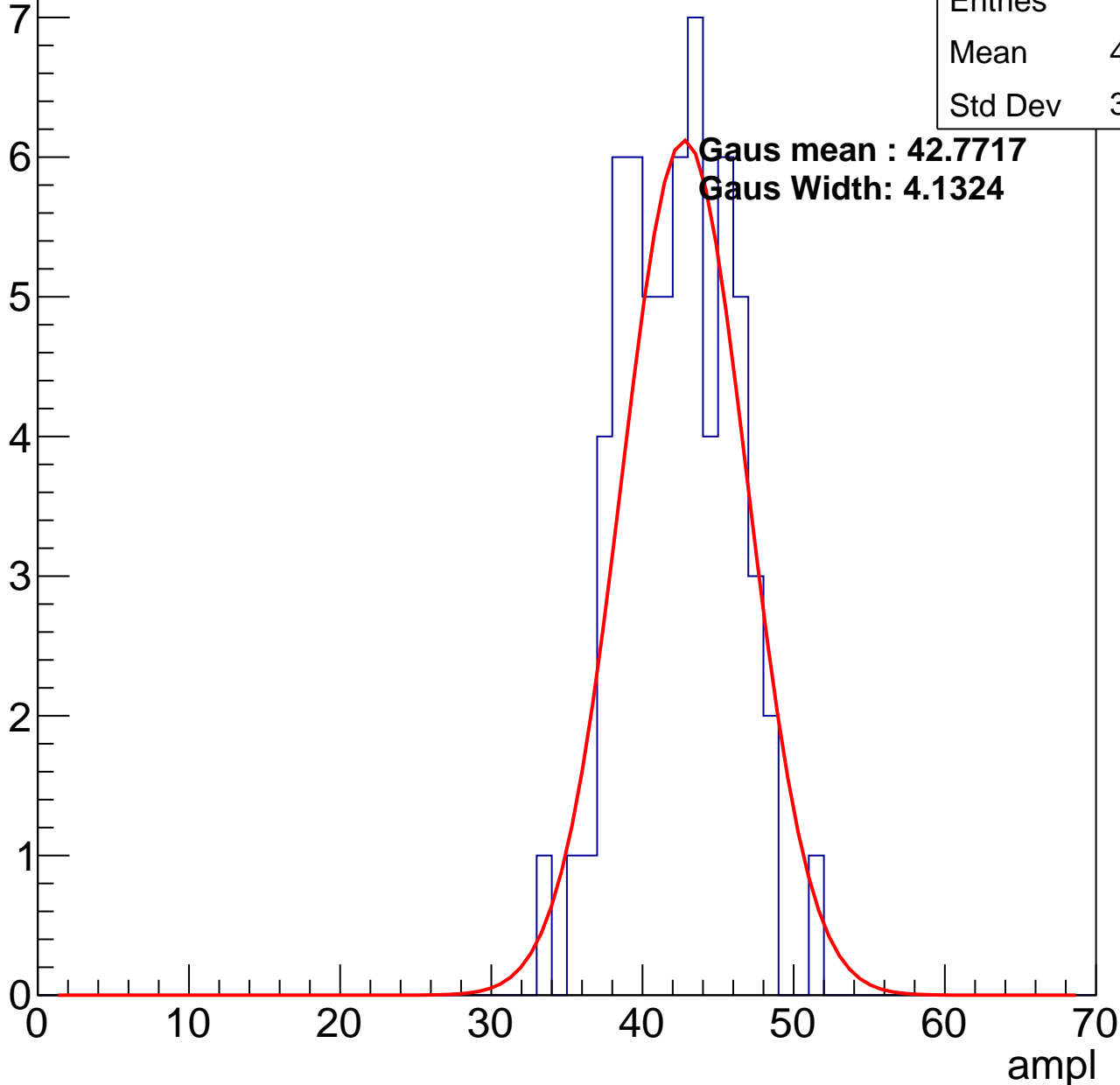


B1L102S, U18-ch38, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.84
Std Dev	3.617

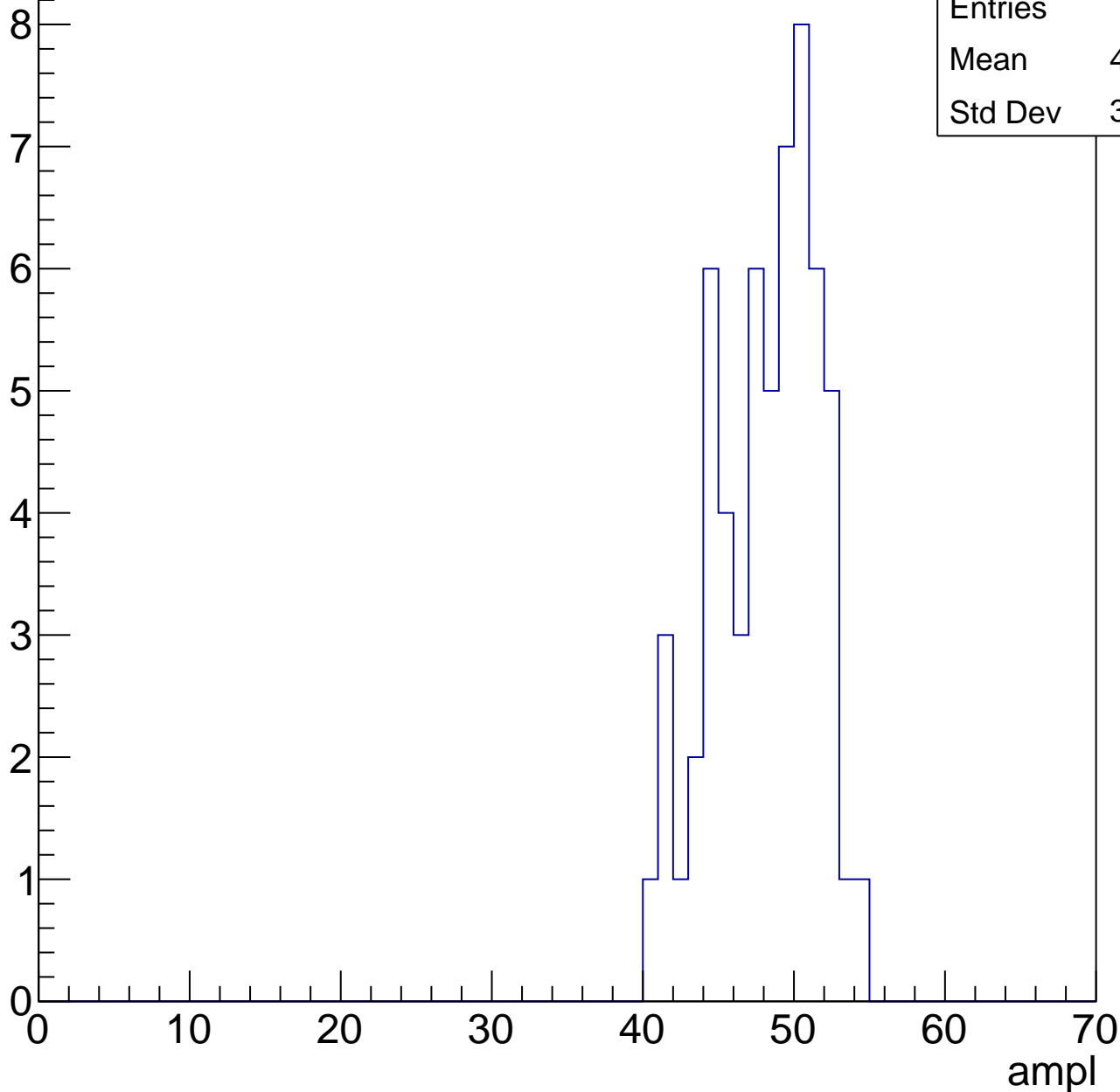


B1L102S, U18-ch38, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.64
Std Dev	3.379

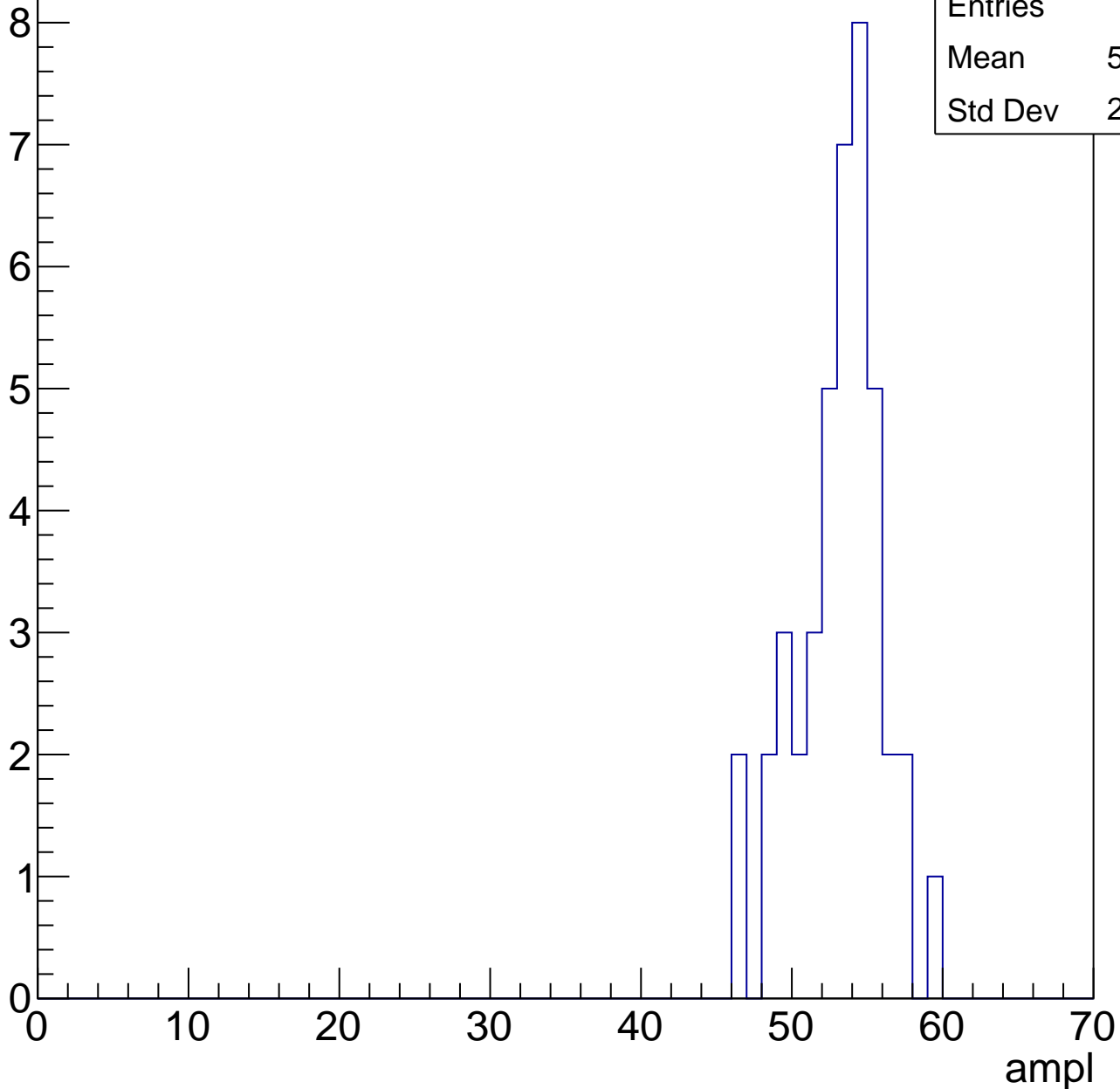


B1L102S, U18-ch38, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	52.64
Std Dev	2.844

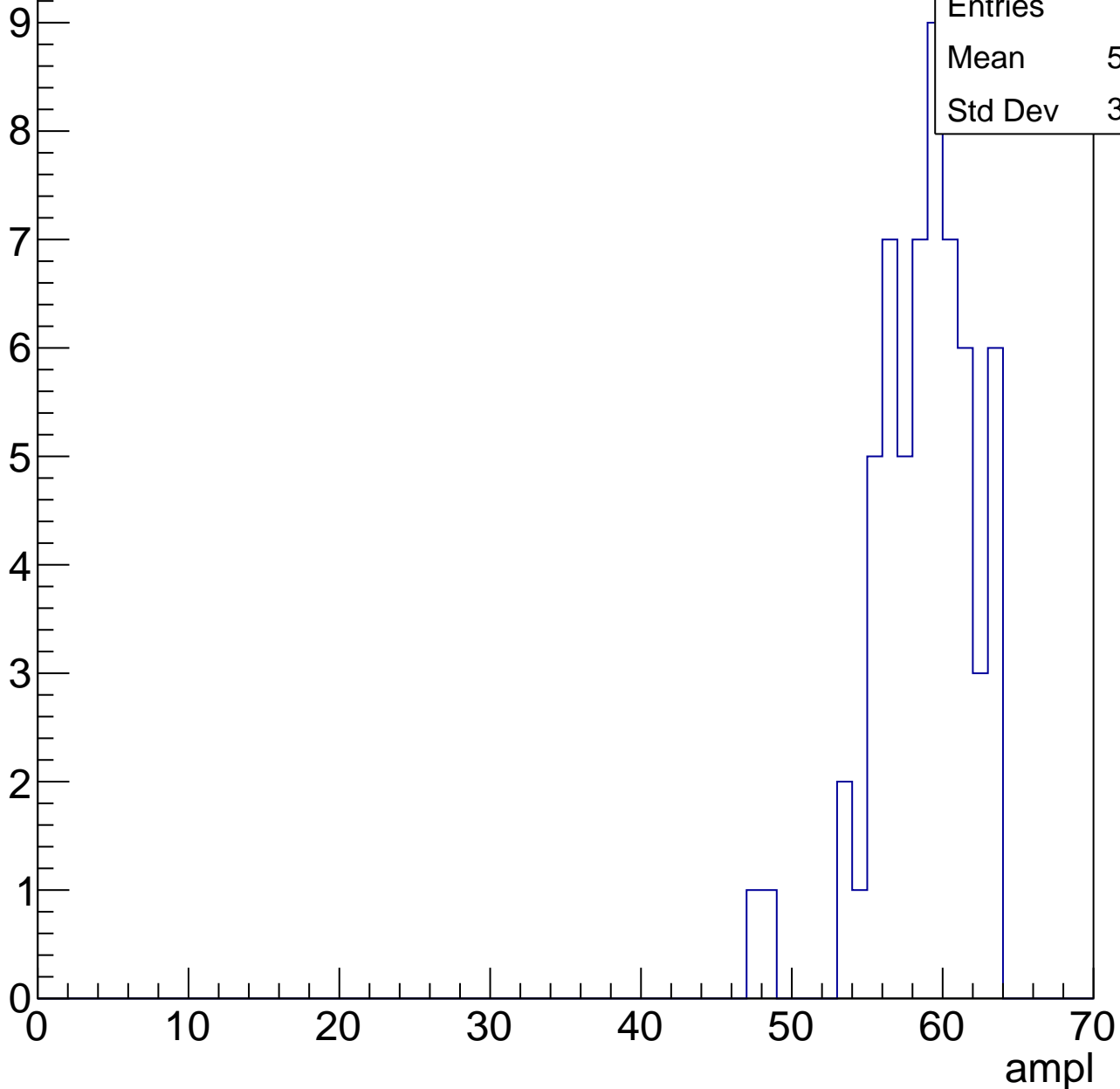


B1L102S, U18-ch38, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	58.23
Std Dev	3.293

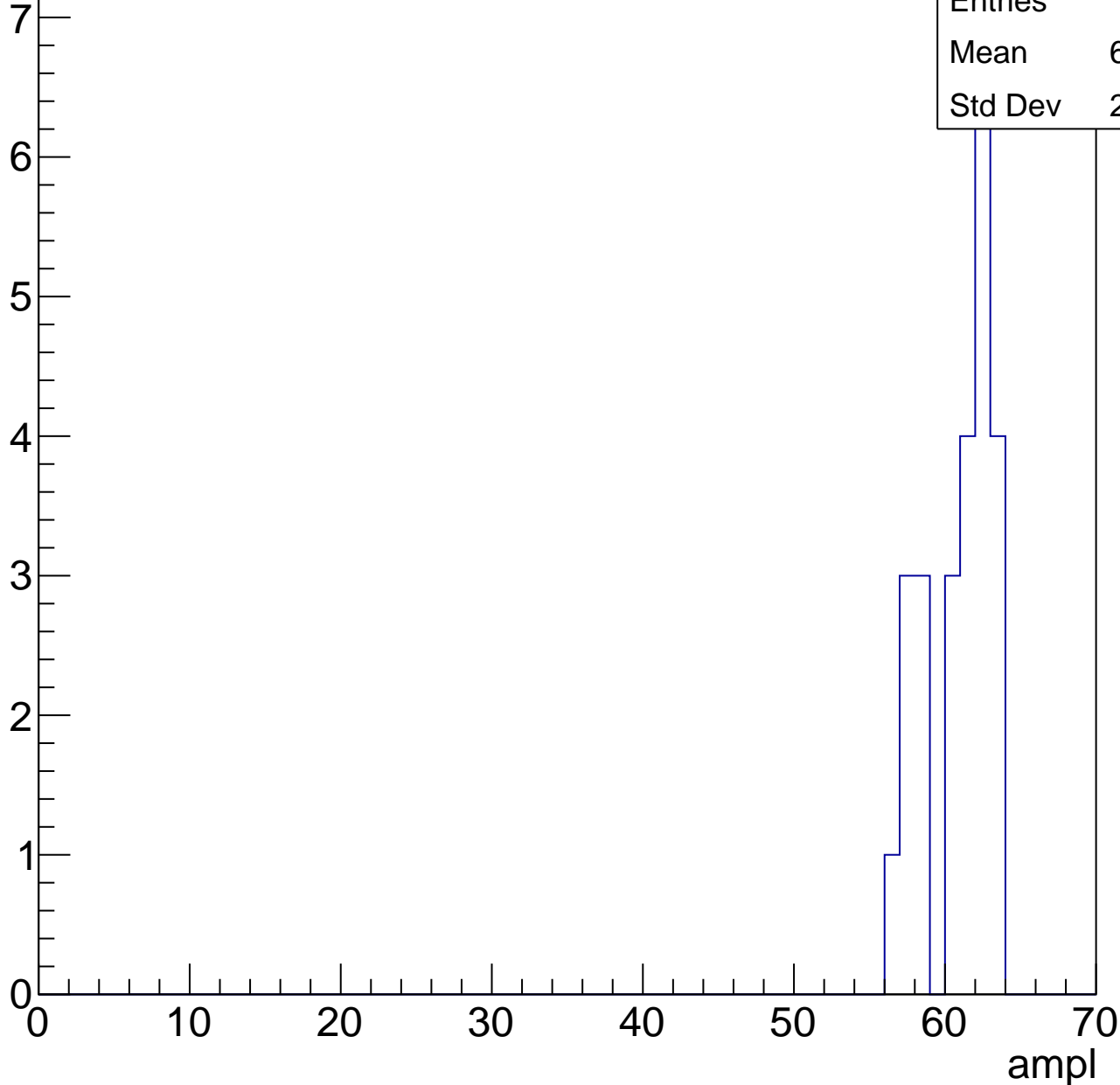


B1L102S, U18-ch38, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	60.44
Std Dev	2.174



B1L102S, U18-ch38, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U18-ch39, adc0

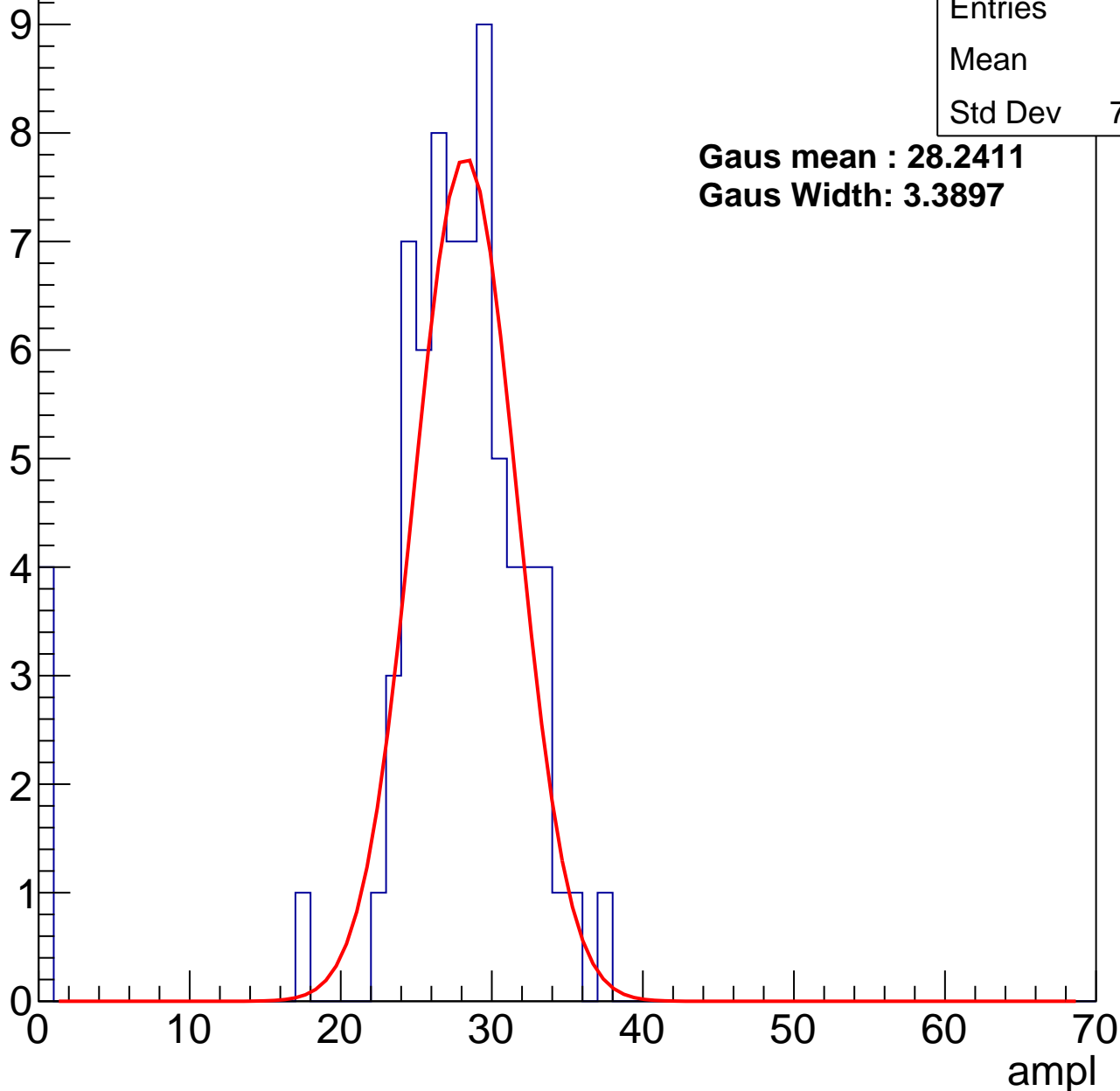
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	26.3
Std Dev	7.174

Gaus mean : 28.2411

Gaus Width: 3.3897



B1L102S, U18-ch39, adc1

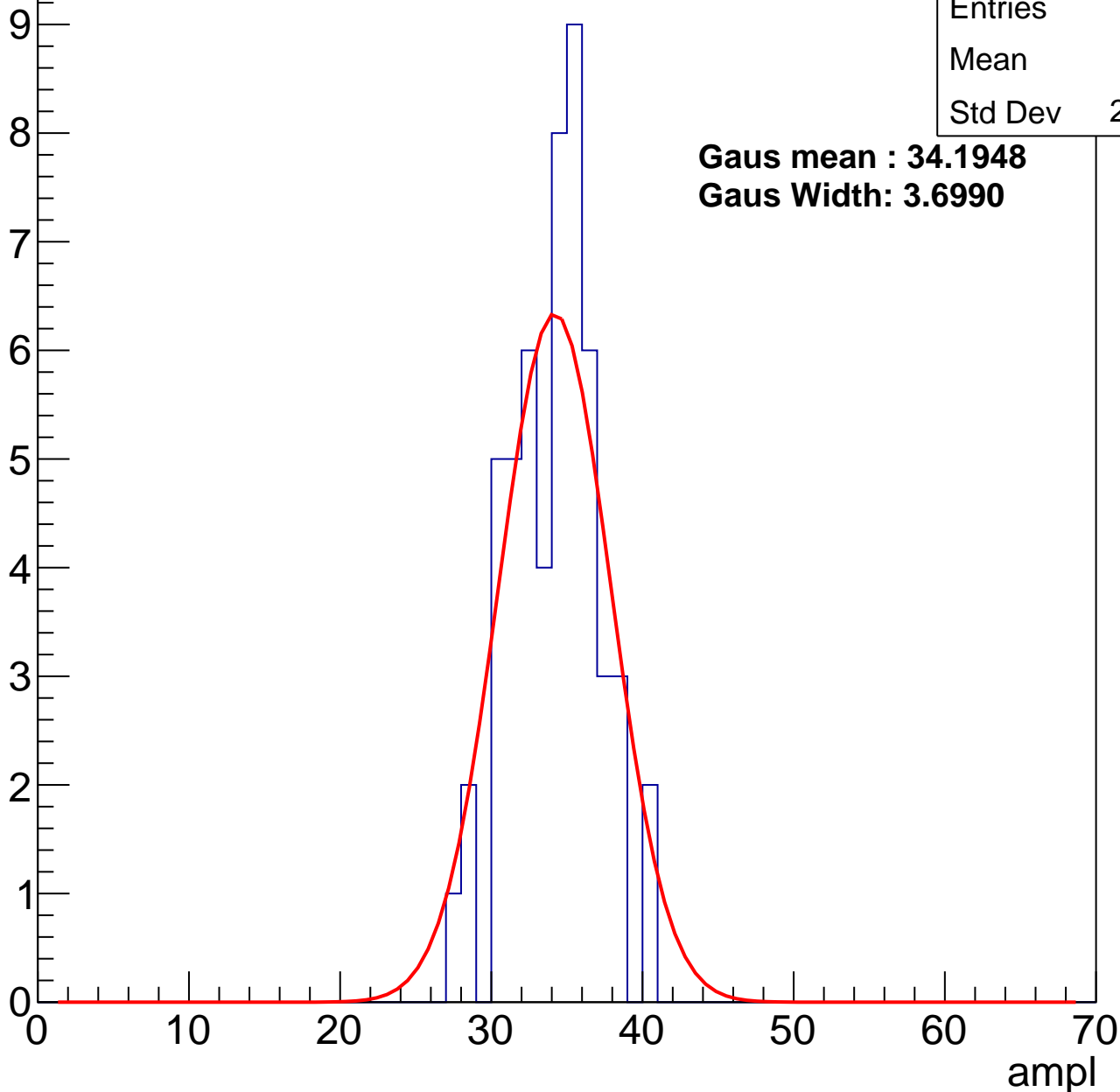
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	33.7
Std Dev	2.884

Gaus mean : 34.1948

Gaus Width: 3.6990



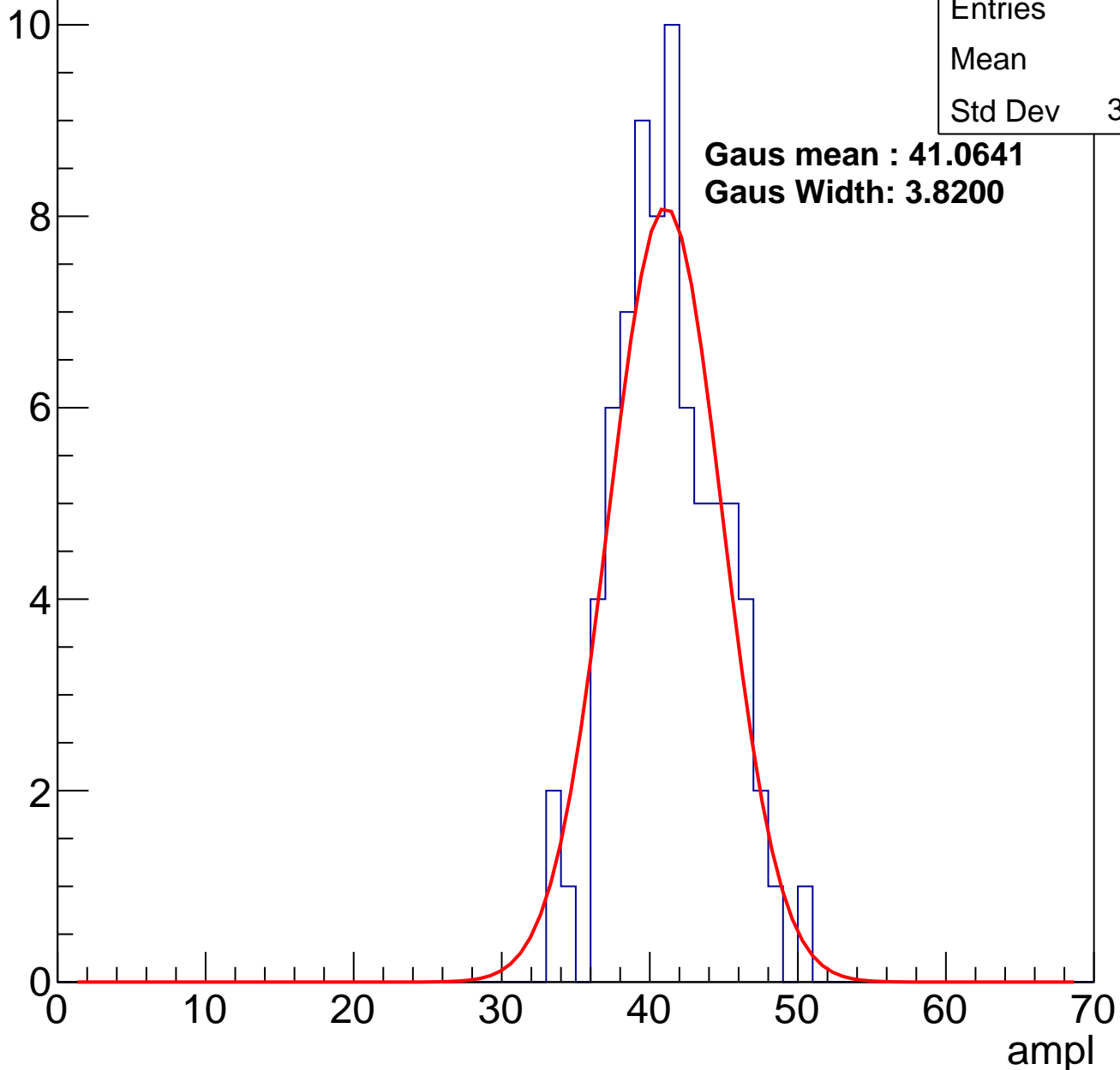
B1L102S, U18-ch39, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	40.8
Std Dev	3.498

Gaus mean : 41.0641
Gaus Width: 3.8200

Entry

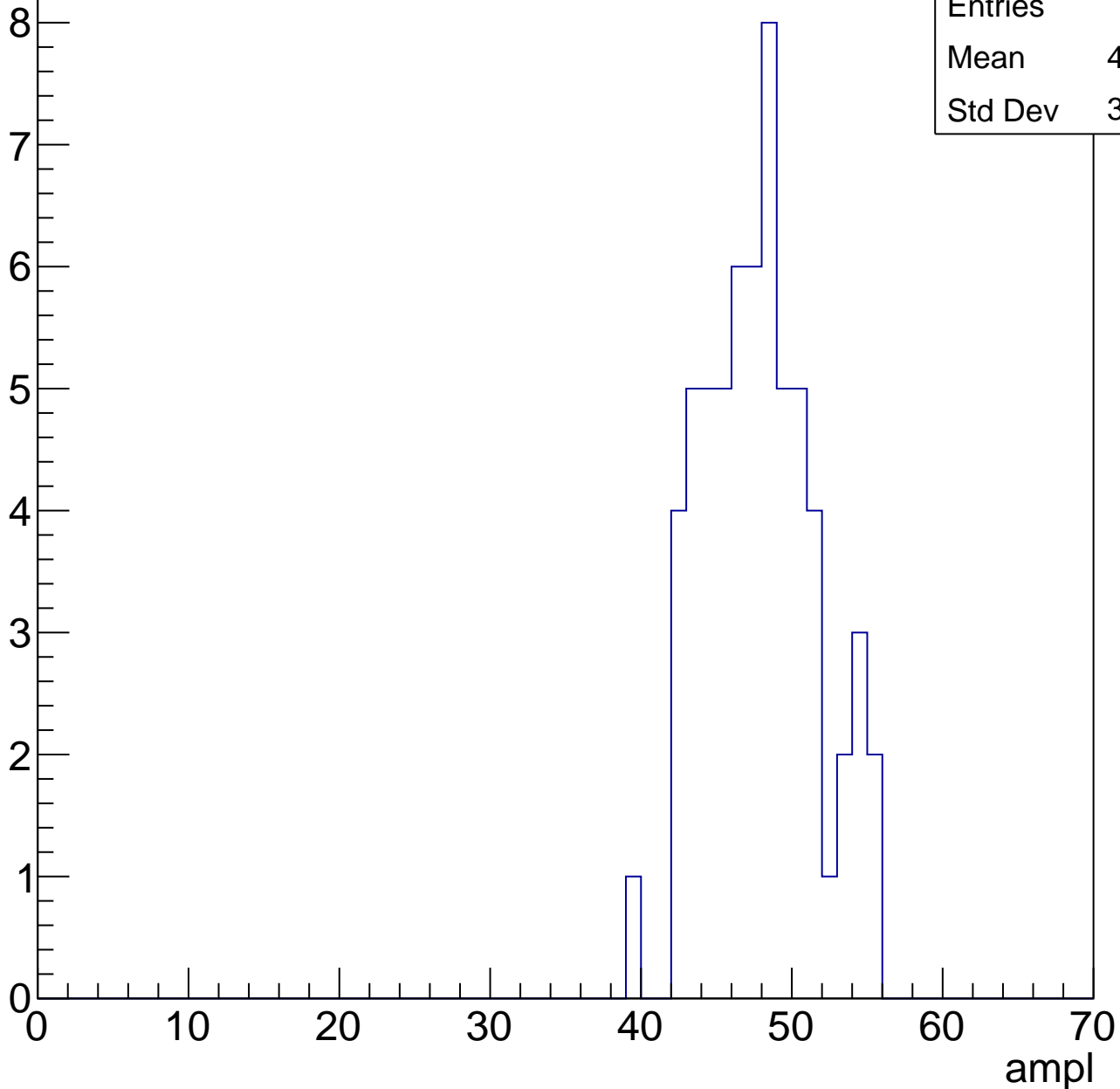


B1L102S, U18-ch39, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	47.39
Std Dev	3.625

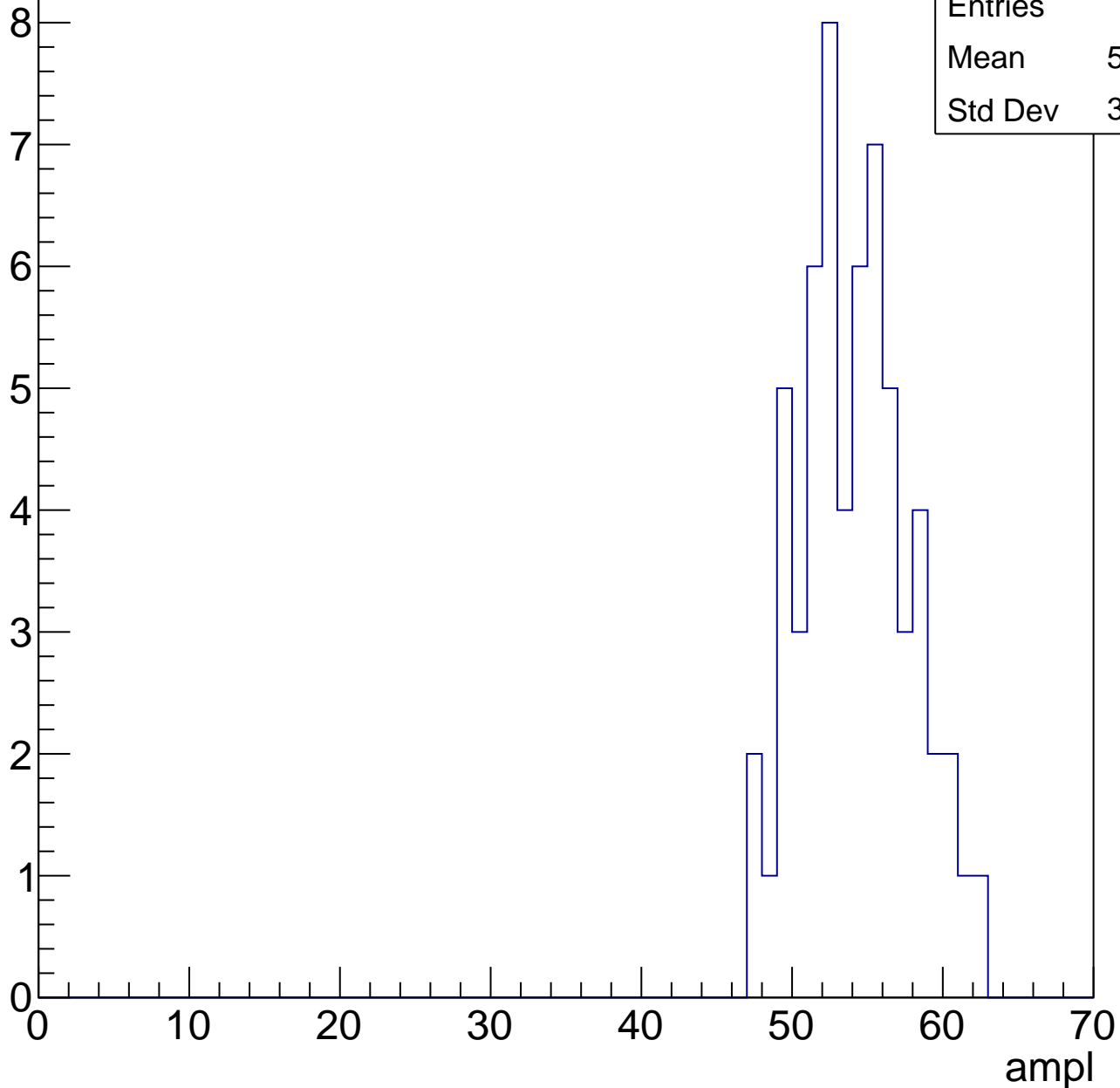


B1L102S, U18-ch39, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.73
Std Dev	3.525

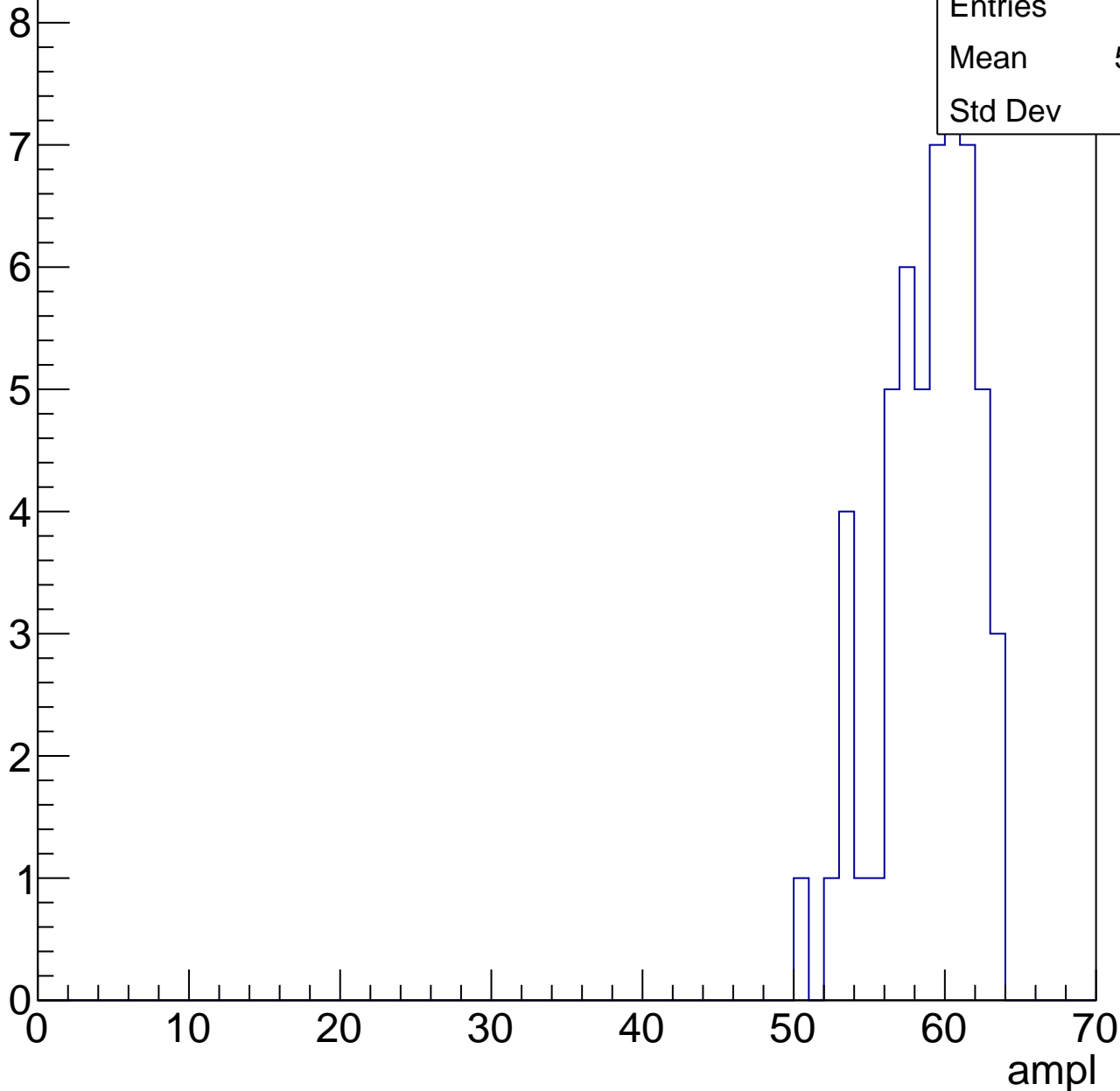


B1L102S, U18-ch39, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.41
Std Dev	3.04

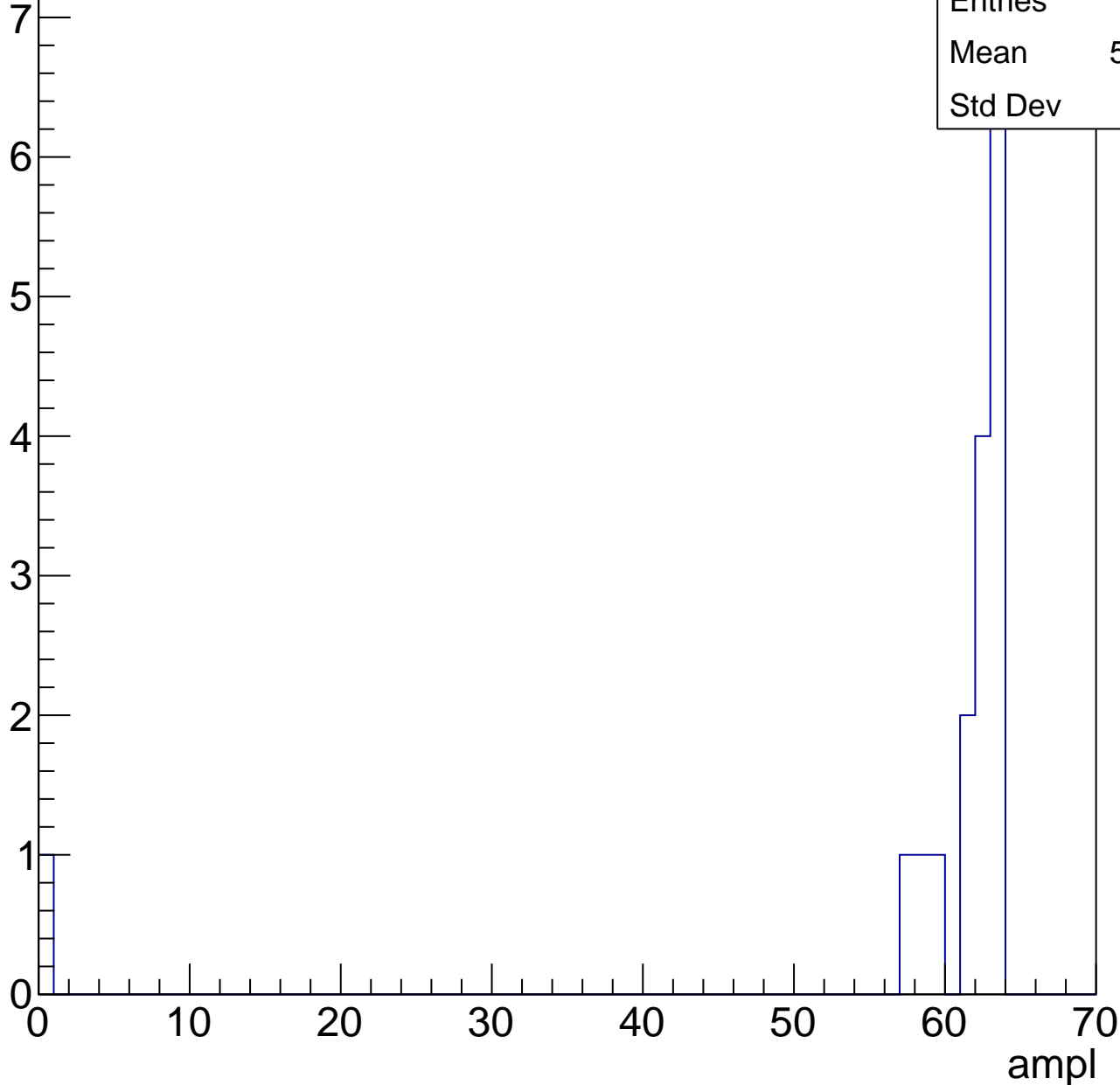


B1L102S, U18-ch39, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	57.94
Std Dev	14.6



B1L102S, U18-ch39, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch40, adc0

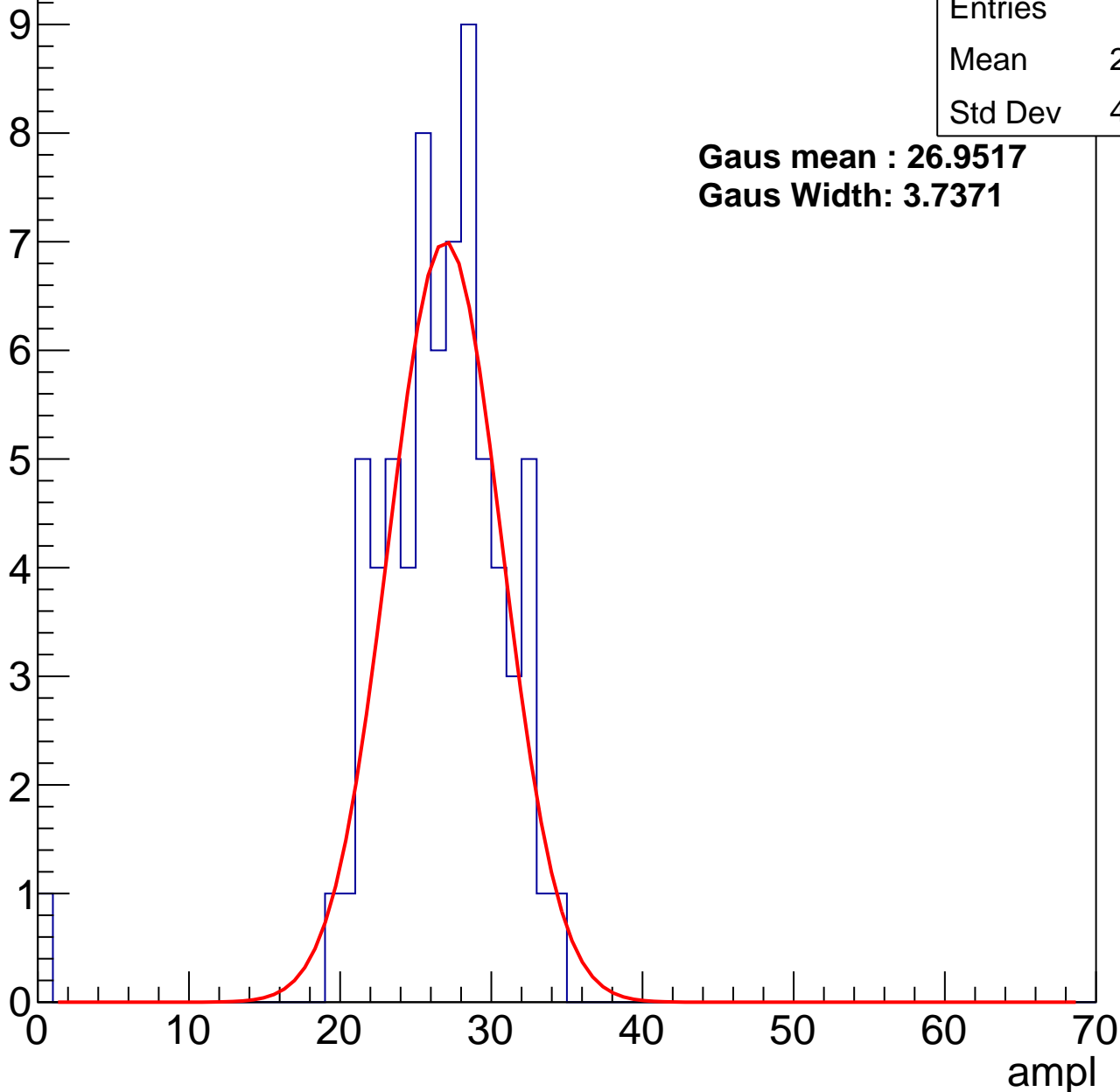
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	26.07
Std Dev	4.688

Gaus mean : 26.9517

Gaus Width: 3.7371



B1L102S, U18-ch40, adc1

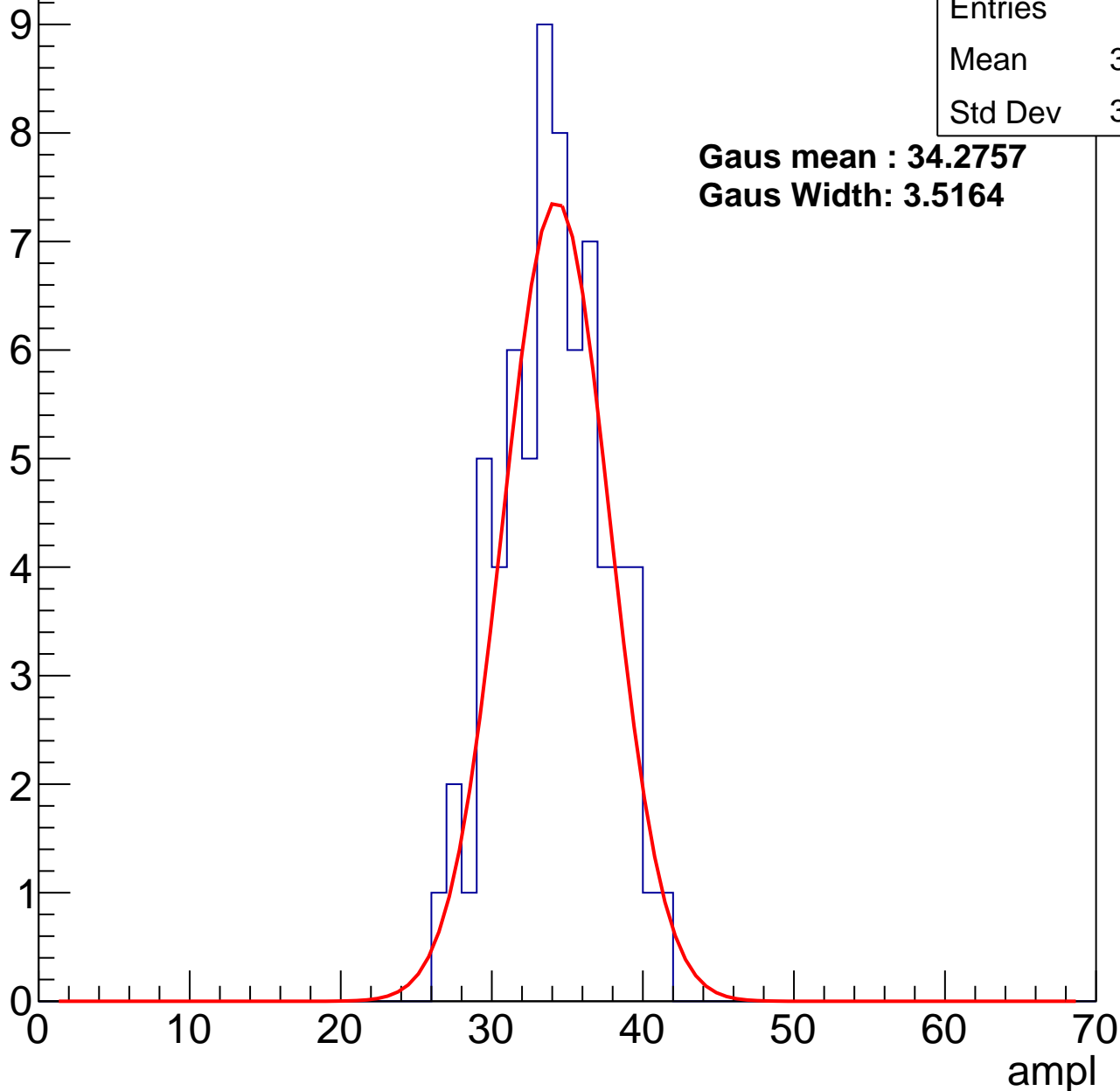
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	33.63
Std Dev	3.386

Gaus mean : 34.2757

Gaus Width: 3.5164



B1L102S, U18-ch40, adc2

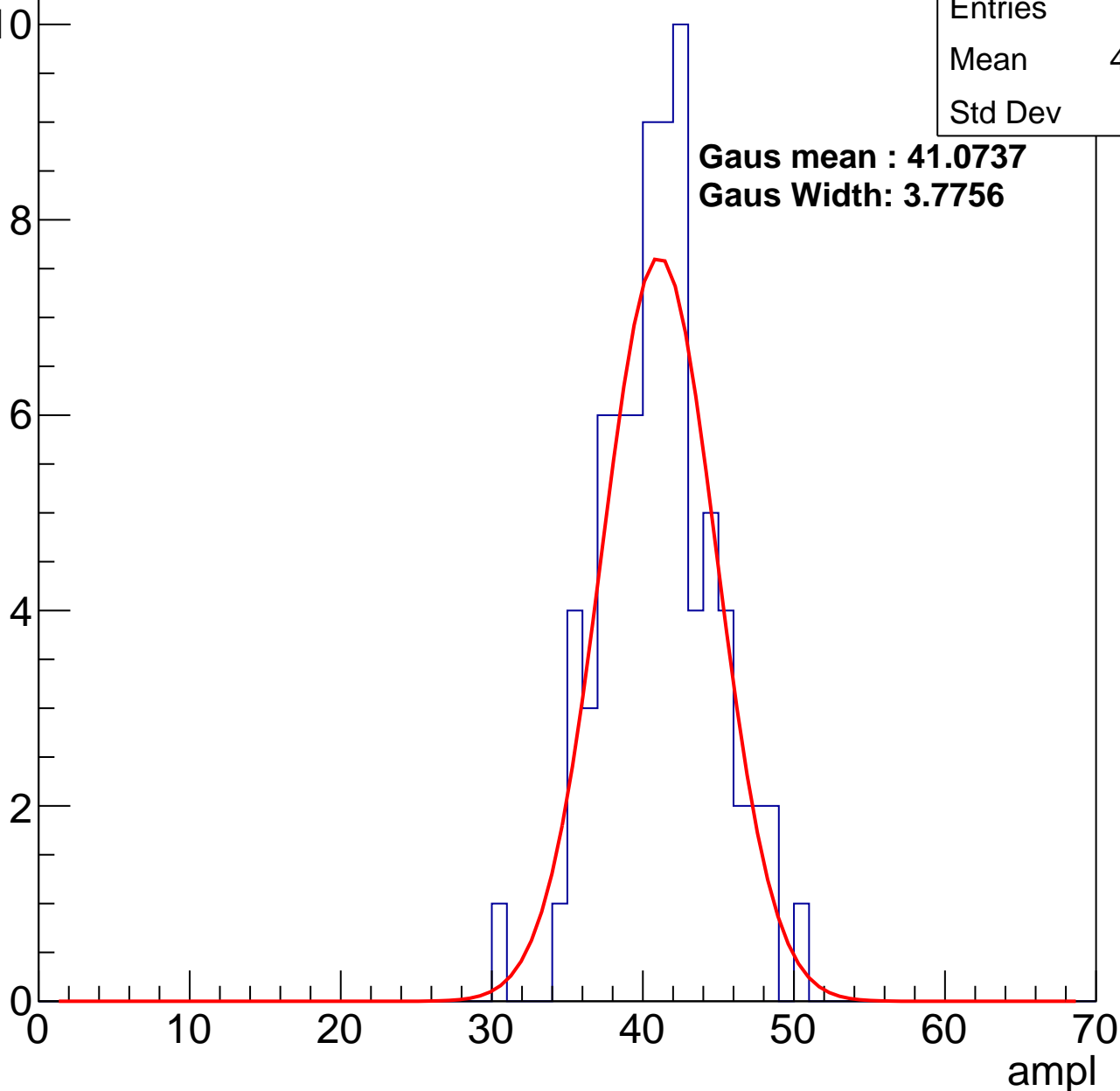
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	40.65
Std Dev	3.65

Gaus mean : 41.0737

Gaus Width: 3.7756

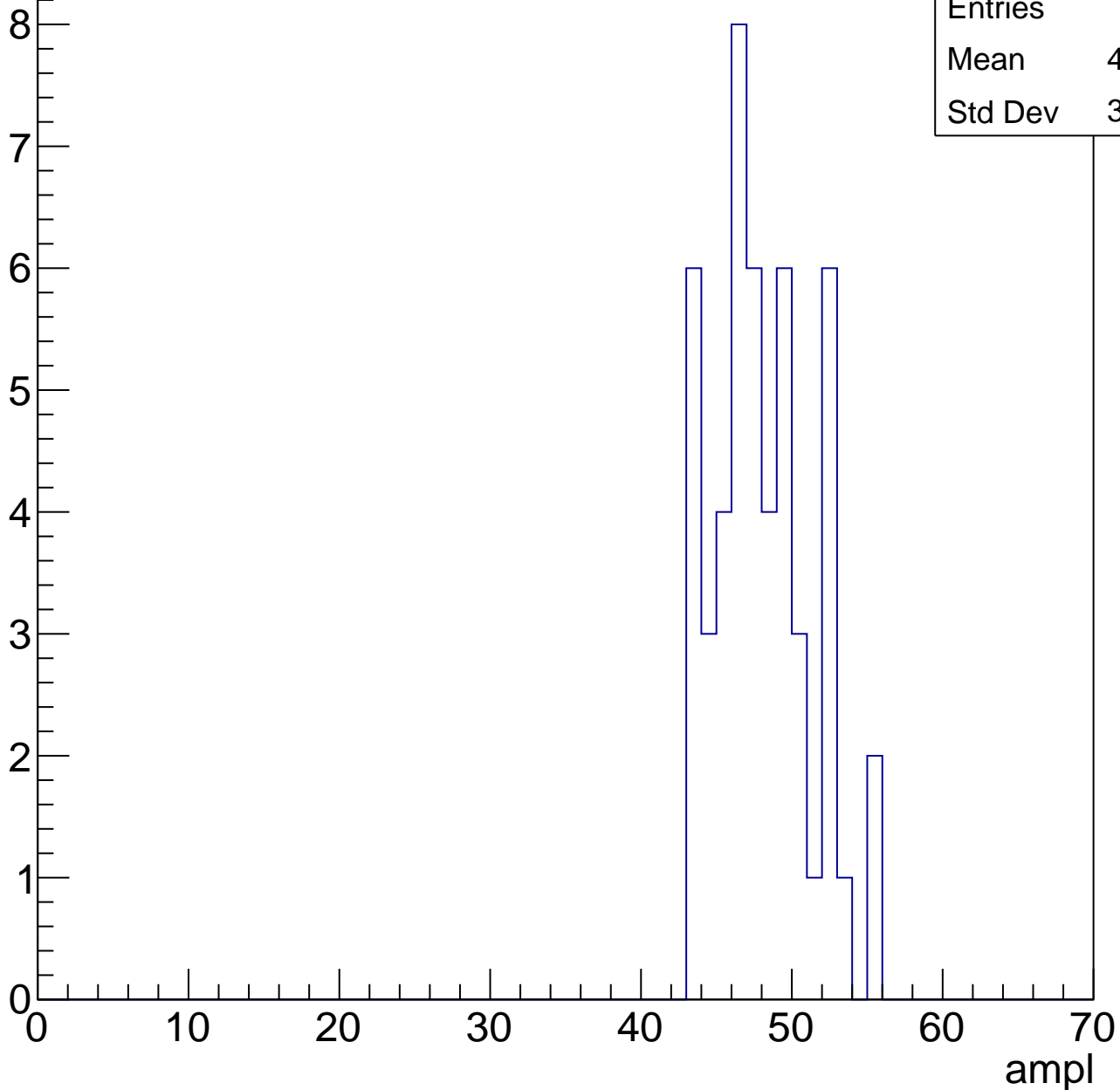


B1L102S, U18-ch40, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	47.64
Std Dev	3.198

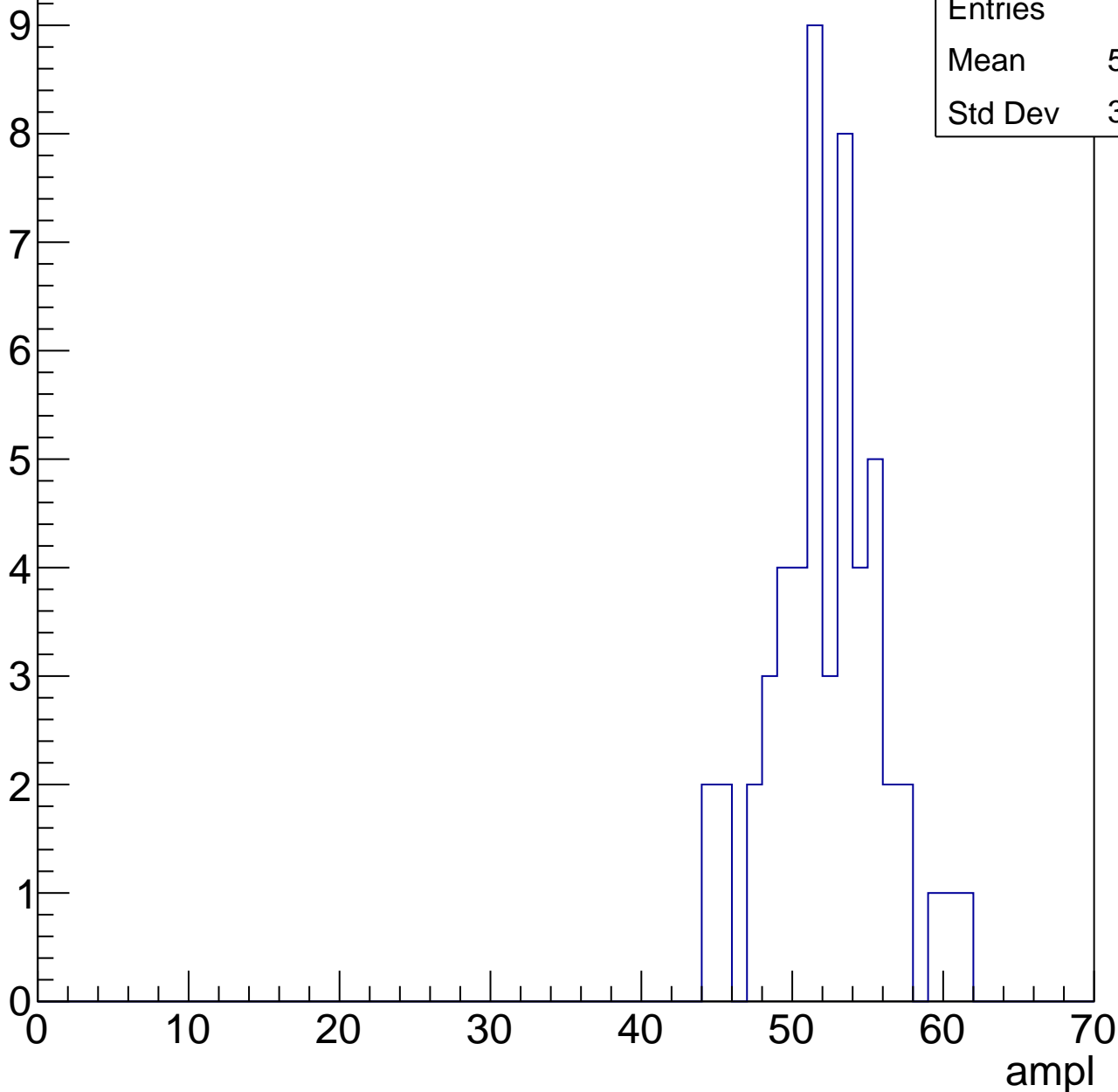


B1L102S, U18-ch40, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	51.85
Std Dev	3.708

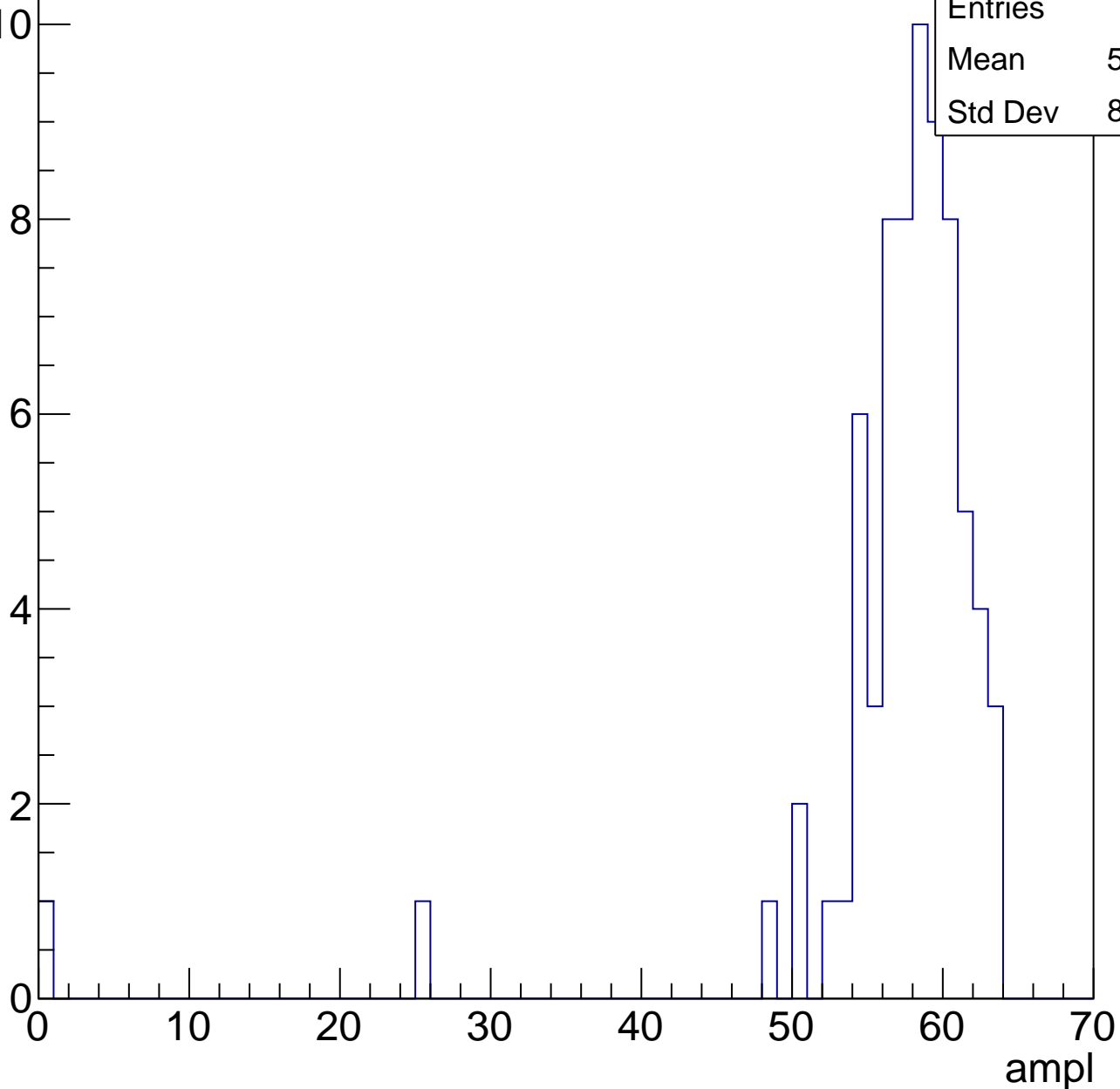


B1L102S, U18-ch40, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	56.39
Std Dev	8.347

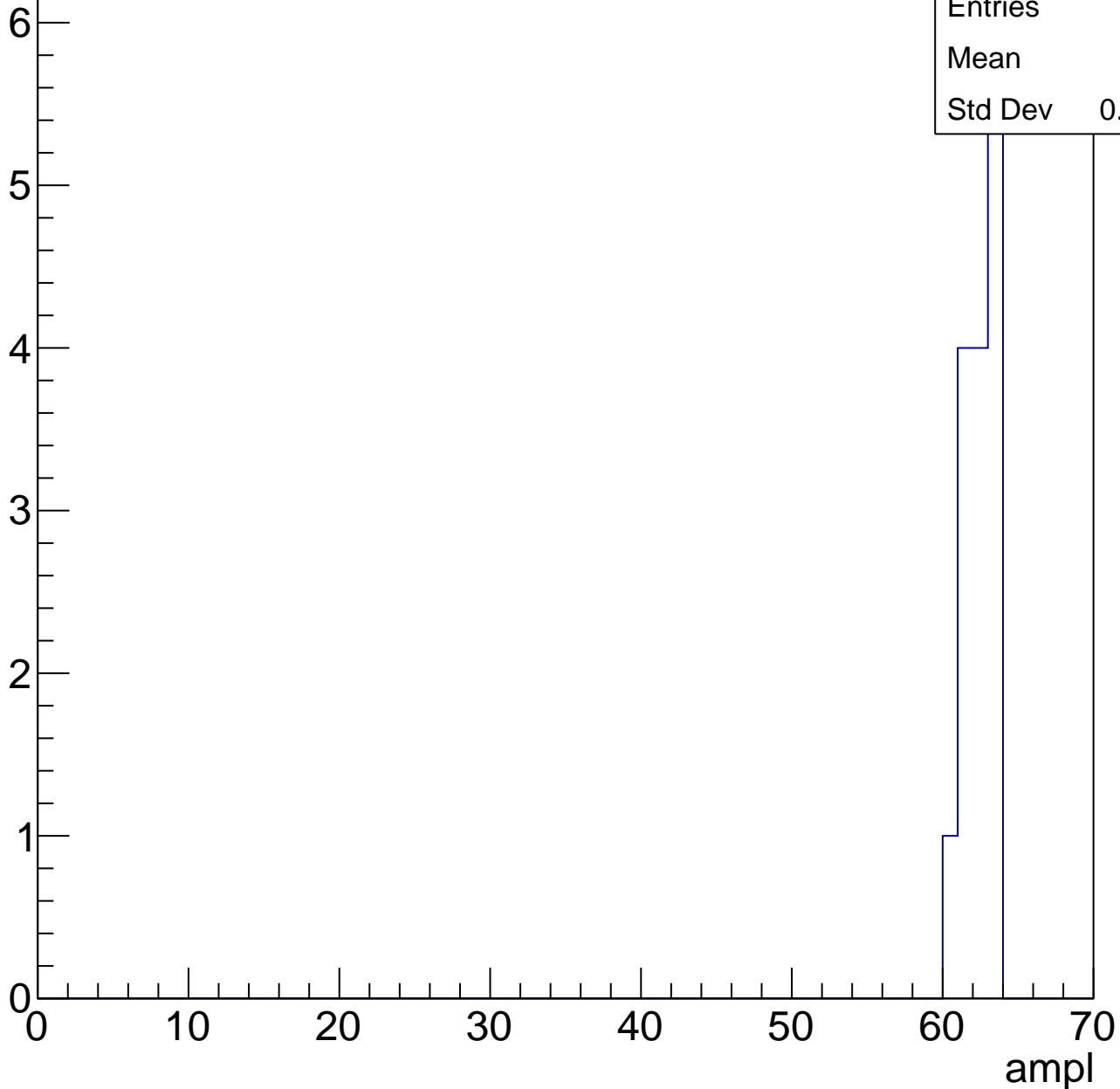


B1L102S, U18-ch40, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	62
Std Dev	0.9661



B1L102S, U18-ch40, adc7

calib_packv5_042523_0143.root, FC#11, port A2

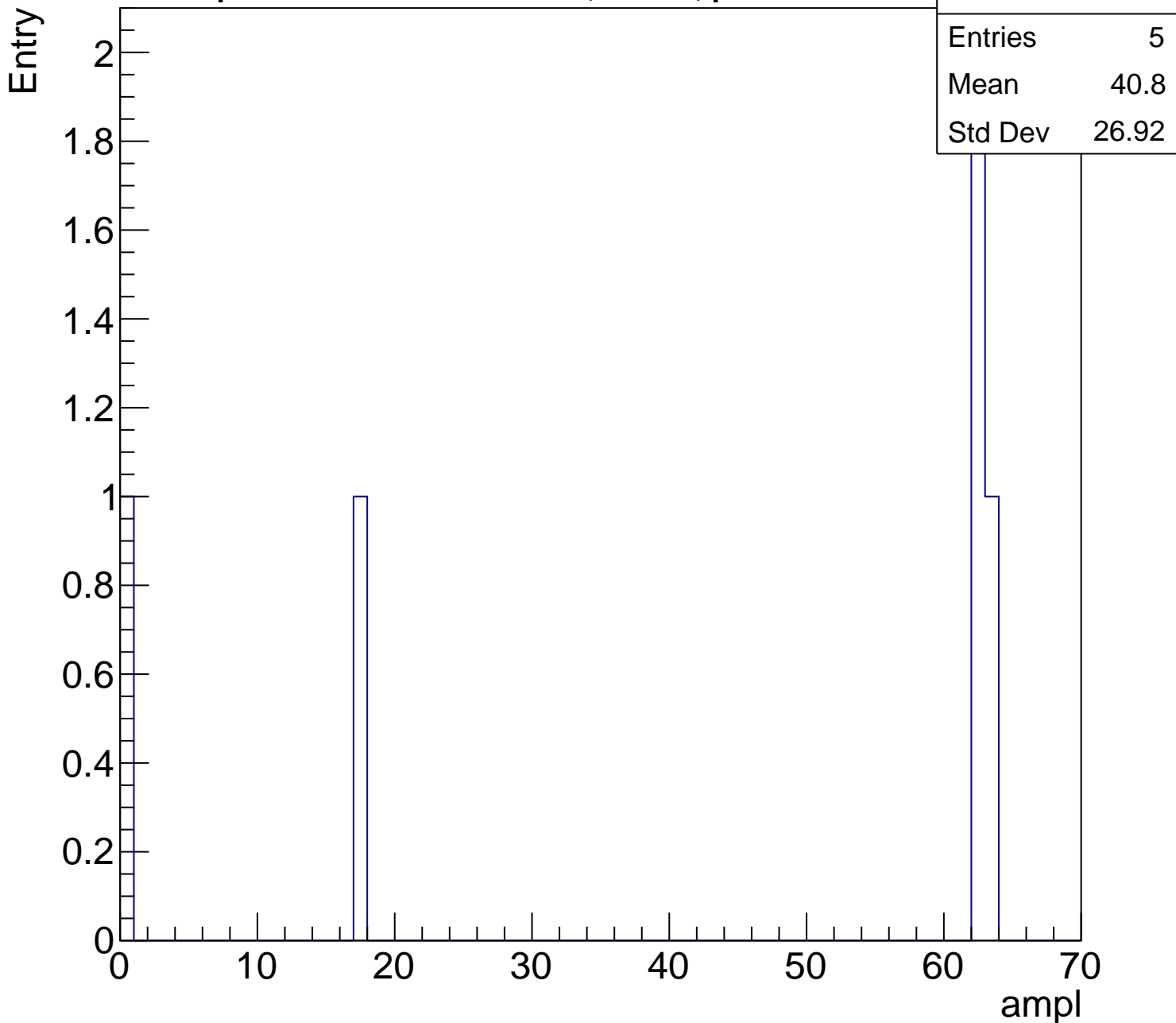
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	40.8
Std Dev	26.92

0 10 20 30 40 50 60 70

ampl



B1L102S, U18-ch41, adc0

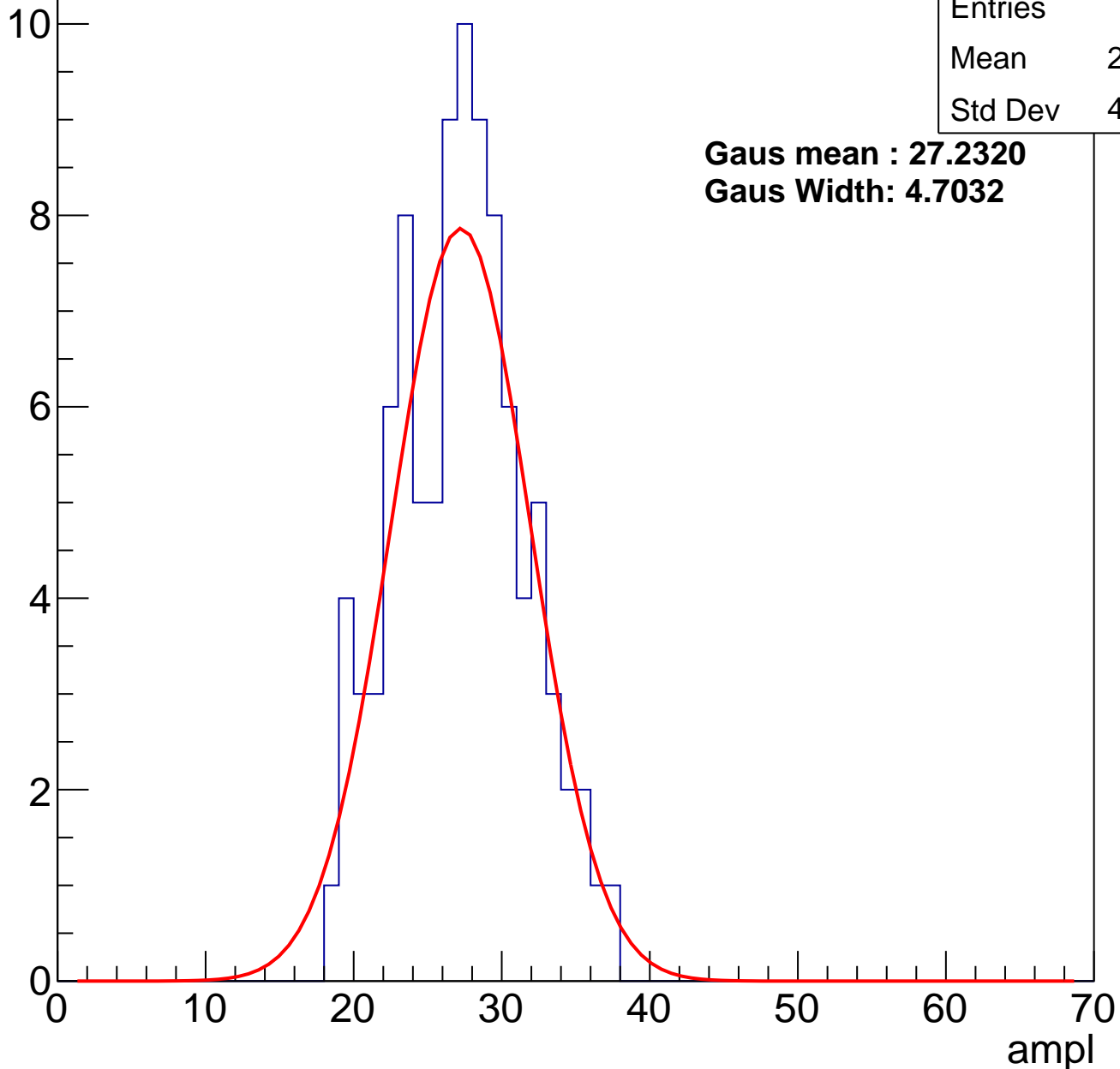
calib_packv5_042523_0143.root, FC#11, port A2

Entries	95
Mean	26.74
Std Dev	4.275

Gaus mean : 27.2320

Gaus Width: 4.7032

Entry



B1L102S, U18-ch41, adc1

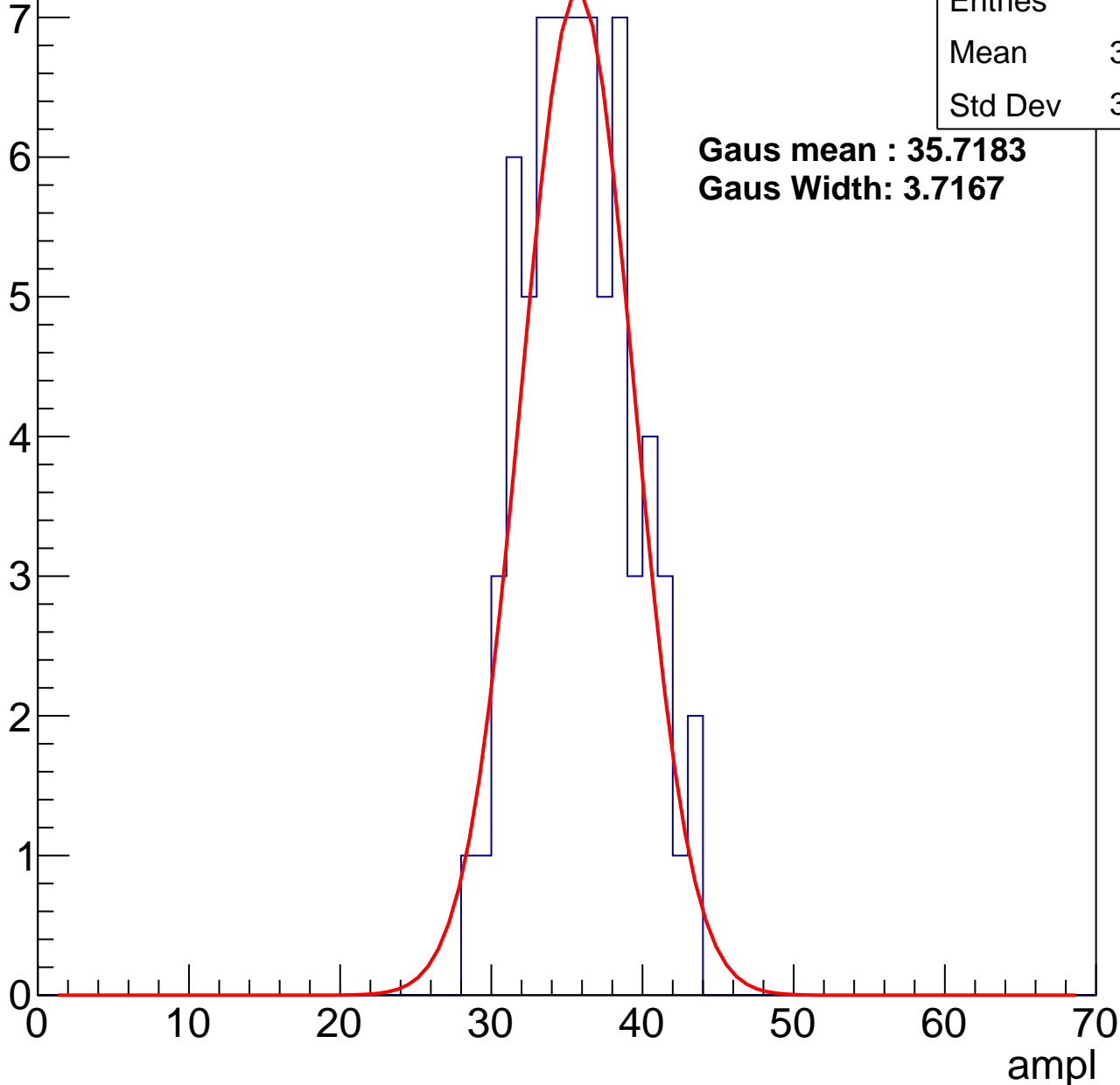
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	35.33
Std Dev	3.513

Gaus mean : 35.7183

Gaus Width: 3.7167



B1L102S, U18-ch41, adc2

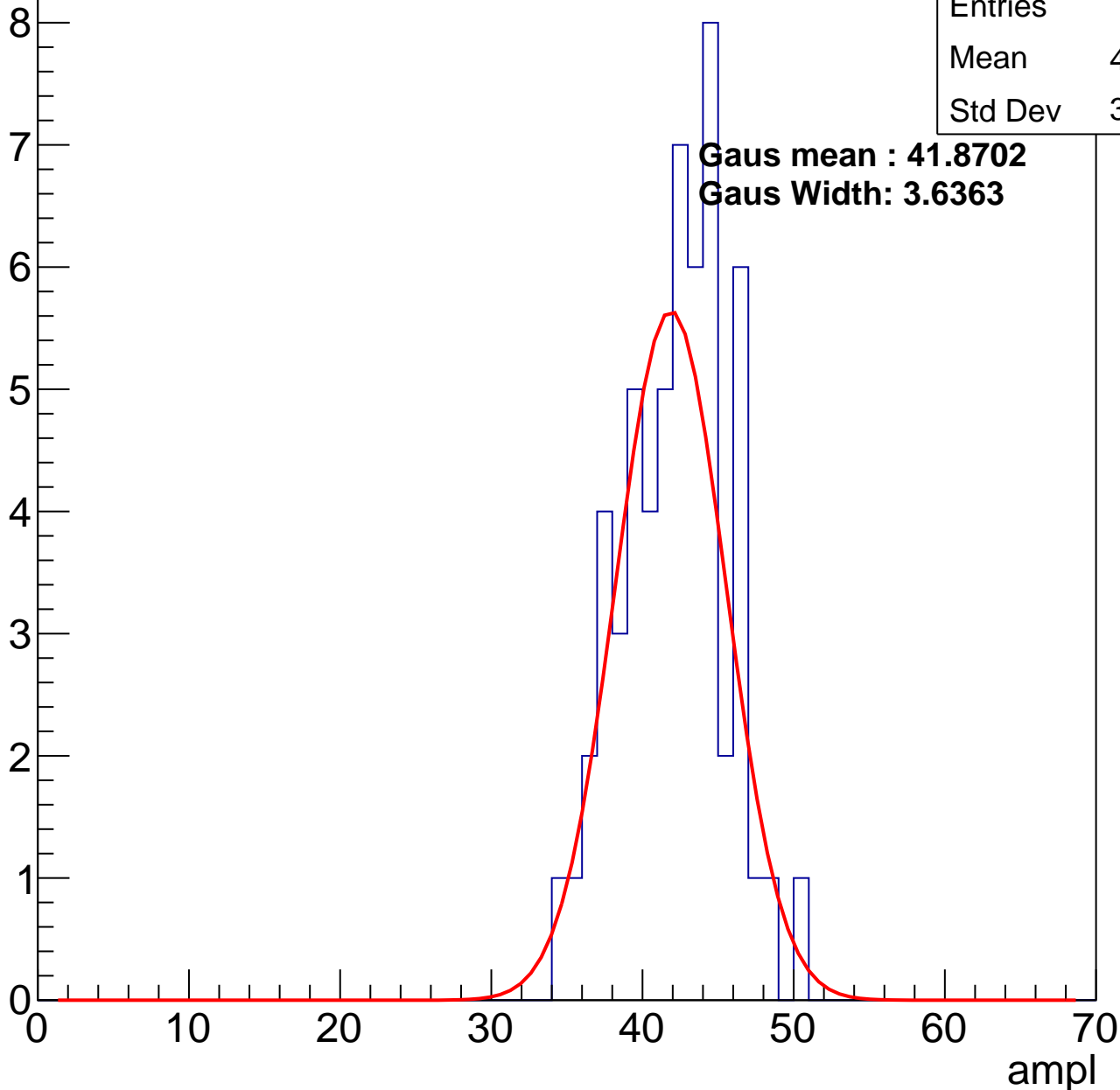
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	41.72
Std Dev	3.432

Gaus mean : 41.8702

Gaus Width: 3.6363

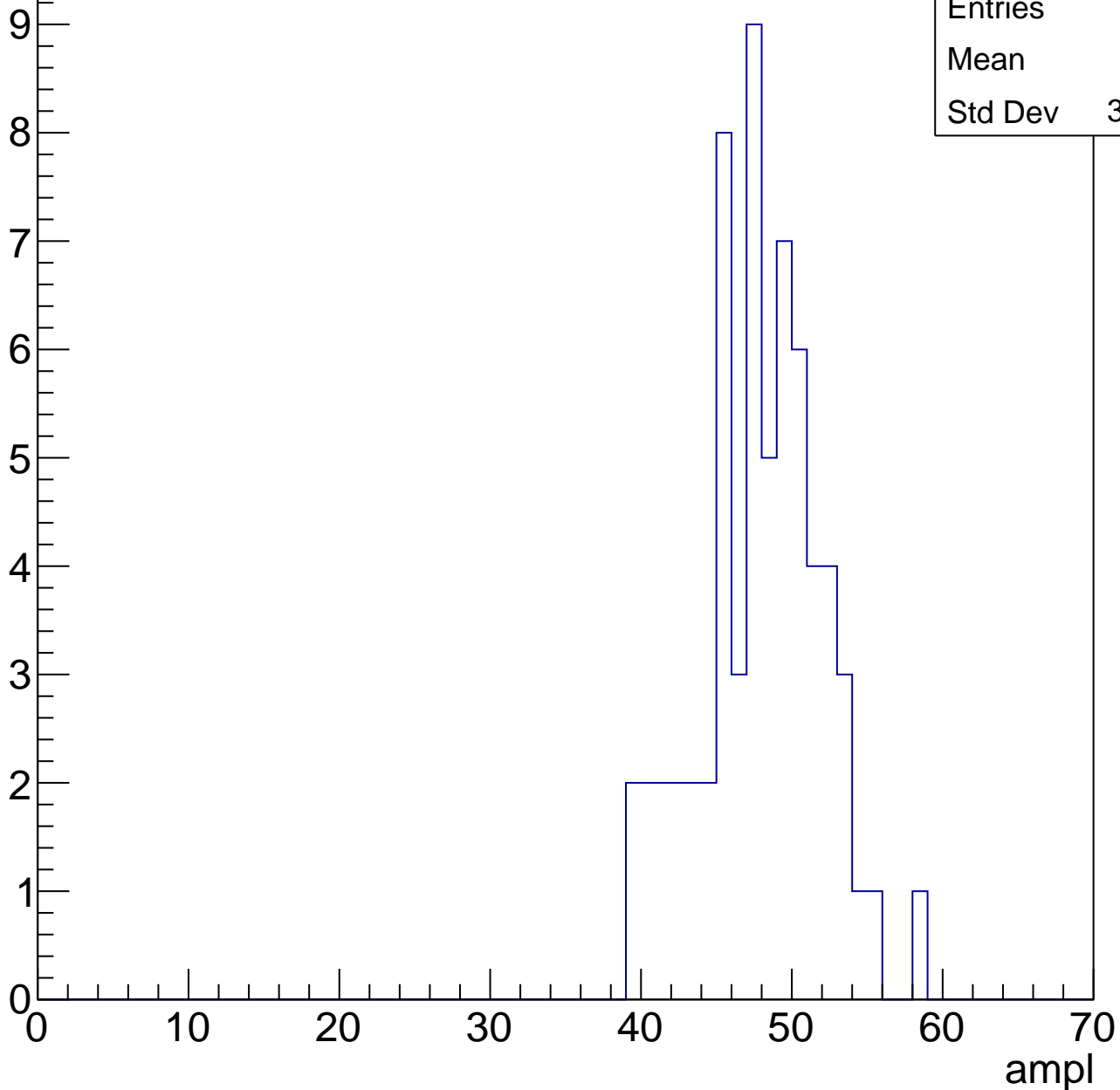


B1L102S, U18-ch41, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

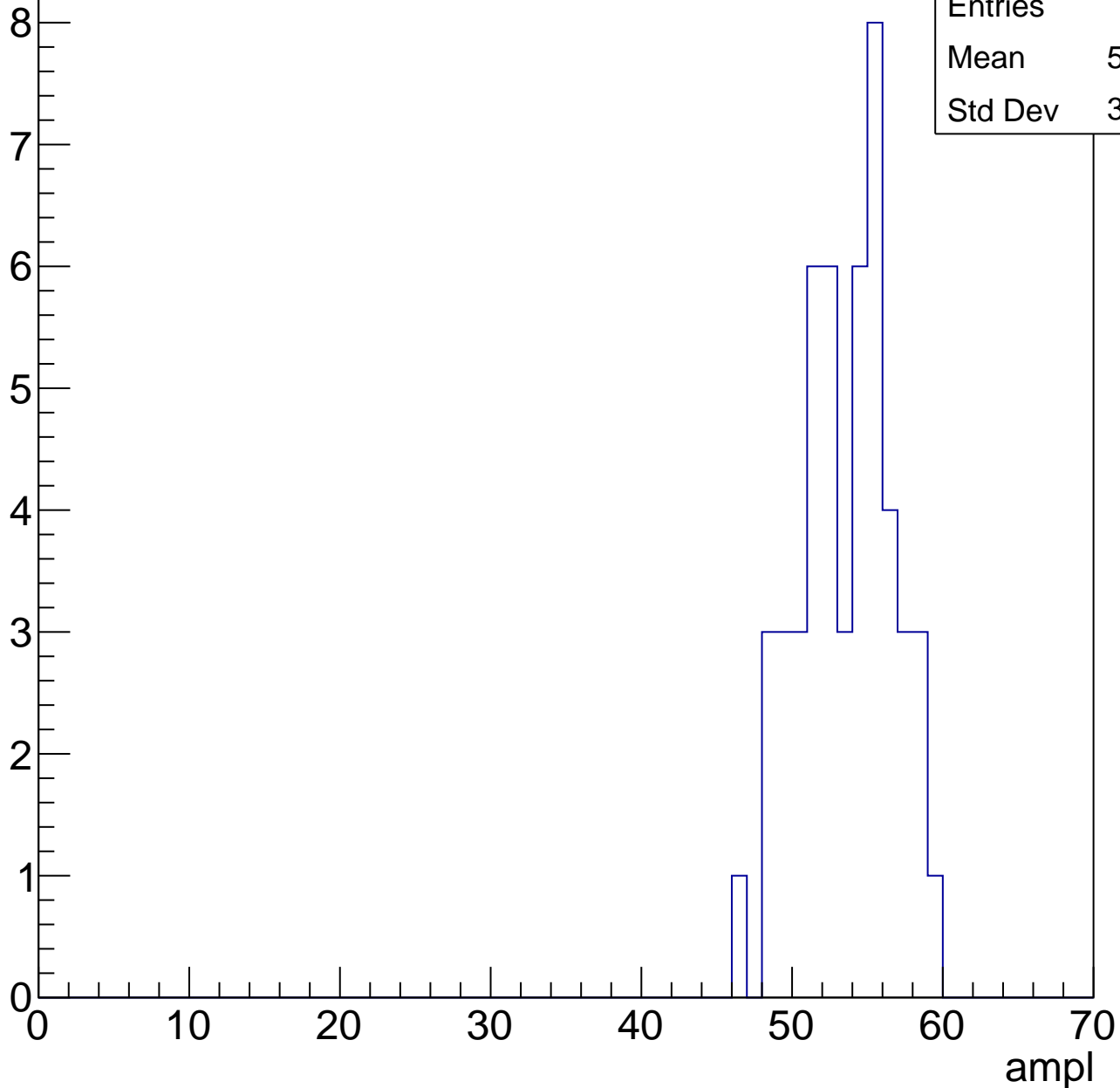
Entries	64
Mean	47.5
Std Dev	3.973



B1L102S, U18-ch41, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

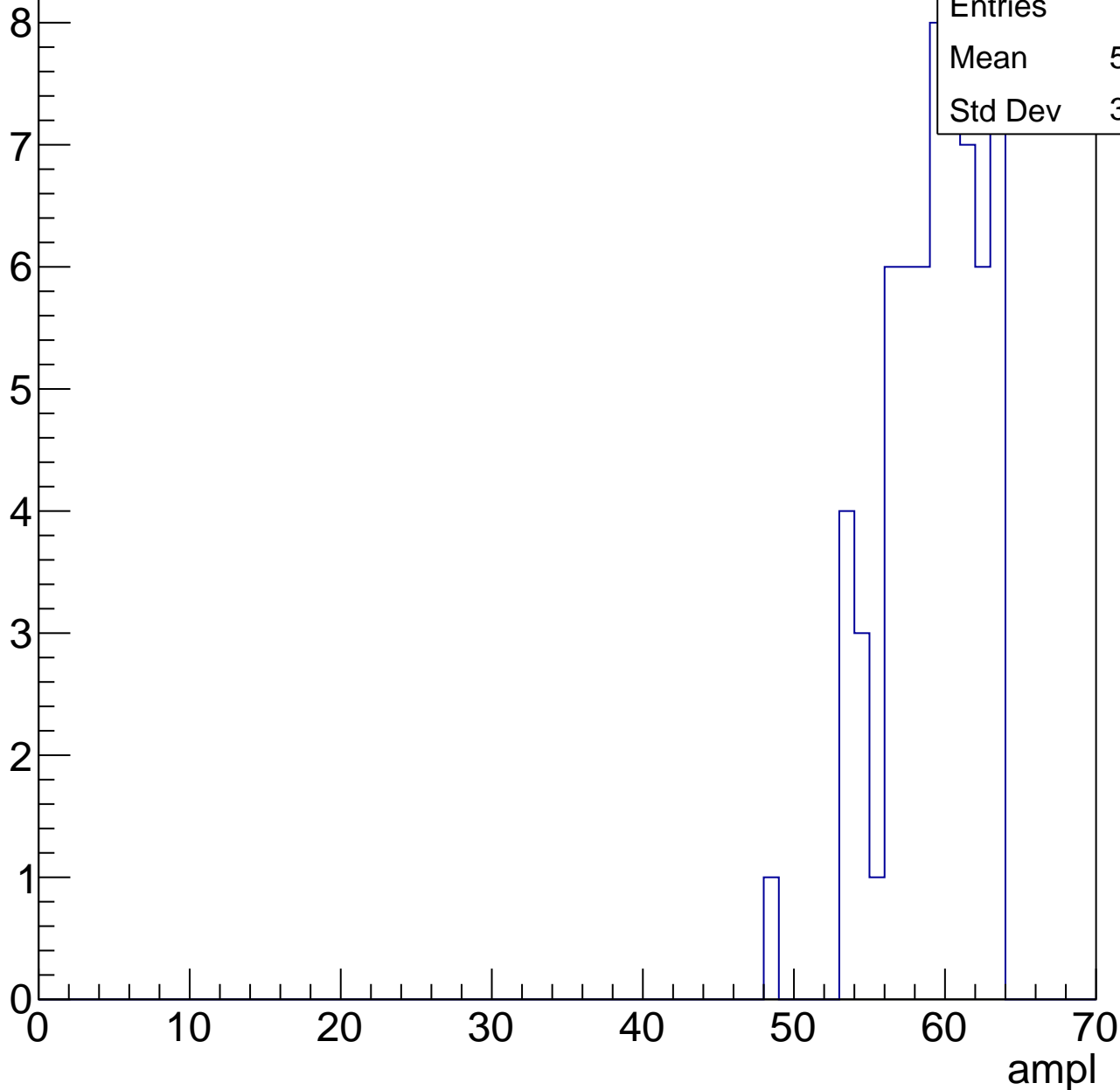


B1L102S, U18-ch41, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

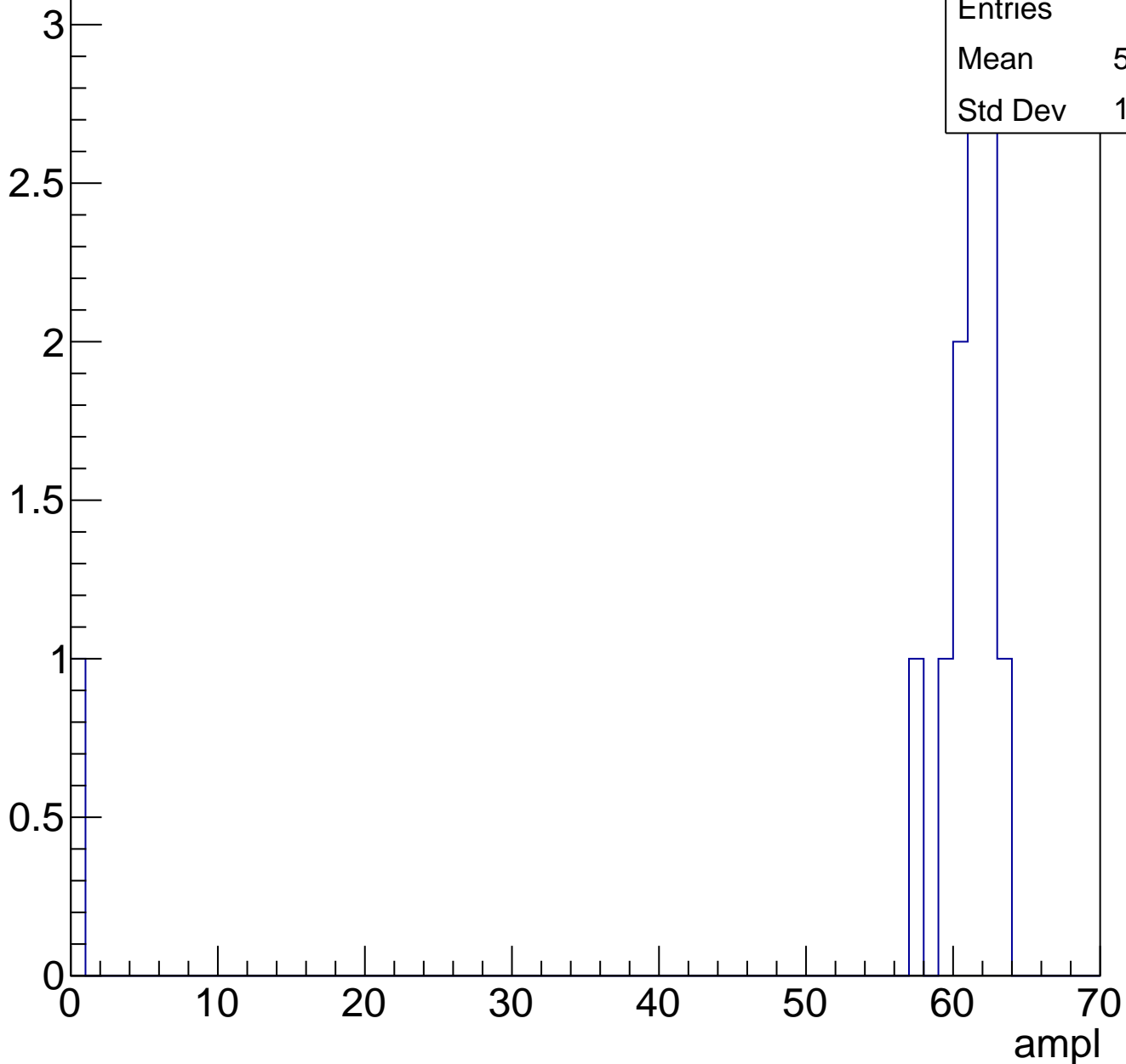
Entries	64
Mean	58.72
Std Dev	3.194



B1L102S, U18-ch41, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch41, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch42, adc0

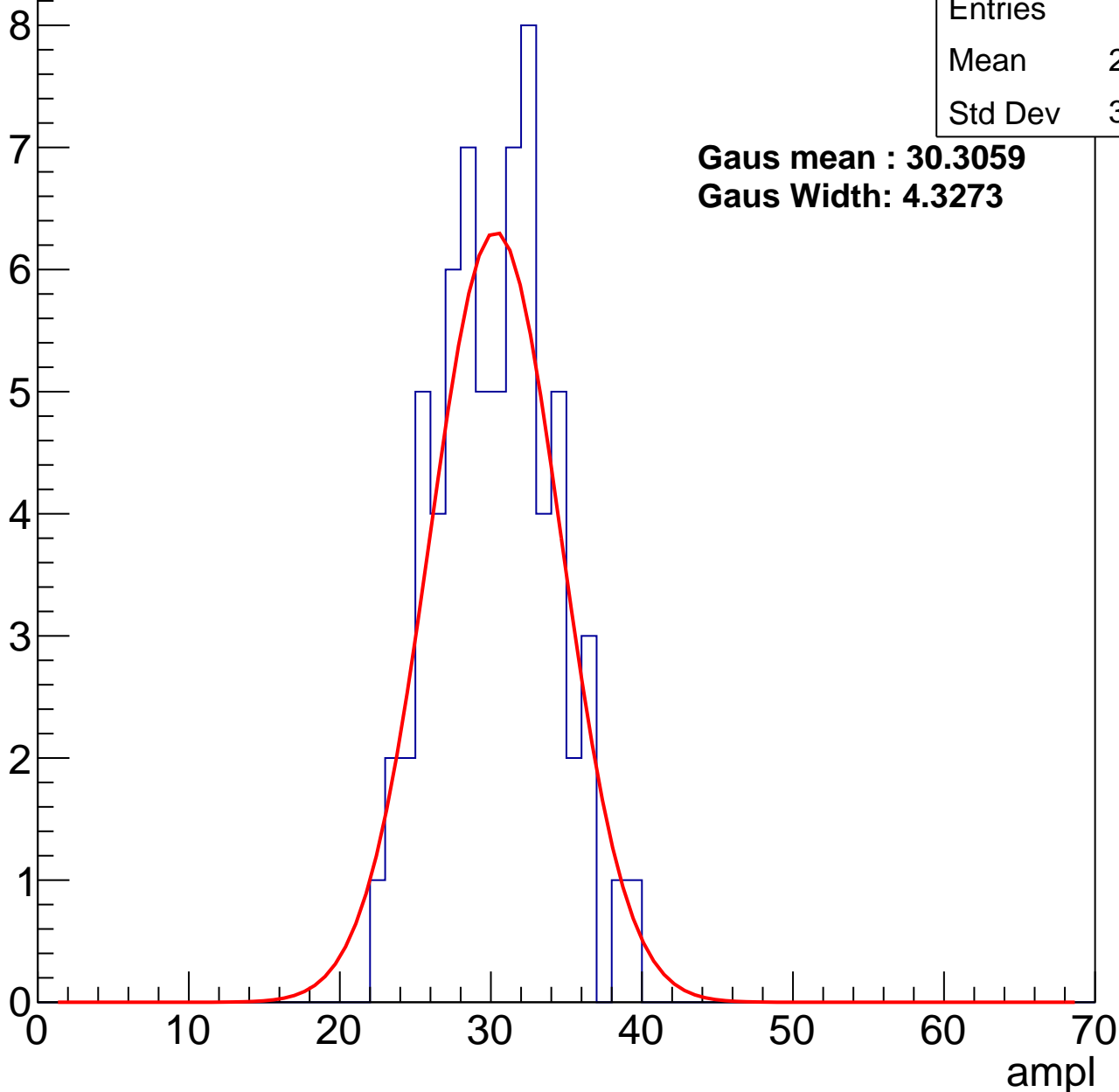
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.82
Std Dev	3.769

Gaus mean : 30.3059

Gaus Width: 4.3273



B1L102S, U18-ch42, adc1

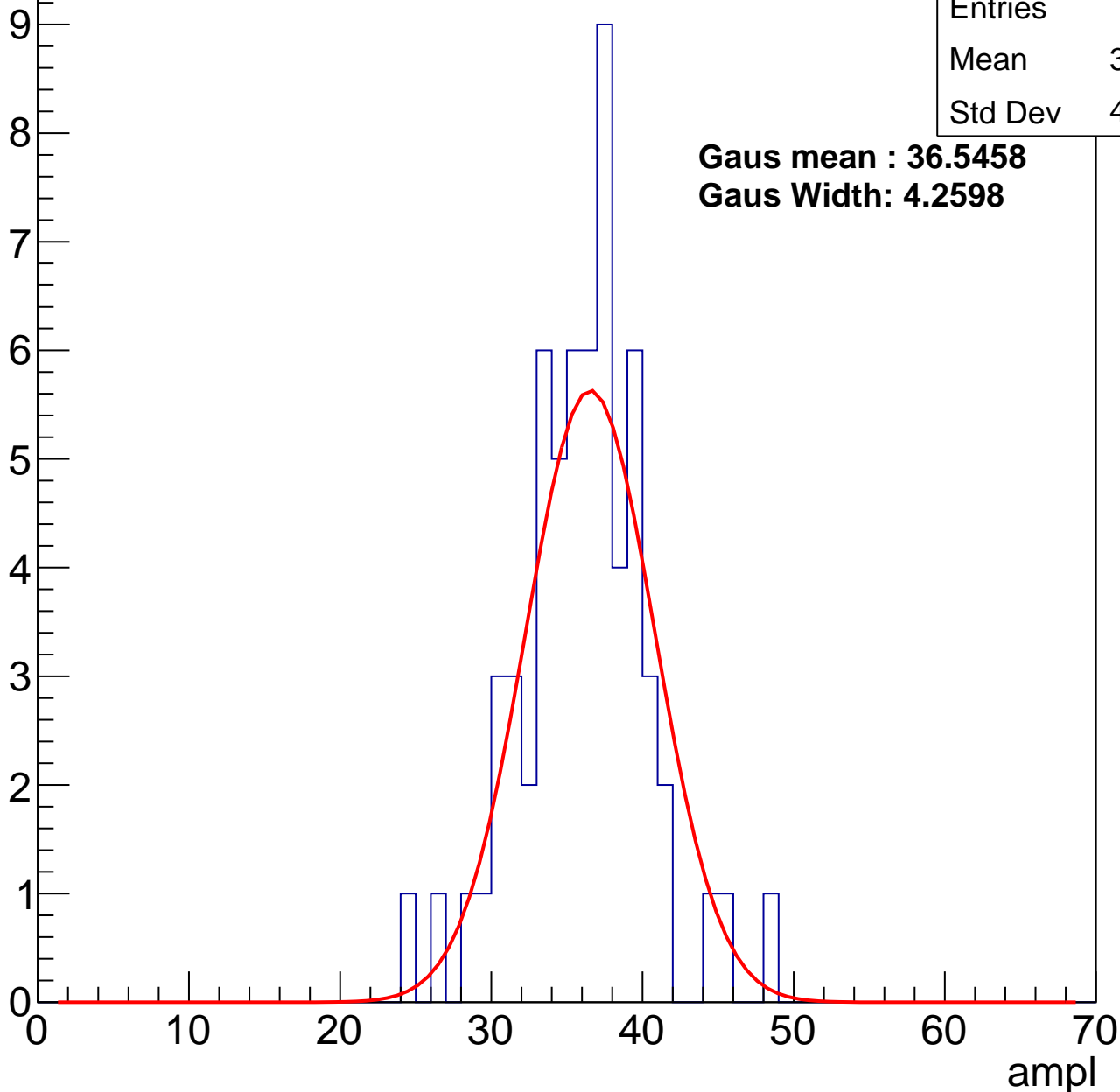
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	35.58
Std Dev	4.226

Gaus mean : 36.5458

Gaus Width: 4.2598



B1L102S, U18-ch42, adc2

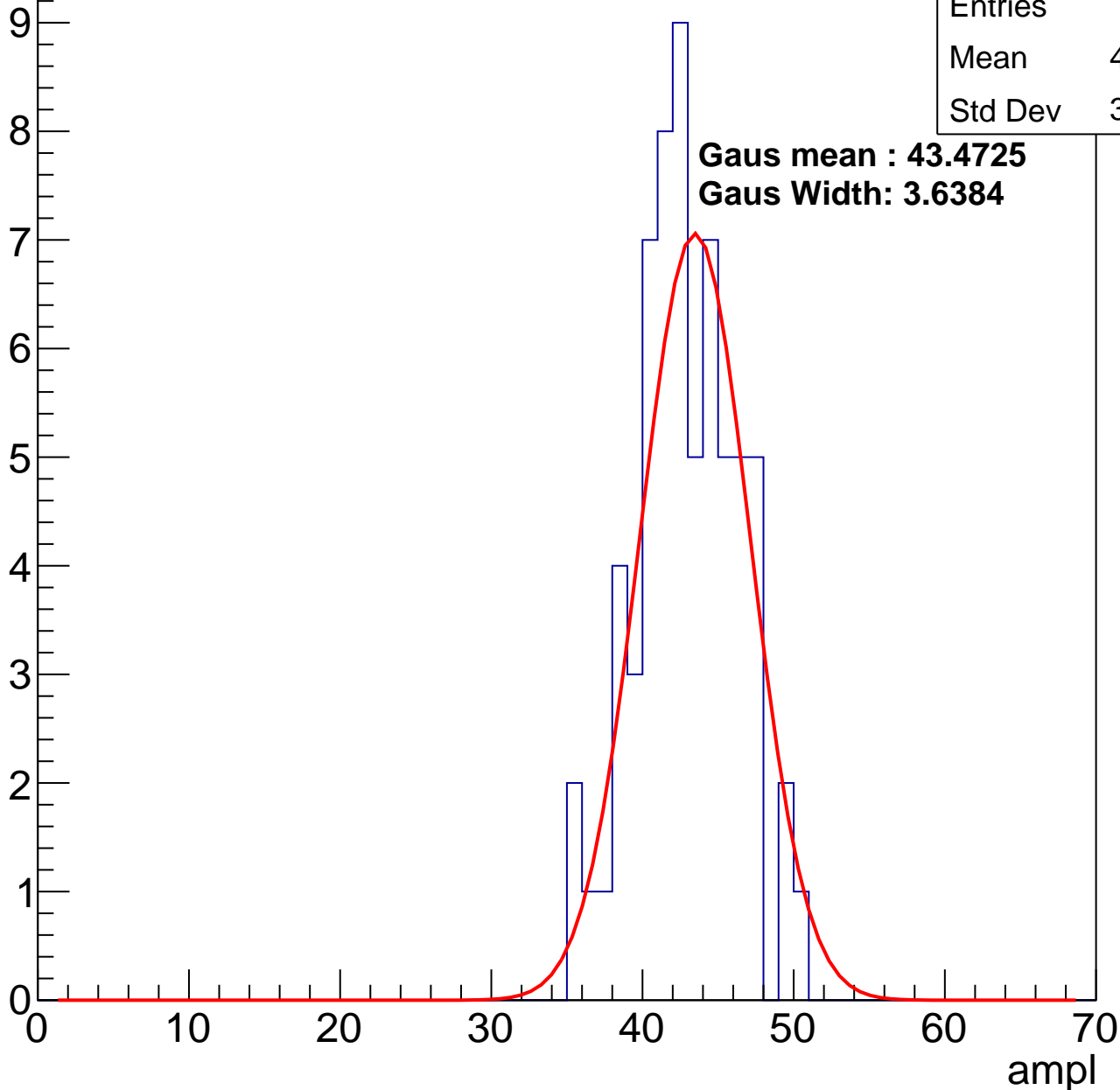
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.45
Std Dev	3.324

Gaus mean : 43.4725

Gaus Width: 3.6384

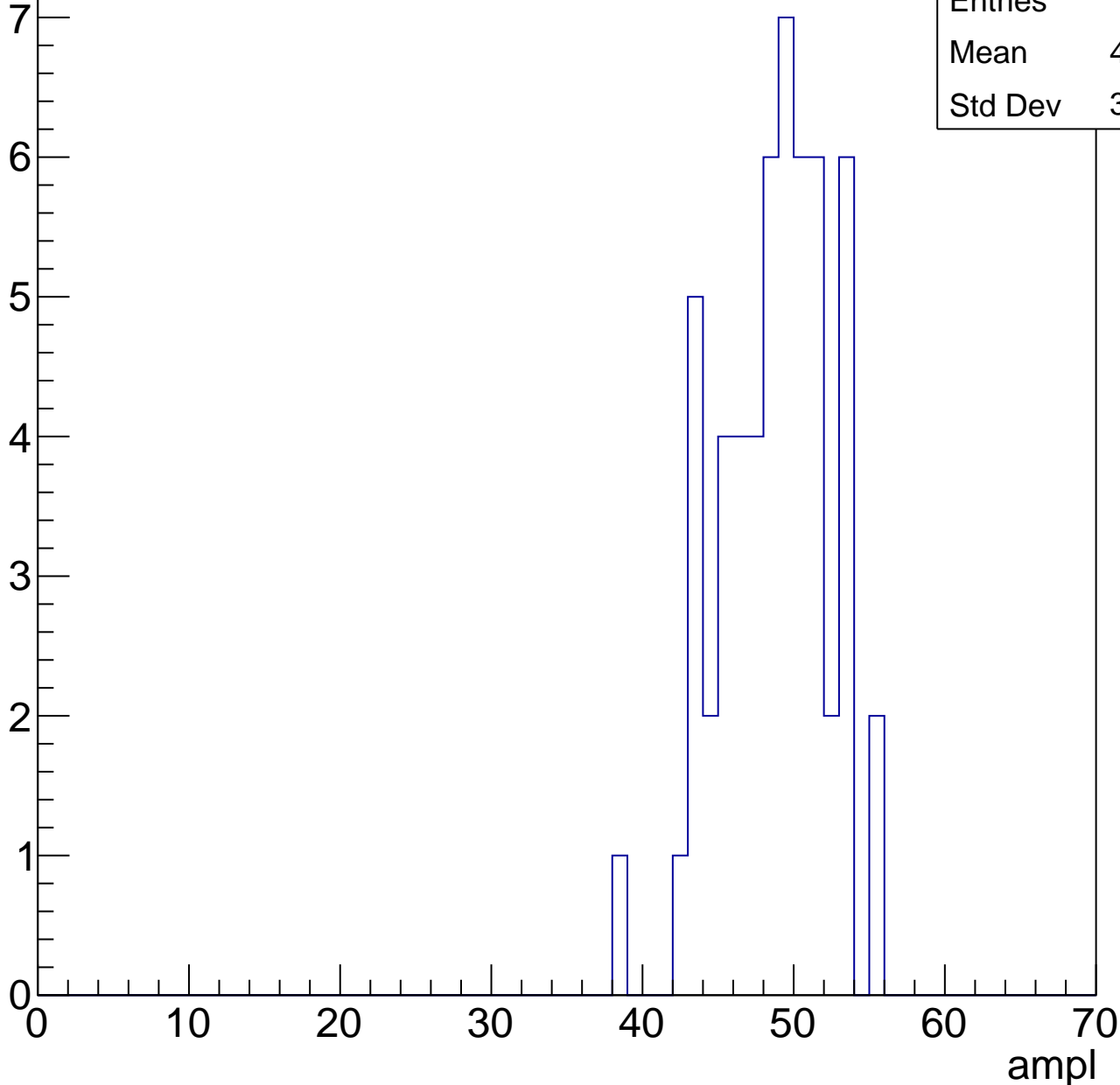


B1L102S, U18-ch42, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.29
Std Dev	3.559



B1L102S, U18-ch42, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	59
Mean	54.19
Std Dev	3.51

Entry

10

8

6

4

2

0

0

10

20

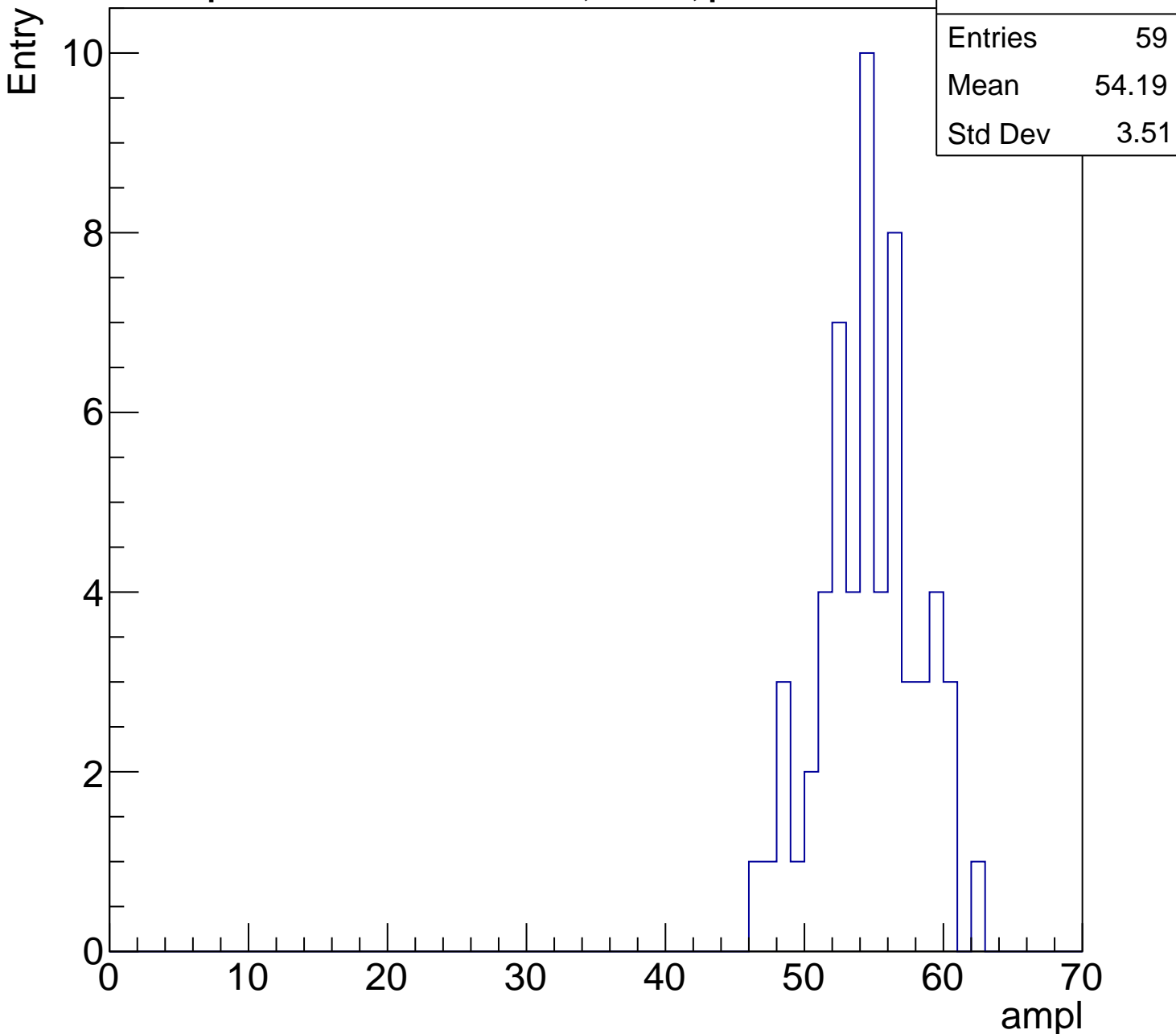
30

40

50

60

ampl

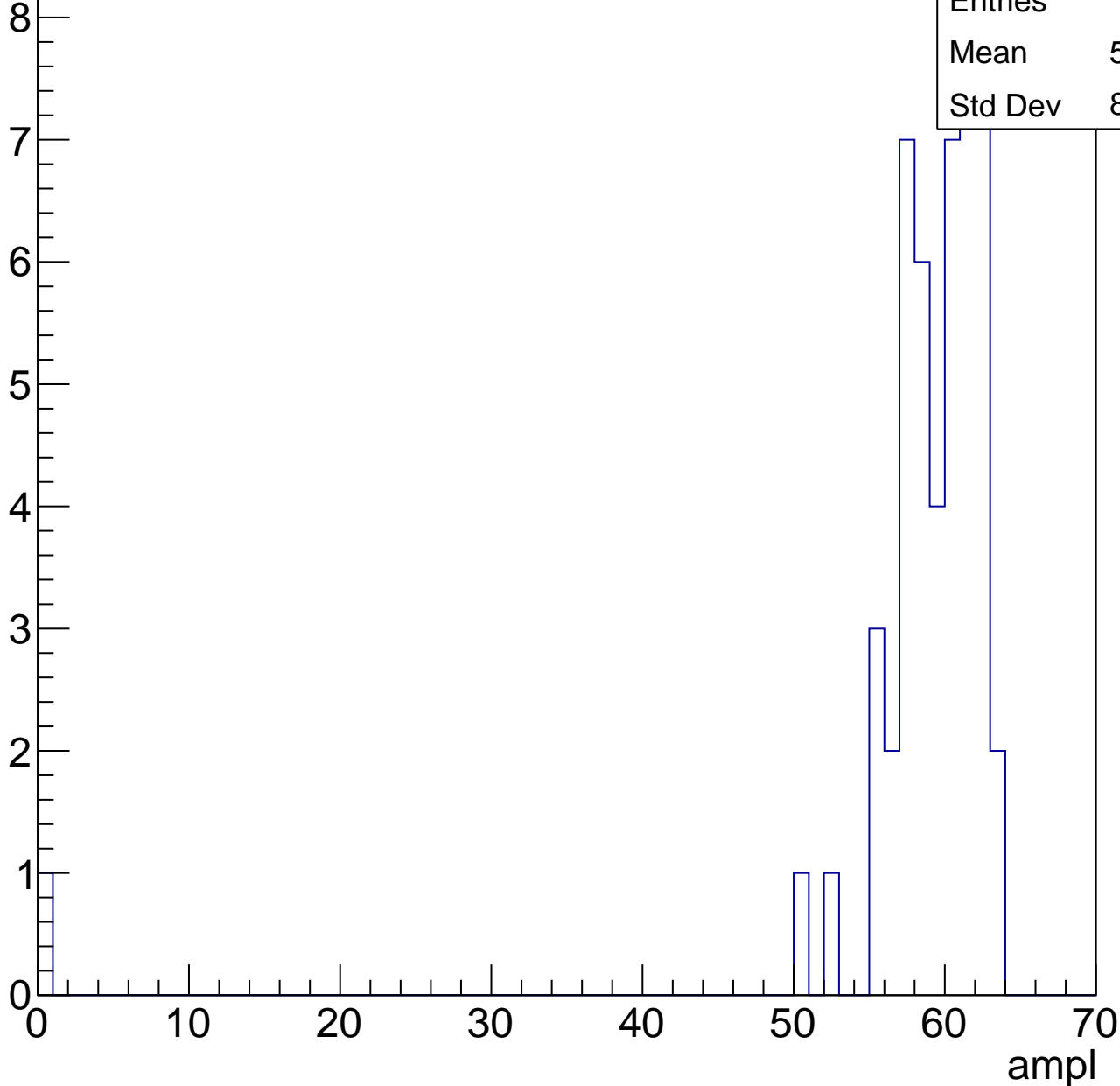


B1L102S, U18-ch42, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.84
Std Dev	8.703

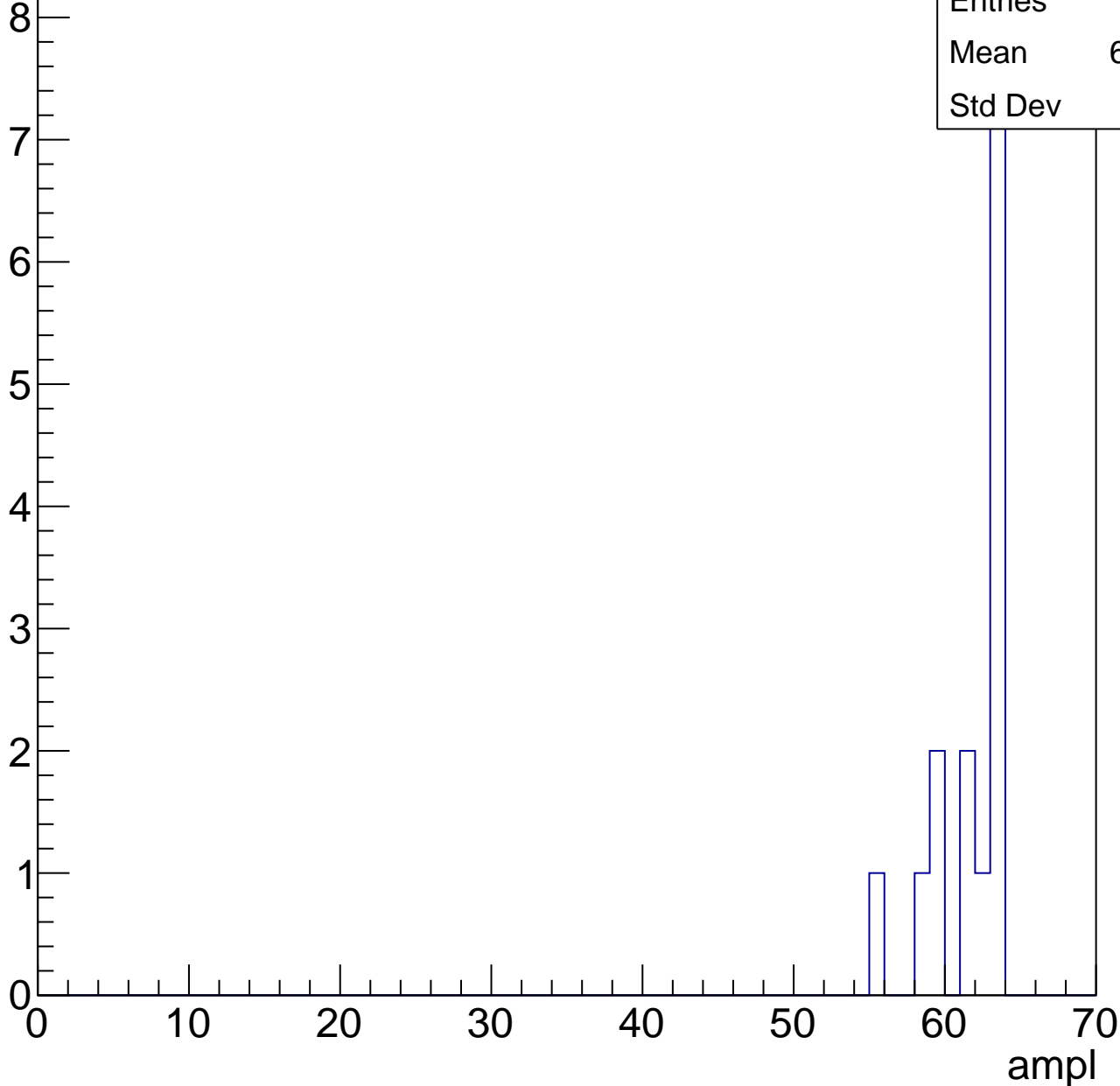


B1L102S, U18-ch42, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.27
Std Dev	2.38



B1L102S, U18-ch42, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch43, adc0

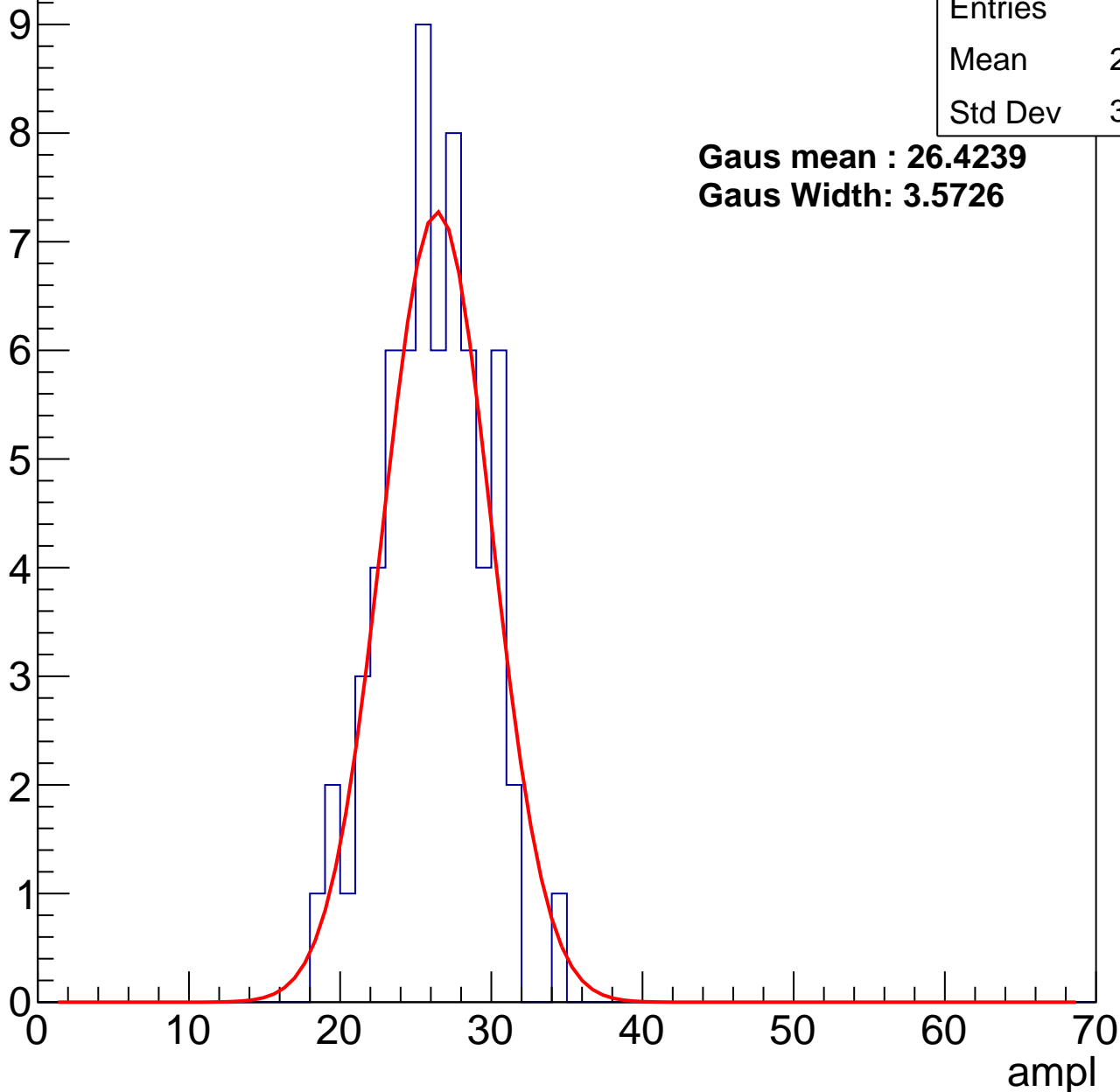
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	25.63
Std Dev	3.266

Gaus mean : 26.4239

Gaus Width: 3.5726



B1L102S, U18-ch43, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	33.46
Std Dev	3.229

Gaus mean : 33.6548

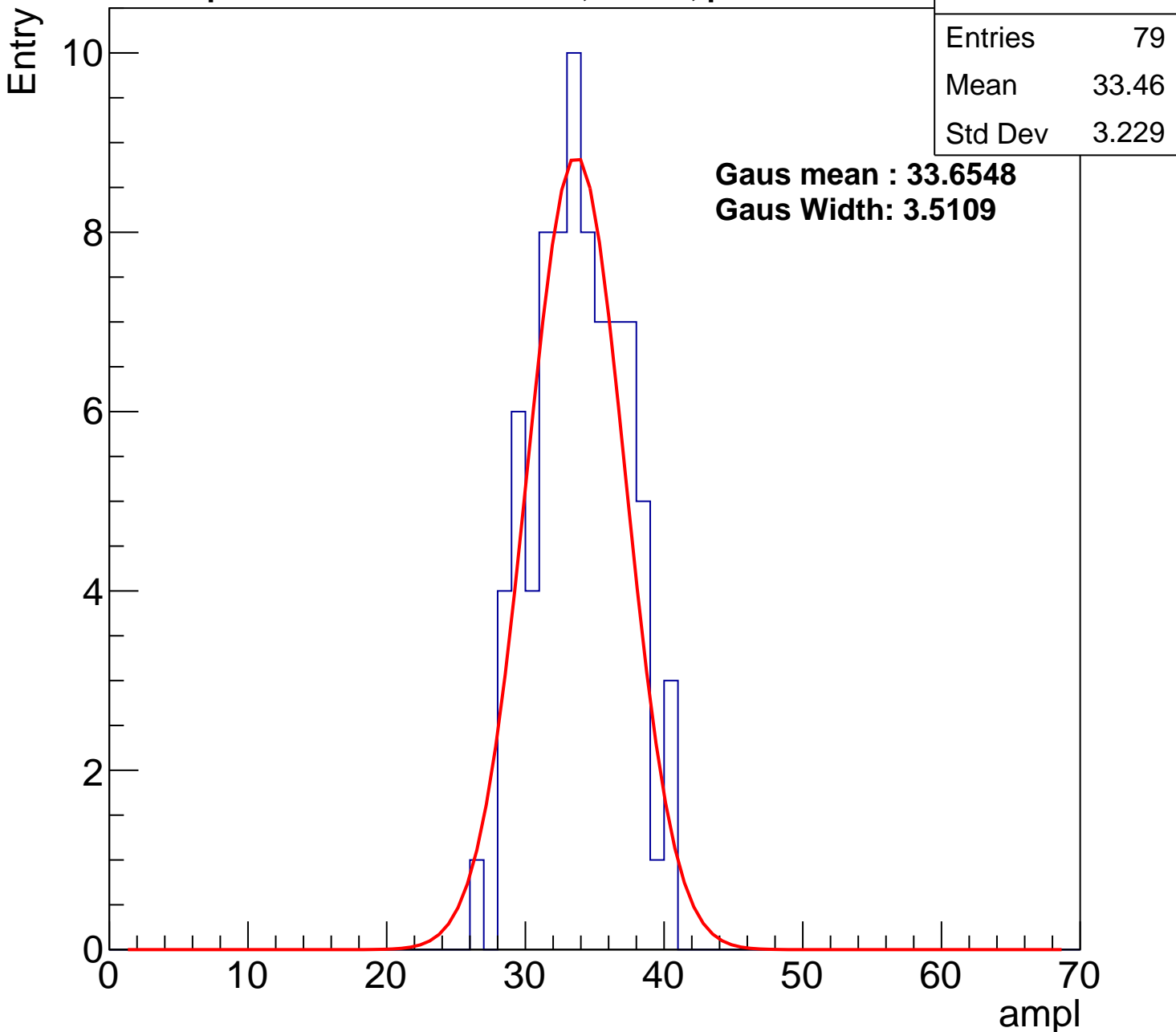
Gaus Width: 3.5109

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch43, adc2

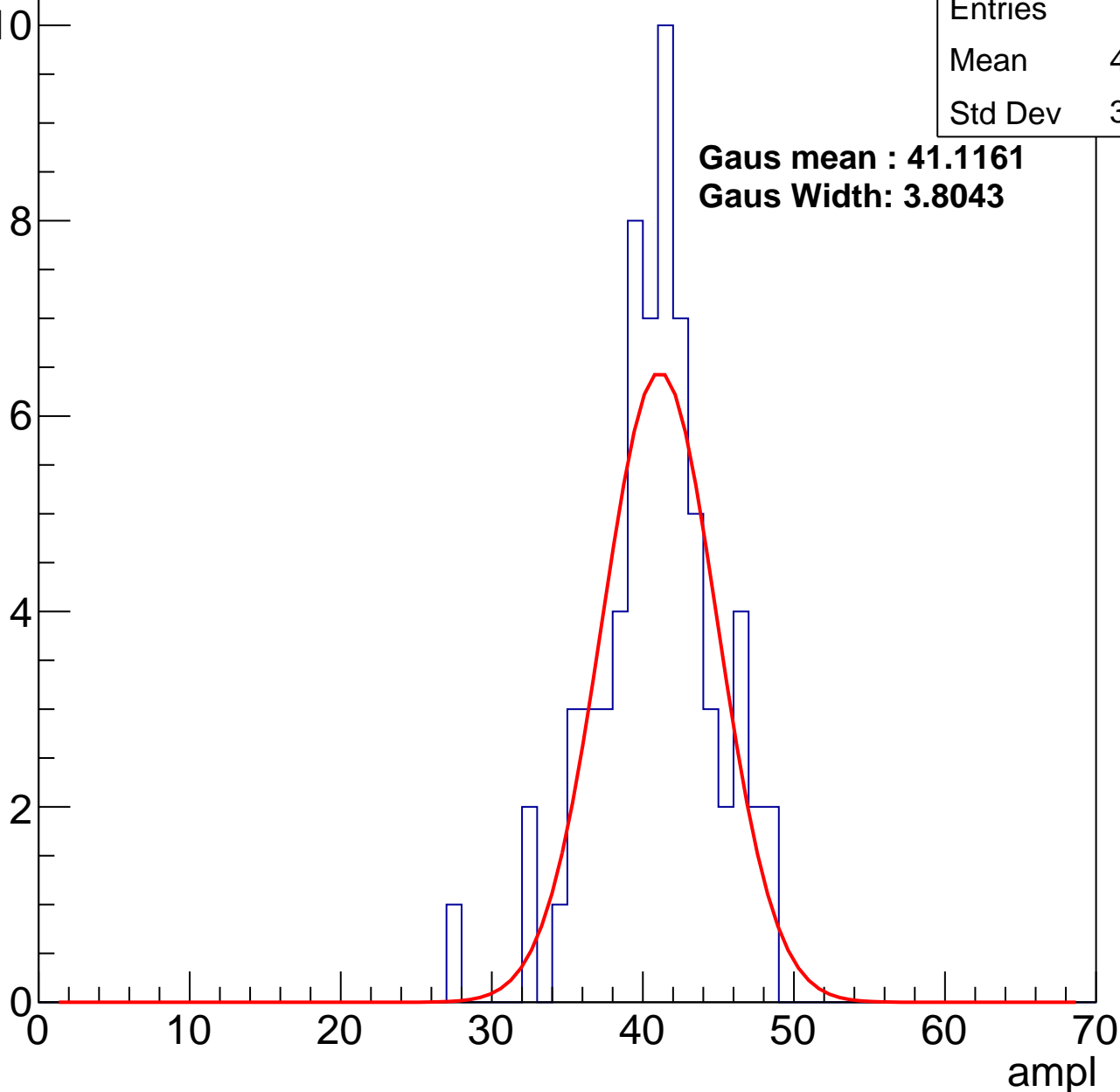
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	40.42
Std Dev	3.952

Gaus mean : 41.1161

Gaus Width: 3.8043

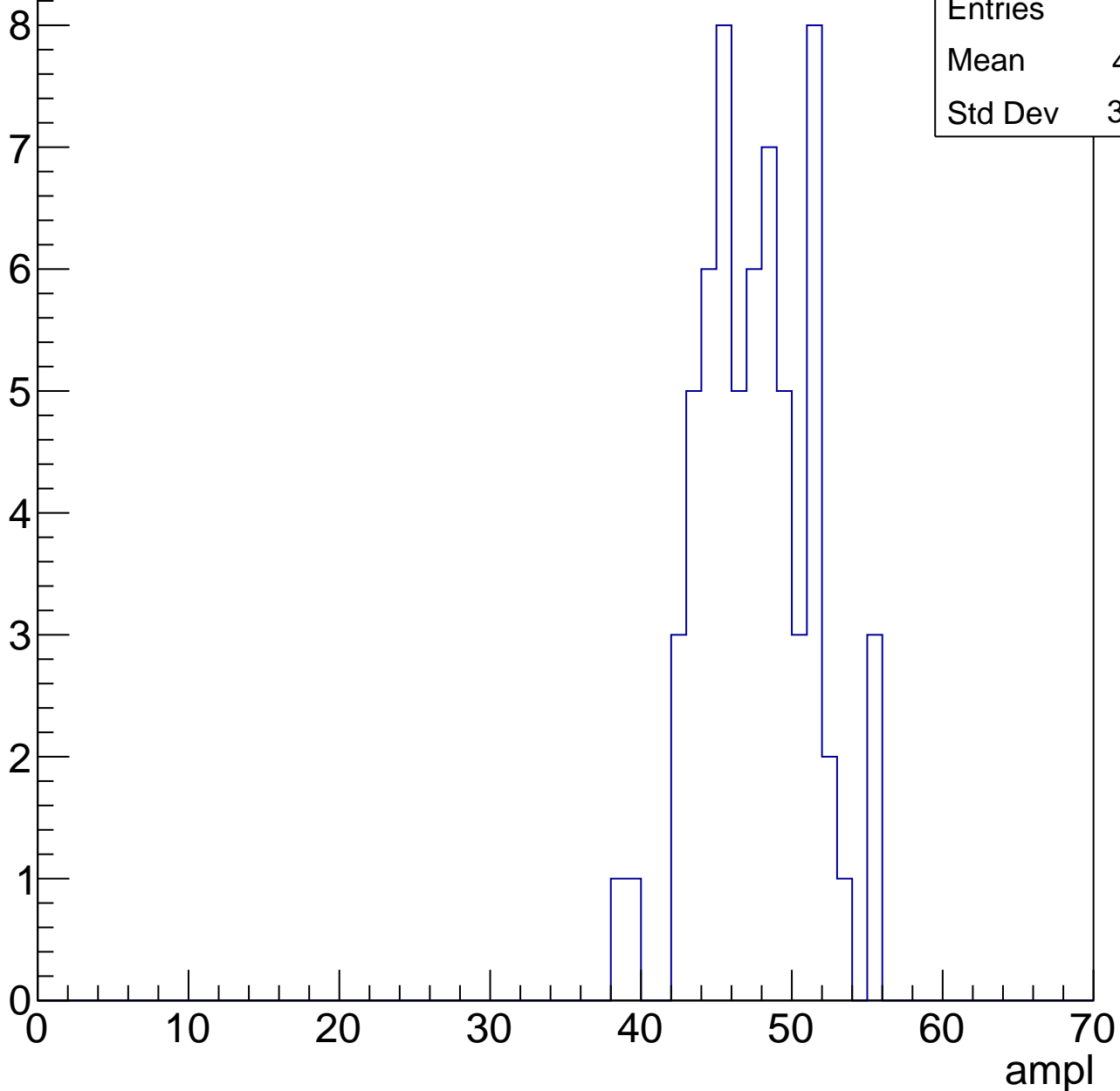


B1L102S, U18-ch43, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	47.11
Std Dev	3.645

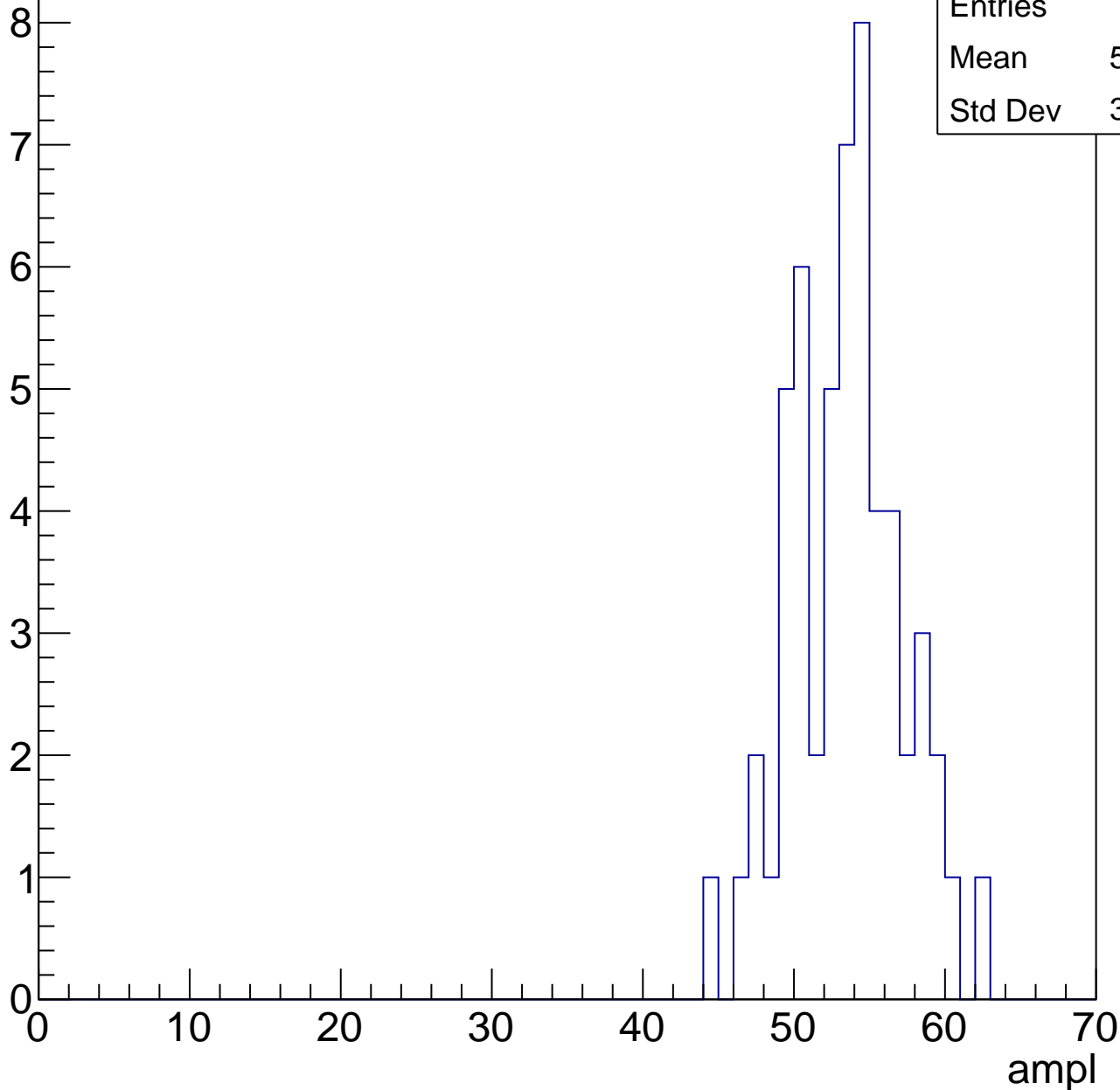


B1L102S, U18-ch43, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	52.98
Std Dev	3.675

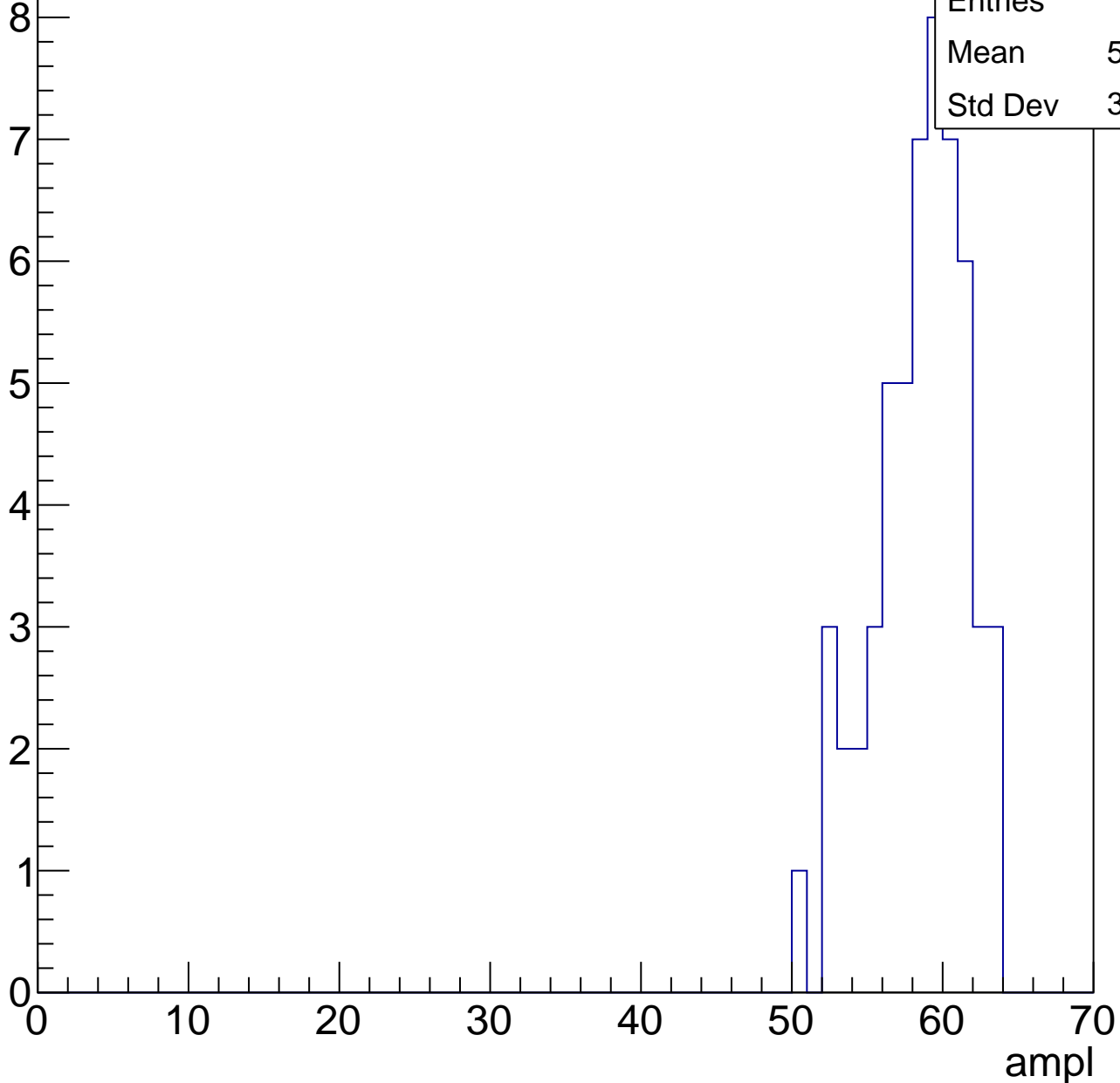


B1L102S, U18-ch43, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	57.98
Std Dev	3.072

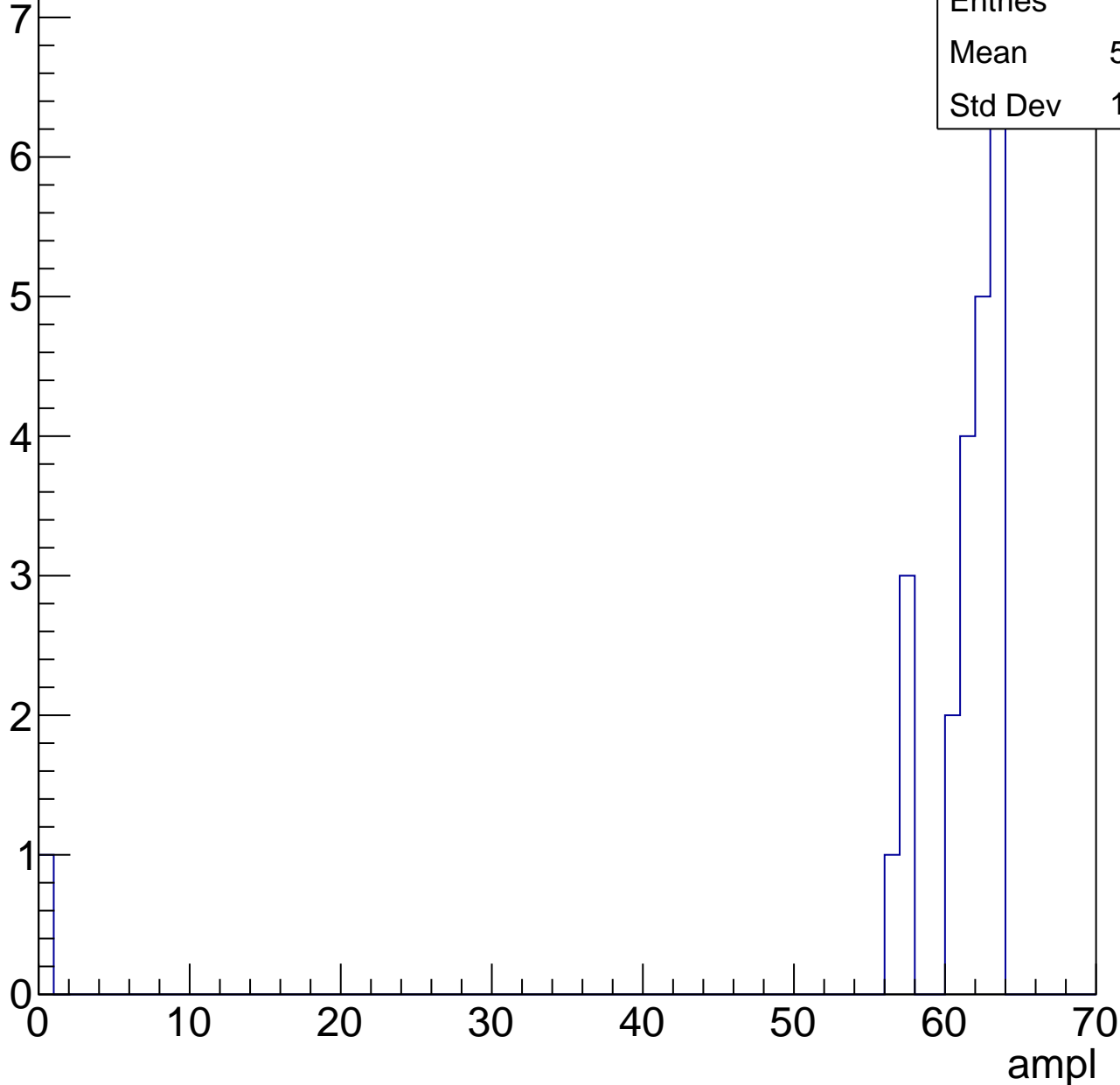


B1L102S, U18-ch43, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	23
Mean	58.35
Std Dev	12.63



B1L102S, U18-ch43, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch44, adc0

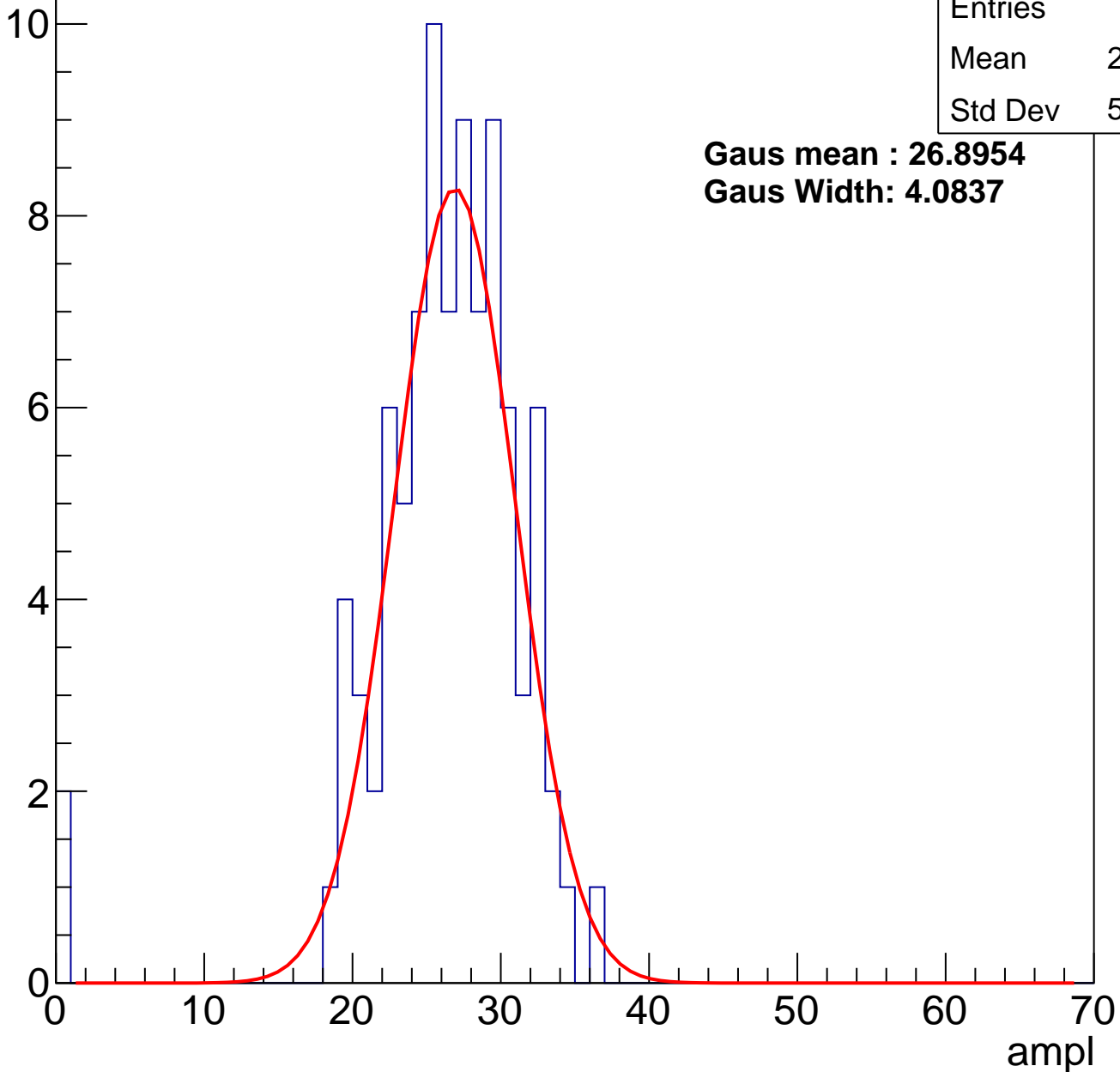
calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	25.76
Std Dev	5.458

Gaus mean : 26.8954

Gaus Width: 4.0837

Entry



B1L102S, U18-ch44, adc1

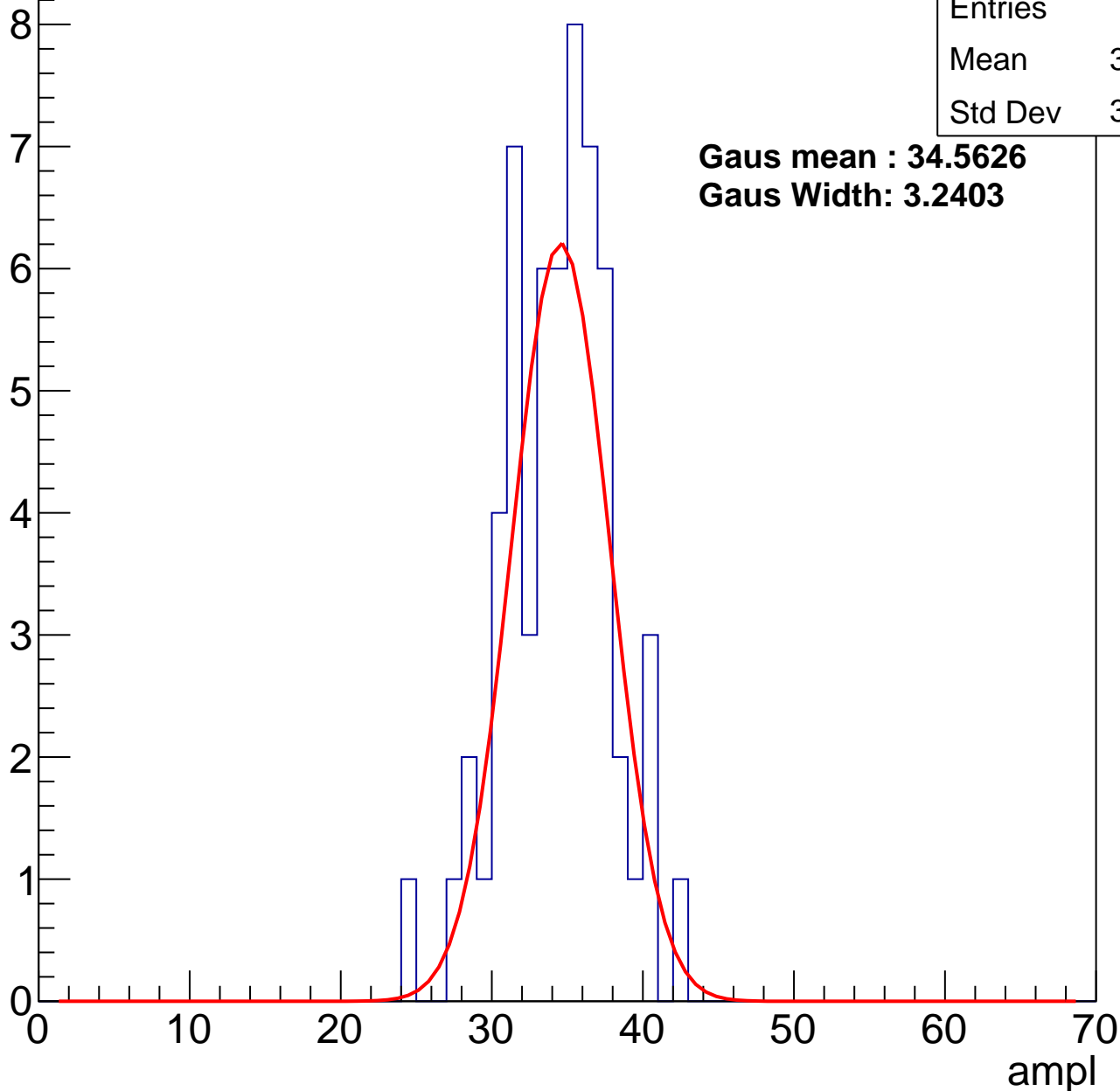
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	33.93
Std Dev	3.468

Gaus mean : 34.5626

Gaus Width: 3.2403



B1L102S, U18-ch44, adc2

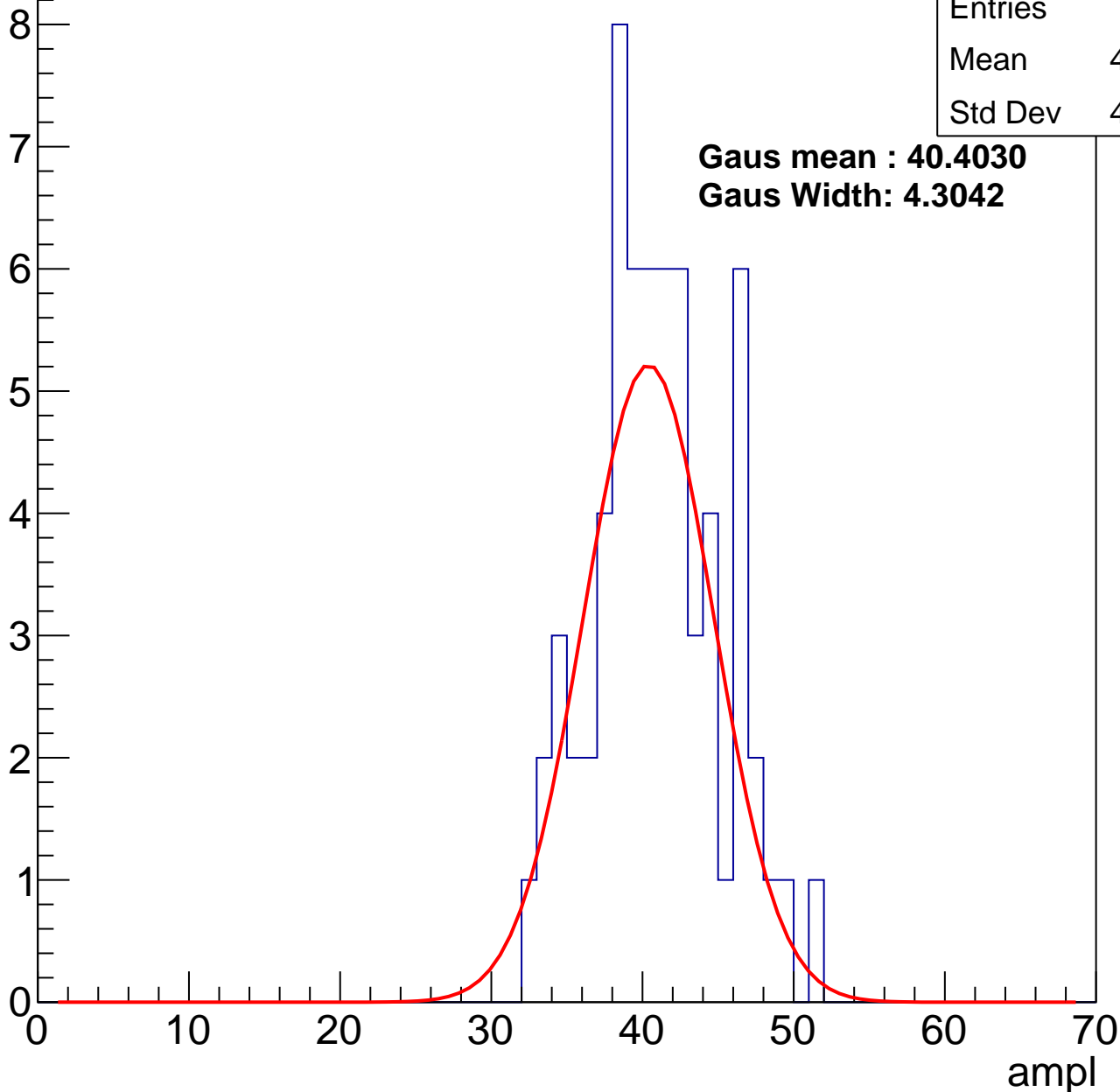
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.52
Std Dev	4.177

Gaus mean : 40.4030

Gaus Width: 4.3042

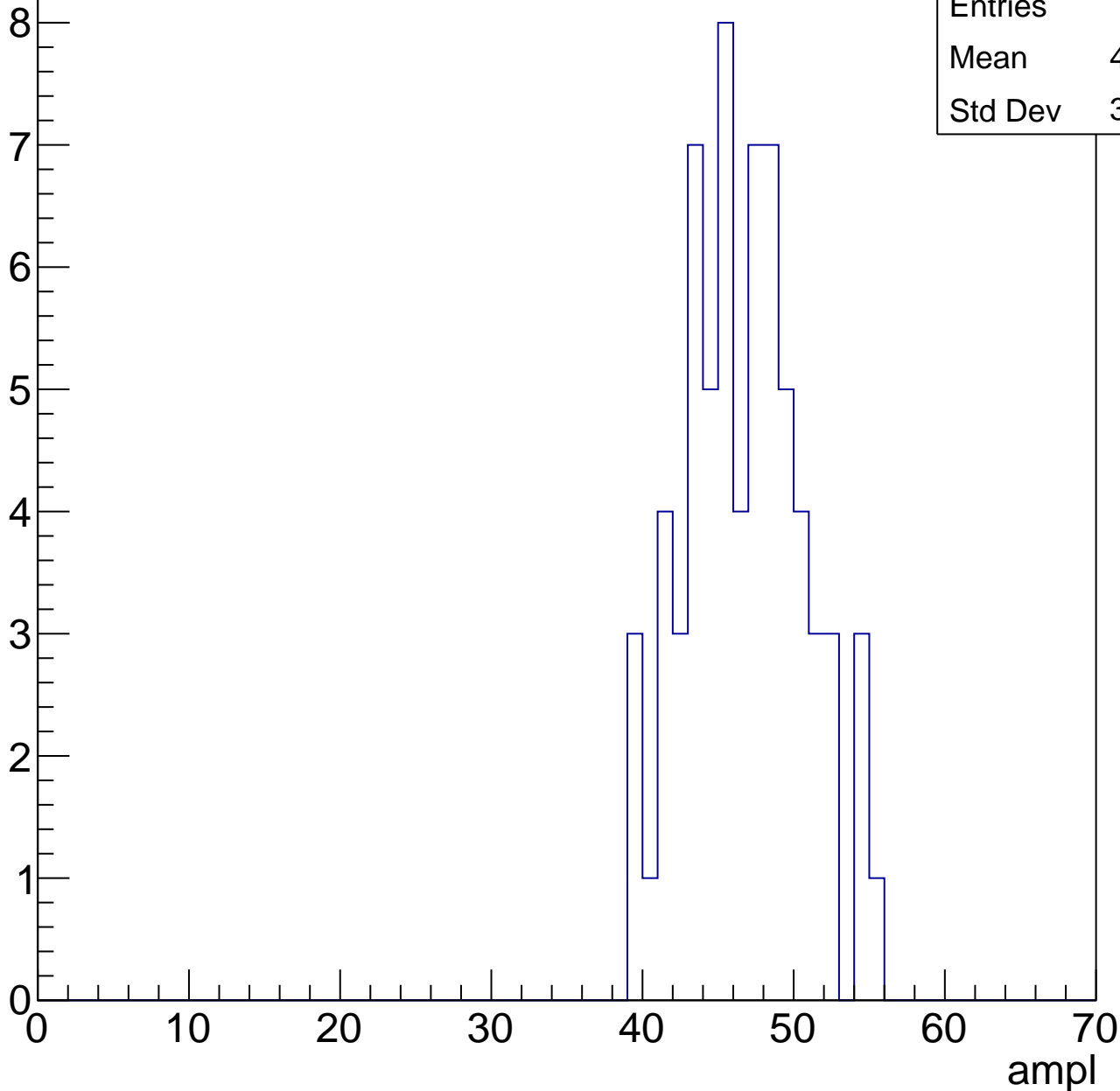


B1L102S, U18-ch44, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	46.29
Std Dev	3.866

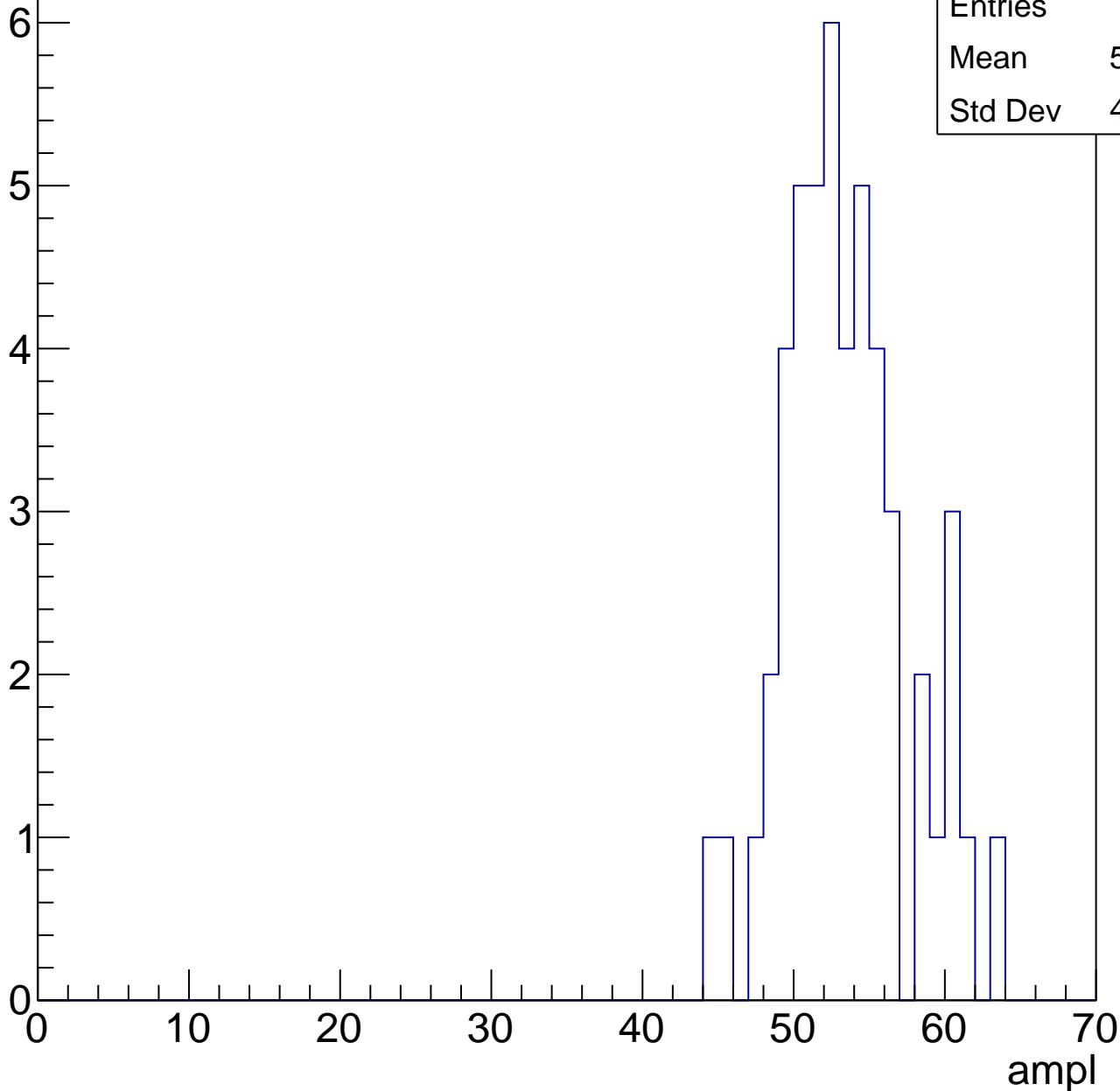


B1L102S, U18-ch44, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	52.94
Std Dev	4.068

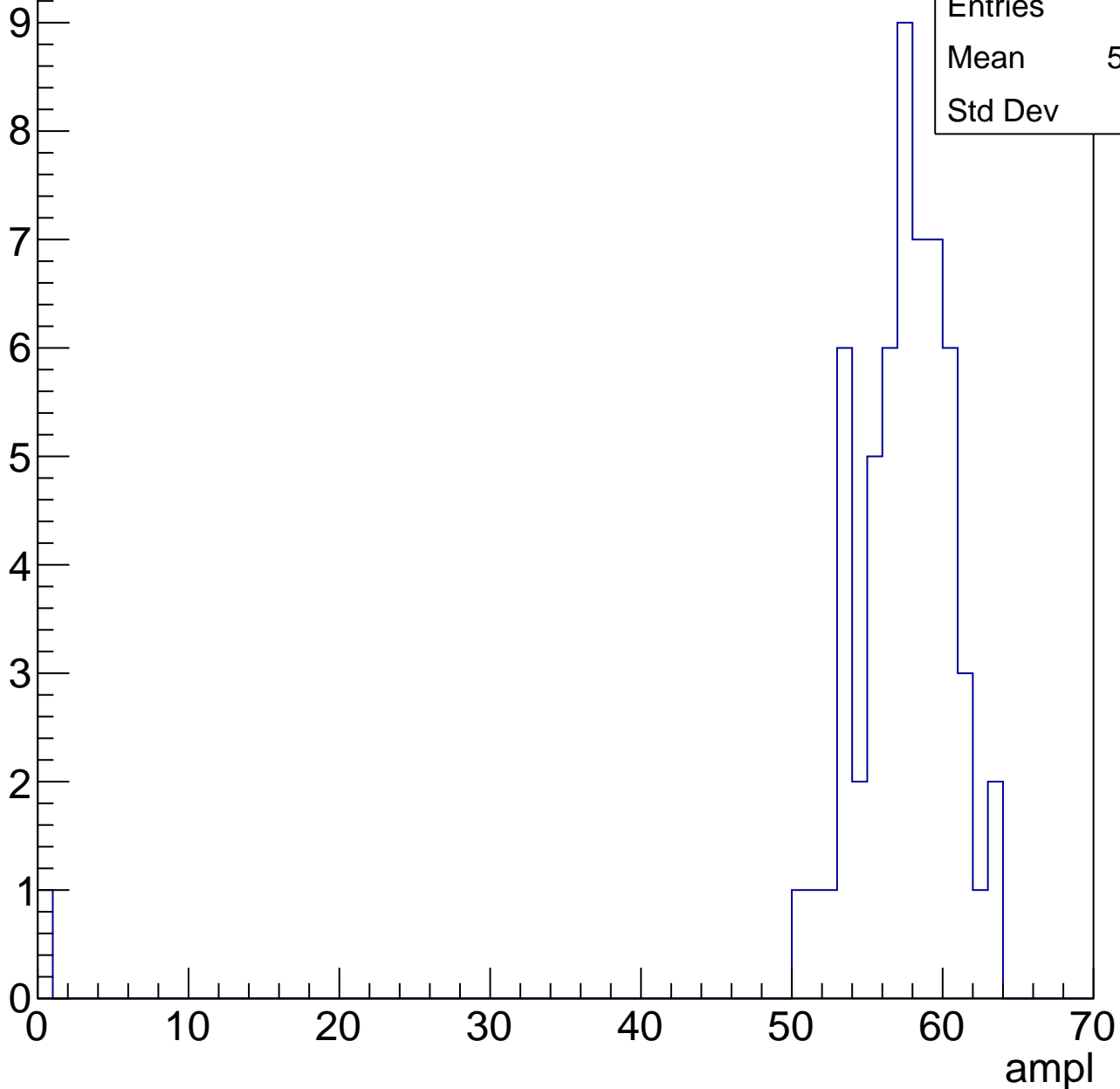


B1L102S, U18-ch44, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	56.09
Std Dev	7.97

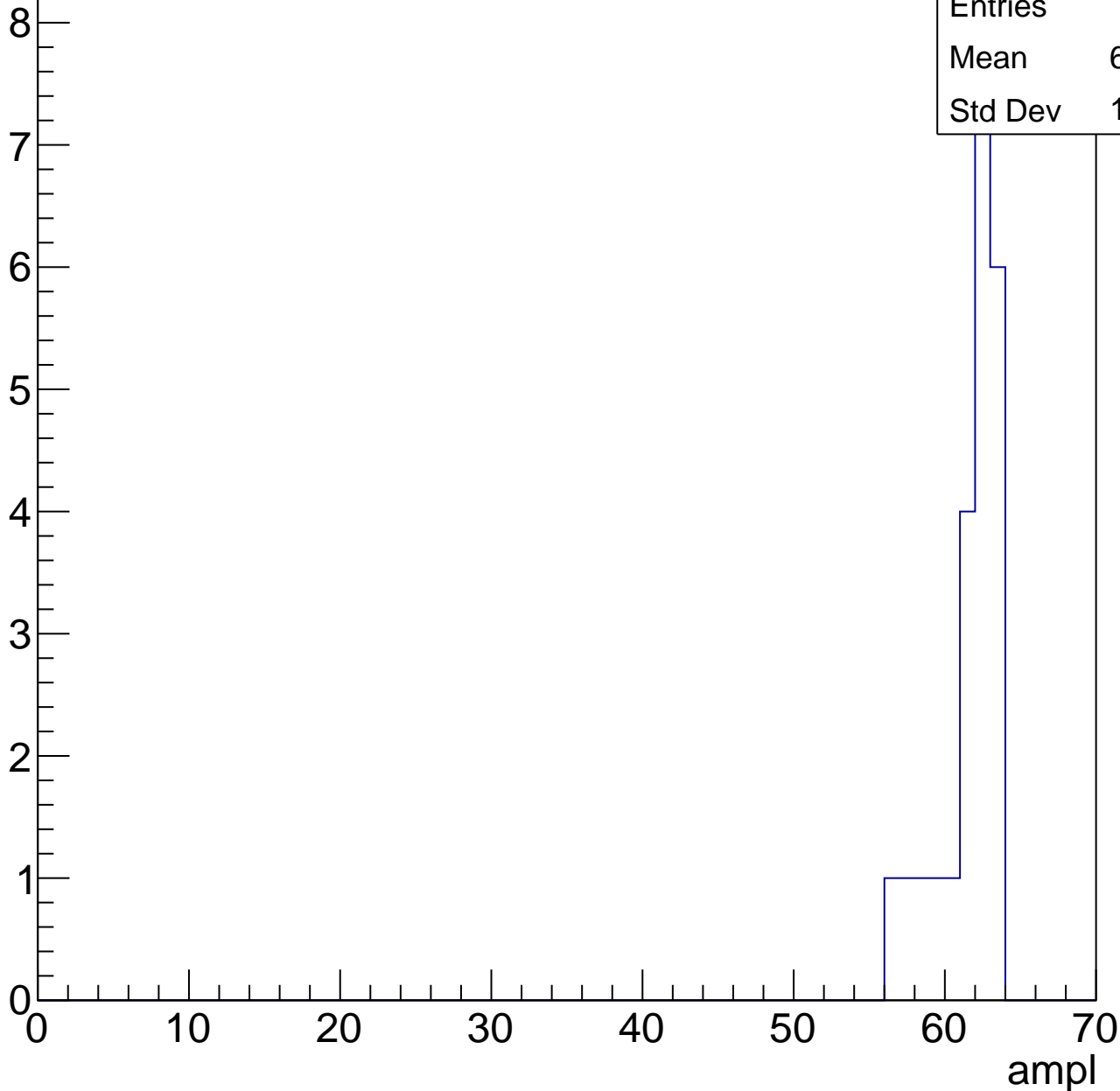


B1L102S, U18-ch44, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

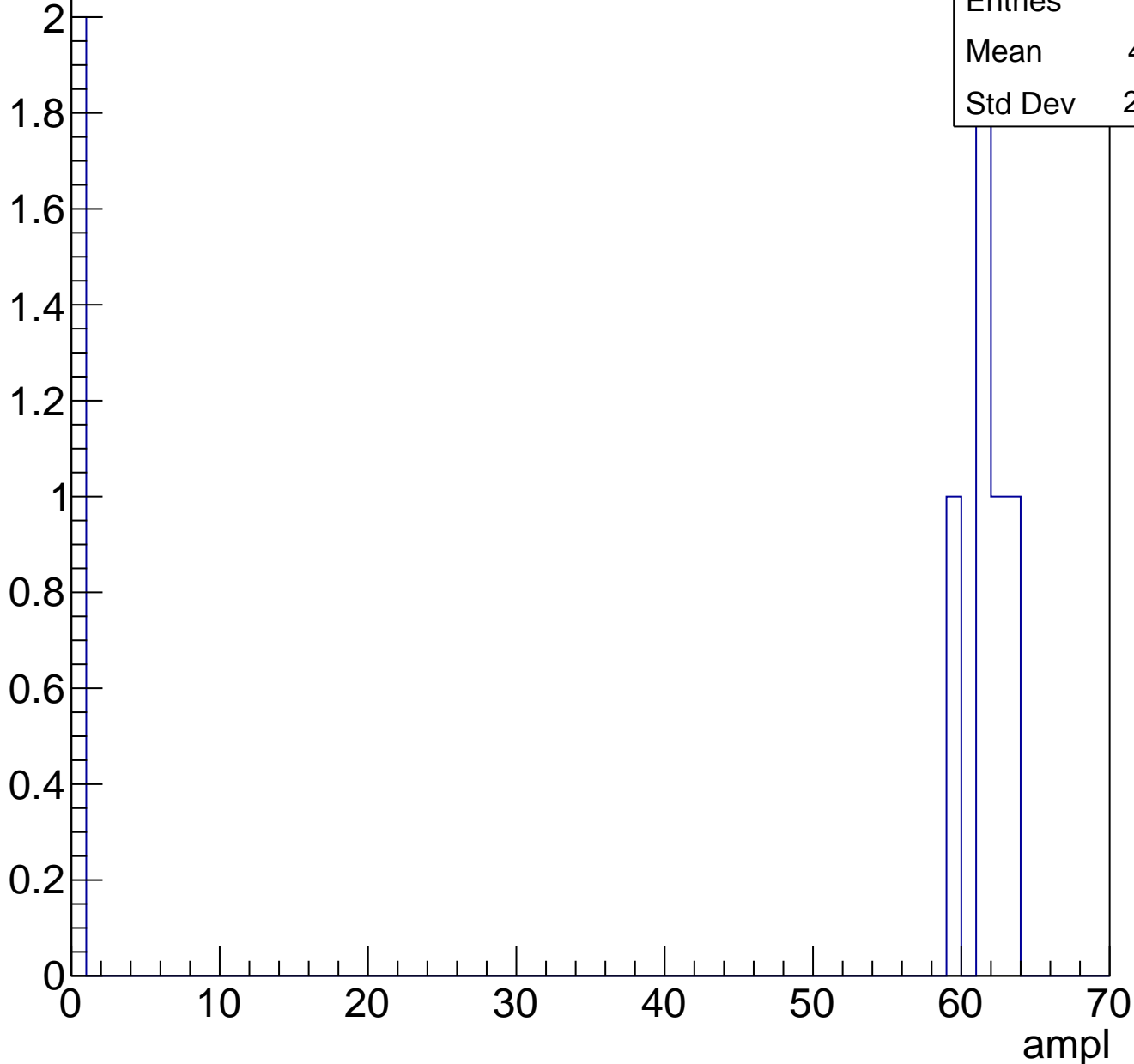
Entries	23
Mean	61.22
Std Dev	1.933



B1L102S, U18-ch44, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch45, adc0

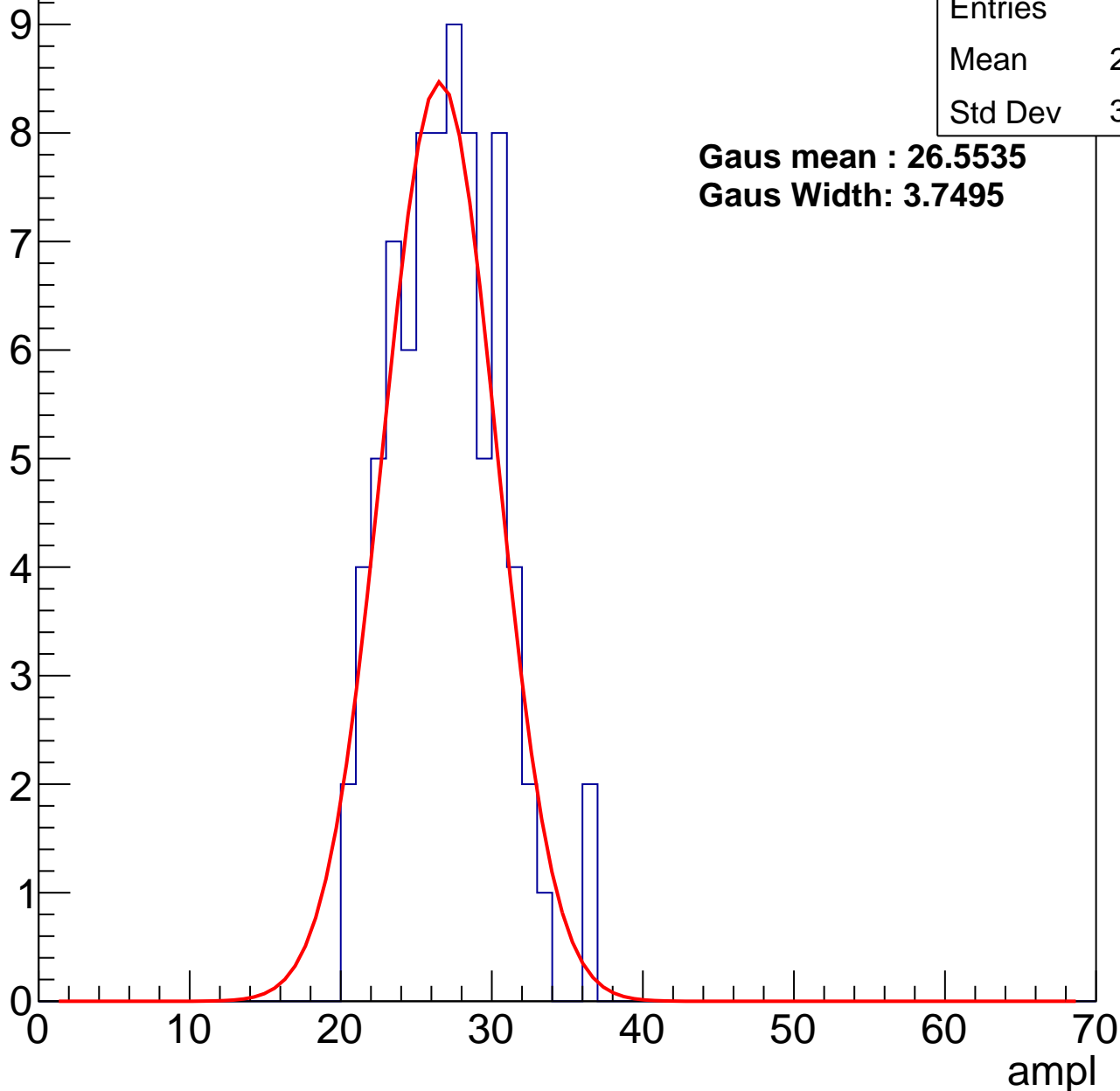
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	26.48
Std Dev	3.493

Gaus mean : 26.5535

Gaus Width: 3.7495



B1L102S, U18-ch45, adc1

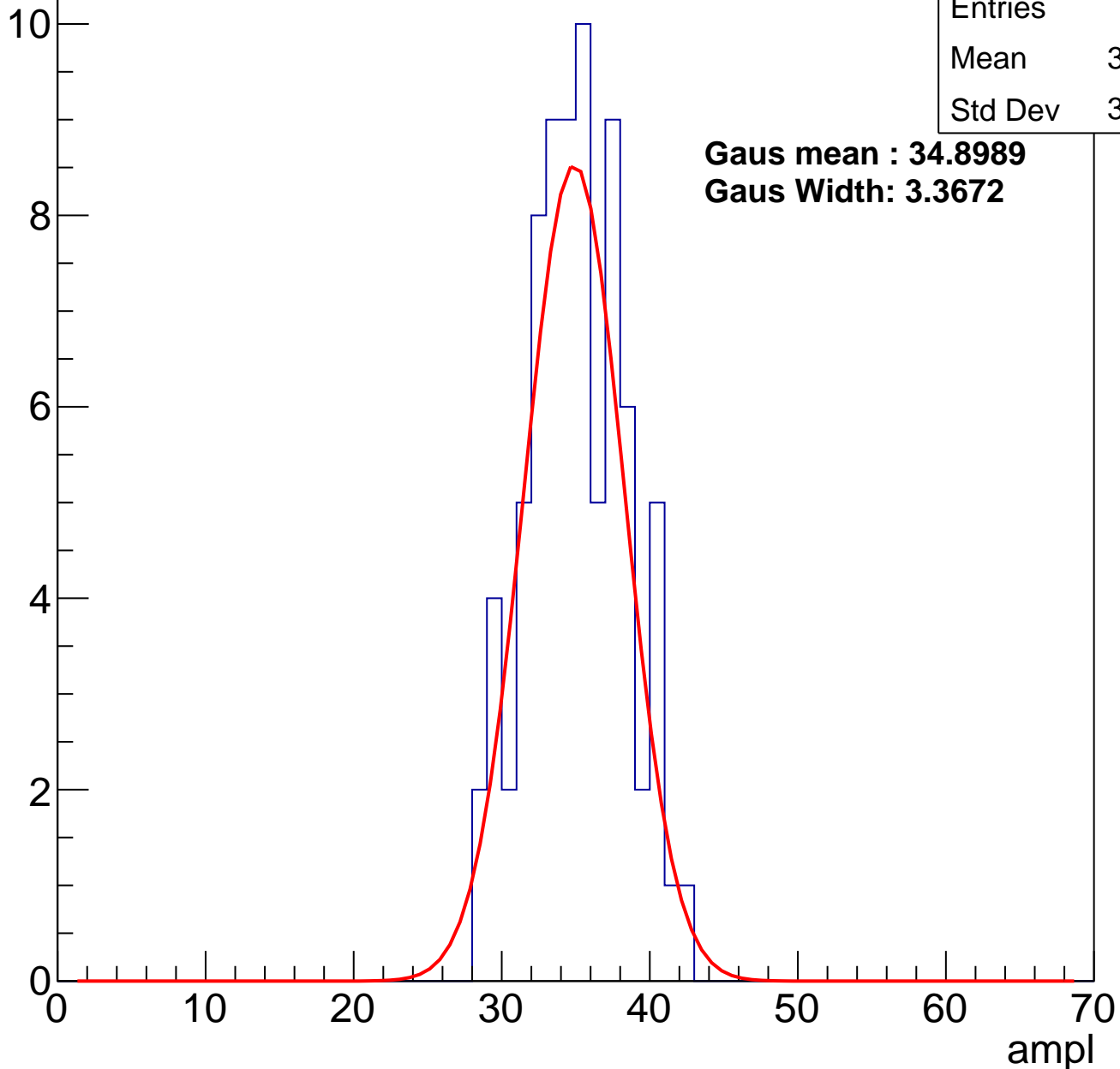
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	34.59
Std Dev	3.228

Gaus mean : 34.8989

Gaus Width: 3.3672

Entry



B1L102S, U18-ch45, adc2

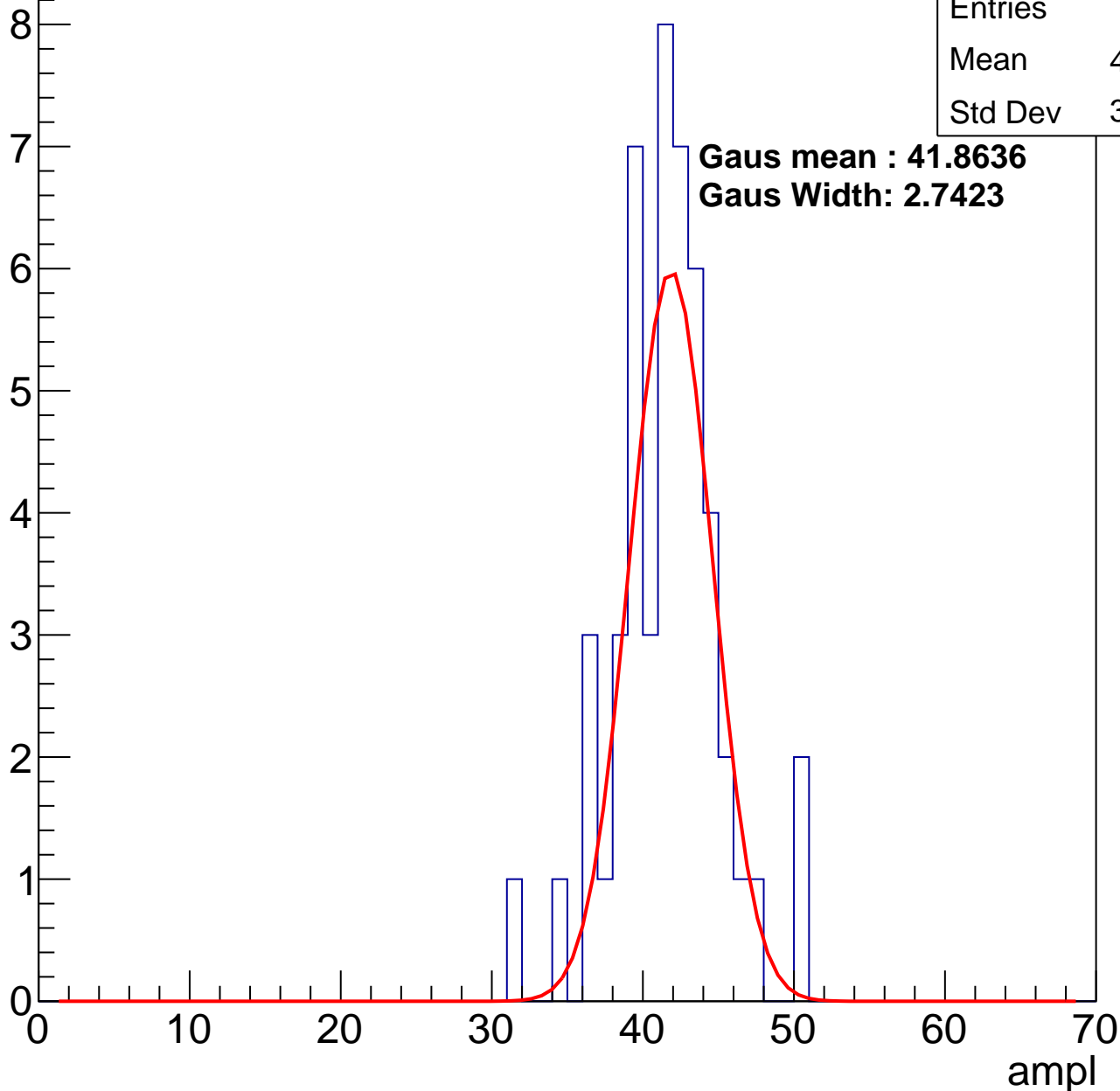
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	41.12
Std Dev	3.519

Gaus mean : 41.8636

Gaus Width: 2.7423

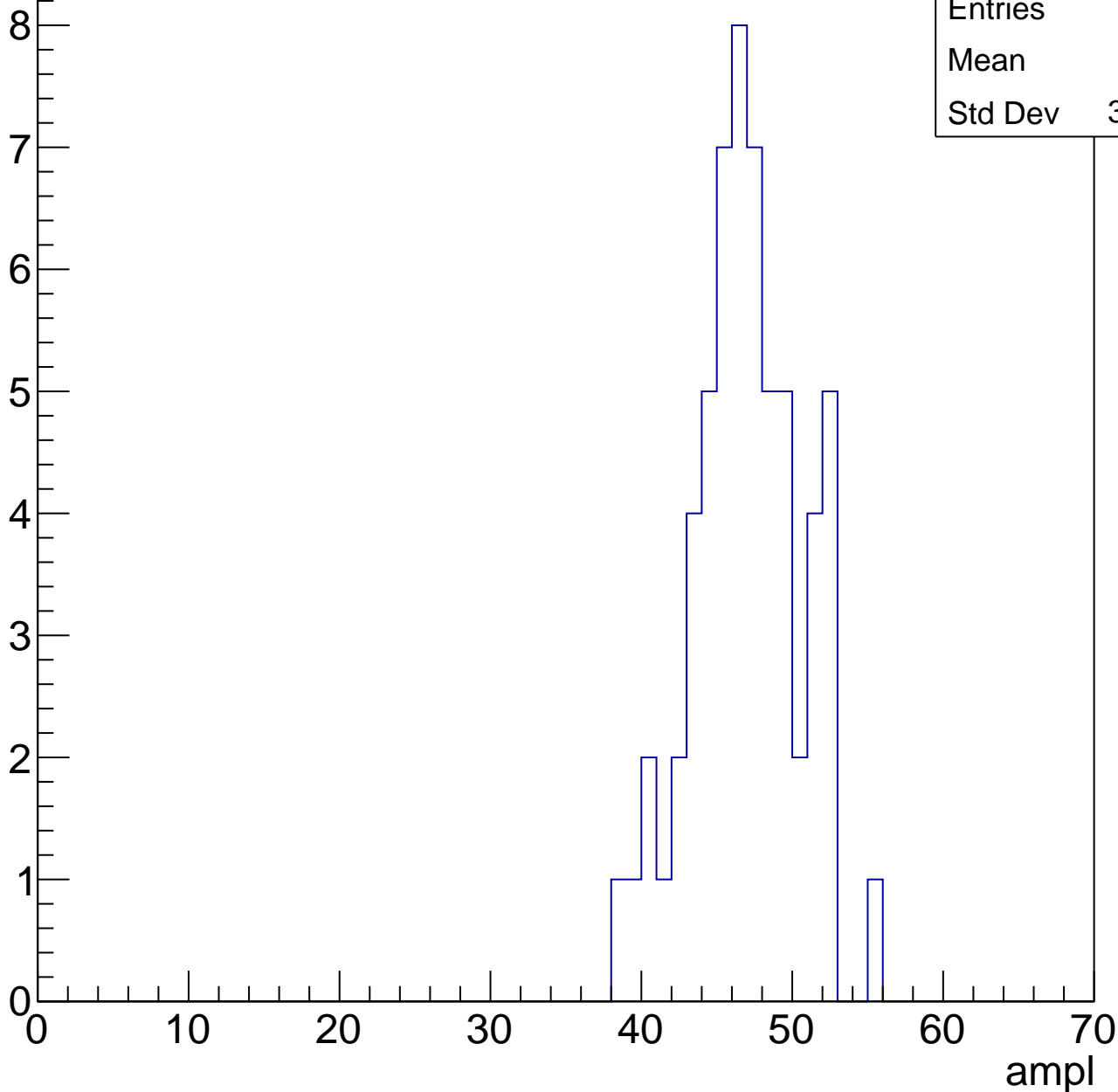


B1L102S, U18-ch45, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	46.5
Std Dev	3.547

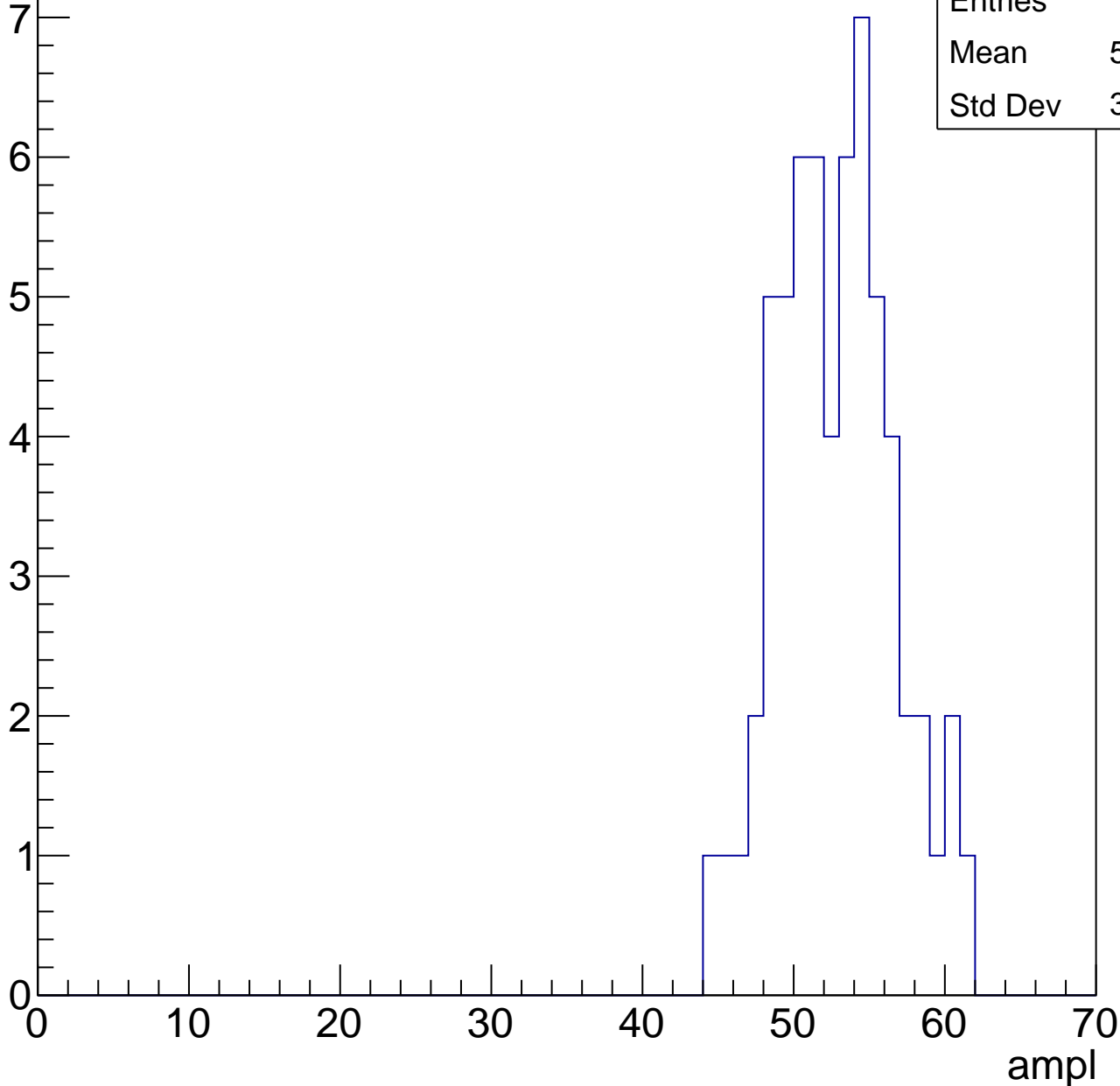


B1L102S, U18-ch45, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

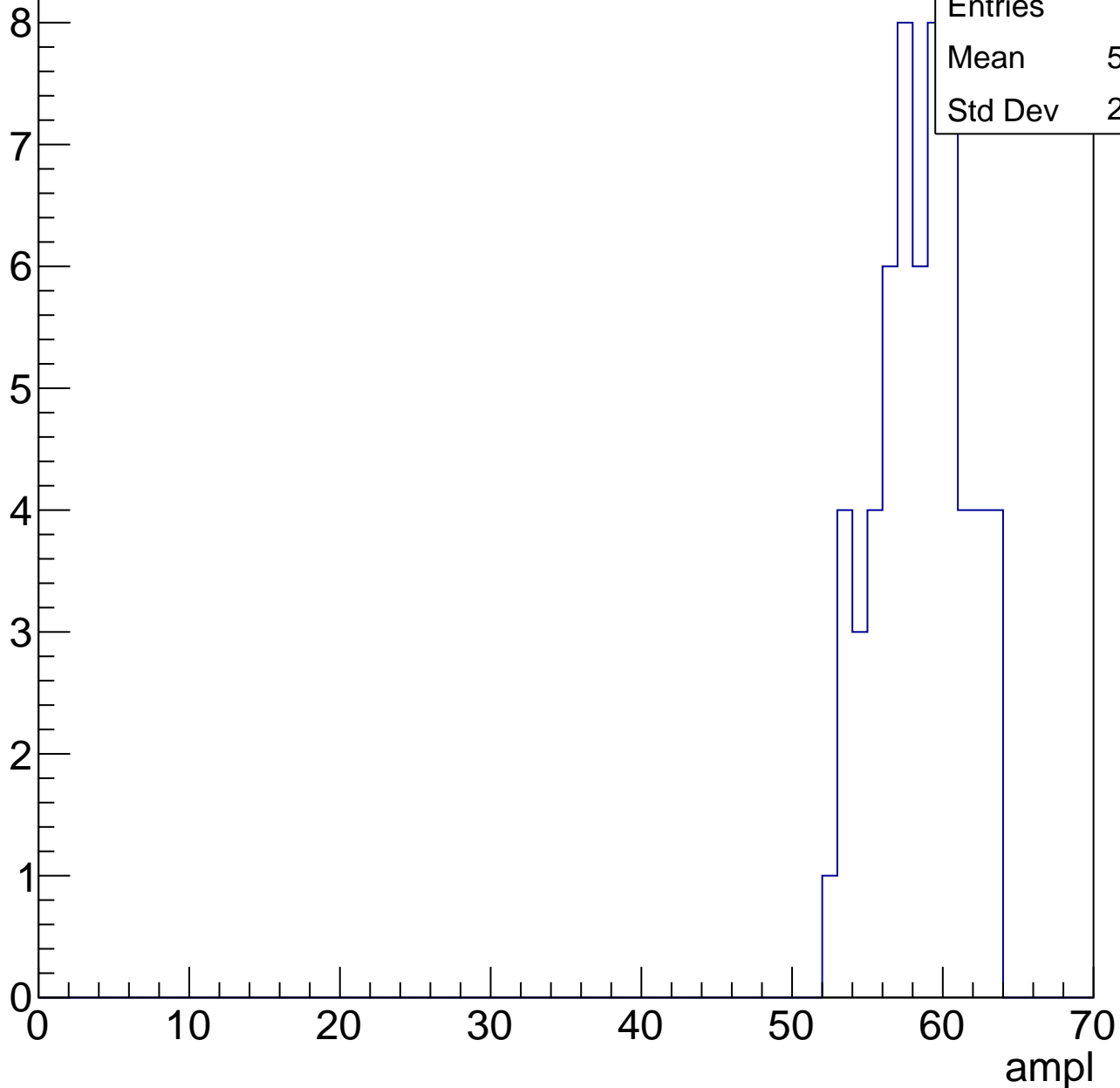
Entries	61
Mean	52.34
Std Dev	3.776



B1L102S, U18-ch45, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

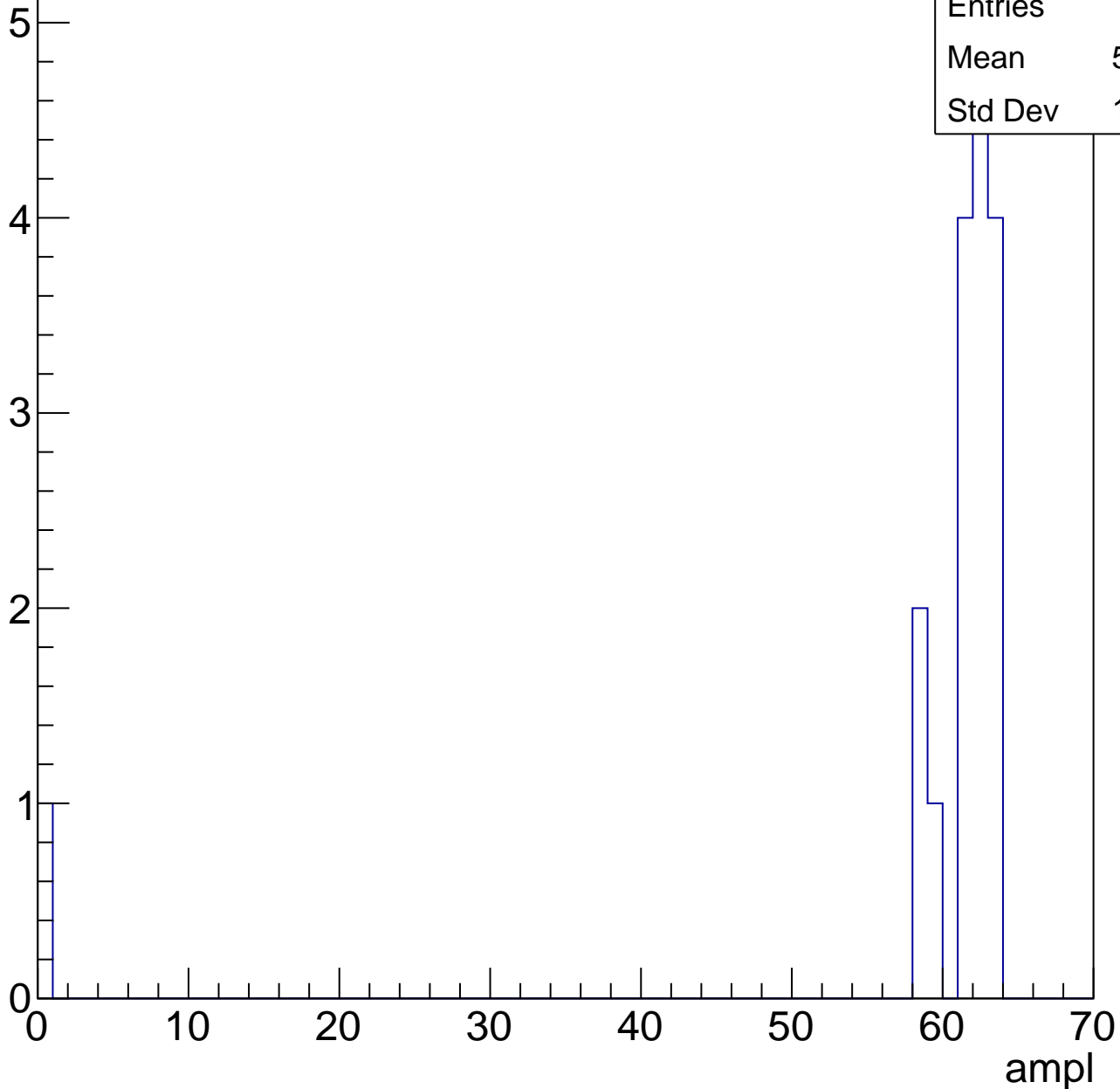


B1L102S, U18-ch45, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

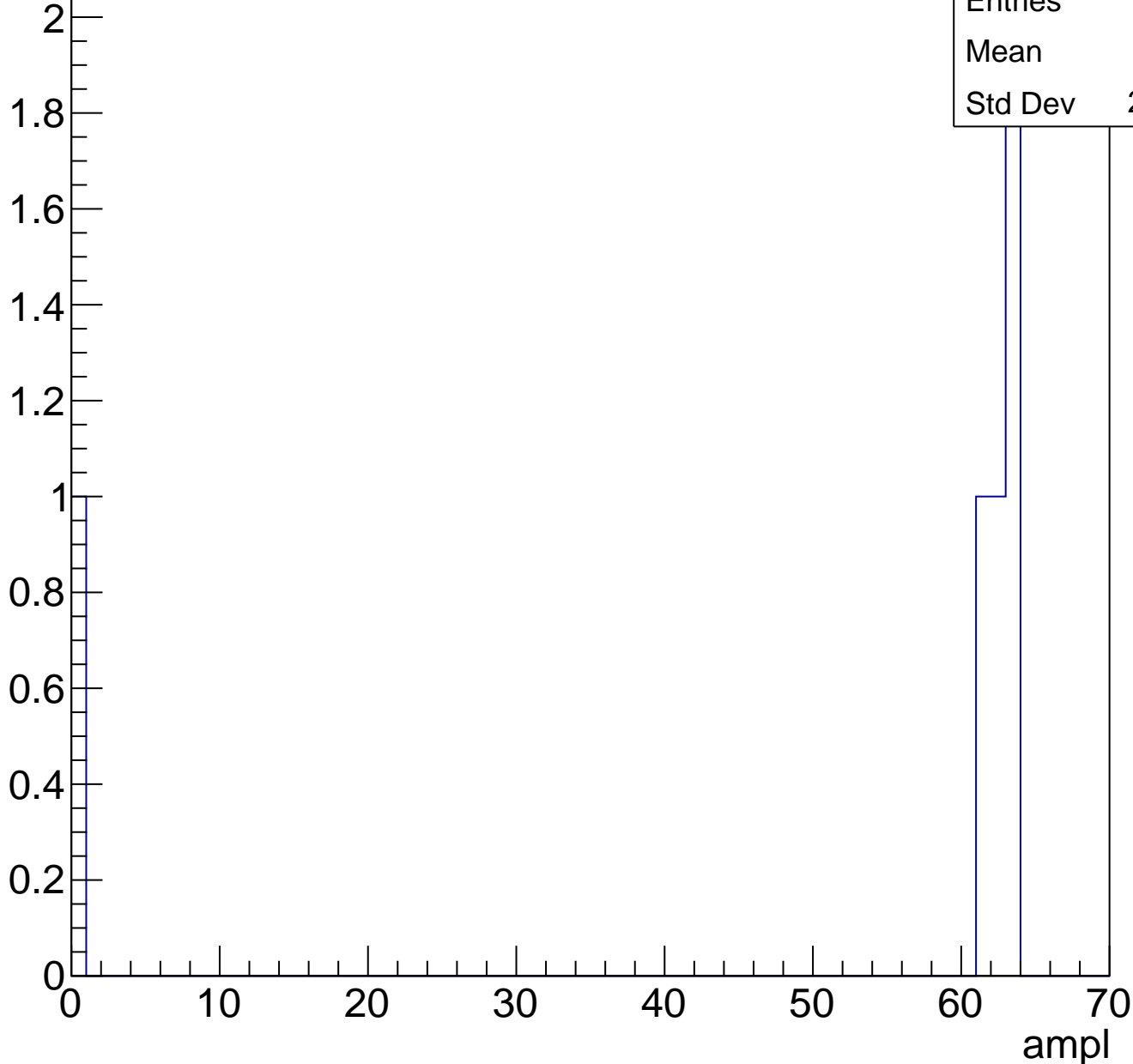
Entries	17
Mean	57.71
Std Dev	14.51



B1L102S, U18-ch45, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch46, adc0

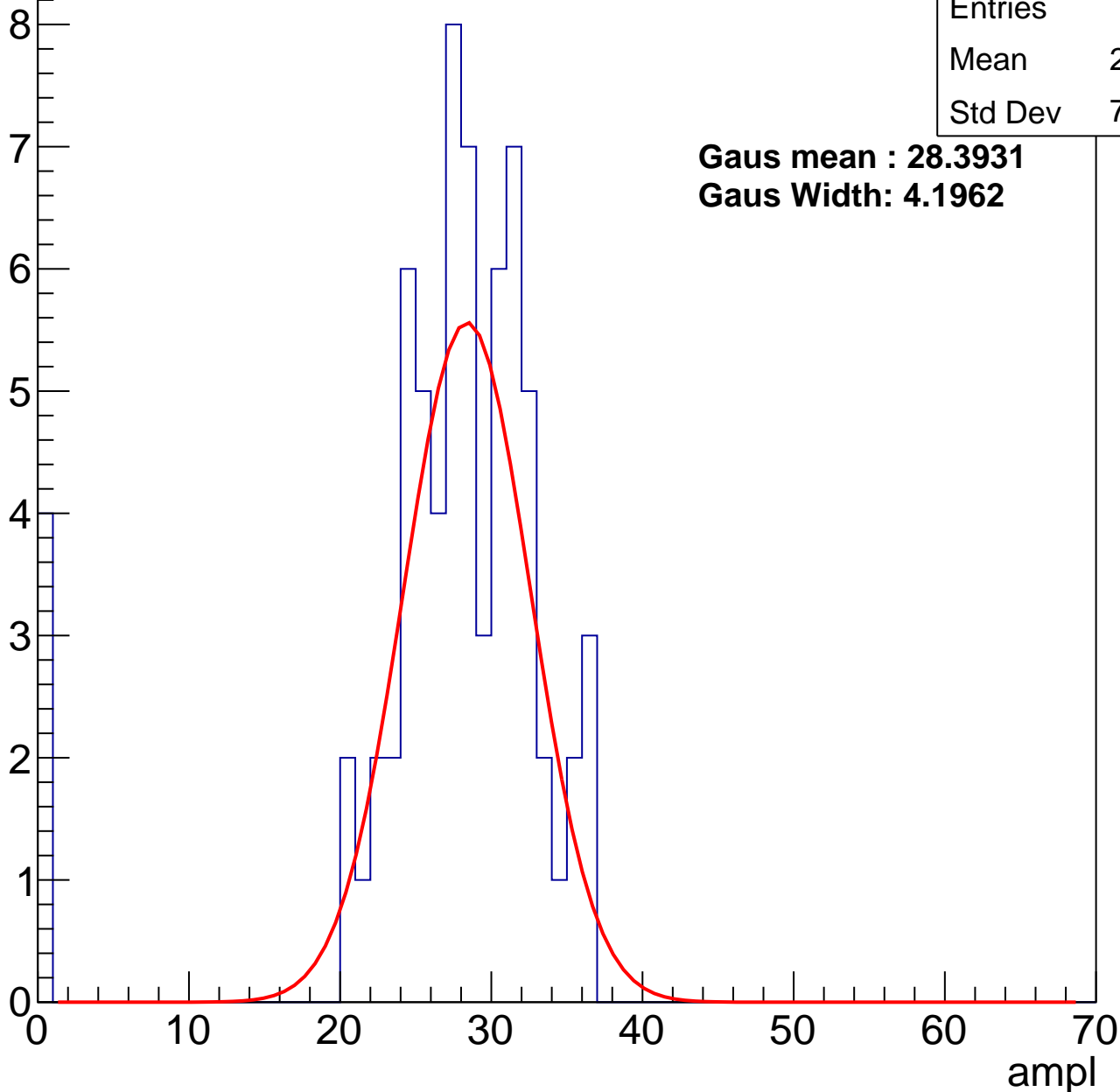
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	26.54
Std Dev	7.557

Gaus mean : 28.3931

Gaus Width: 4.1962



B1L102S, U18-ch46, adc1

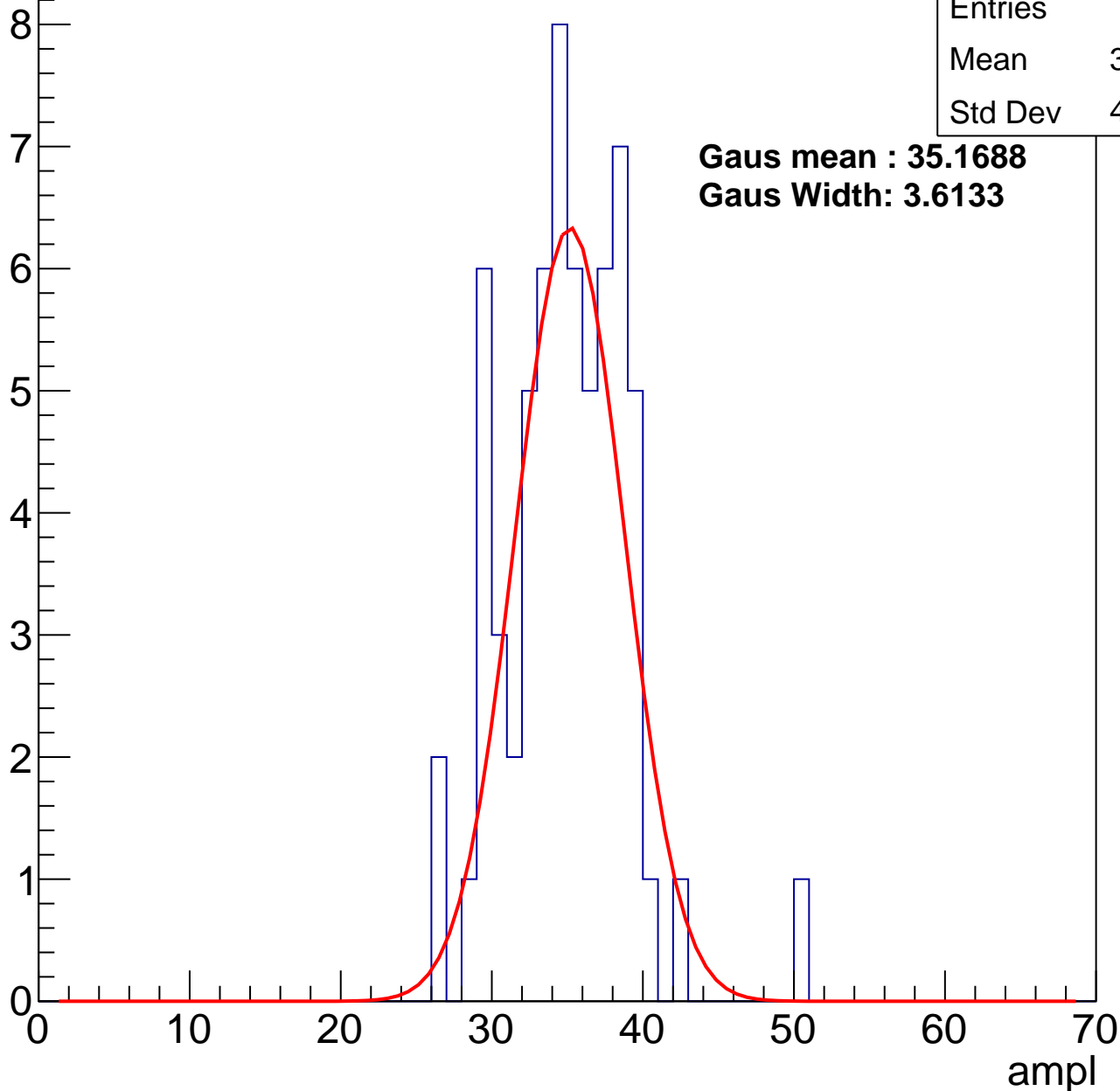
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.48
Std Dev	4.046

Gaus mean : 35.1688

Gaus Width: 3.6133



B1L102S, U18-ch46, adc2

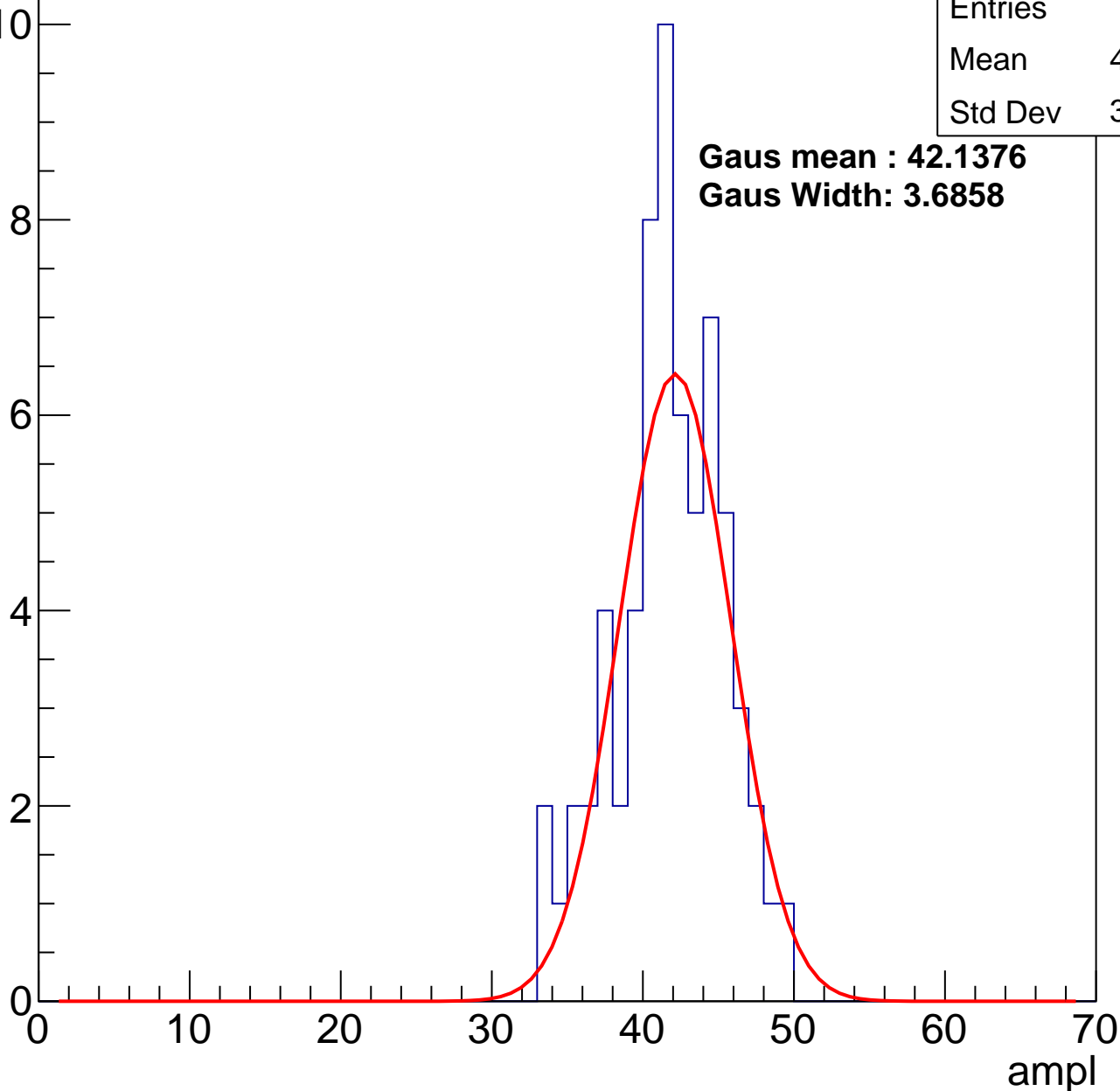
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	41.25
Std Dev	3.574

Gaus mean : 42.1376

Gaus Width: 3.6858

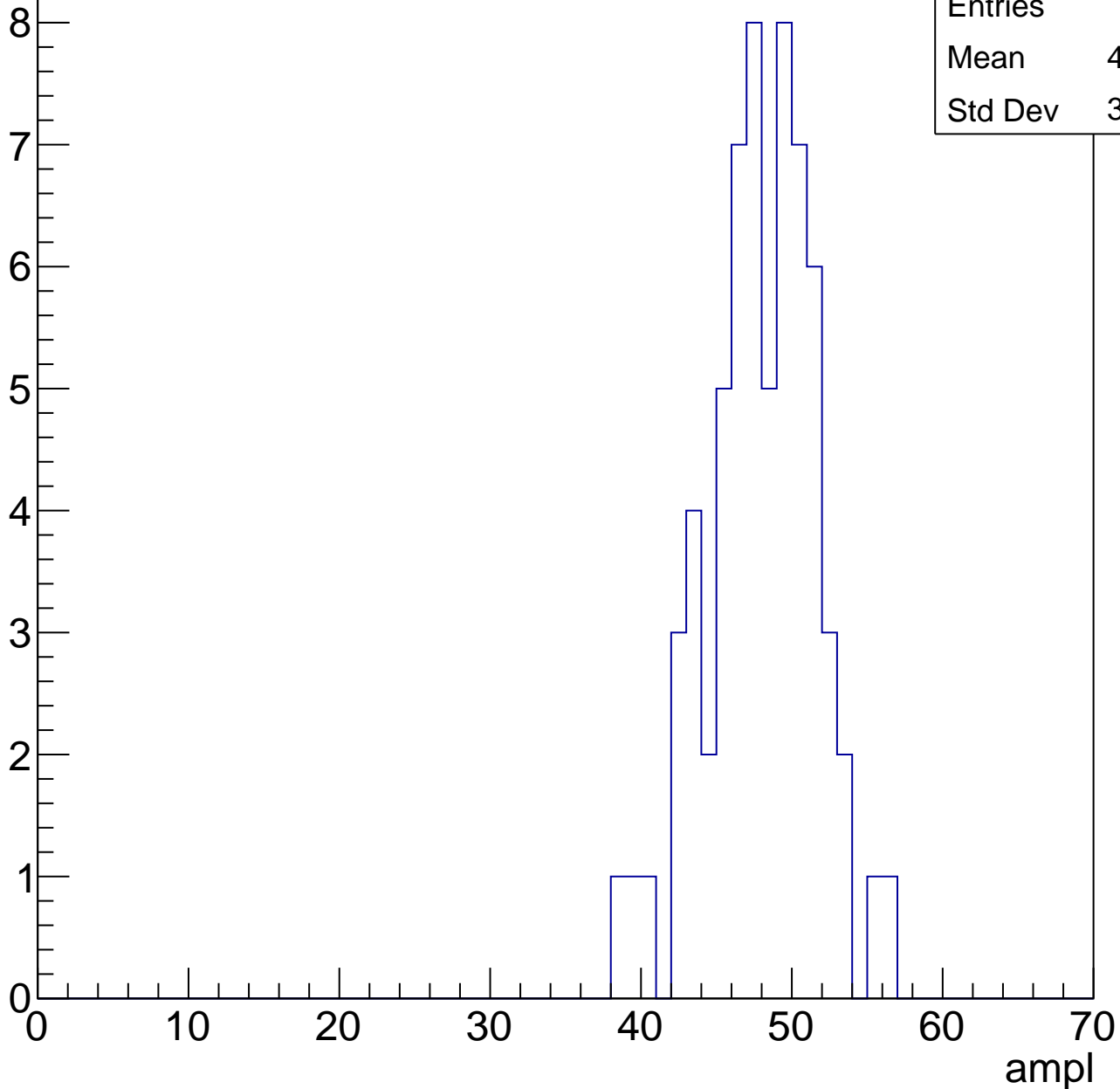


B1L102S, U18-ch46, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.49
Std Dev	3.617

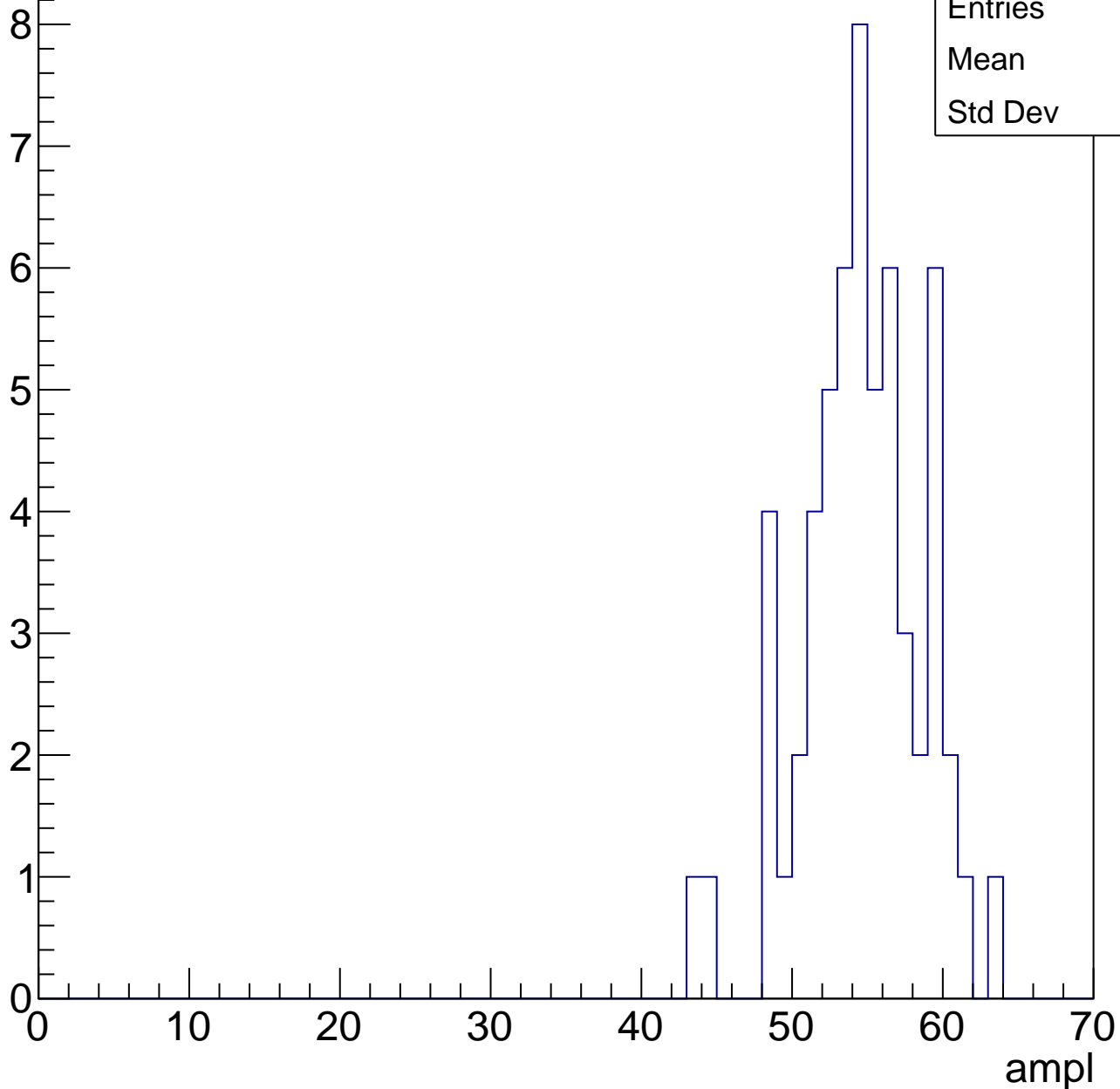


B1L102S, U18-ch46, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	54.1
Std Dev	3.99

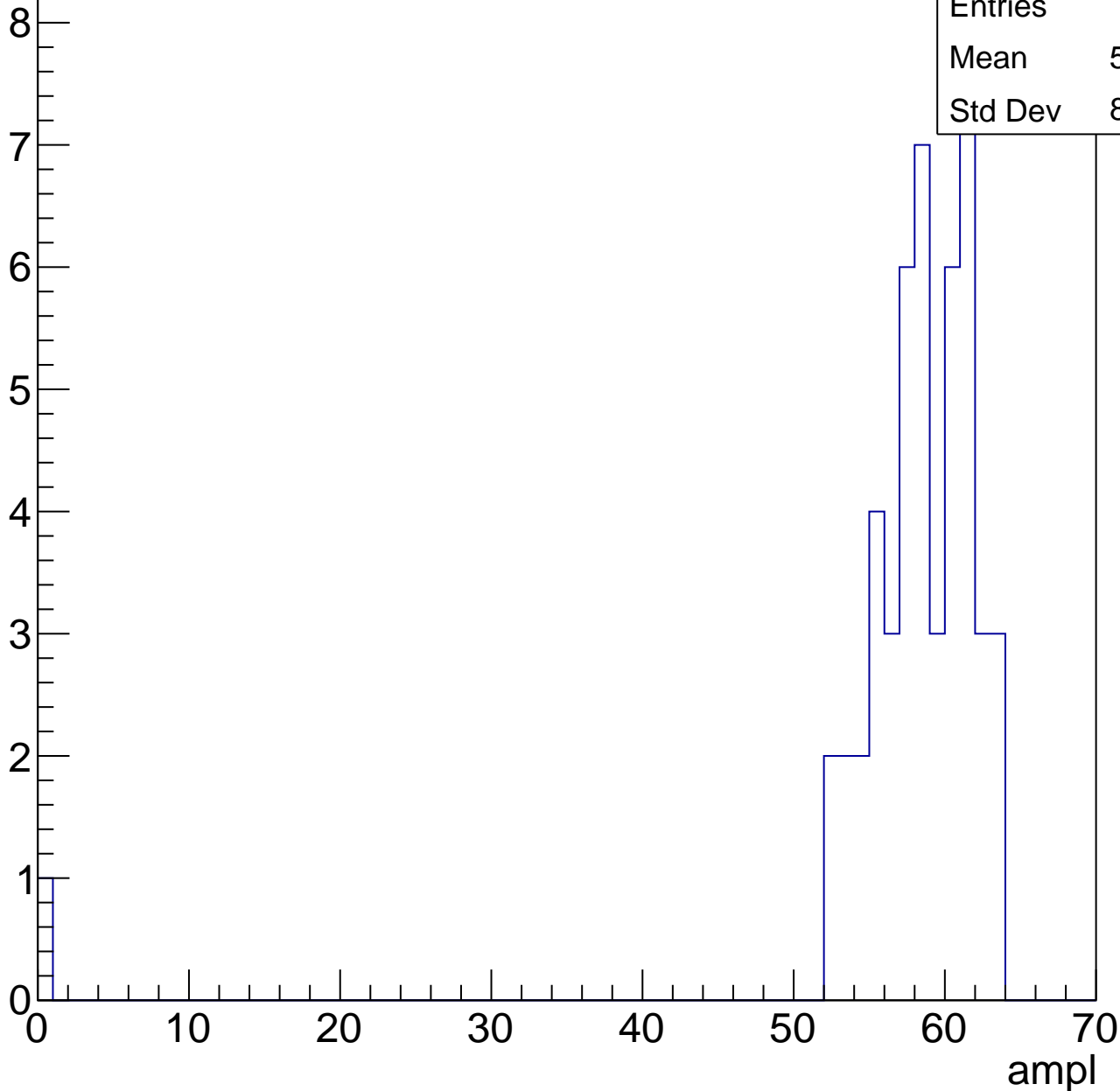


B1L102S, U18-ch46, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.08
Std Dev	8.662

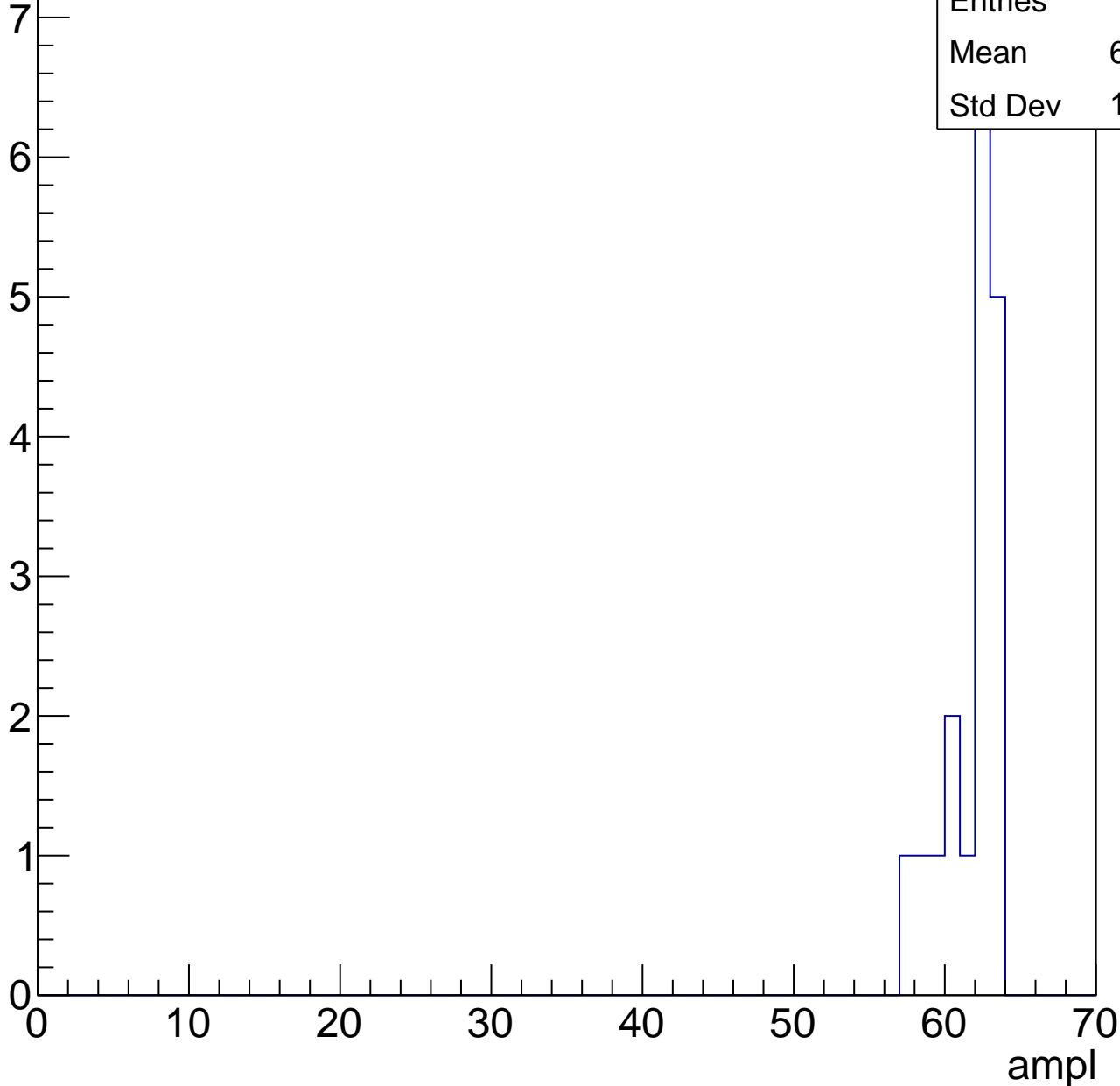


B1L102S, U18-ch46, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	61.33
Std Dev	1.764



B1L102S, U18-ch46, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch47, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	28.4
Std Dev	5.772

Gaus mean : 29.4409

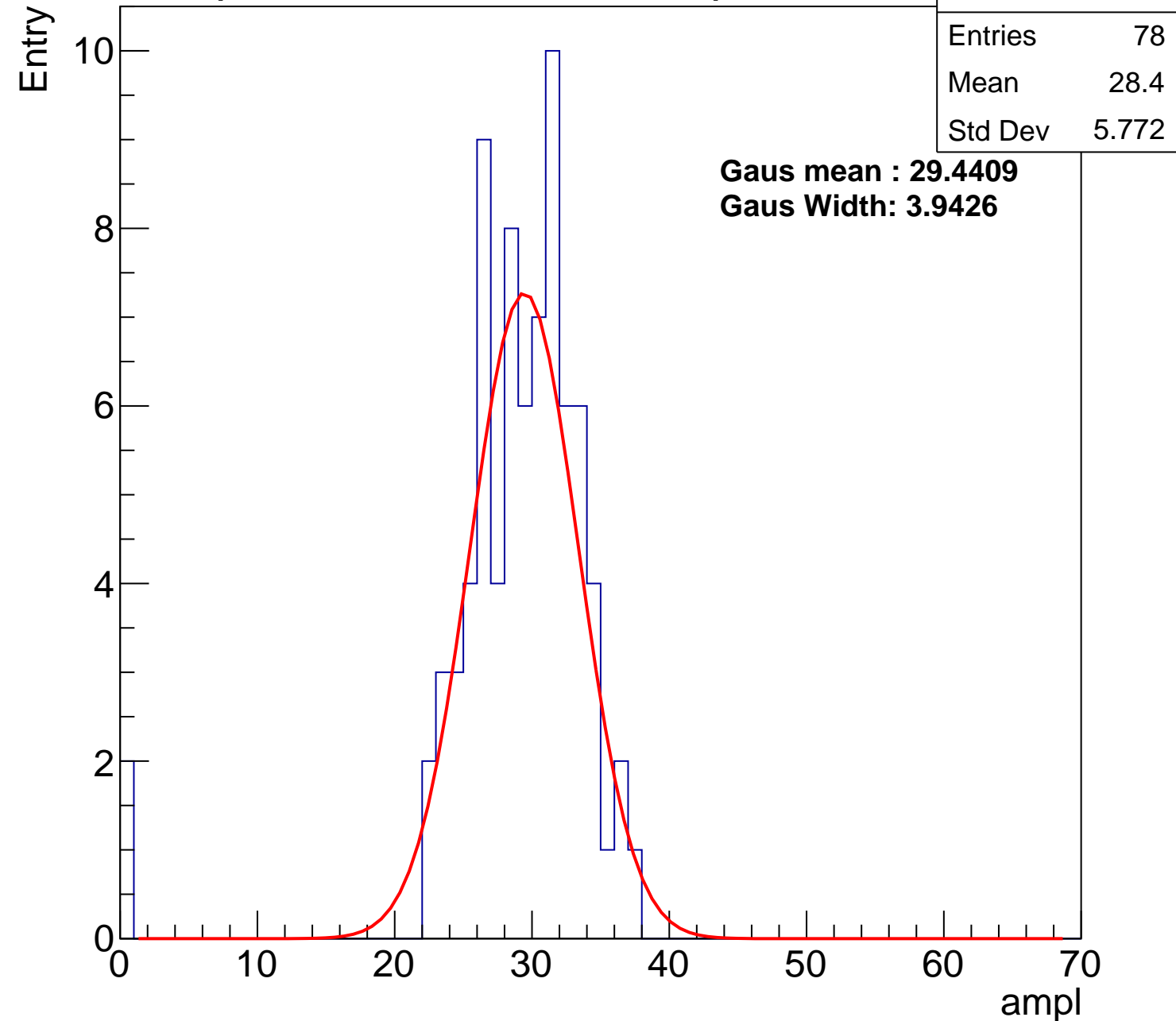
Gaus Width: 3.9426

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch47, adc1

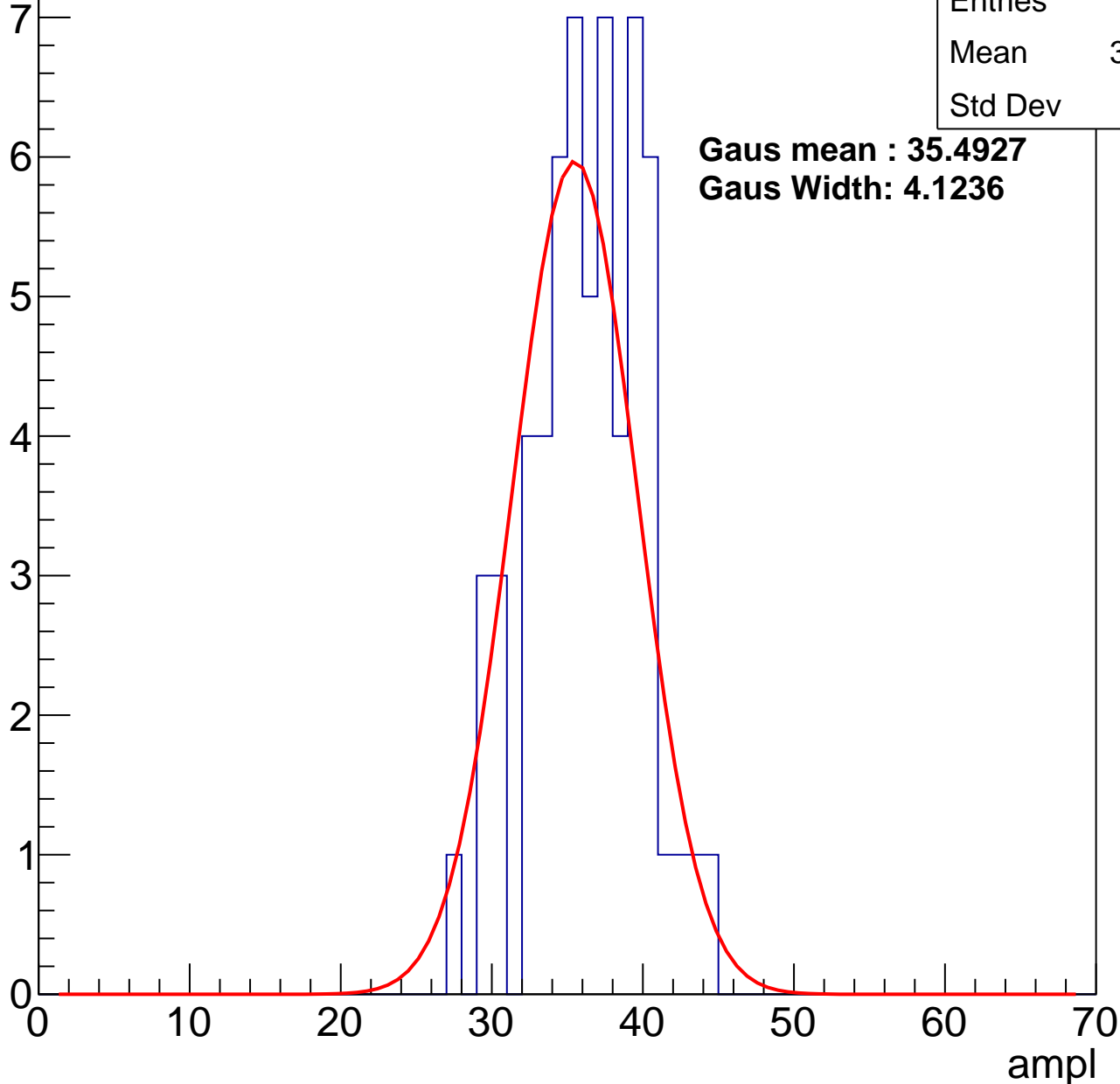
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	35.85
Std Dev	3.67

Gaus mean : 35.4927

Gaus Width: 4.1236



B1L102S, U18-ch47, adc2

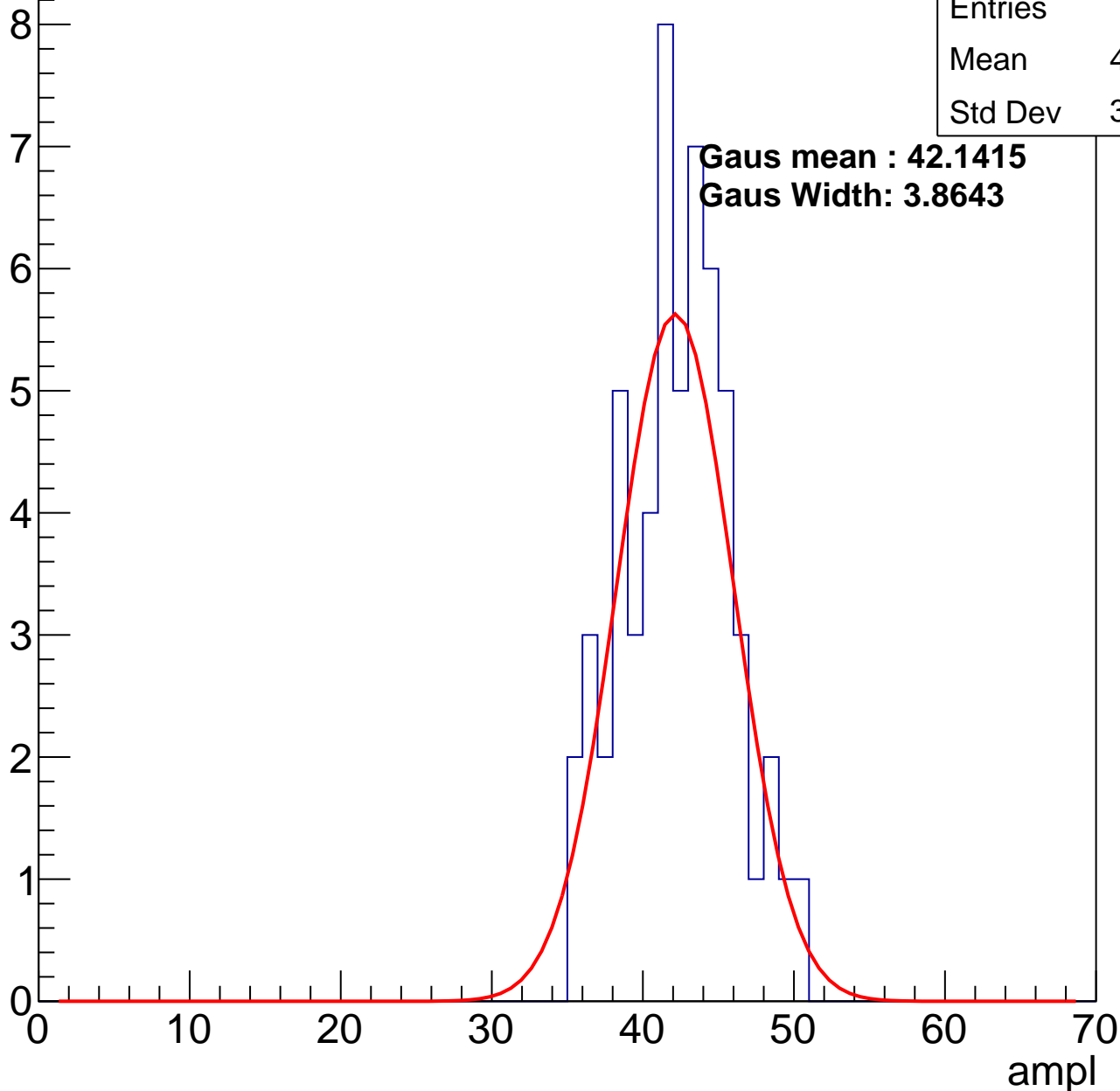
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	41.84
Std Dev	3.522

Gaus mean : 42.1415

Gaus Width: 3.8643

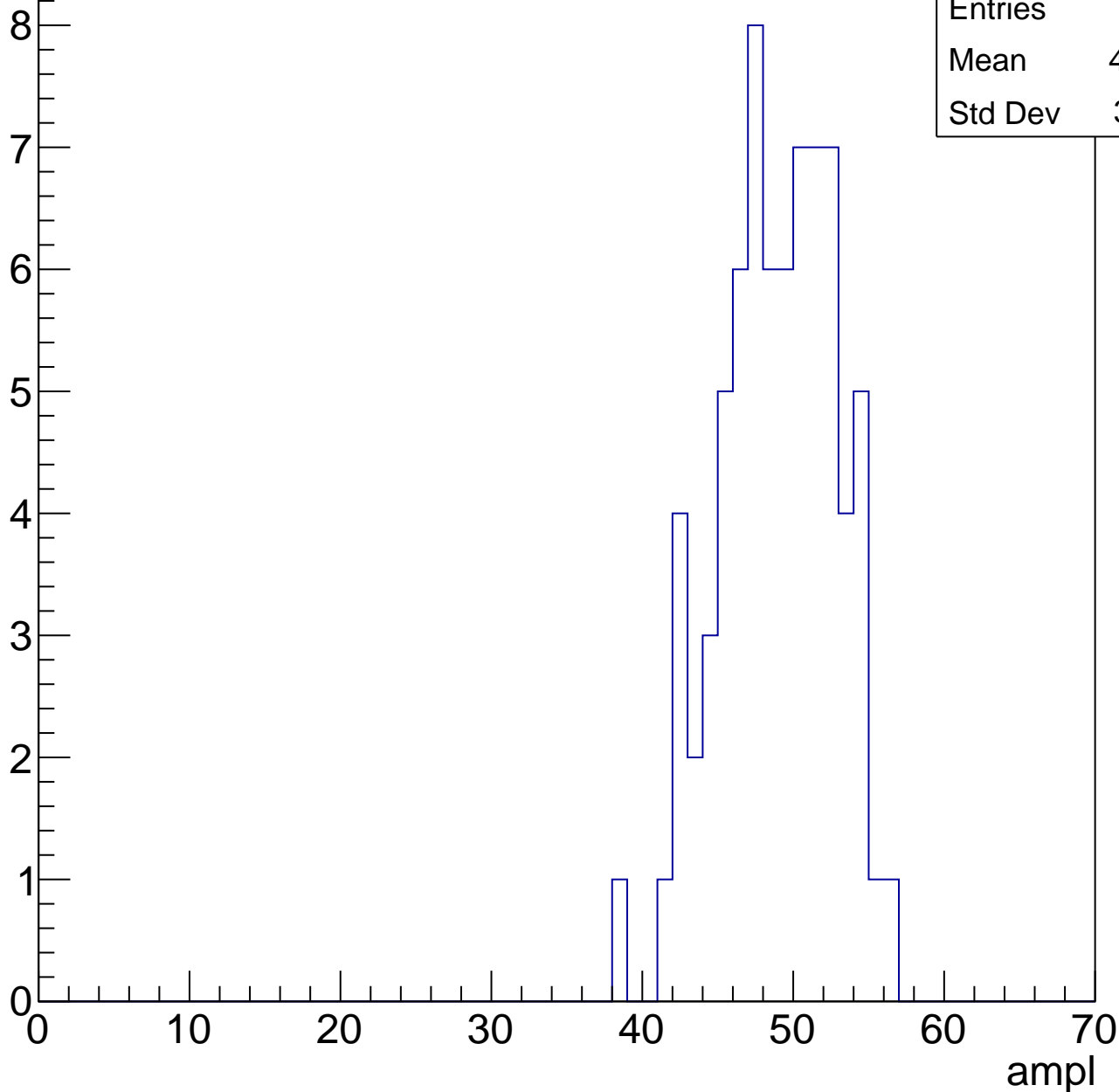


B1L102S, U18-ch47, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	48.49
Std Dev	3.771

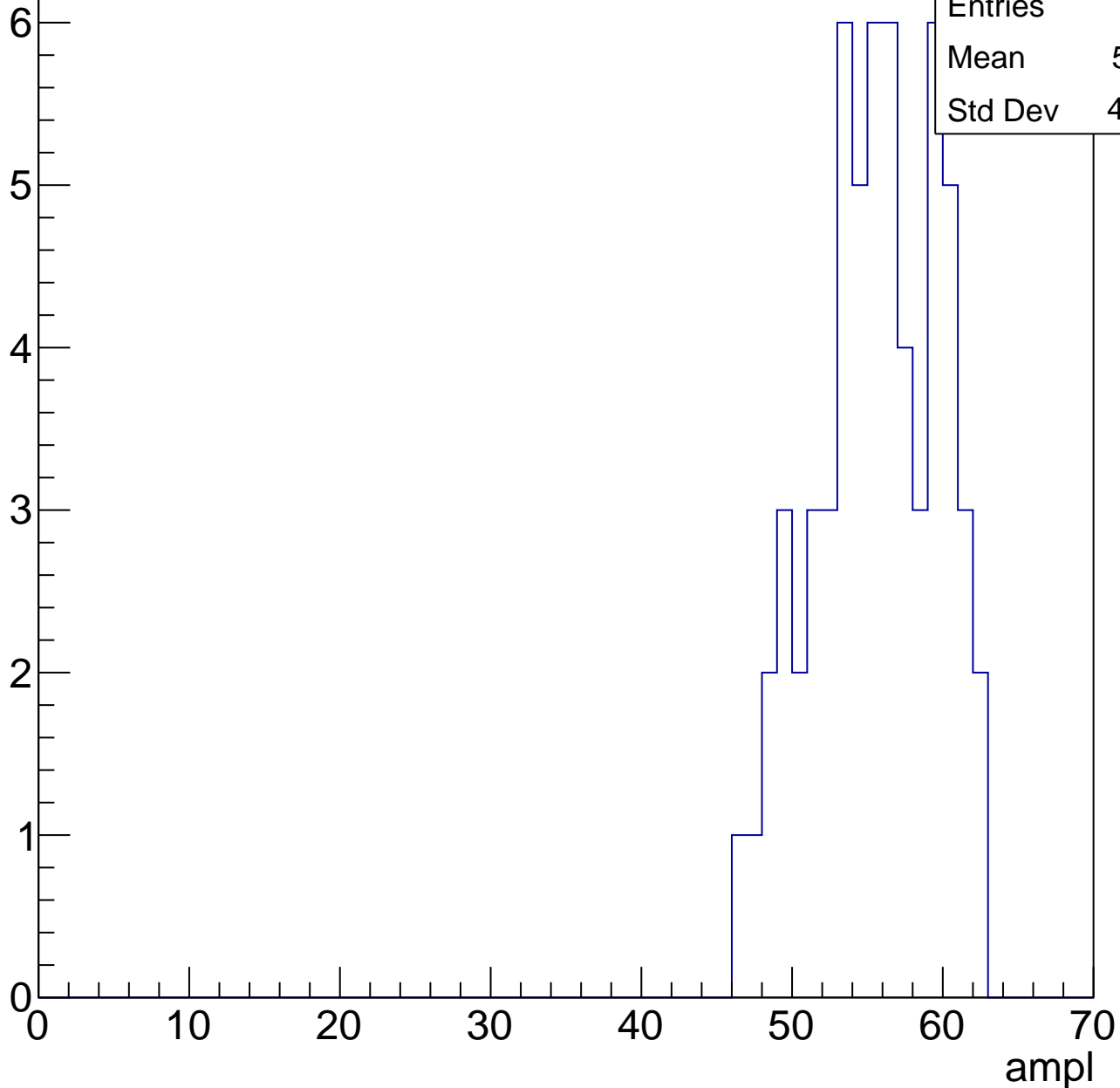


B1L102S, U18-ch47, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	55.11
Std Dev	4.009

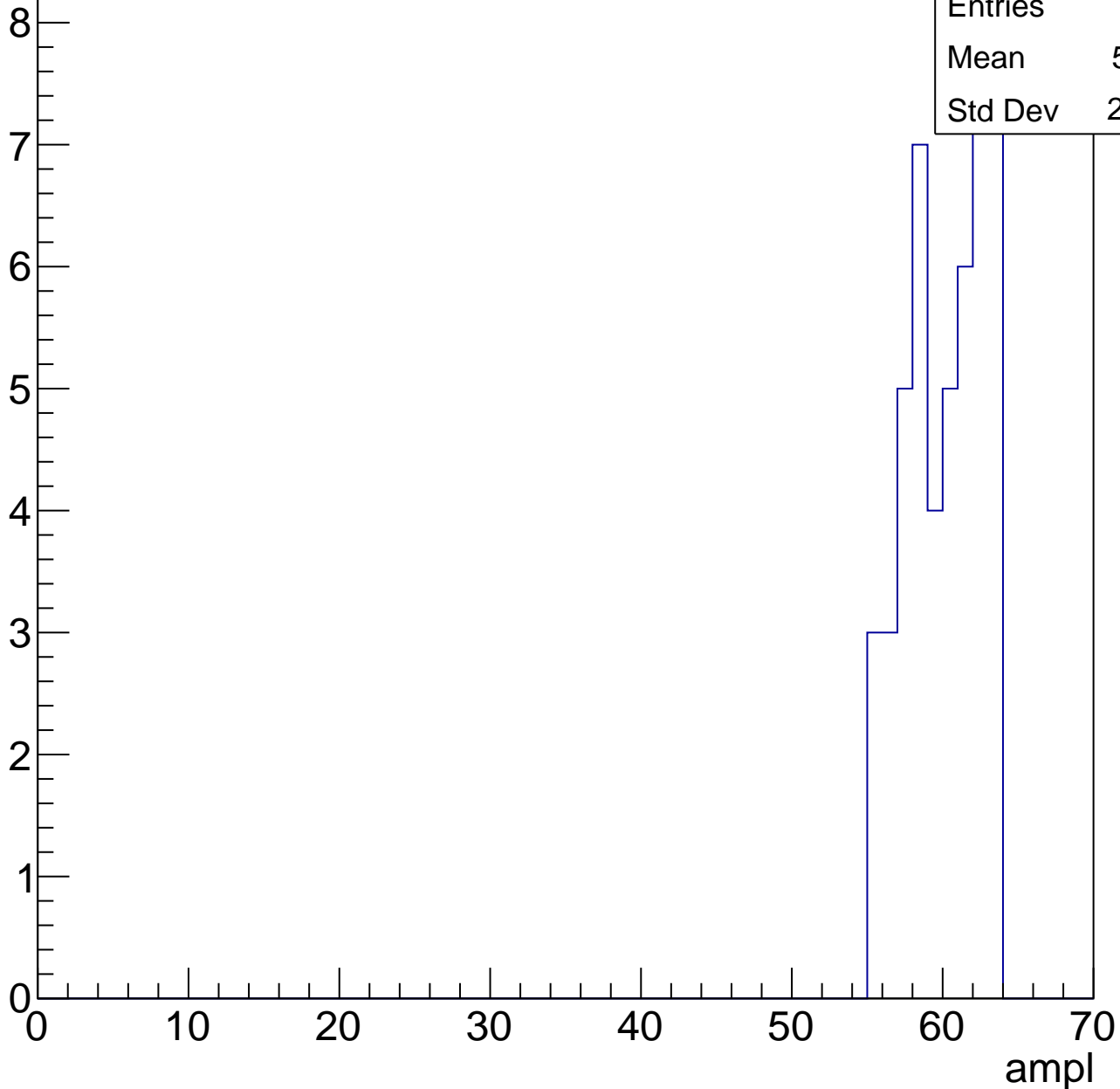


B1L102S, U18-ch47, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	59.71
Std Dev	2.499



B1L102S, U18-ch47, adc6

calib_packv5_042523_0143.root, FC#11, port A2

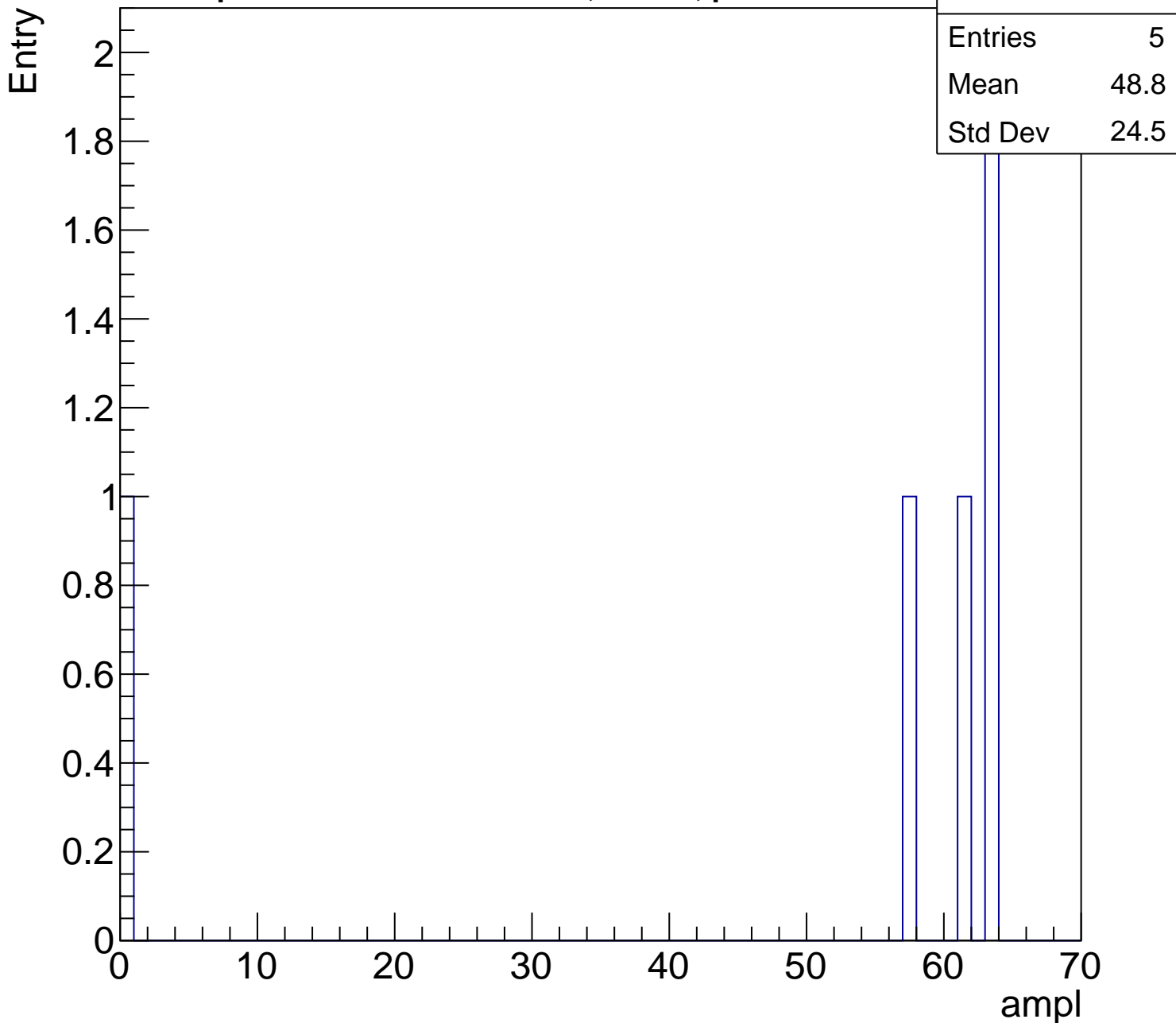
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	48.8
Std Dev	24.5

0 10 20 30 40 50 60 70

ampl



B1L102S, U18-ch47, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch48, adc0

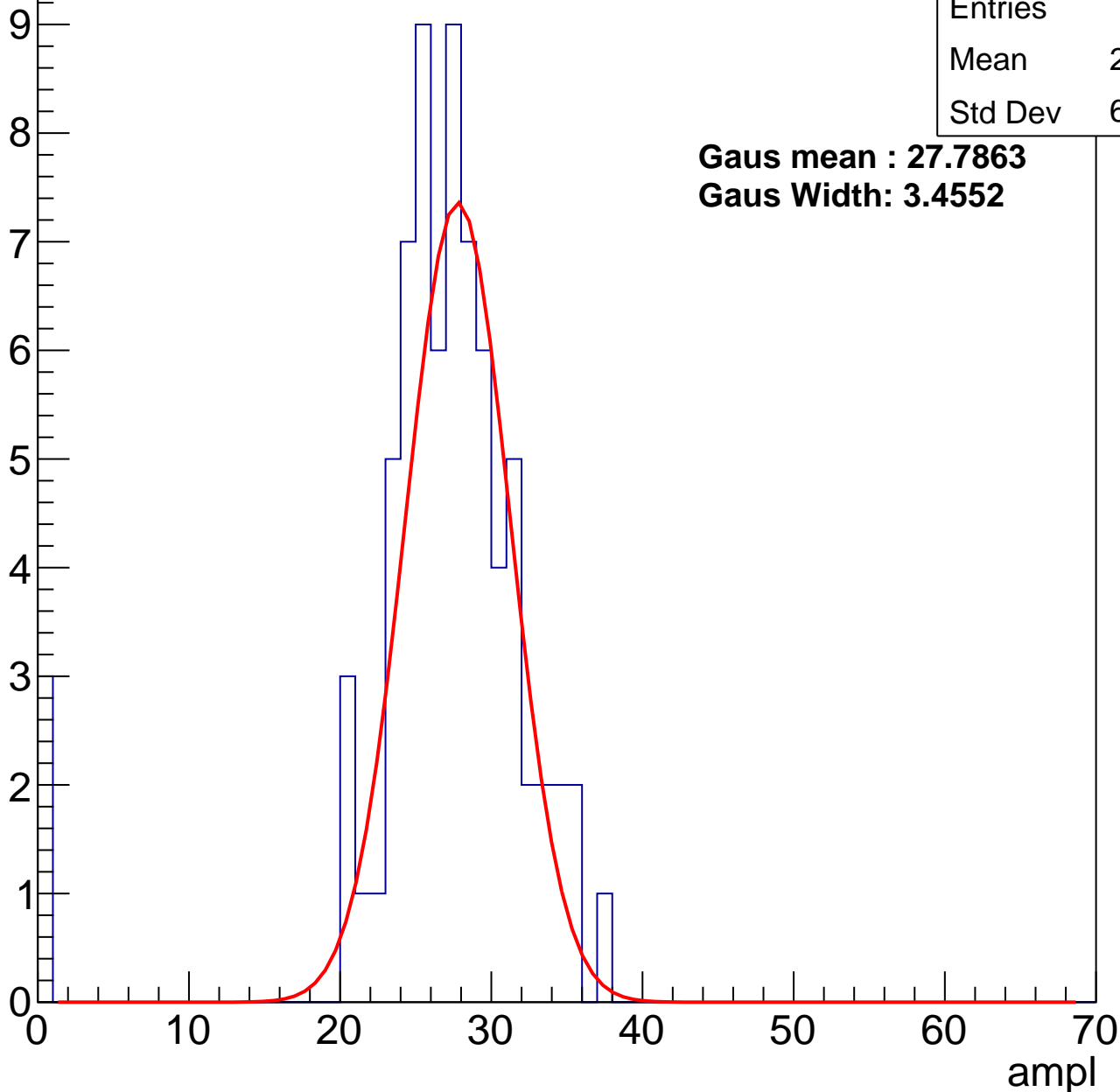
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	26.13
Std Dev	6.448

Gaus mean : 27.7863

Gaus Width: 3.4552



B1L102S, U18-ch48, adc1

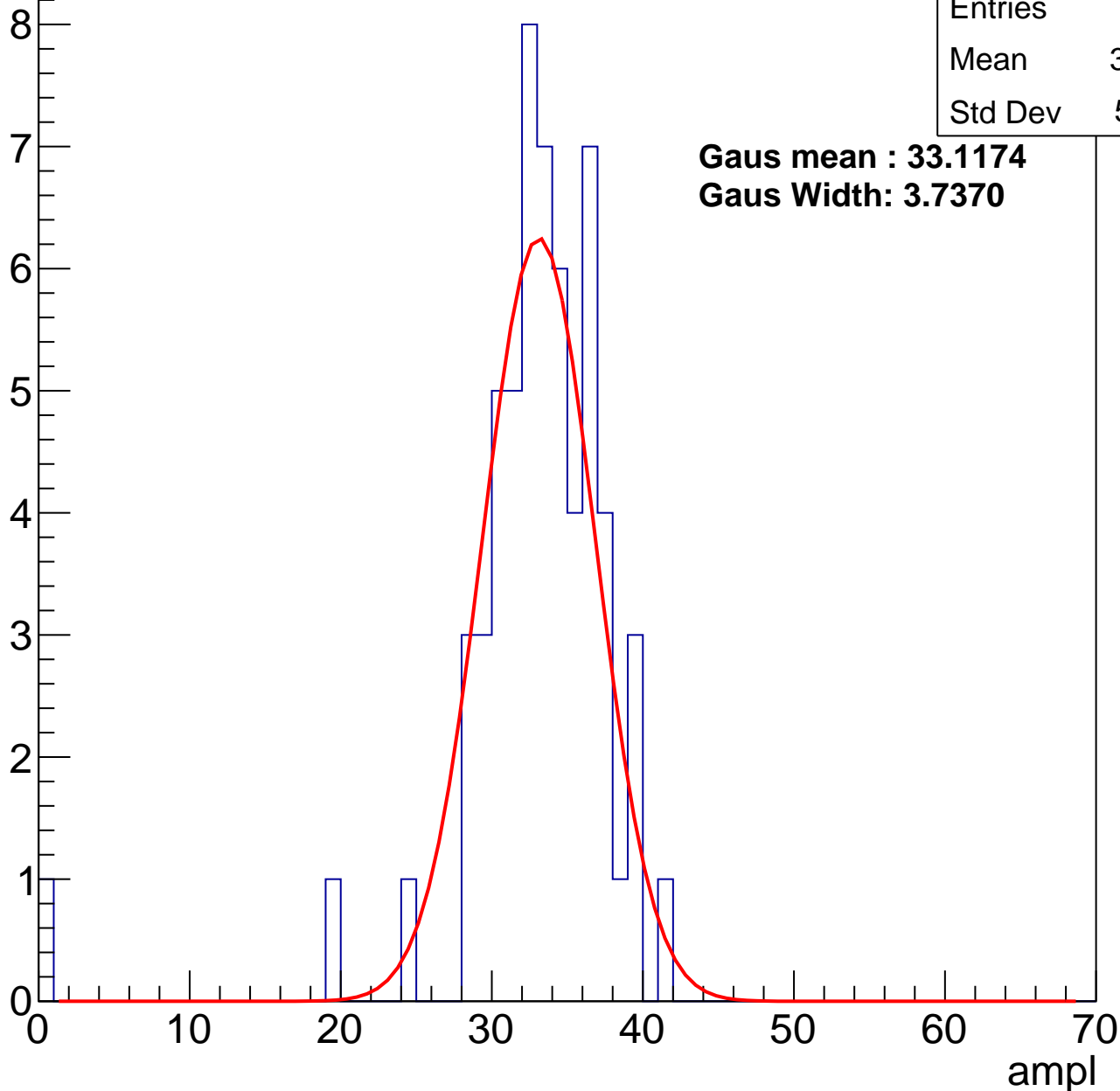
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	32.43
Std Dev	5.611

Gaus mean : 33.1174

Gaus Width: 3.7370



B1L102S, U18-ch48, adc2

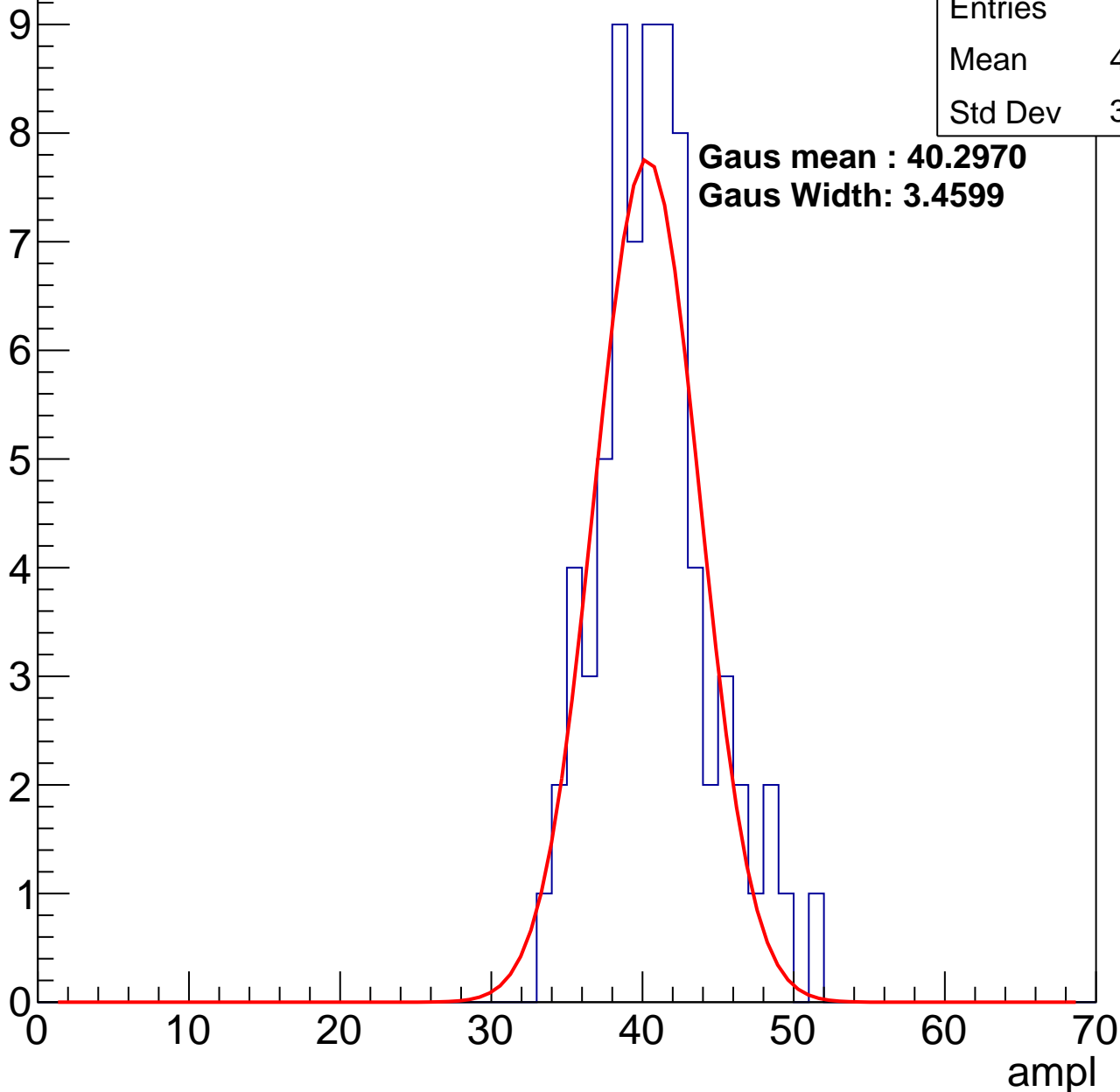
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	40.33
Std Dev	3.672

Gaus mean : 40.2970

Gaus Width: 3.4599

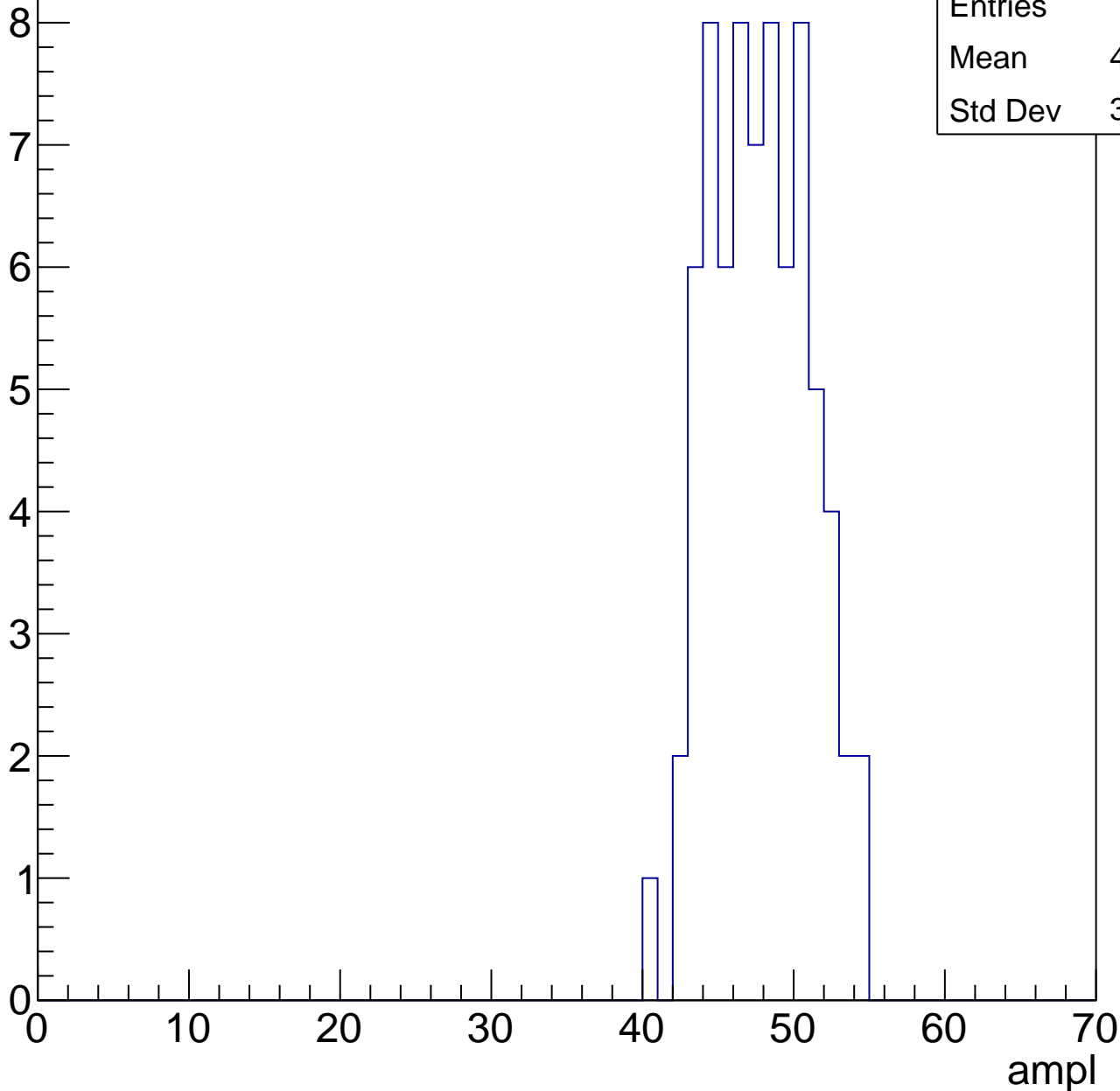


B1L102S, U18-ch48, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	47.34
Std Dev	3.193

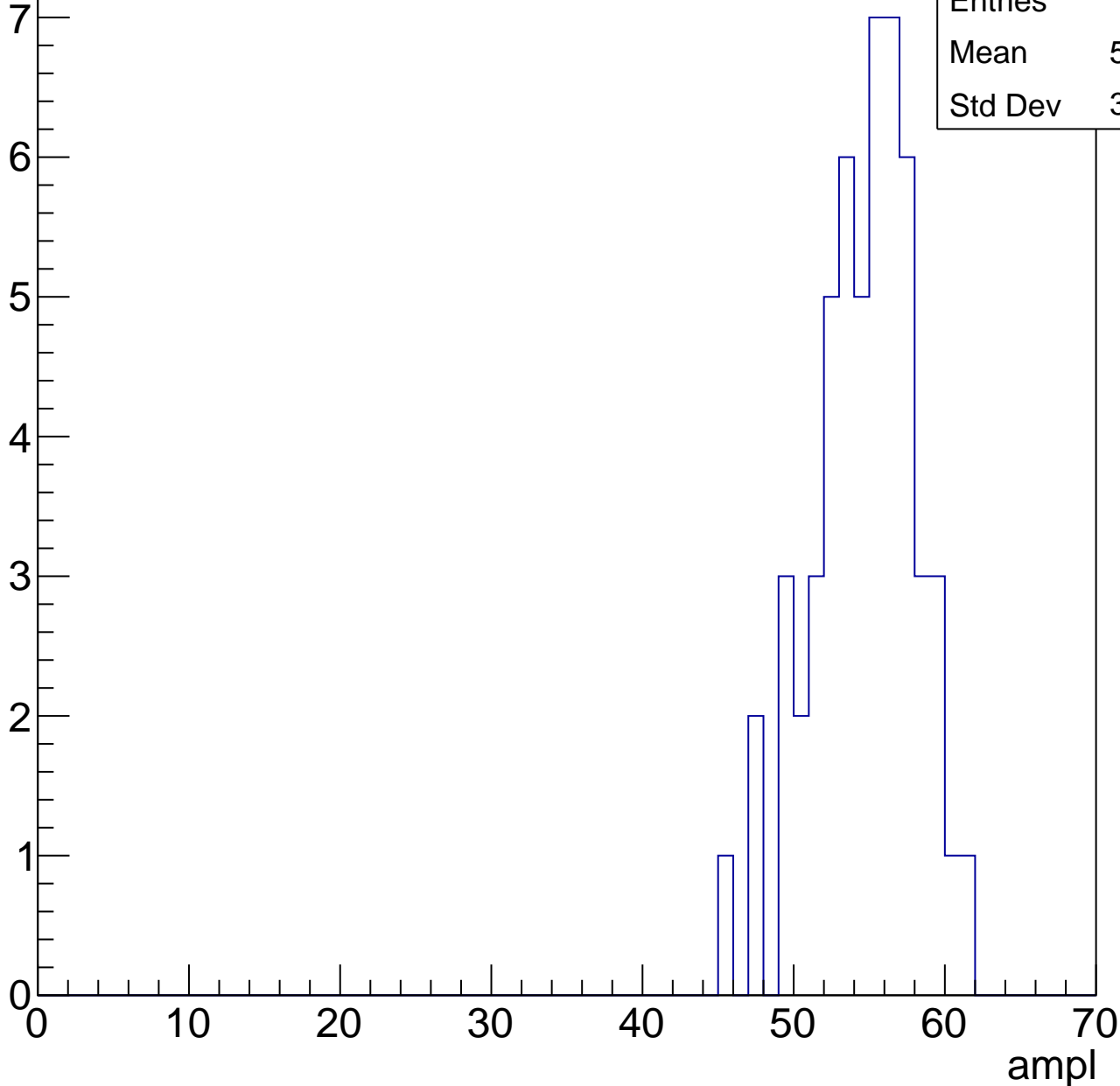


B1L102S, U18-ch48, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.15
Std Dev	3.397



B1L102S, U18-ch48, adc5

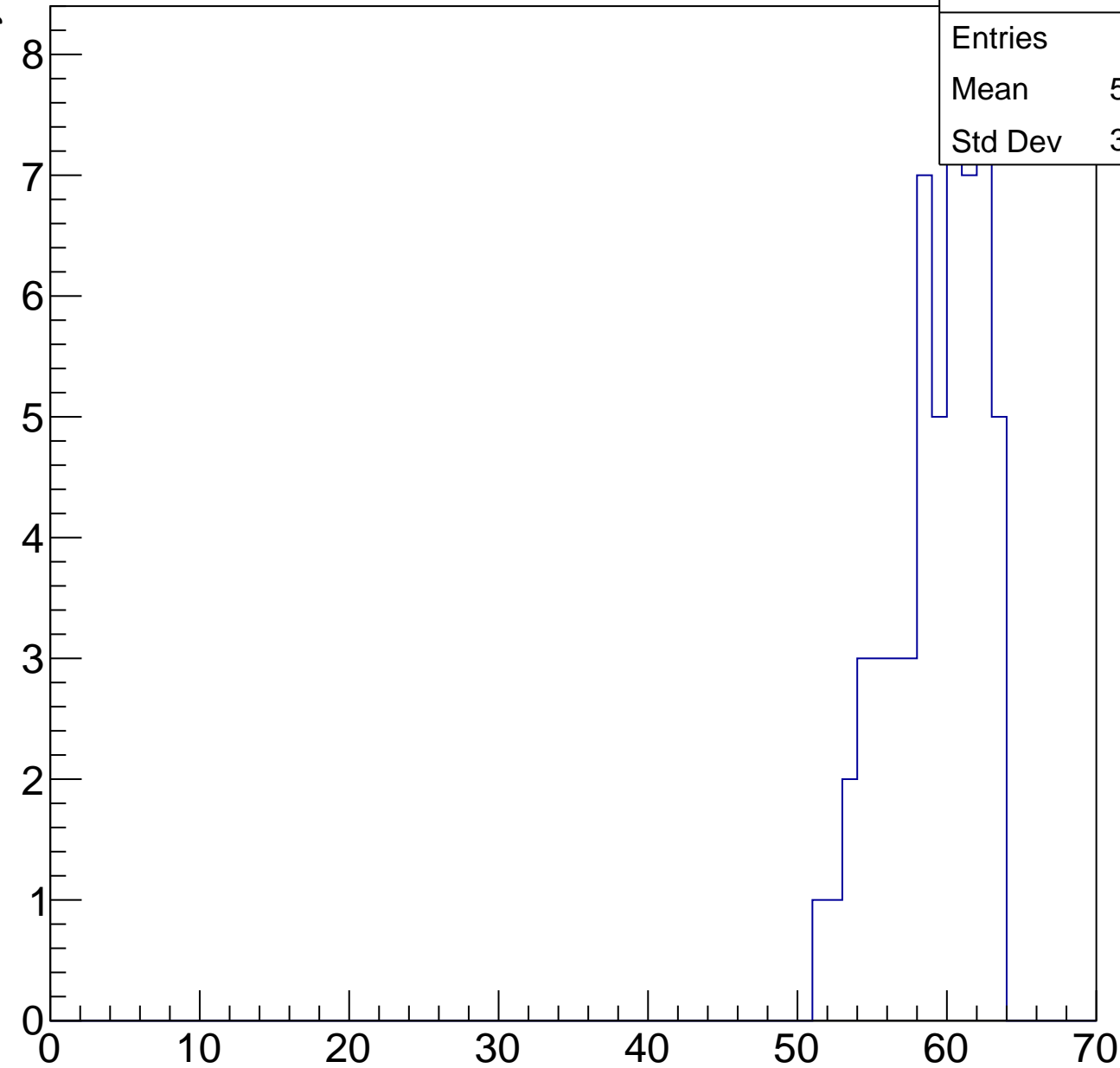
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	58.82
Std Dev	3.106

ampl

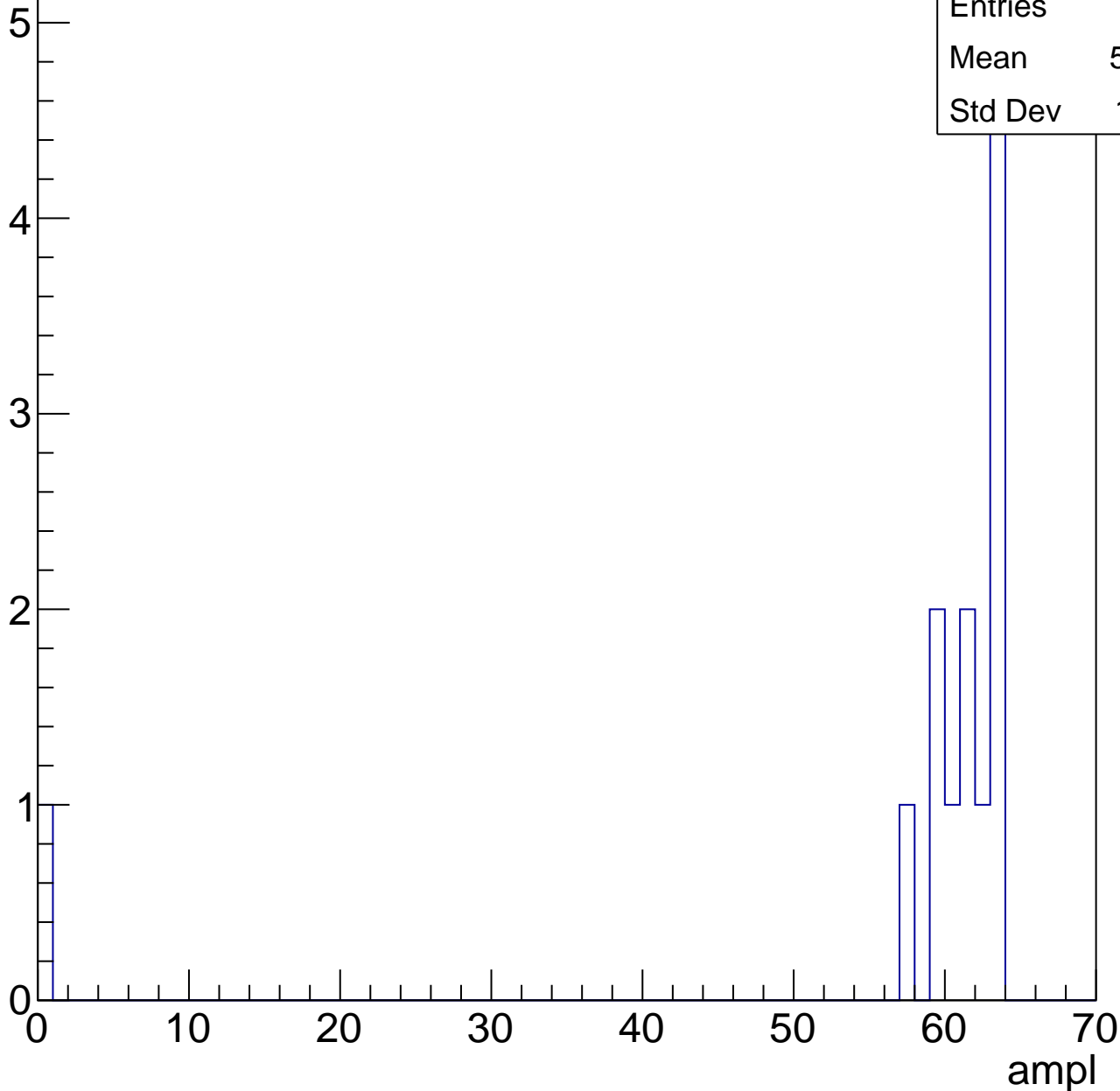


B1L102S, U18-ch48, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.46
Std Dev	16.41



B1L102S, U18-ch48, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch49, adc0

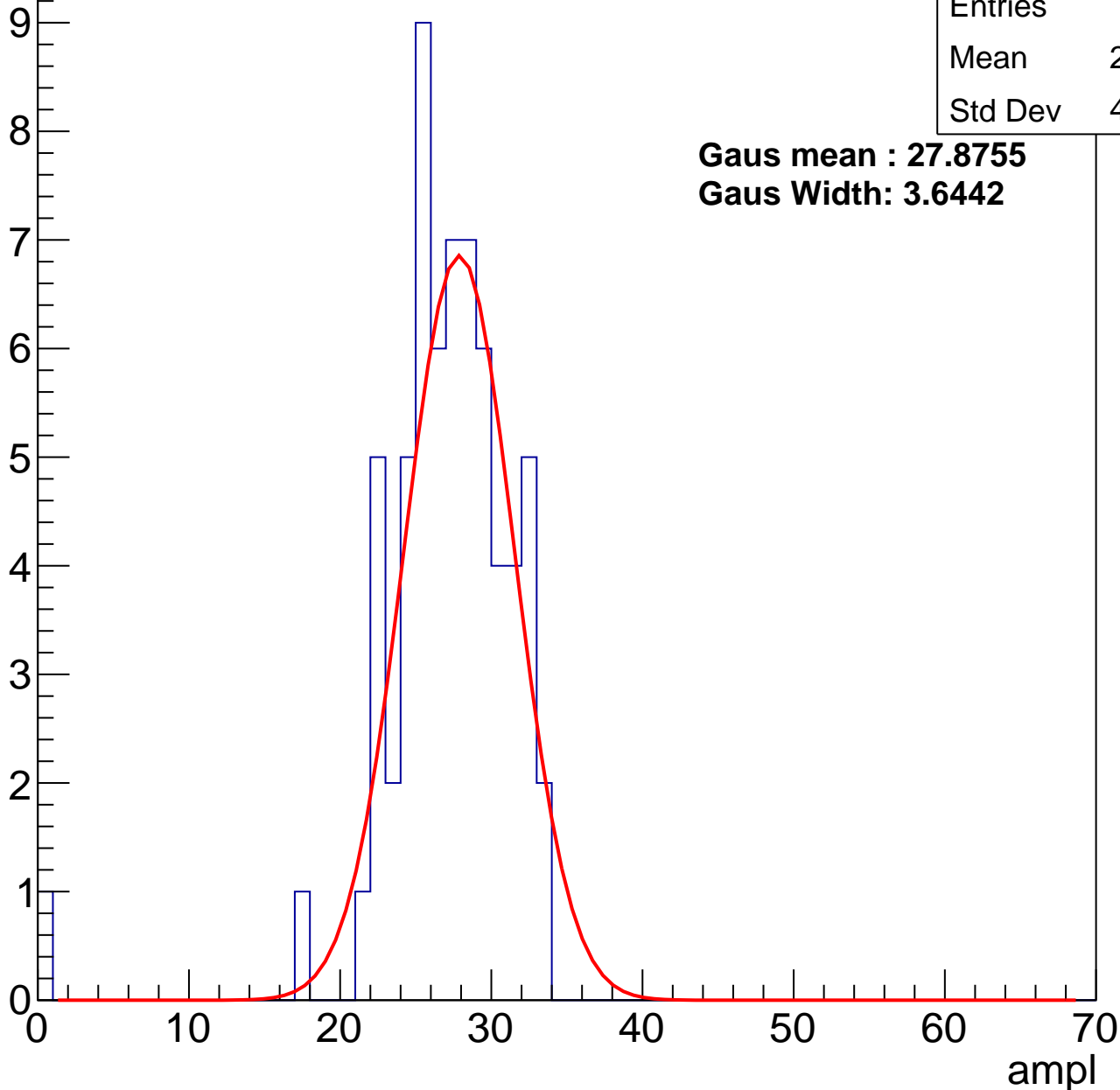
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	26.52
Std Dev	4.678

Gaus mean : 27.8755

Gaus Width: 3.6442



B1L102S, U18-ch49, adc1

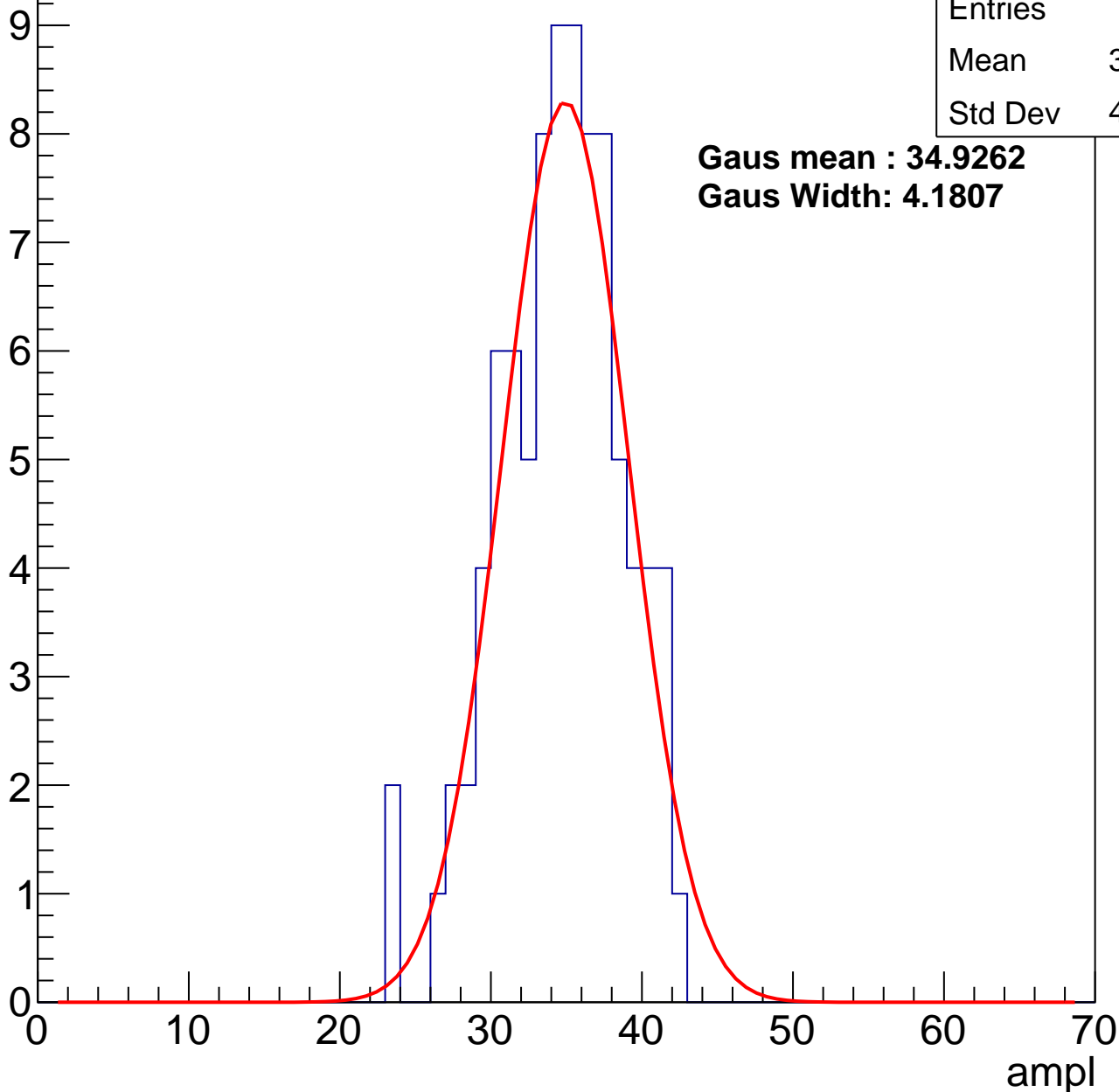
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	34.15
Std Dev	4.058

Gaus mean : 34.9262

Gaus Width: 4.1807



B1L102S, U18-ch49, adc2

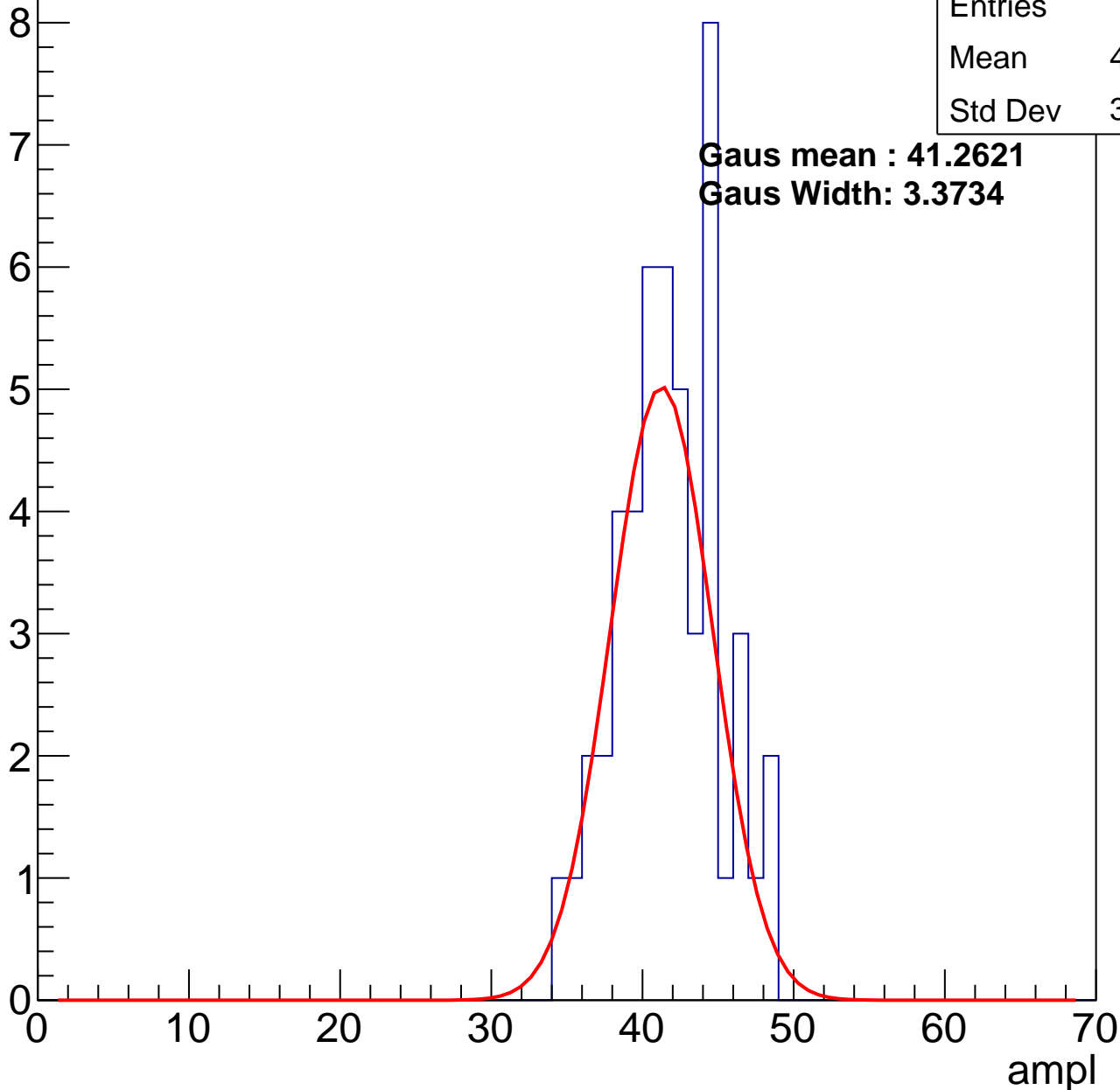
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.35
Std Dev	3.298

Gaus mean : 41.2621

Gaus Width: 3.3734

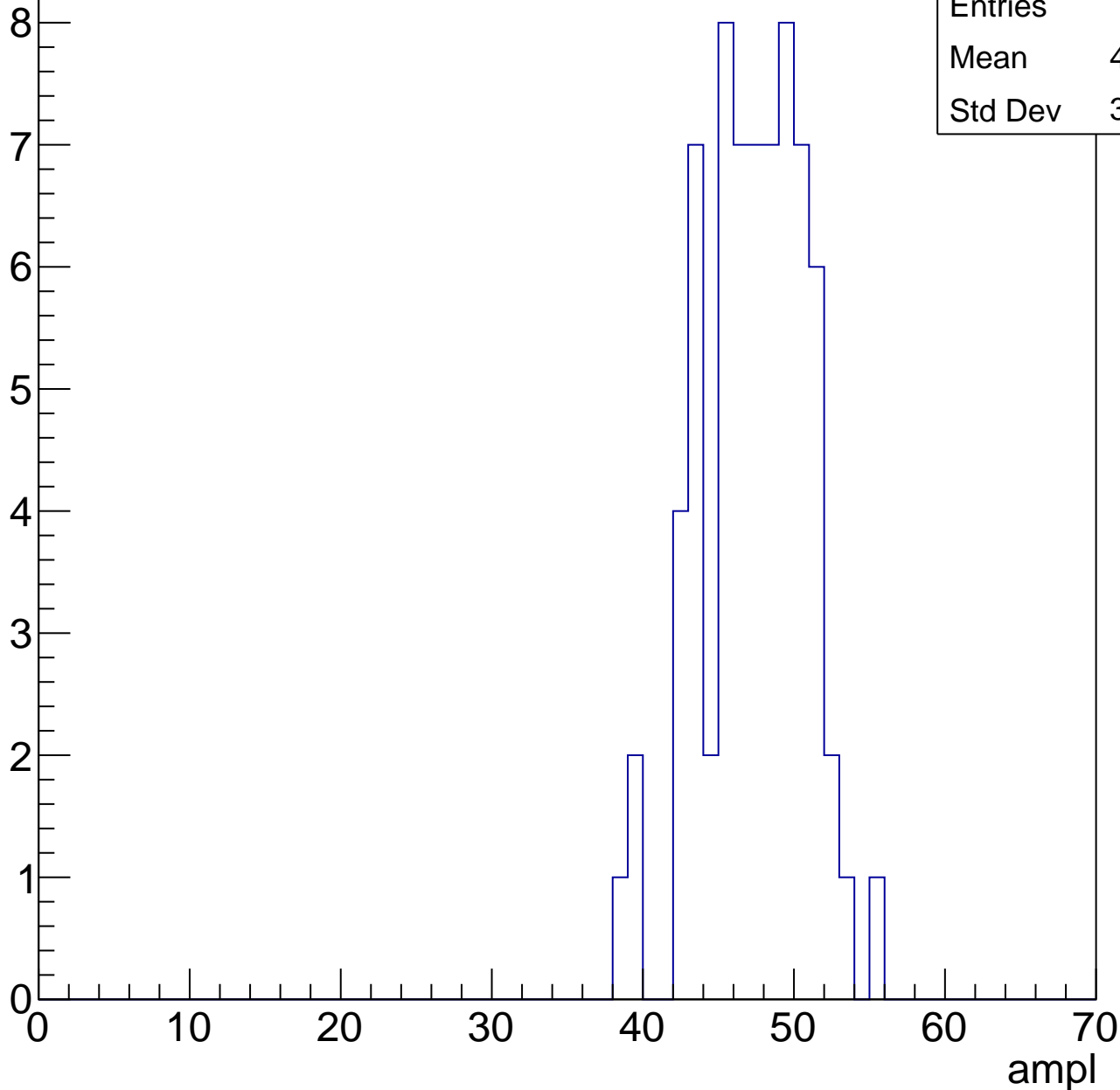


B1L102S, U18-ch49, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	46.86
Std Dev	3.432

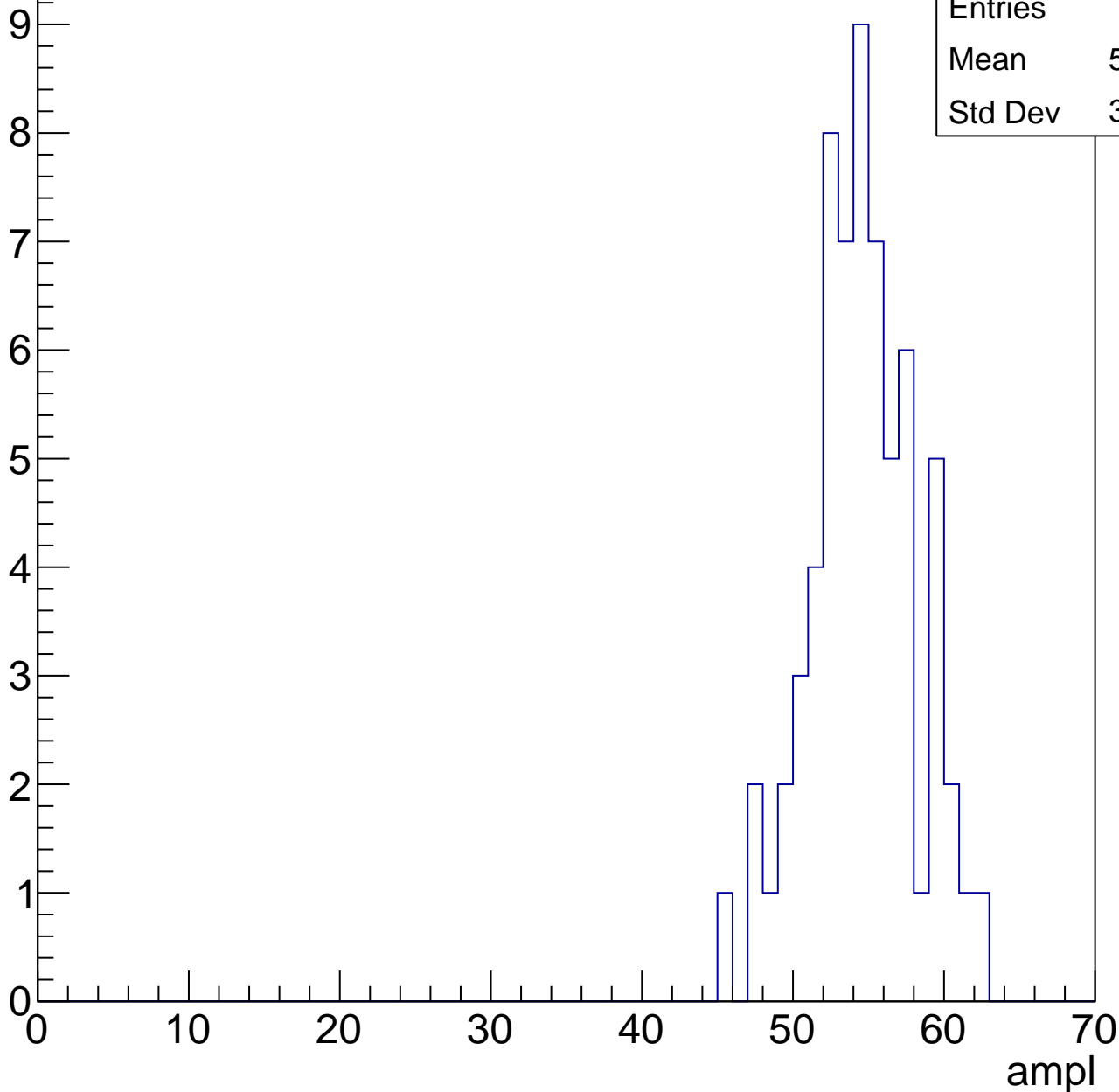


B1L102S, U18-ch49, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

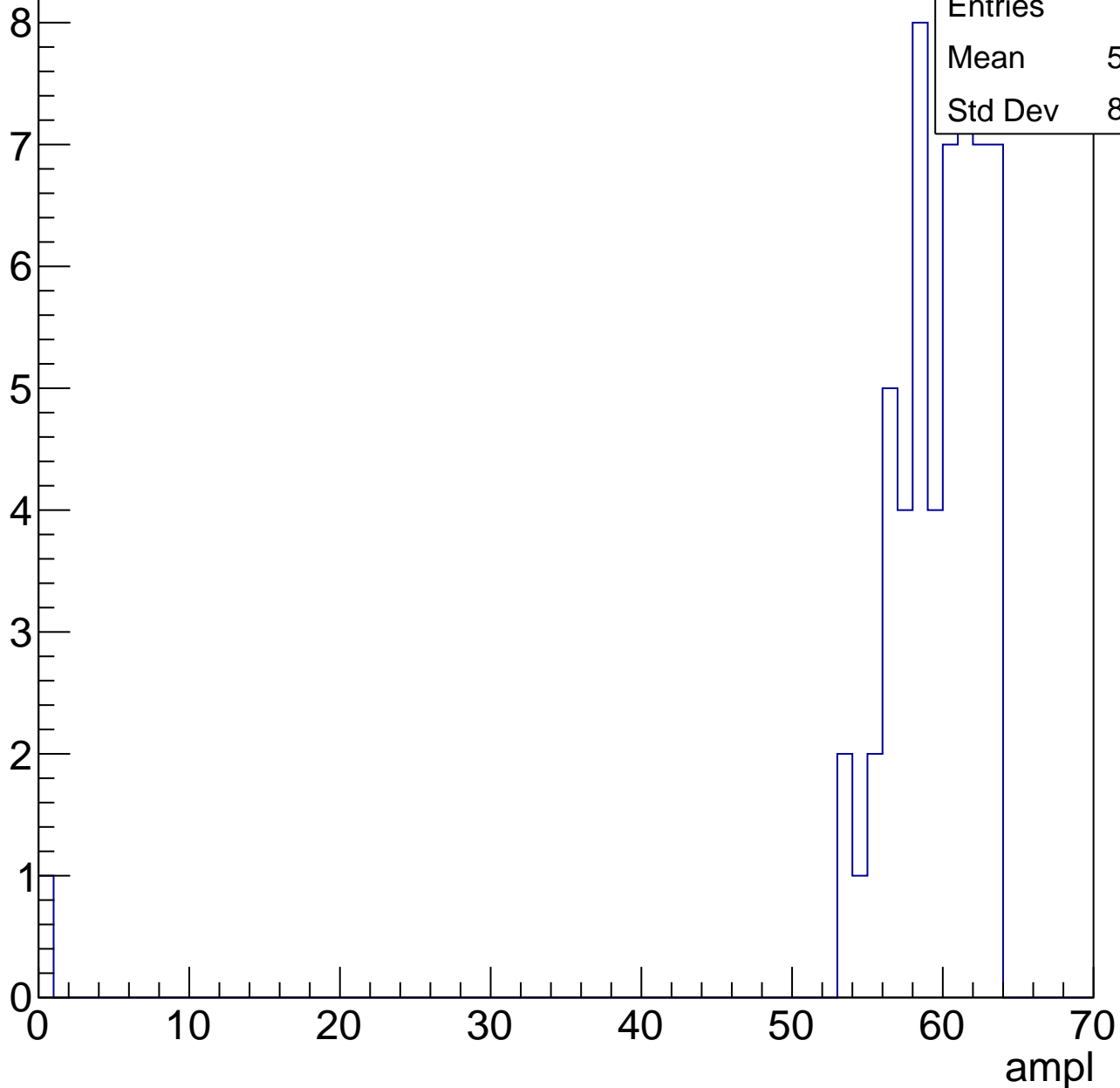
Entries	65
Mean	54.08
Std Dev	3.496



B1L102S, U18-ch49, adc5

calib_packv5_042523_0143.root, FC#11, port A2

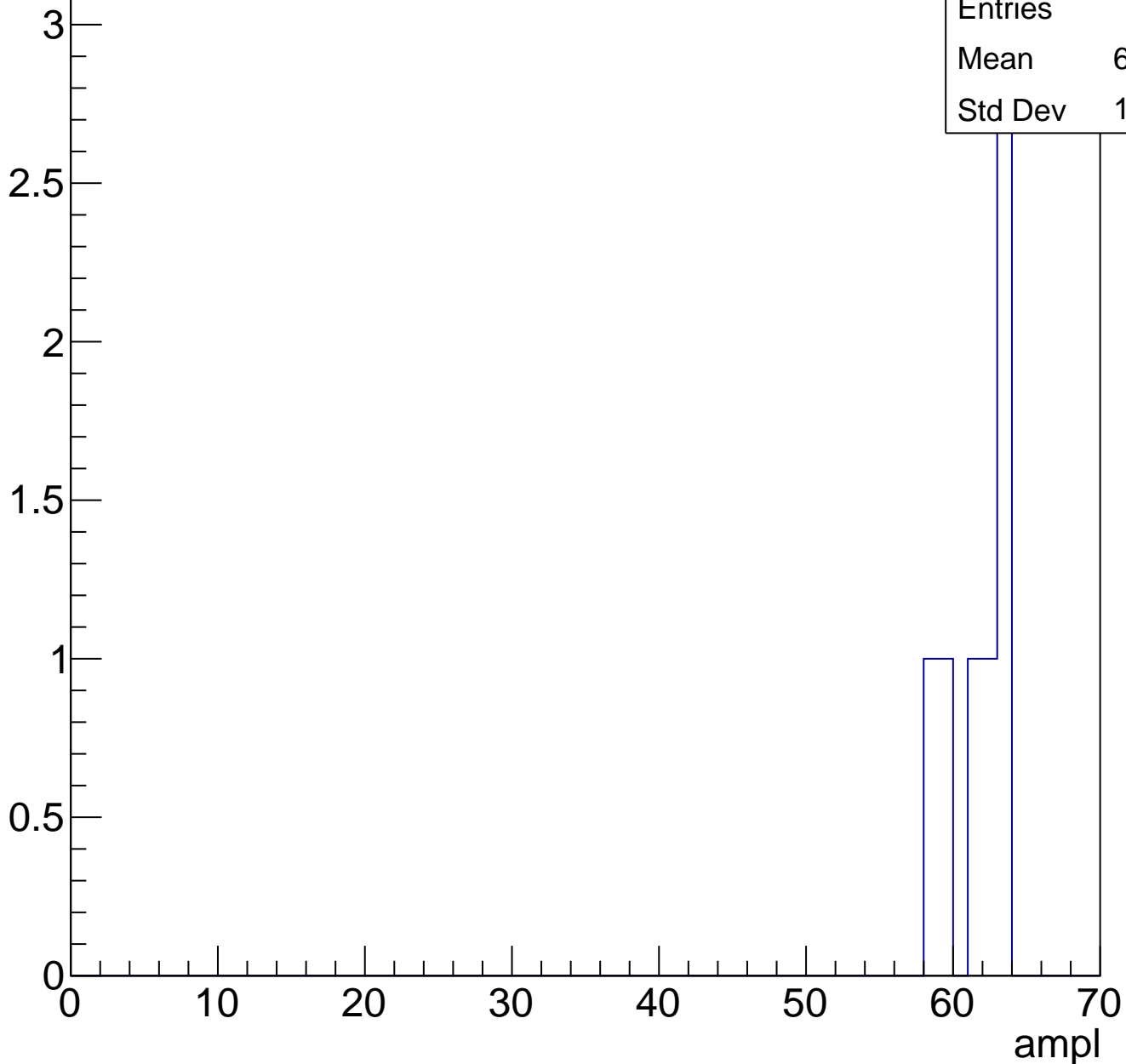
Entry



B1L102S, U18-ch49, adc6

calib_packv5_042523_0143.root, FC#11, port A2

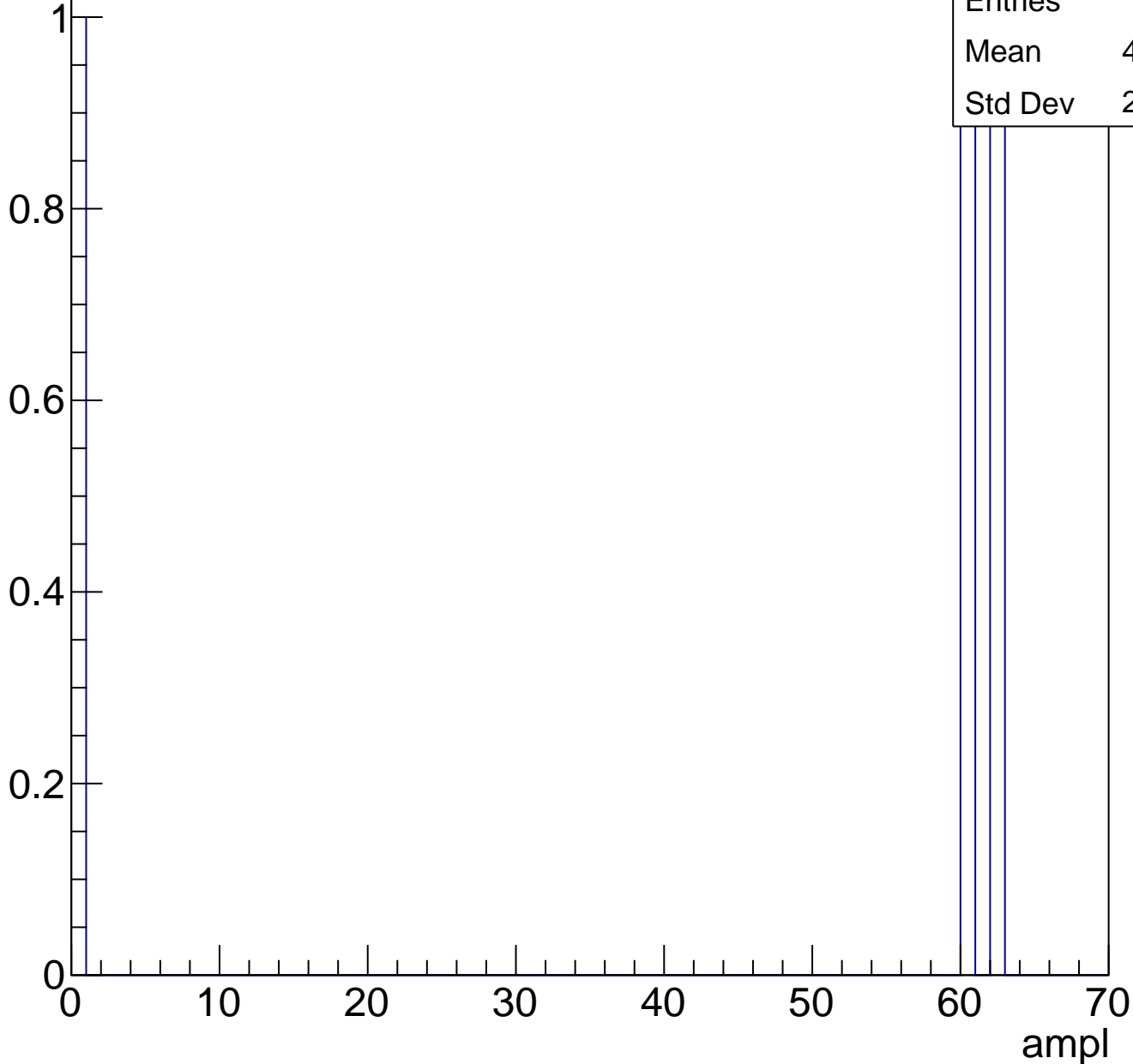
Entry



B1L102S, U18-ch49, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch50, adc0

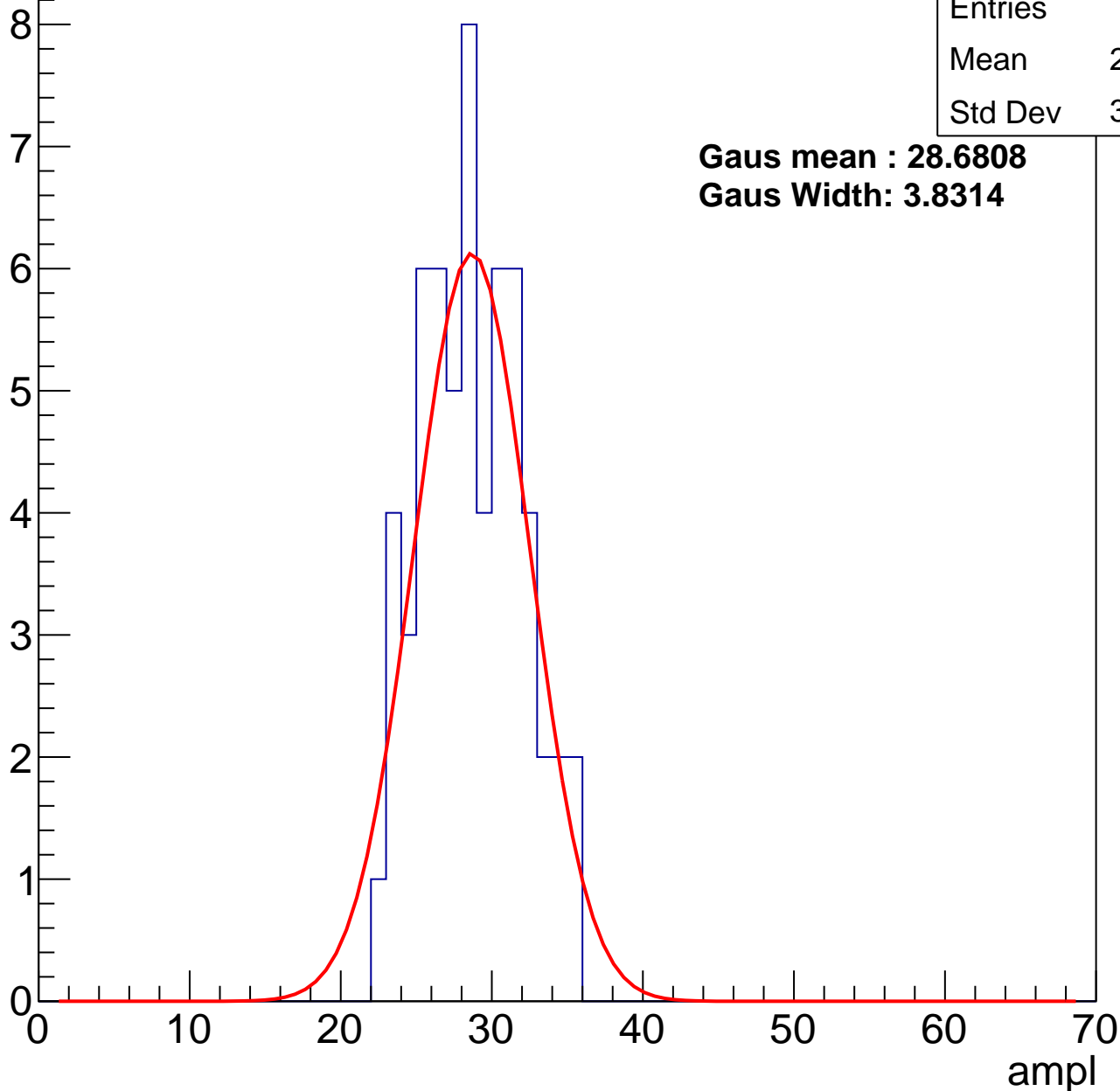
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.22
Std Dev	3.268

Gaus mean : 28.6808

Gaus Width: 3.8314



B1L102S, U18-ch50, adc1

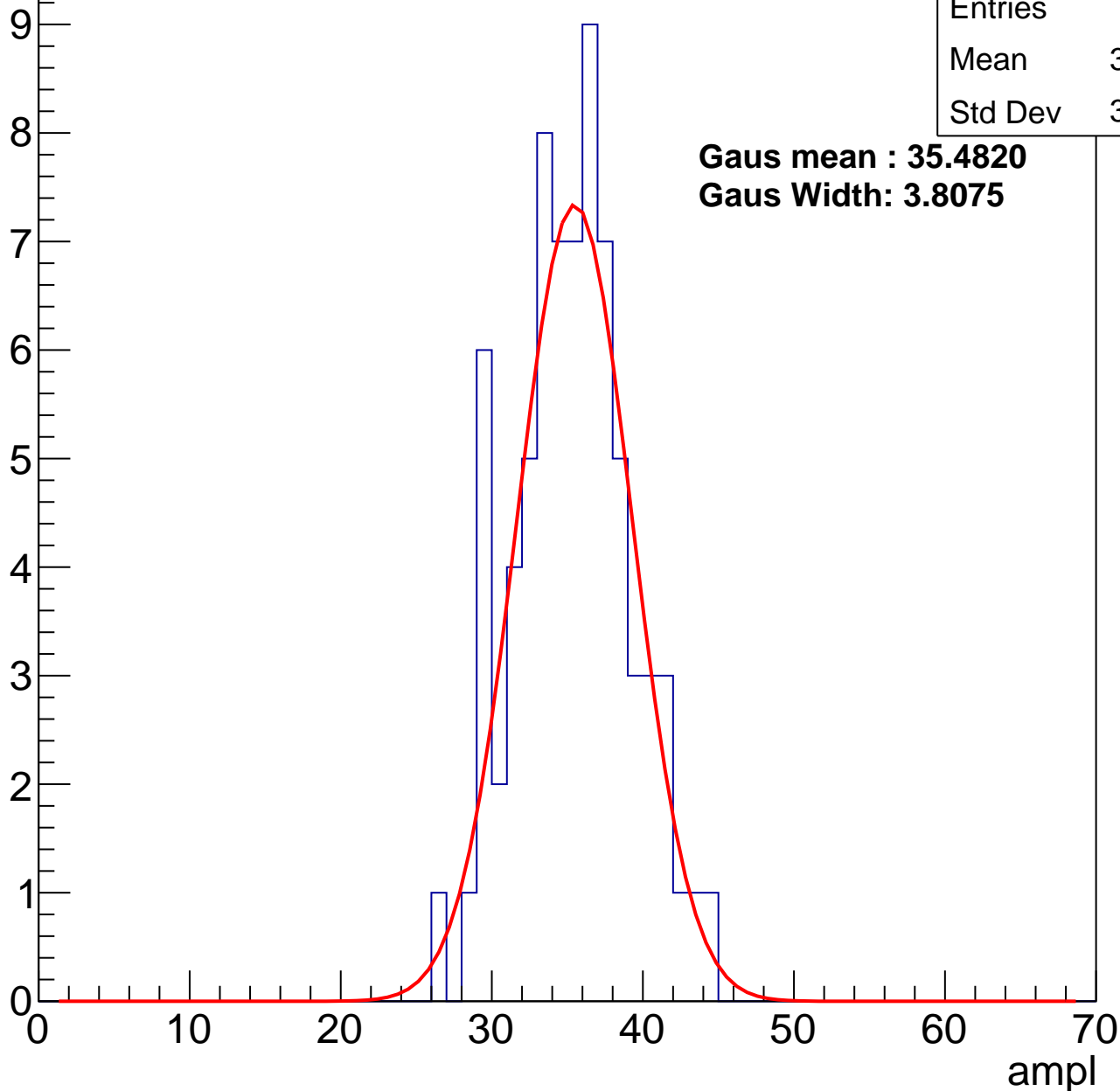
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.88
Std Dev	3.767

Gaus mean : 35.4820

Gaus Width: 3.8075



B1L102S, U18-ch50, adc2

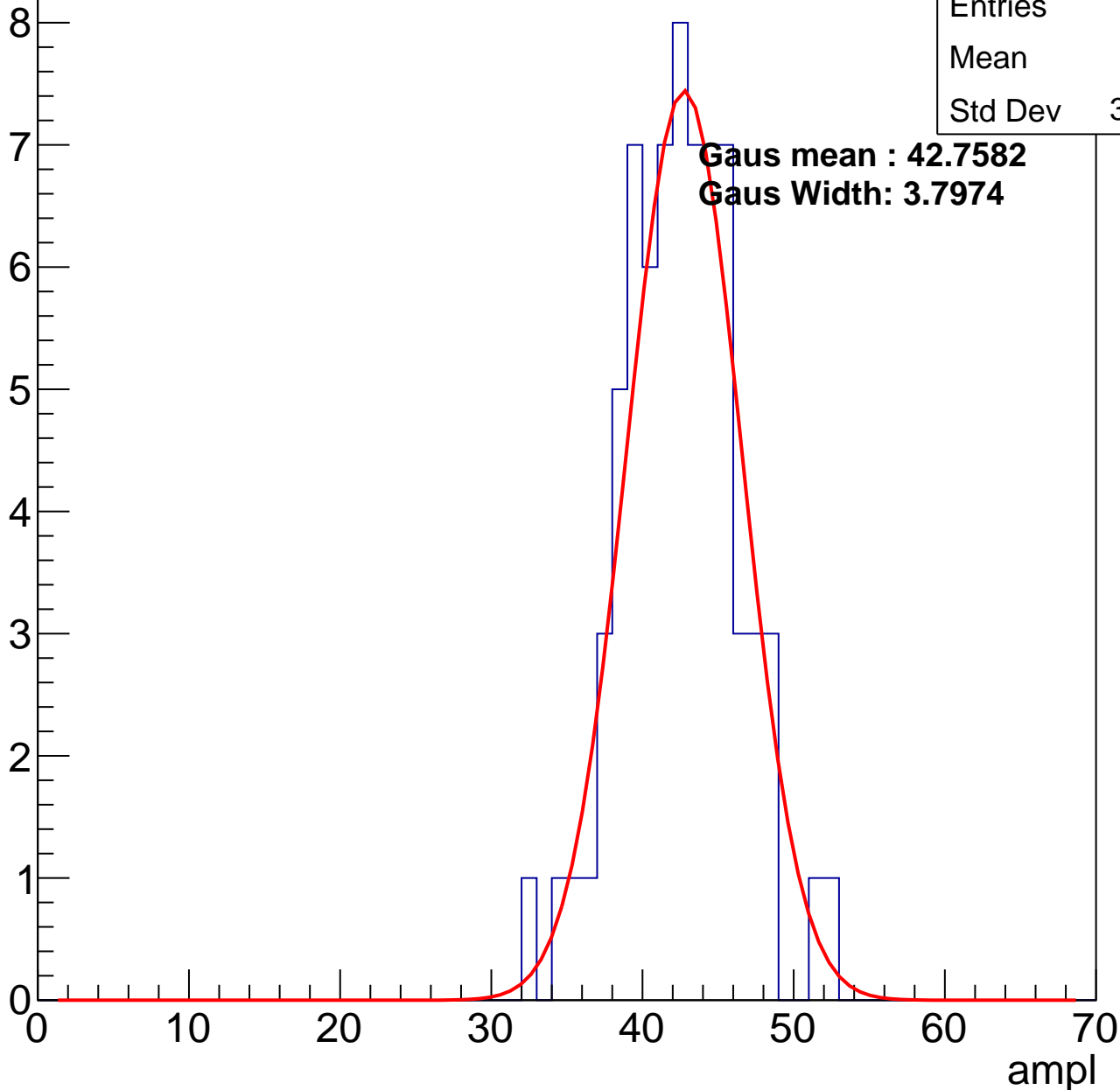
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42
Std Dev	3.742

Gaus mean : 42.7582

Gaus Width: 3.7974

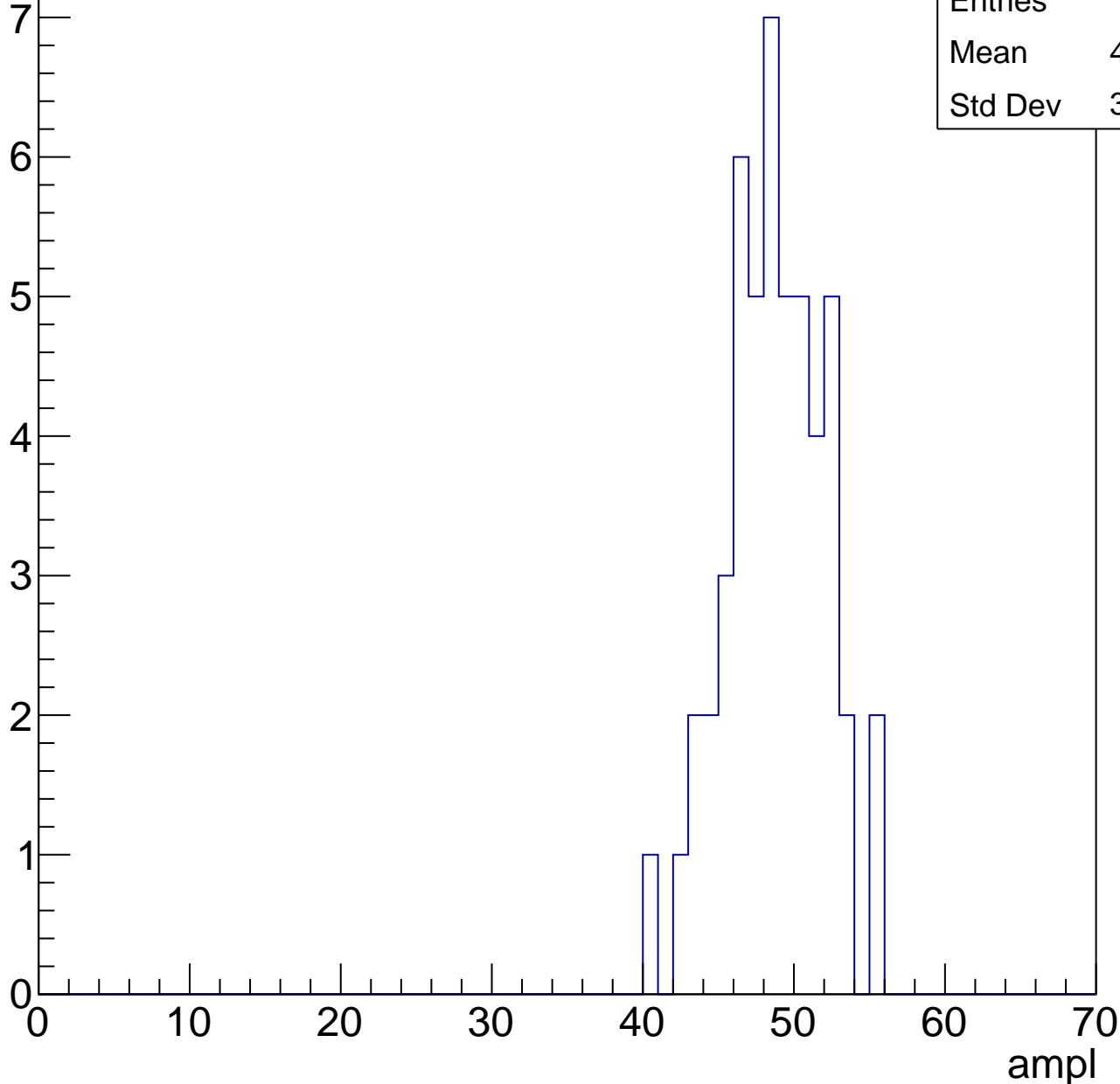


B1L102S, U18-ch50, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	48.26
Std Dev	3.236

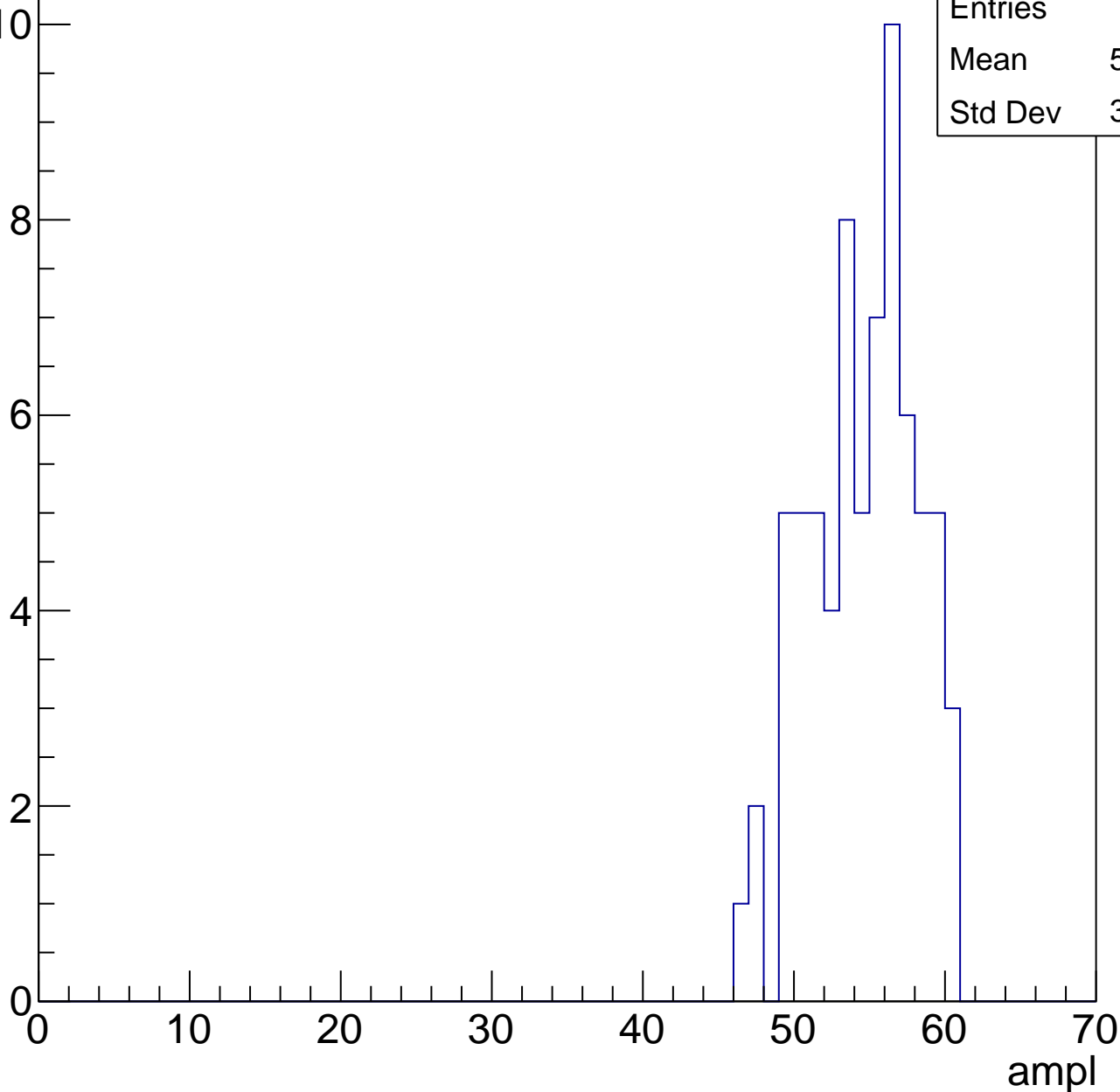


B1L102S, U18-ch50, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	54.14
Std Dev	3.457

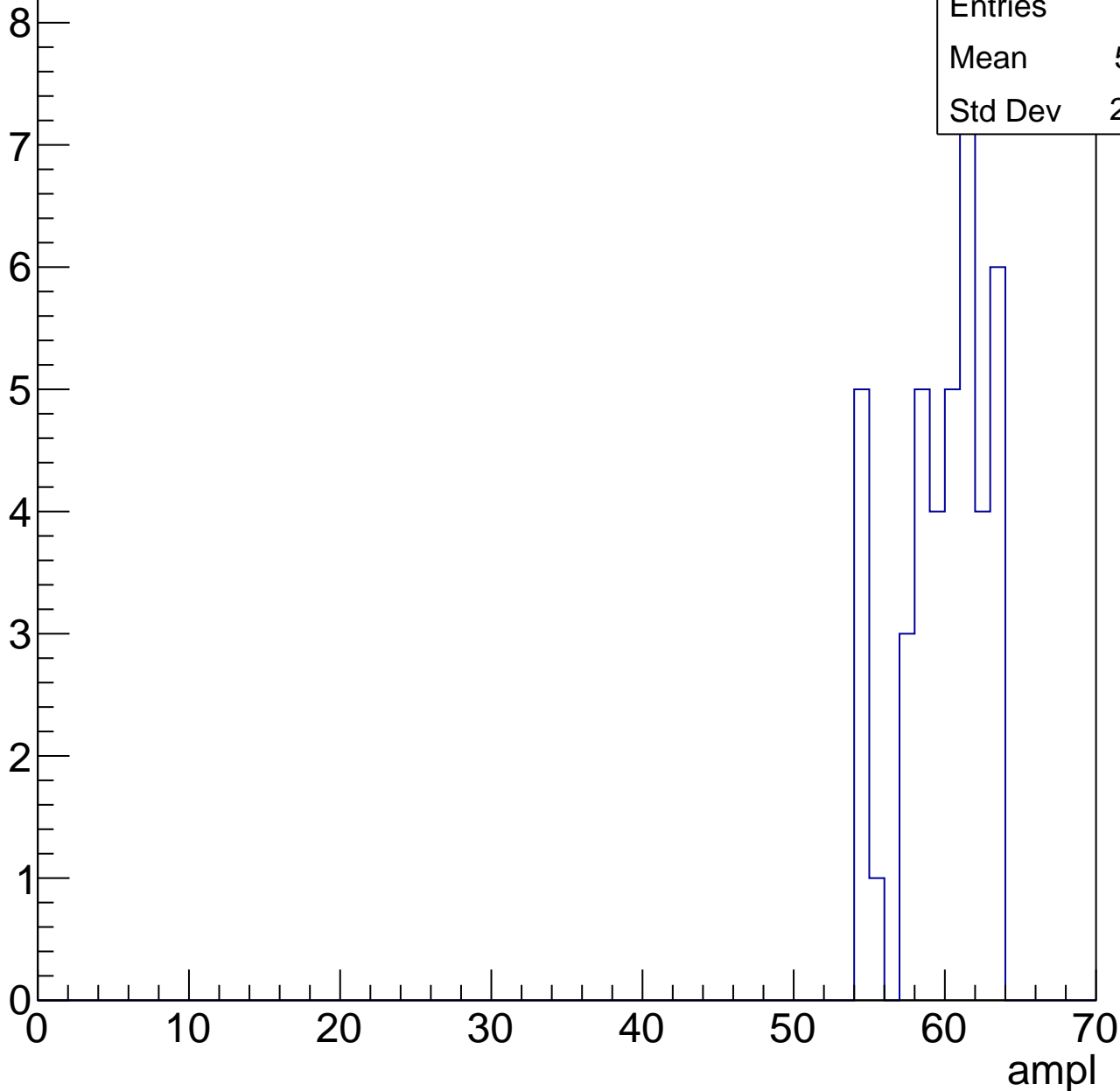


B1L102S, U18-ch50, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	59.41
Std Dev	2.794

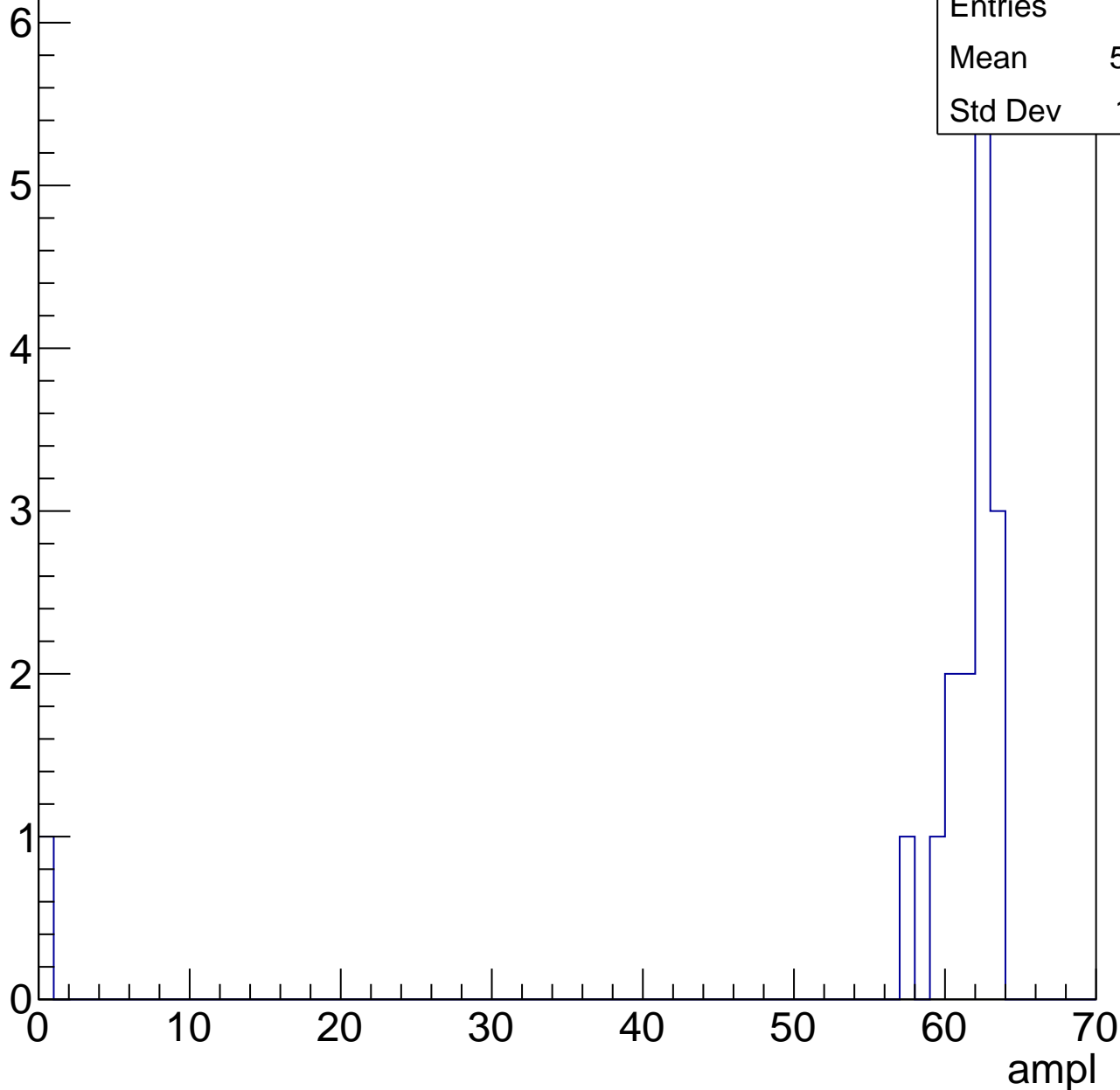


B1L102S, U18-ch50, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

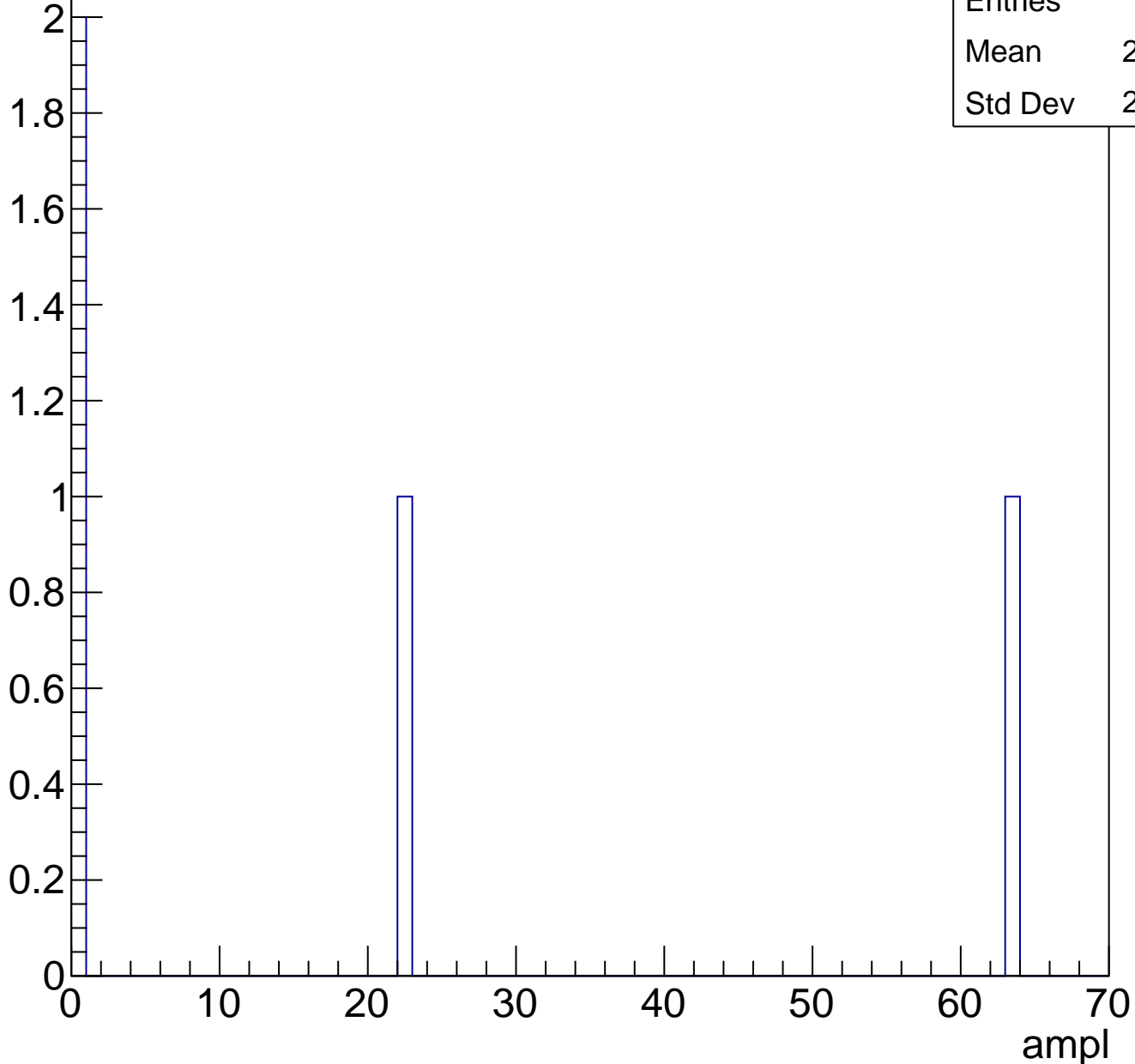
Entries	16
Mean	57.44
Std Dev	14.91



B1L102S, U18-ch50, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B1L102S, U18-ch51, adc0

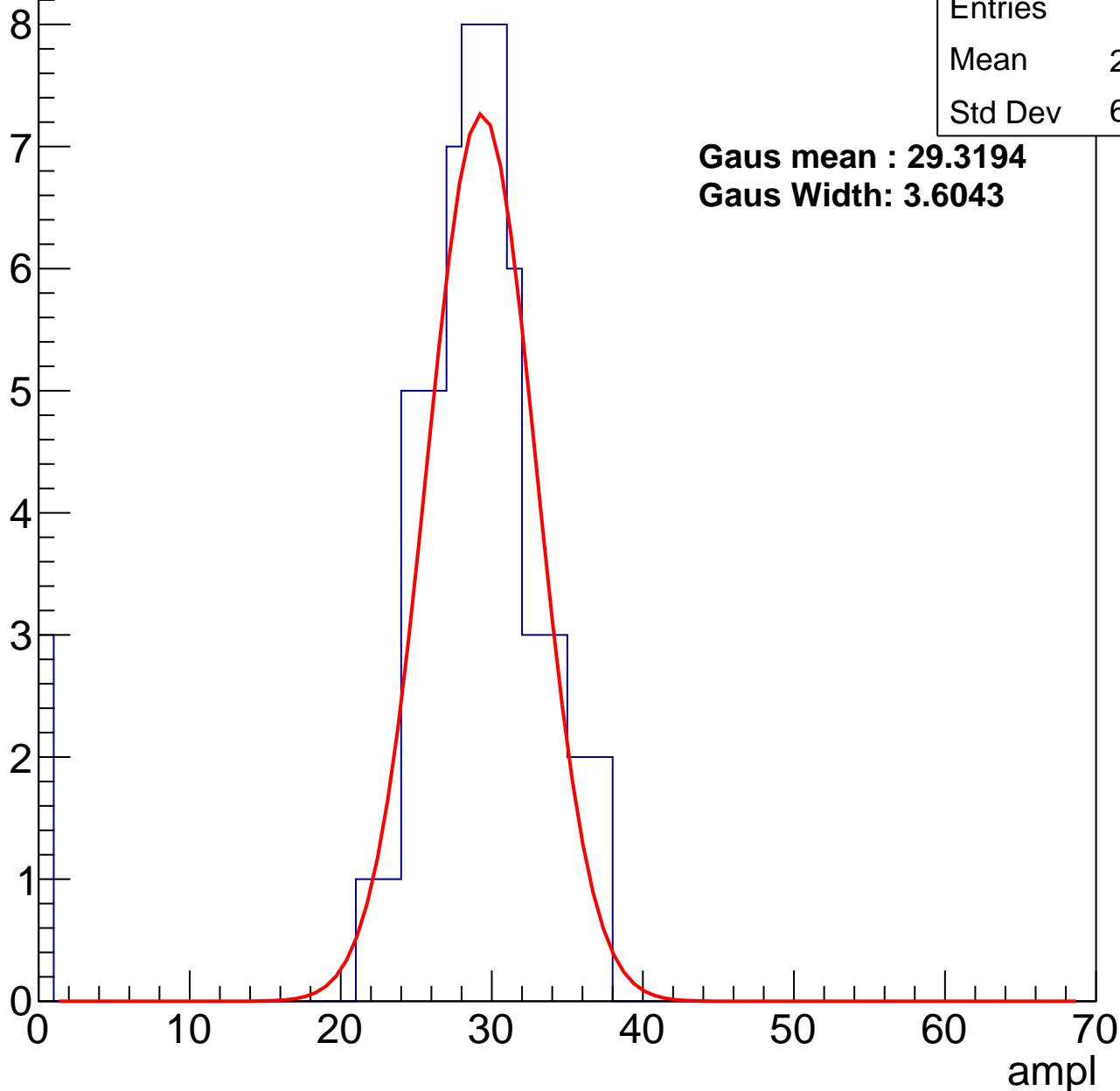
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.74
Std Dev	6.746

Gaus mean : 29.3194

Gaus Width: 3.6043



B1L102S, U18-ch51, adc1

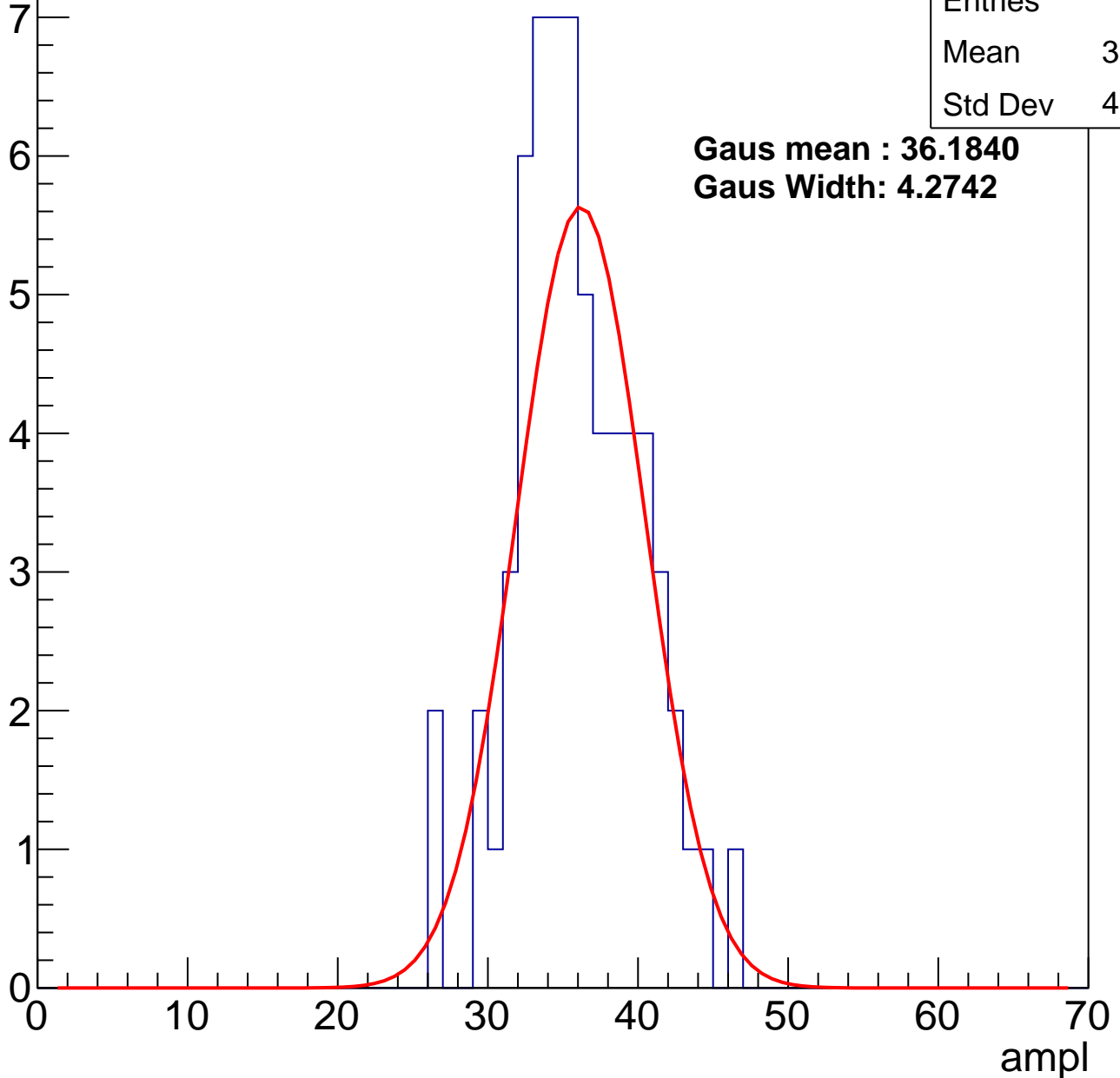
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	35.55
Std Dev	4.089

Gaus mean : 36.1840

Gaus Width: 4.2742



B1L102S, U18-ch51, adc2

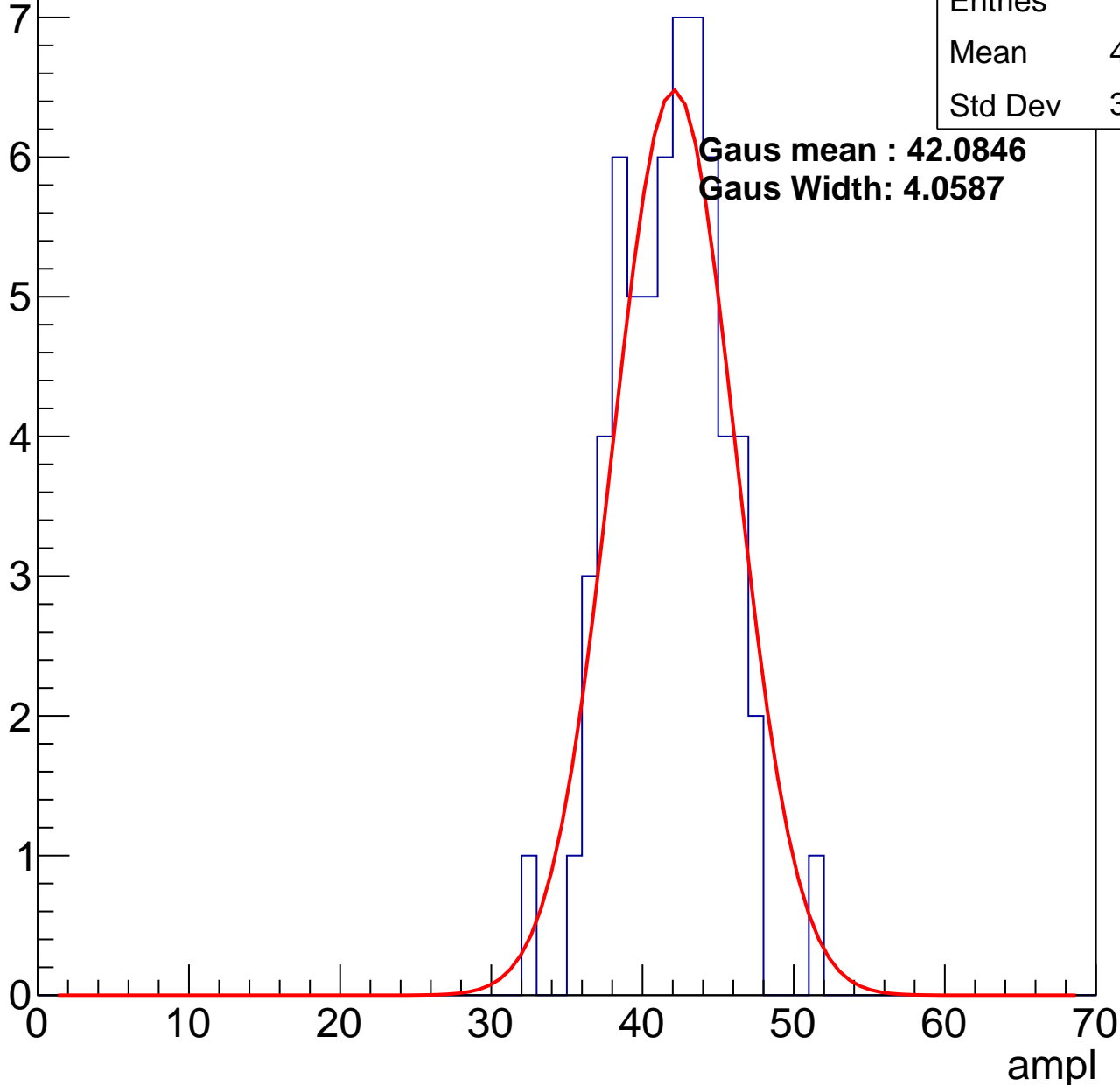
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	41.29
Std Dev	3.489

Gaus mean : 42.0846

Gaus Width: 4.0587

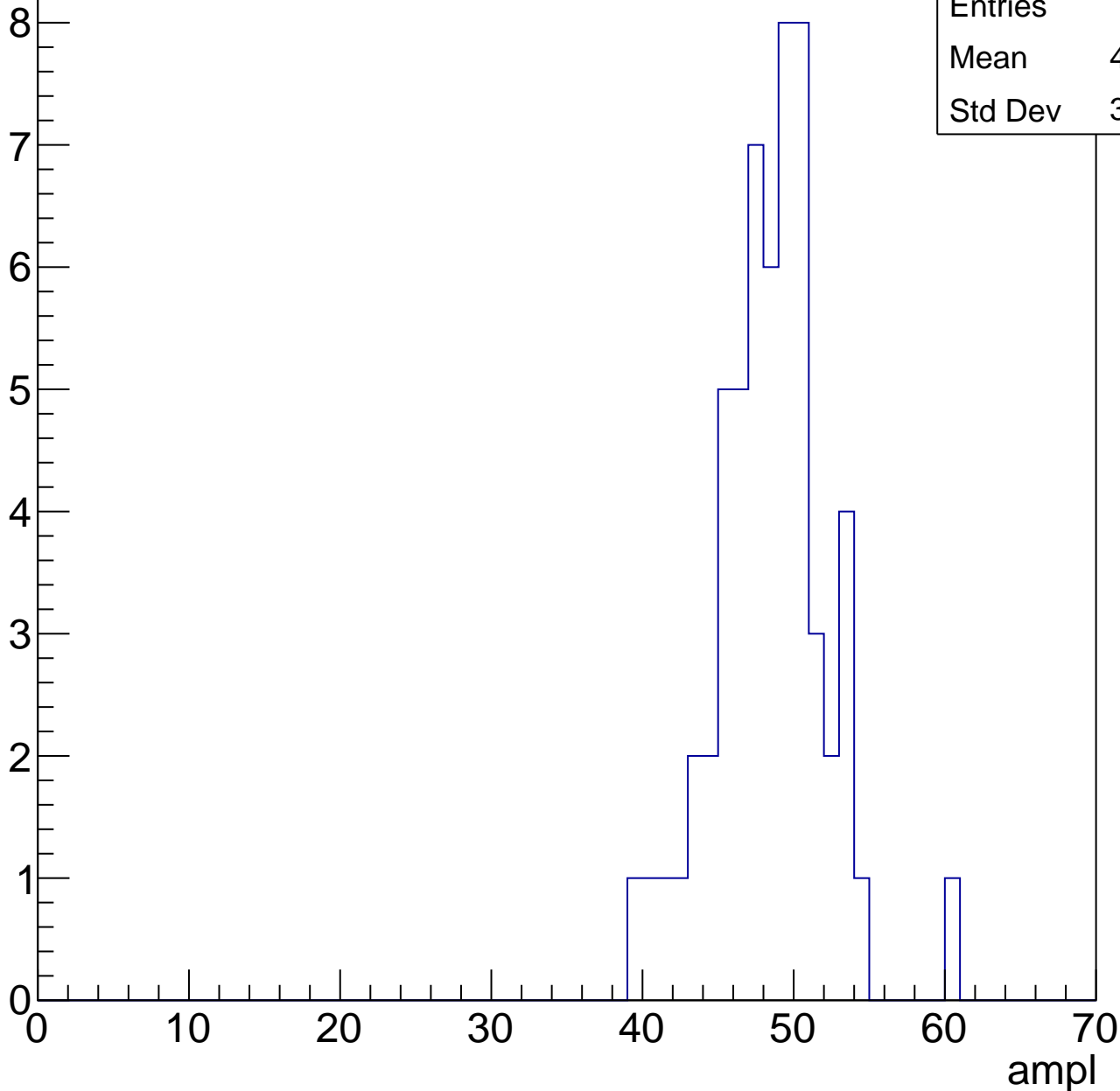


B1L102S, U18-ch51, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.98
Std Dev	3.627

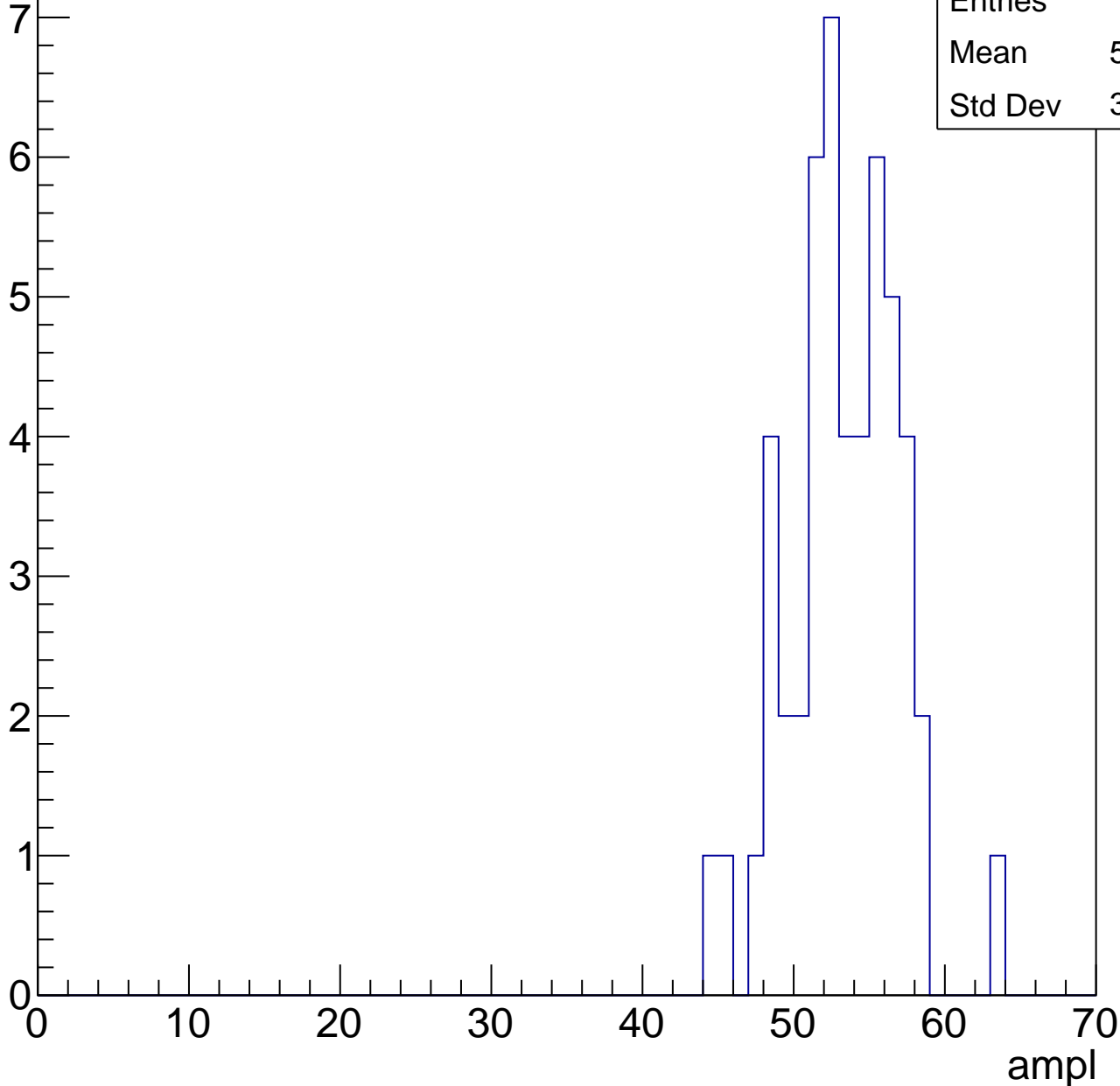


B1L102S, U18-ch51, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	52.82
Std Dev	3.598

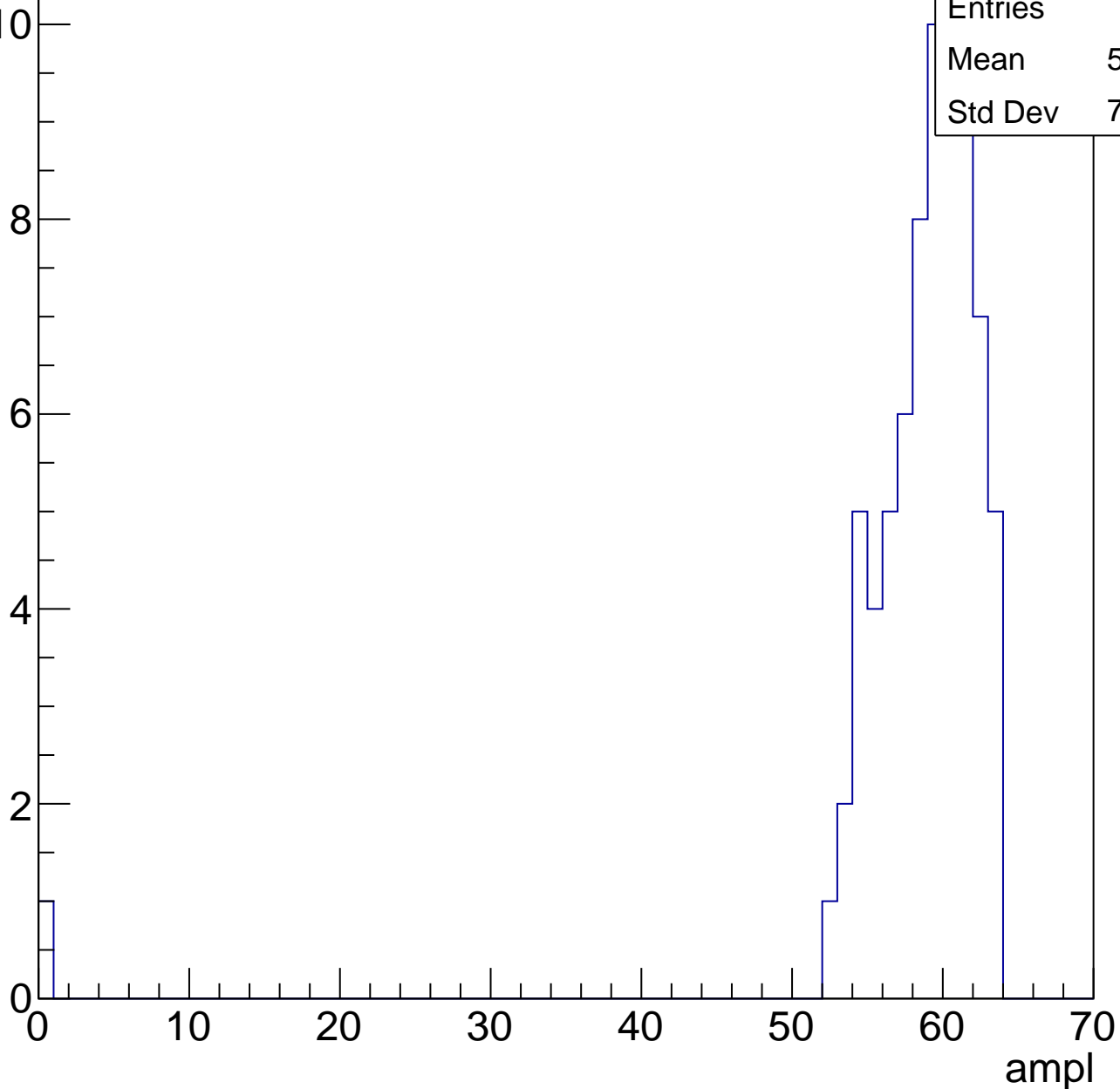


B1L102S, U18-ch51, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

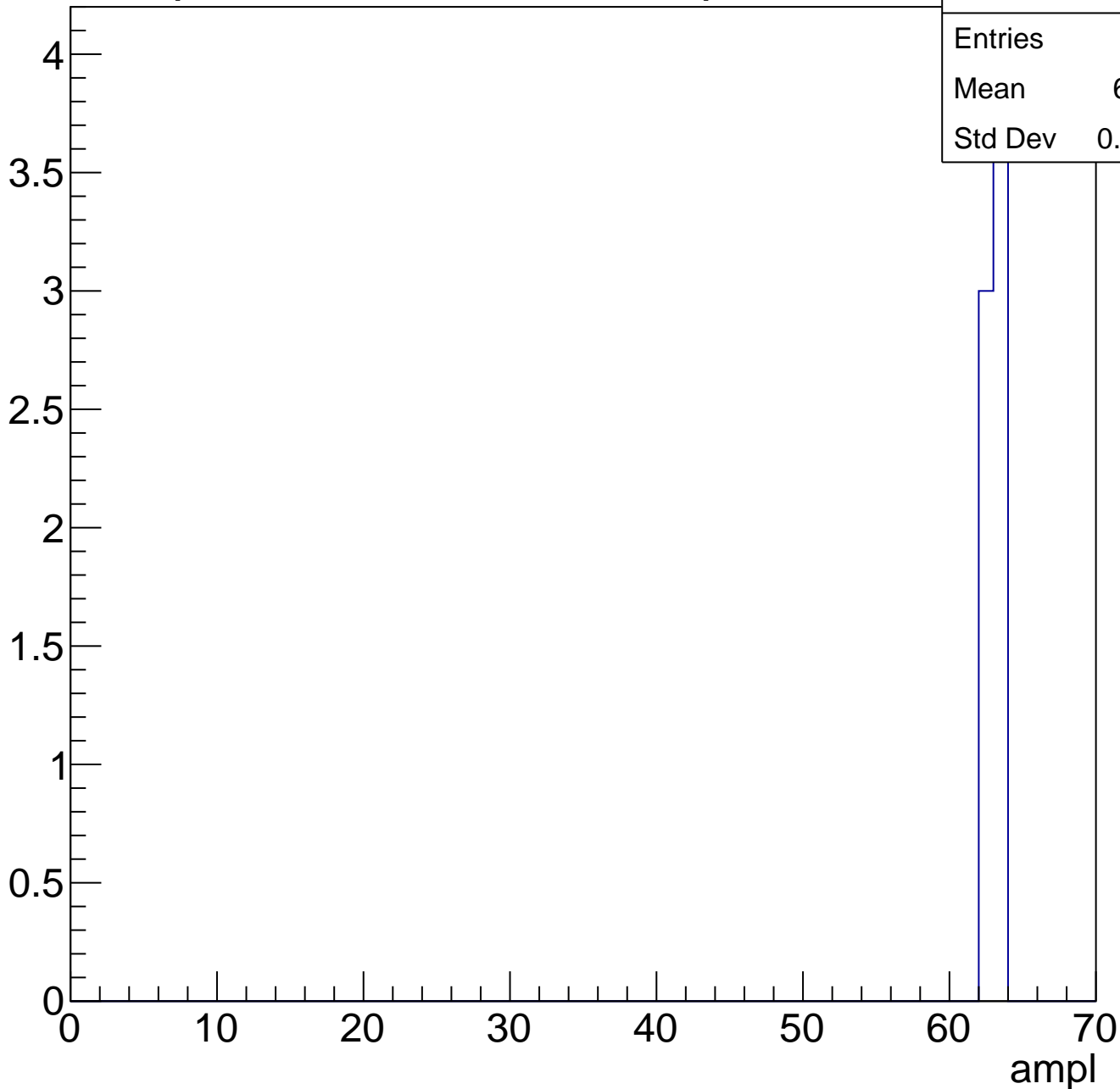
Entries	73
Mean	57.85
Std Dev	7.368



B1L102S, U18-ch51, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch51, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch52, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	28.55
Std Dev	5.923

Gaus mean : 29.8253

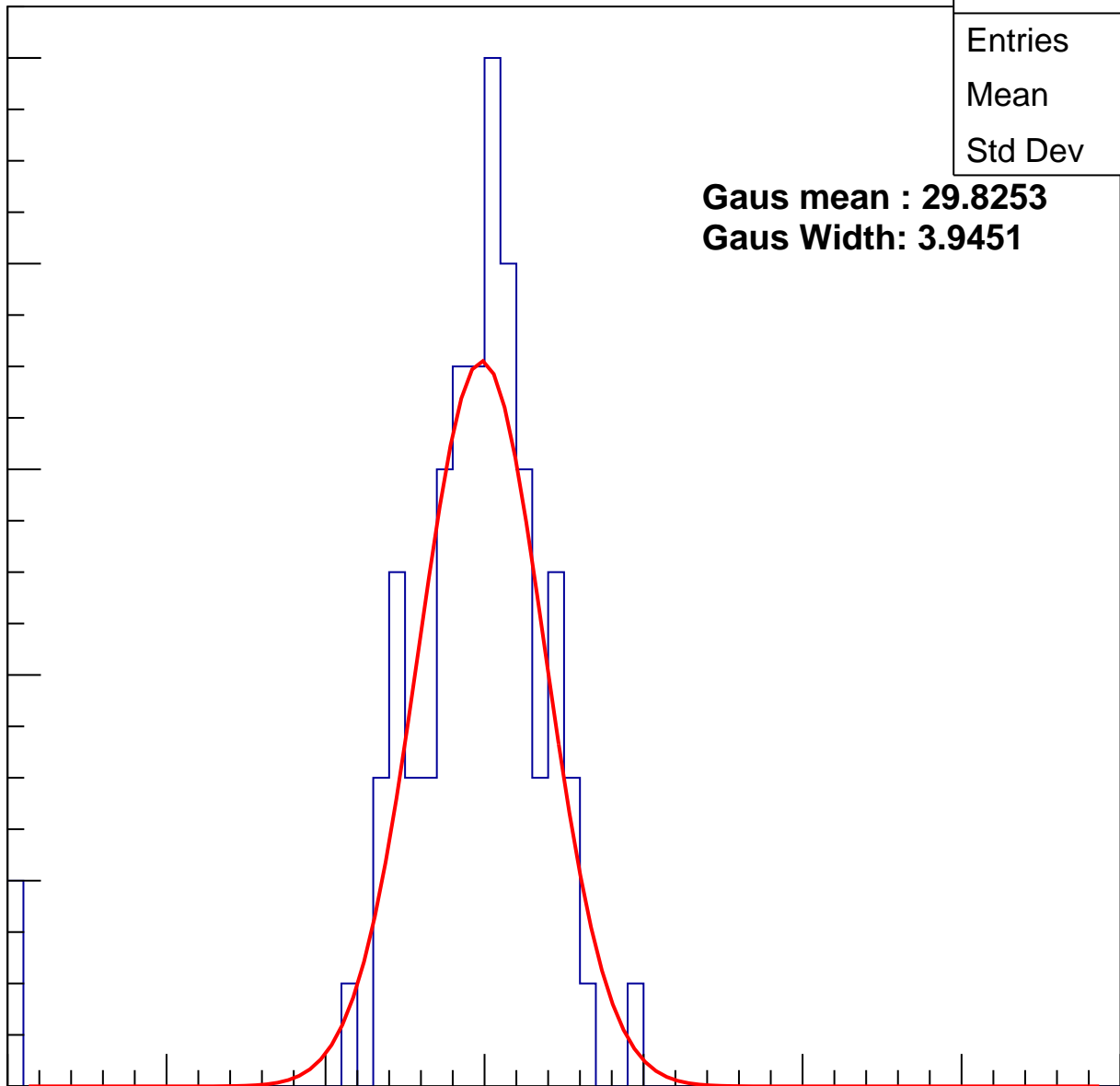
Gaus Width: 3.9451

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch52, adc1

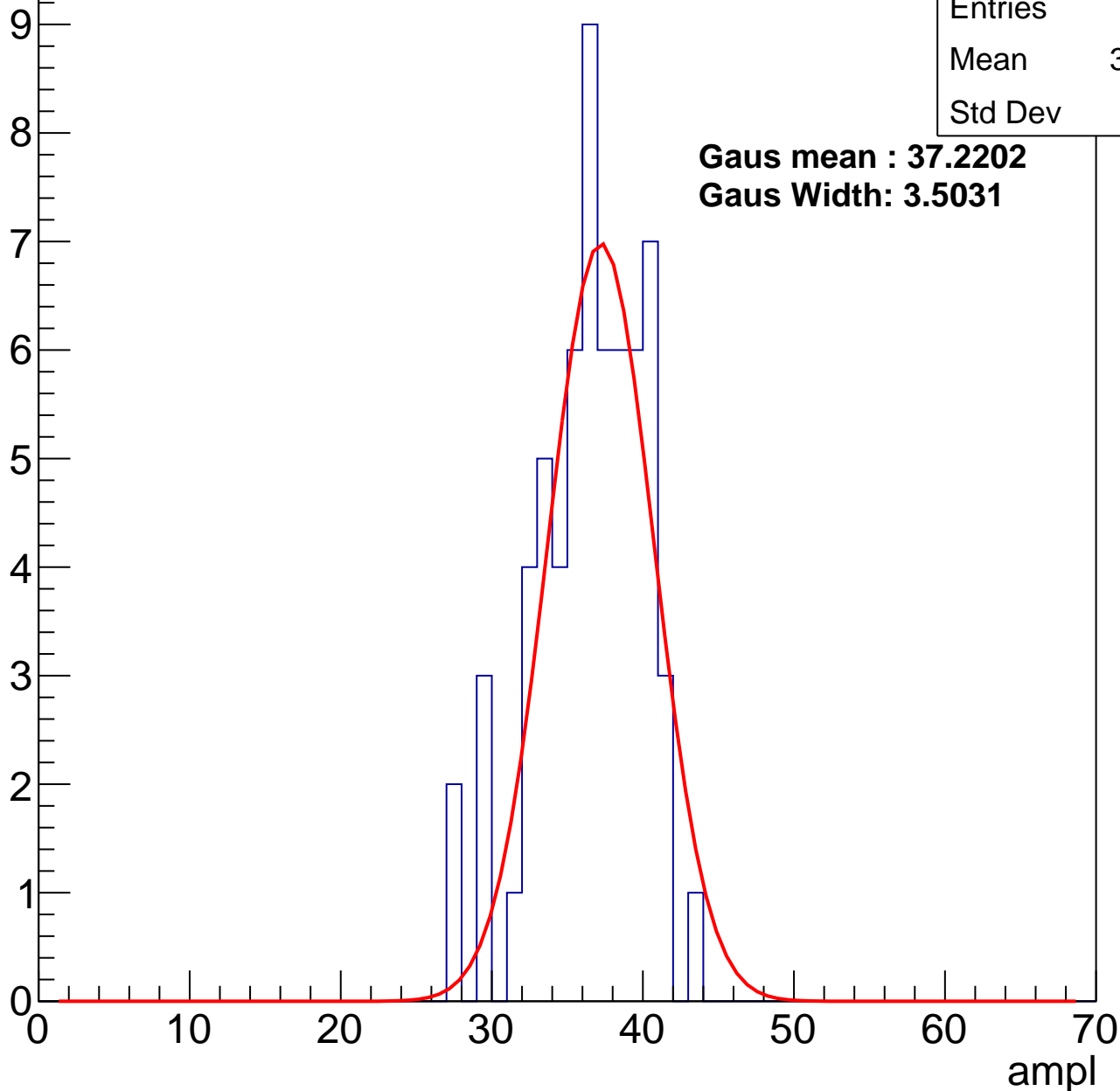
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	35.95
Std Dev	3.53

Gaus mean : 37.2202

Gaus Width: 3.5031



B1L102S, U18-ch52, adc2

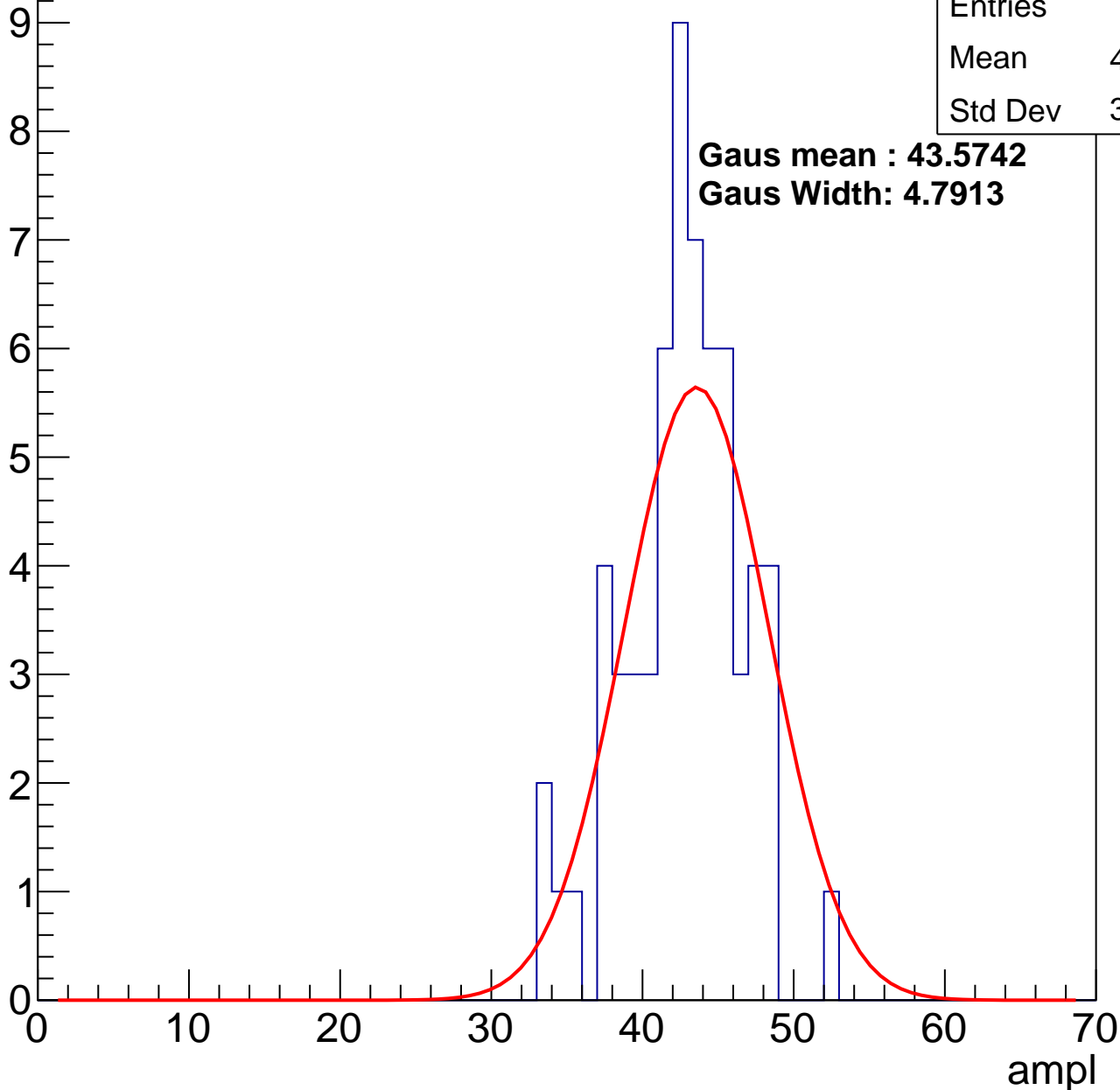
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.27
Std Dev	3.864

Gaus mean : 43.5742

Gaus Width: 4.7913

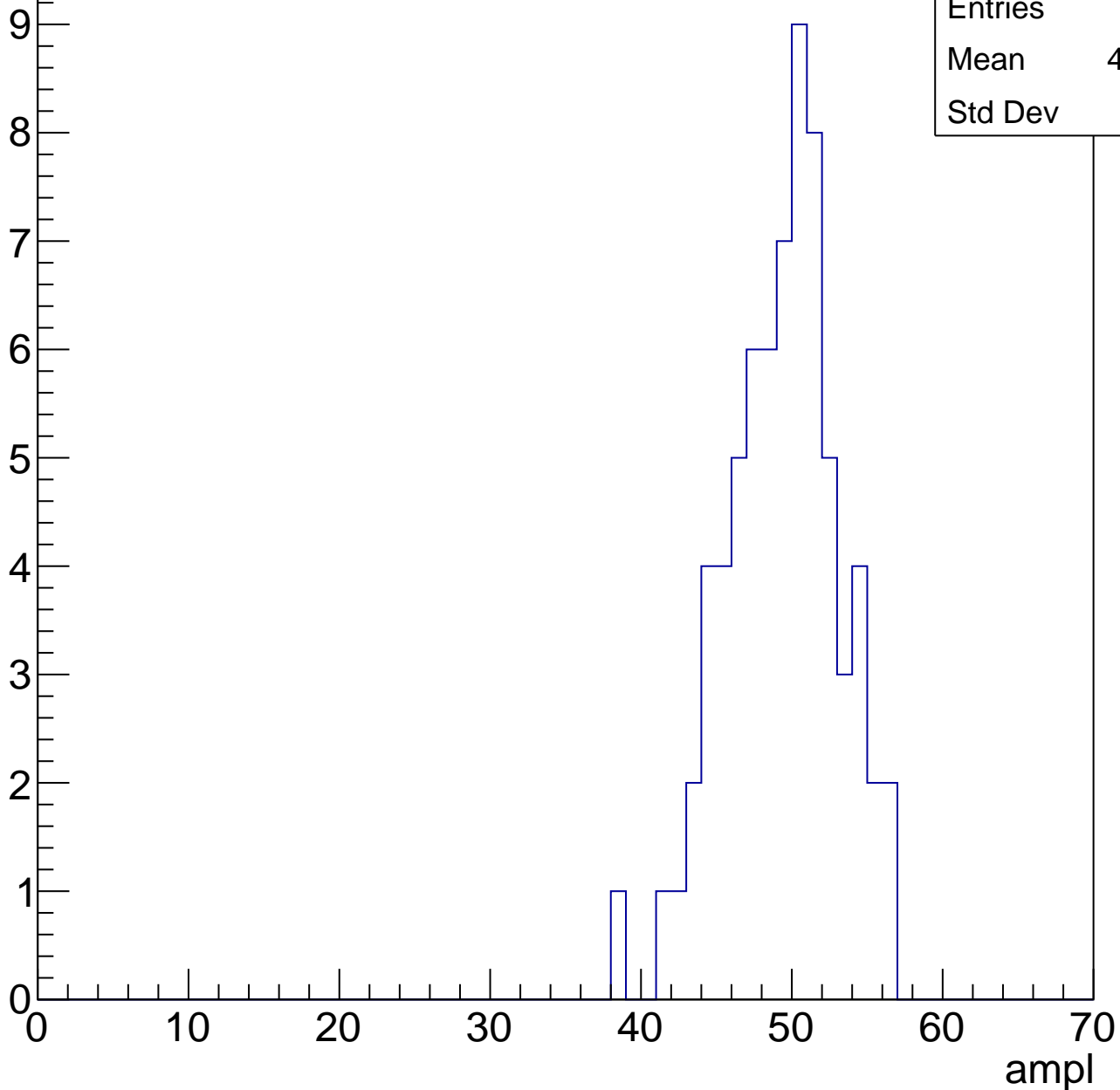


B1L102S, U18-ch52, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.87
Std Dev	3.68

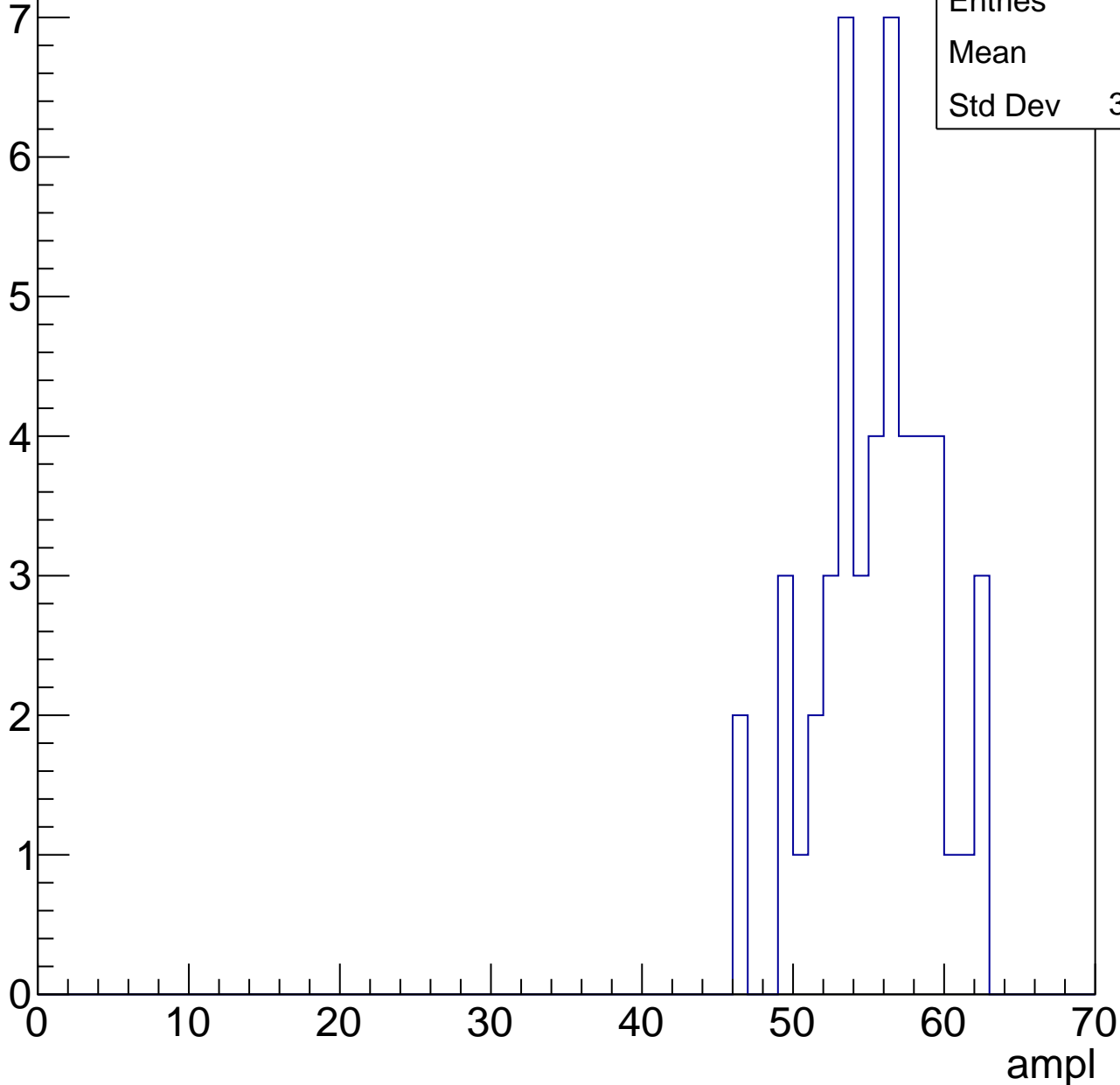


B1L102S, U18-ch52, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	55
Std Dev	3.823

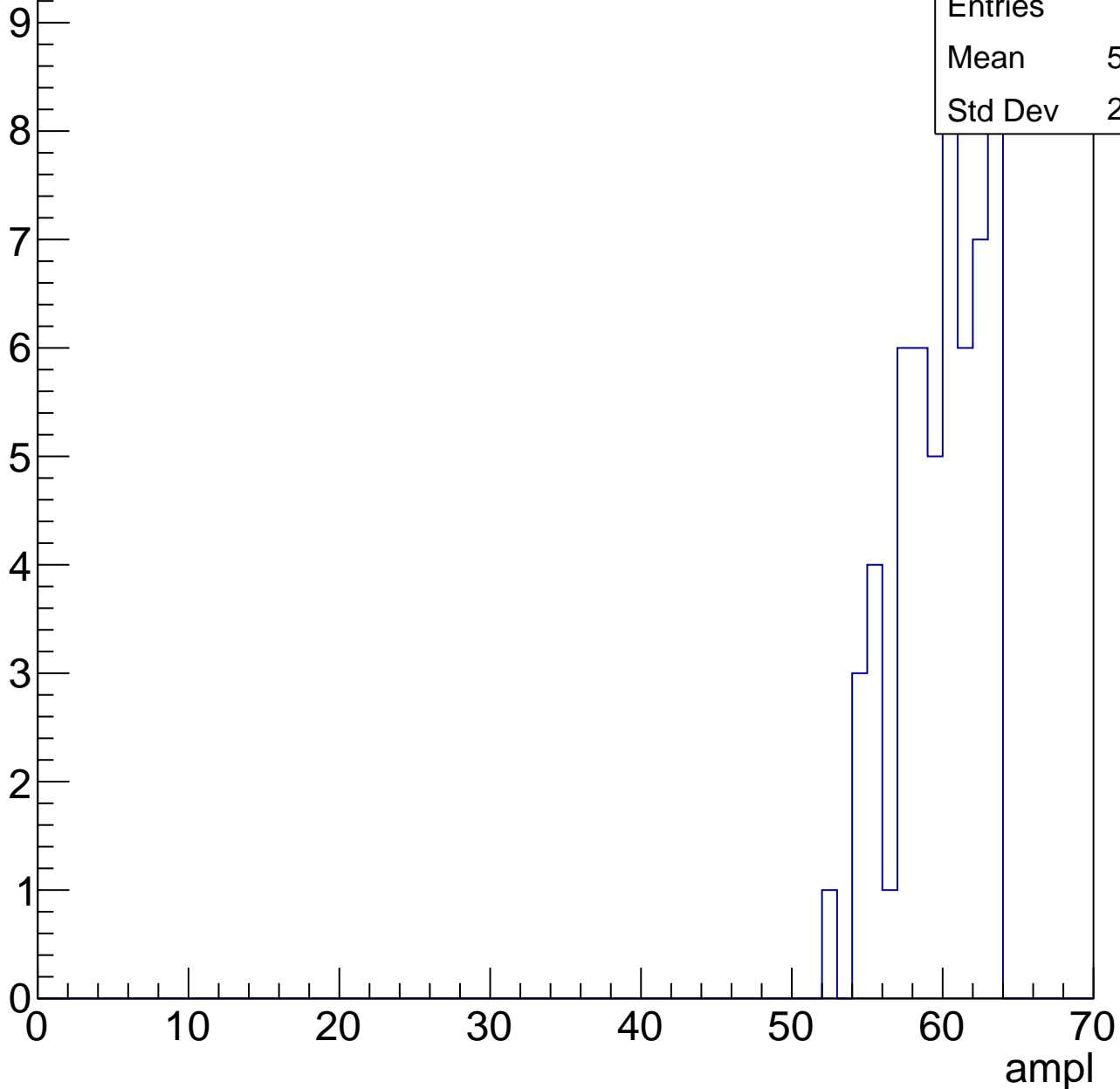


B1L102S, U18-ch52, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

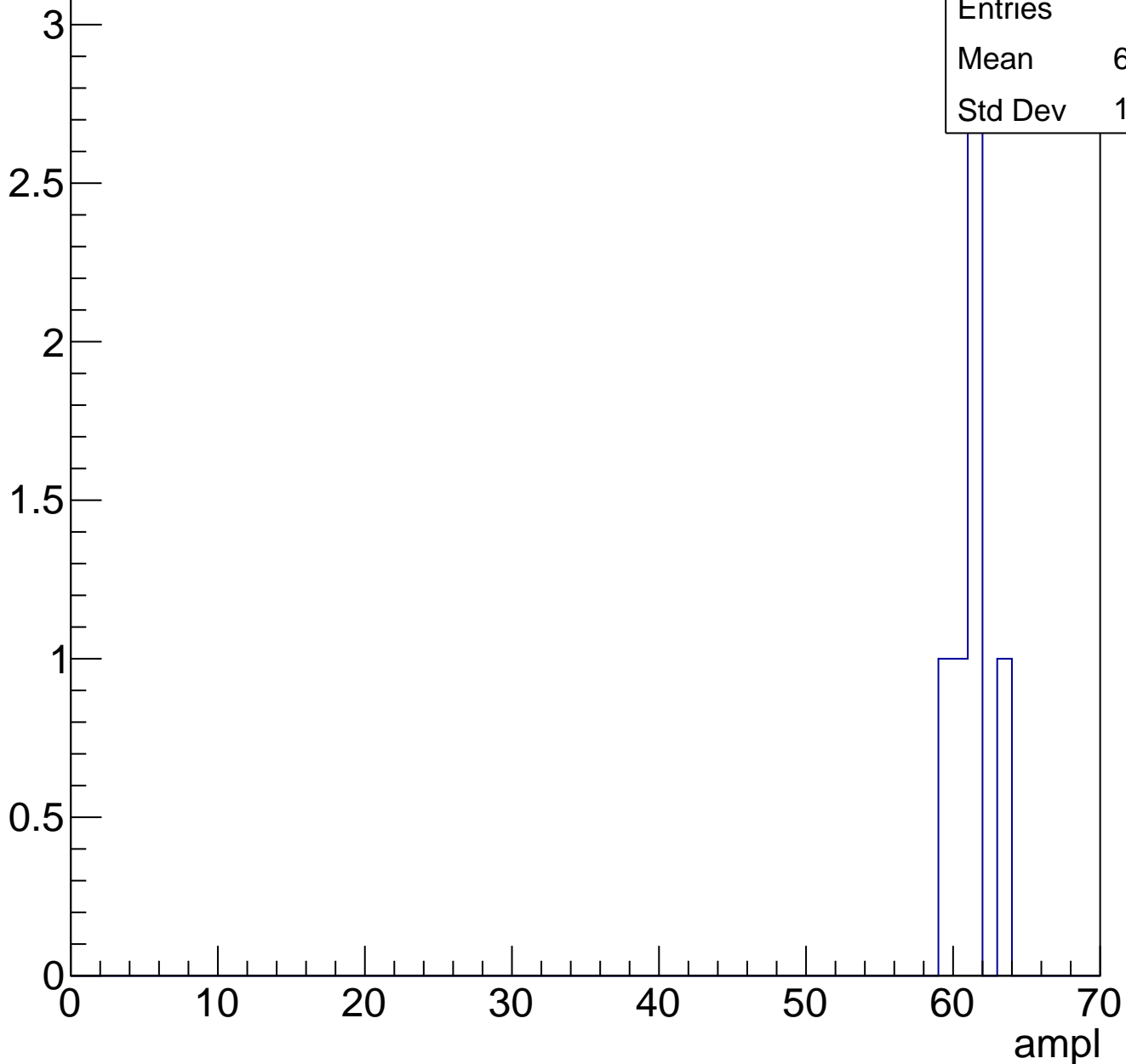
Entries	56
Mean	59.32
Std Dev	2.867



B1L102S, U18-ch52, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch52, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch53, adc0

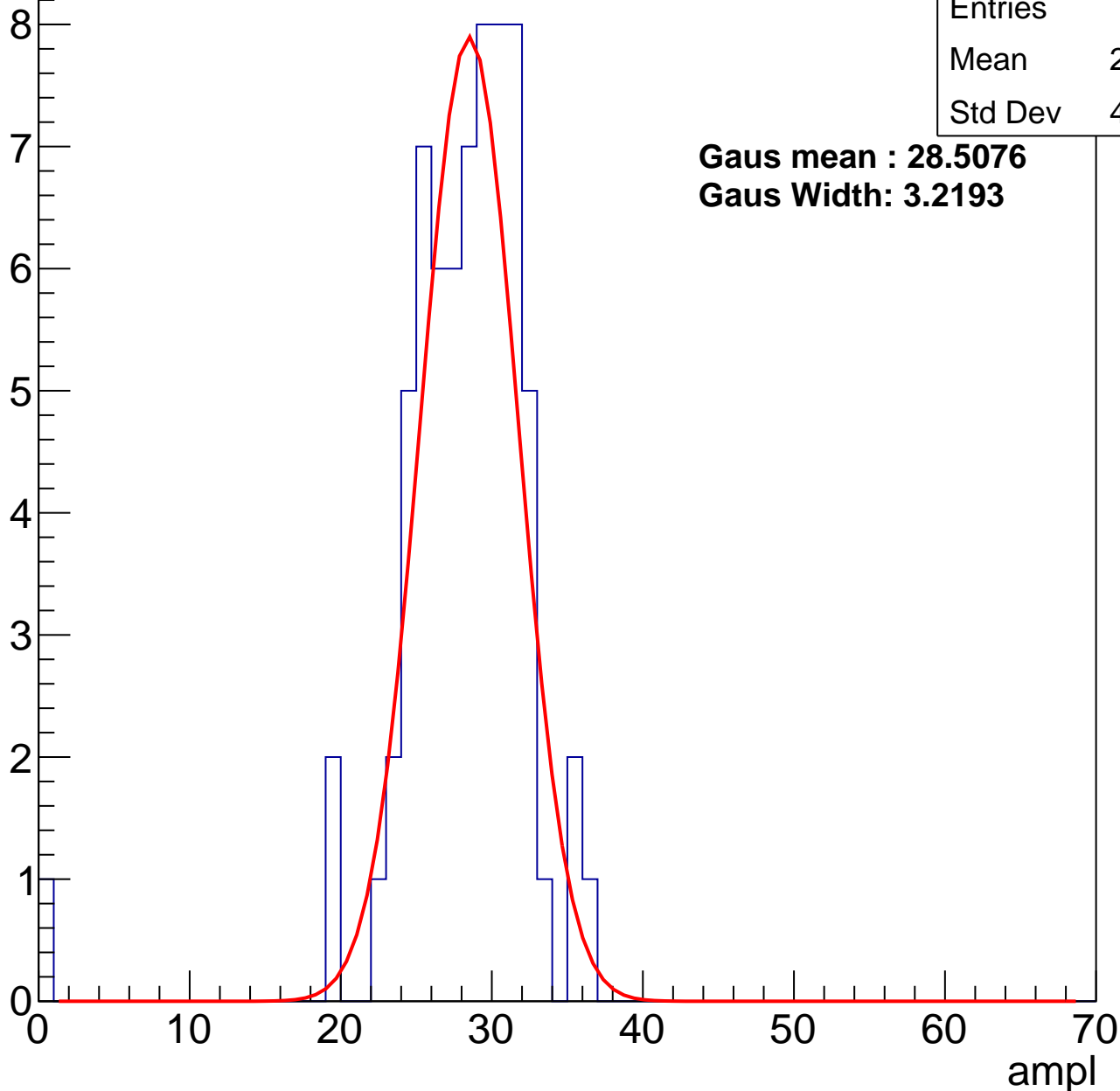
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	27.63
Std Dev	4.742

Gaus mean : 28.5076

Gaus Width: 3.2193



B1L102S, U18-ch53, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	77
Mean	35.13
Std Dev	3.886

Gaus mean : 35.7778

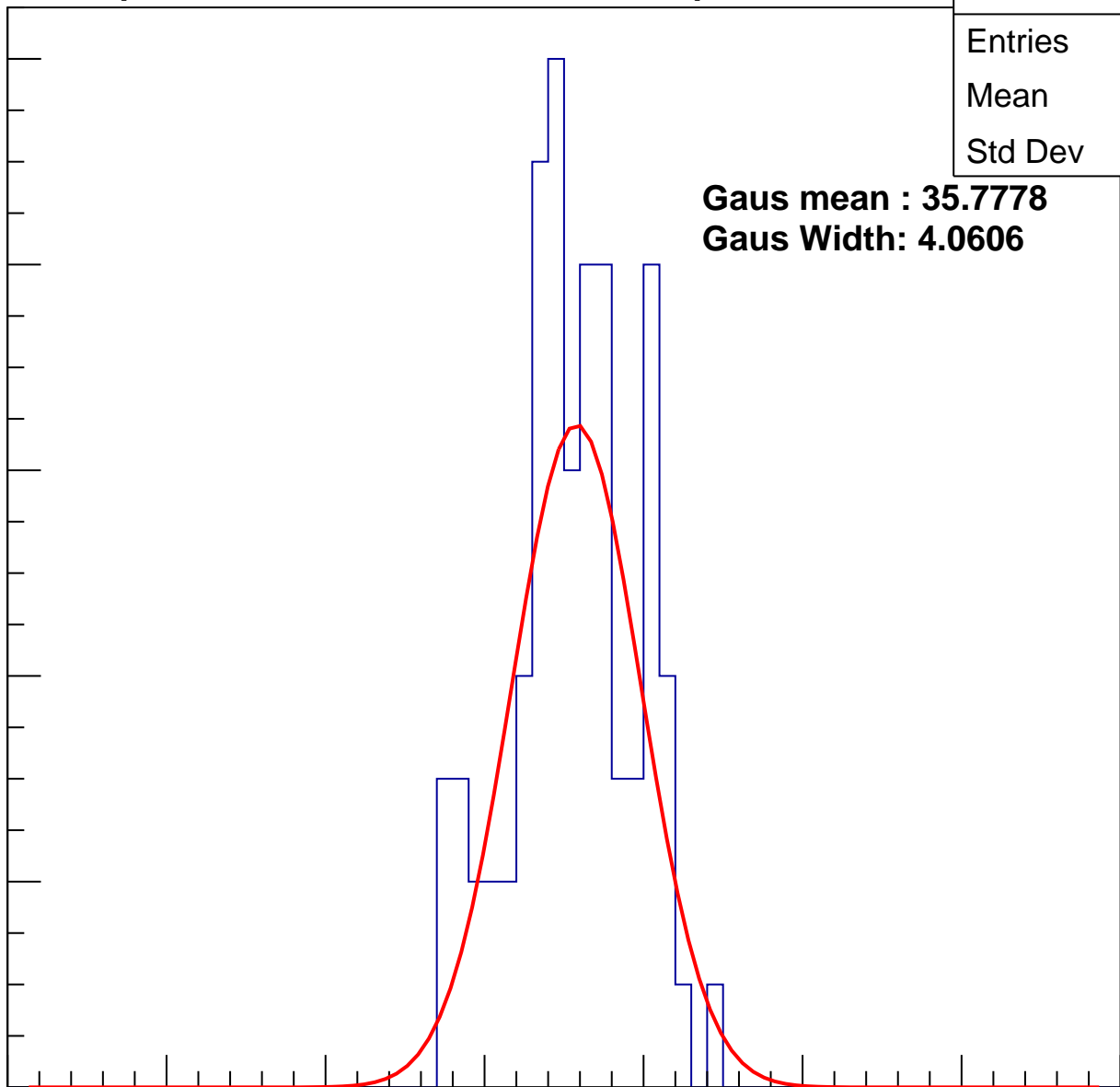
Gaus Width: 4.0606

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch53, adc2

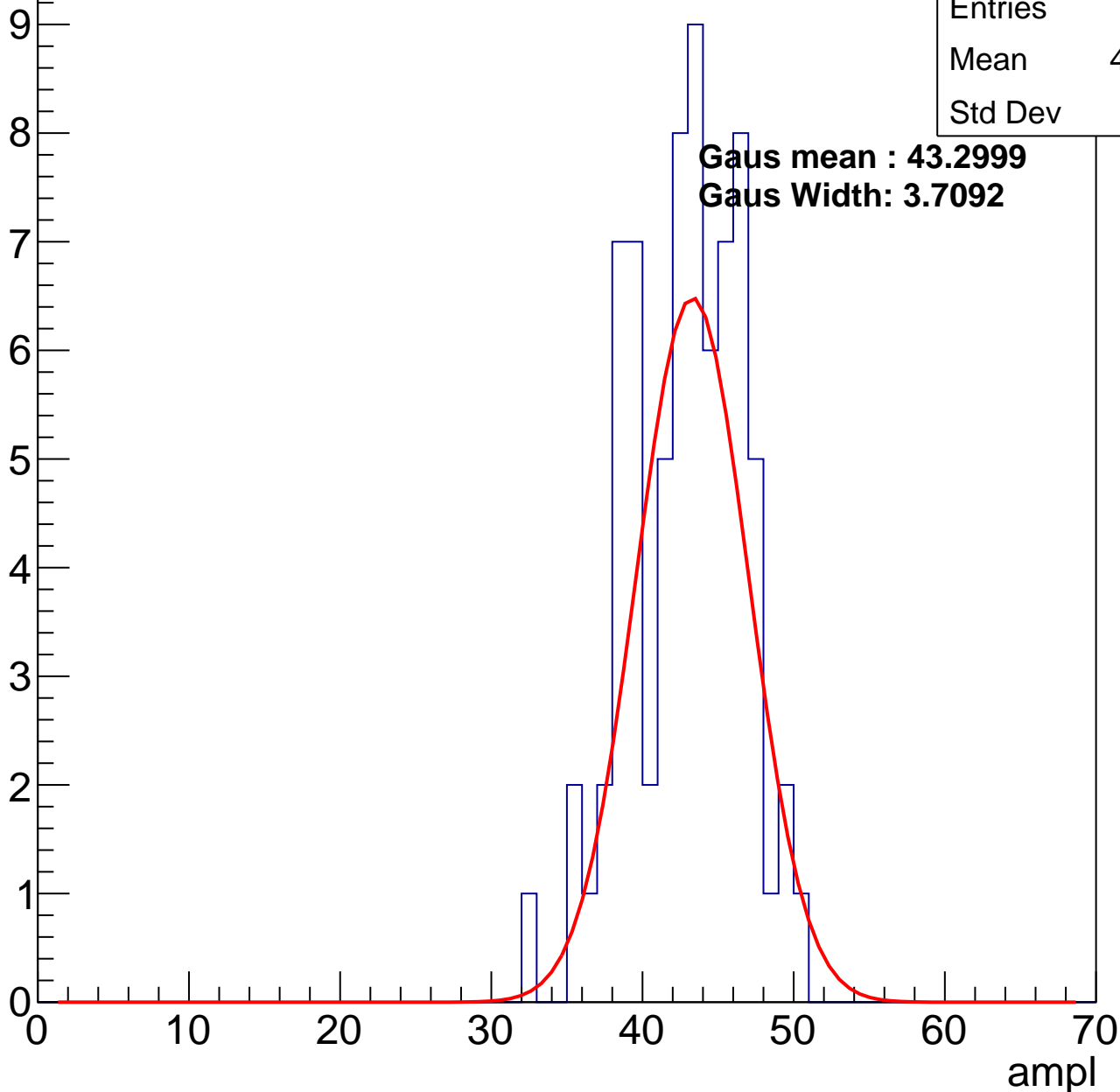
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	42.39
Std Dev	3.69

Gaus mean : 43.2999

Gaus Width: 3.7092

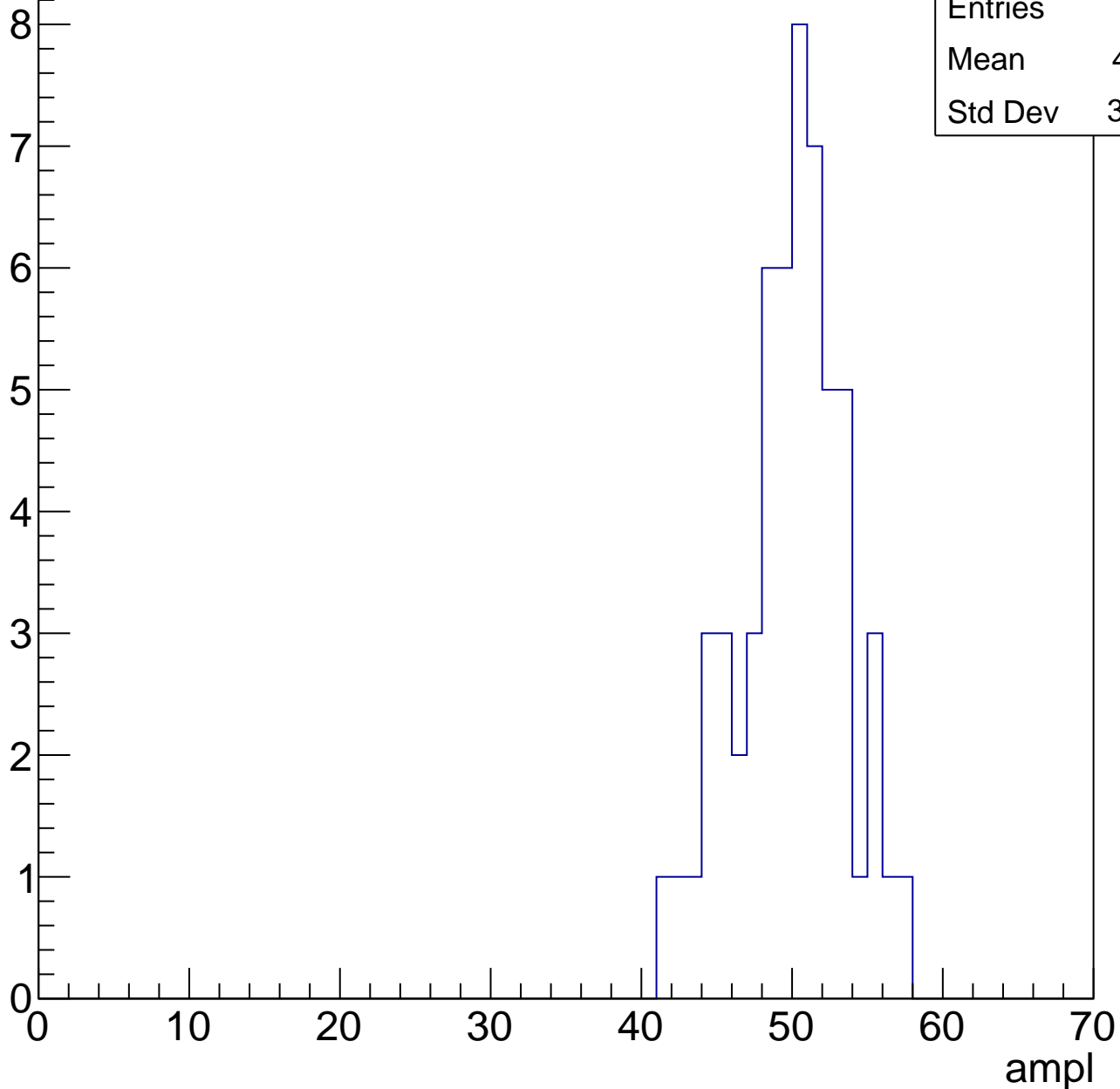


B1L102S, U18-ch53, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	49.51
Std Dev	3.505

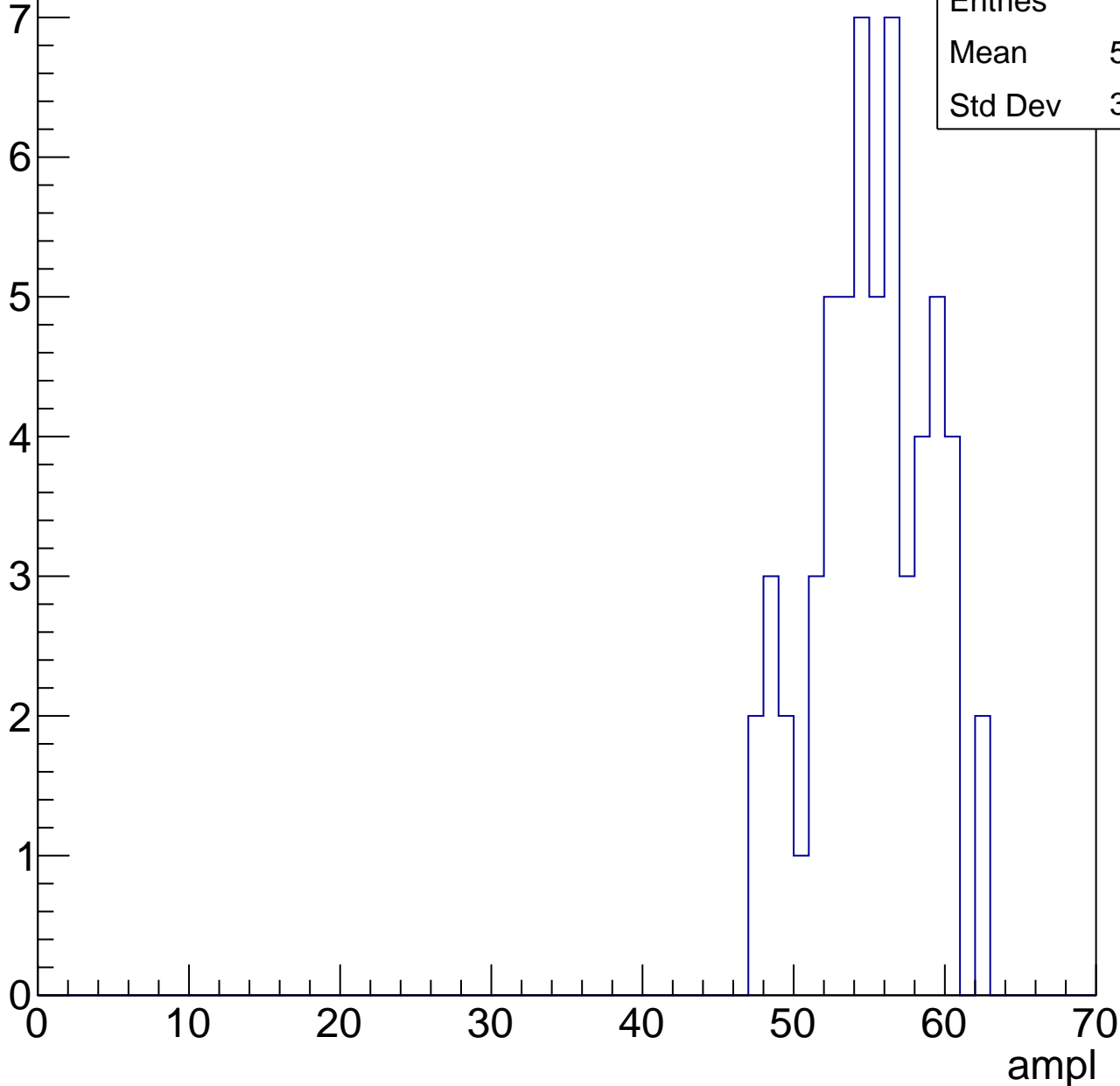


B1L102S, U18-ch53, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

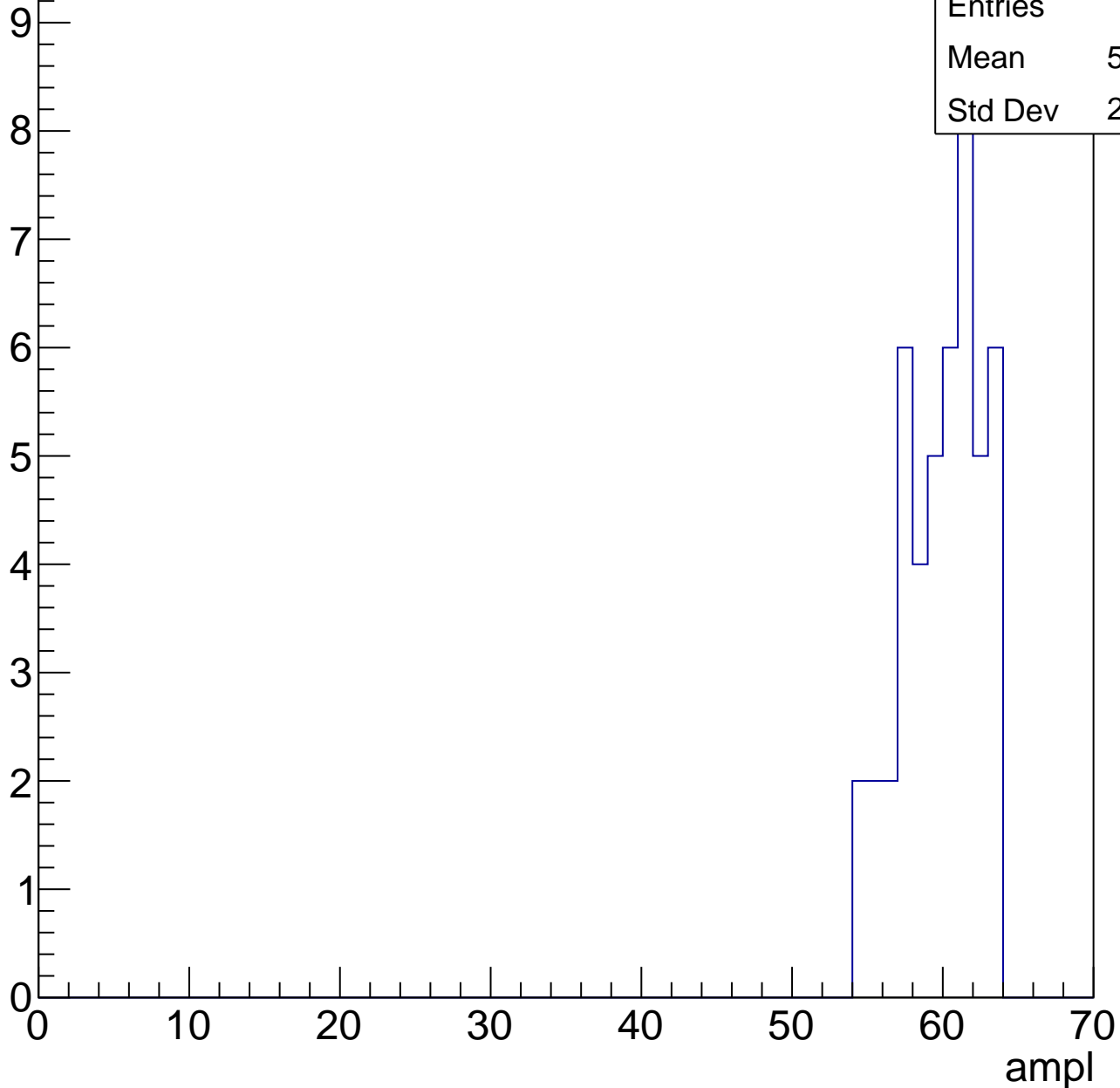
Entries	58
Mean	54.67
Std Dev	3.757



B1L102S, U18-ch53, adc5

calib_packv5_042523_0143.root, FC#11, port A2

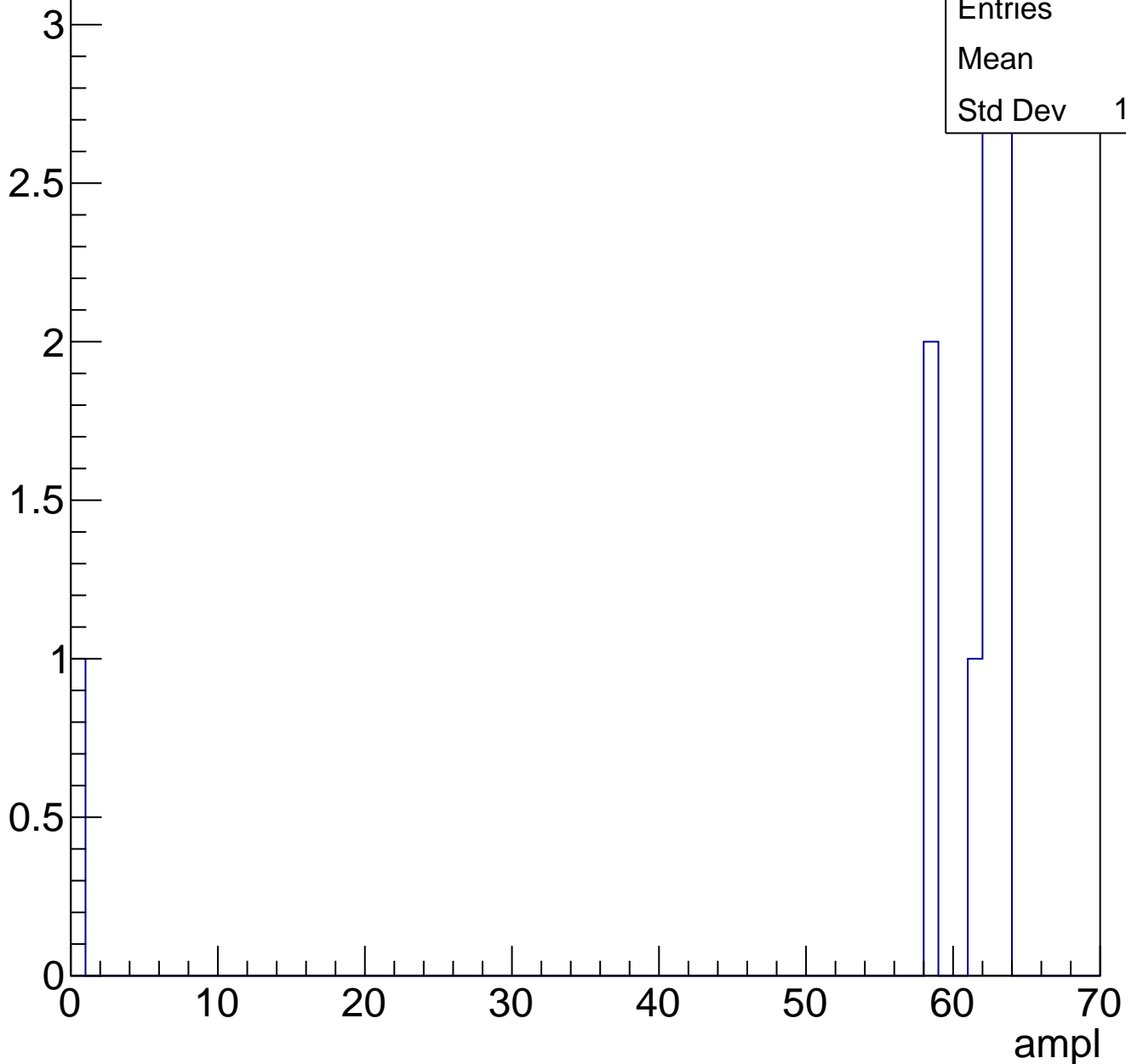
Entry



B1L102S, U18-ch53, adc6

calib_packv5_042523_0143.root, FC#11, port A2

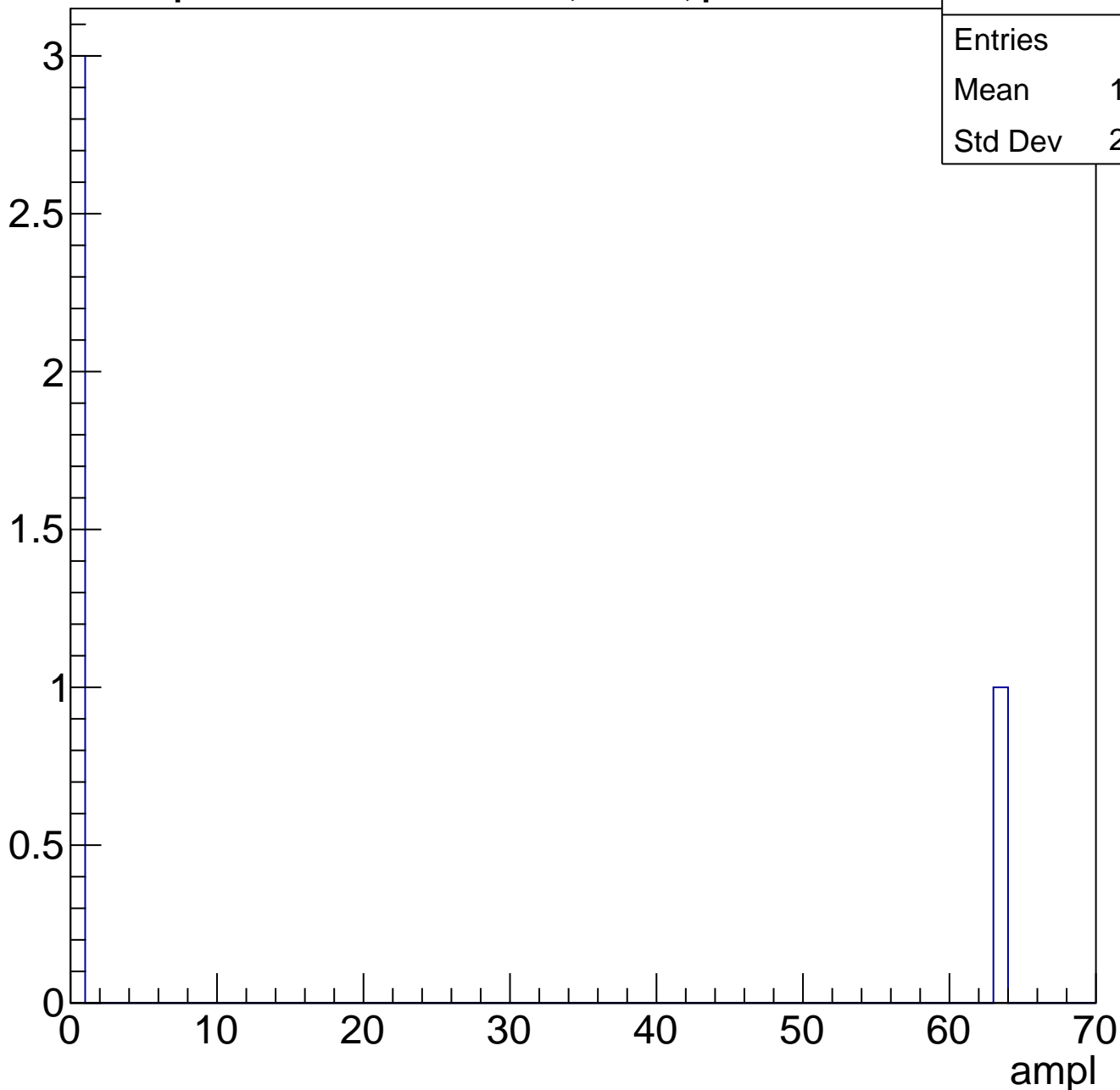
Entry



B1L102S, U18-ch53, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	15.75
Std Dev	27.28

B1L102S, U18-ch54, adc0

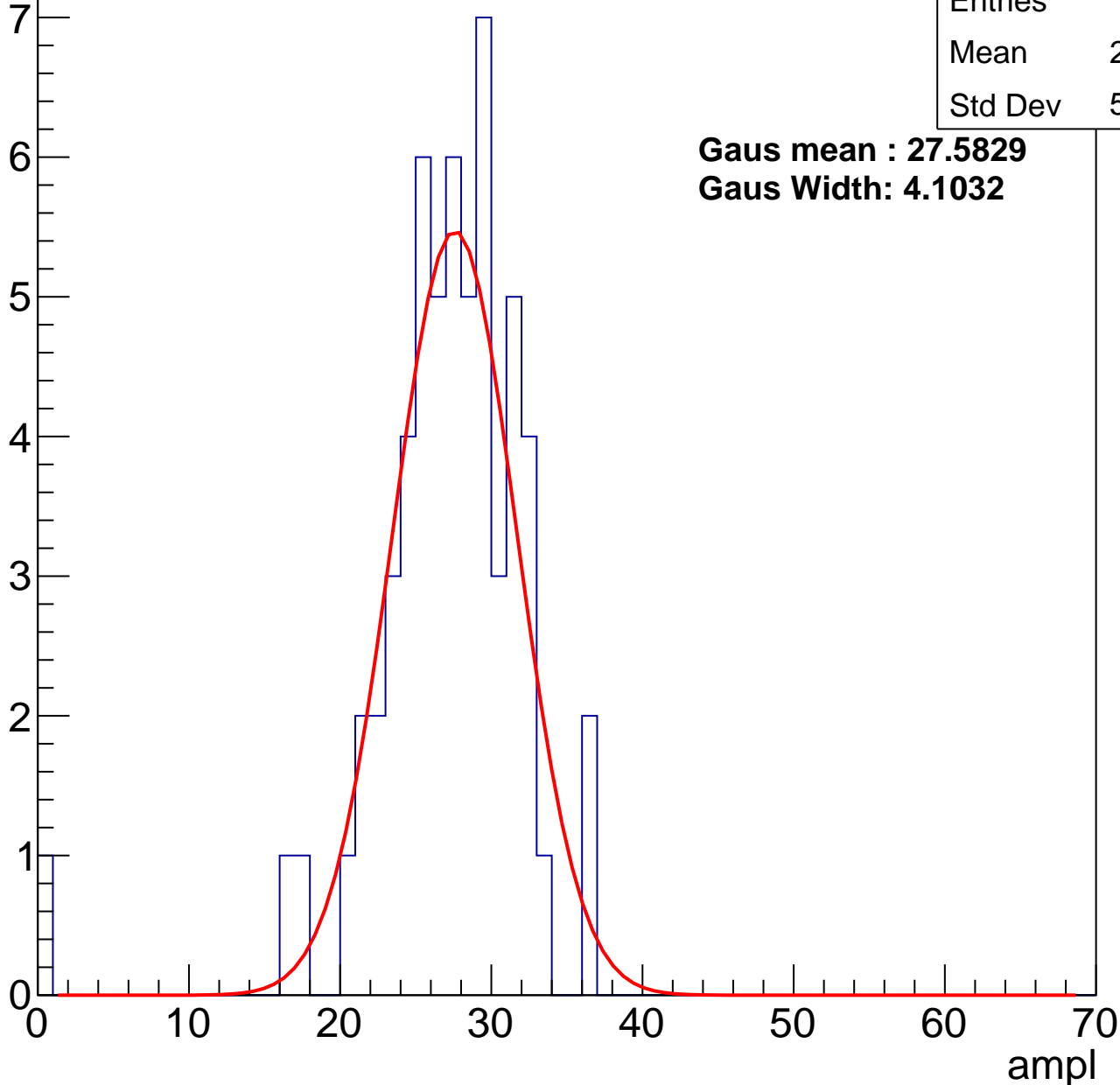
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	26.56
Std Dev	5.305

Gaus mean : 27.5829

Gaus Width: 4.1032



B1L102S, U18-ch54, adc1

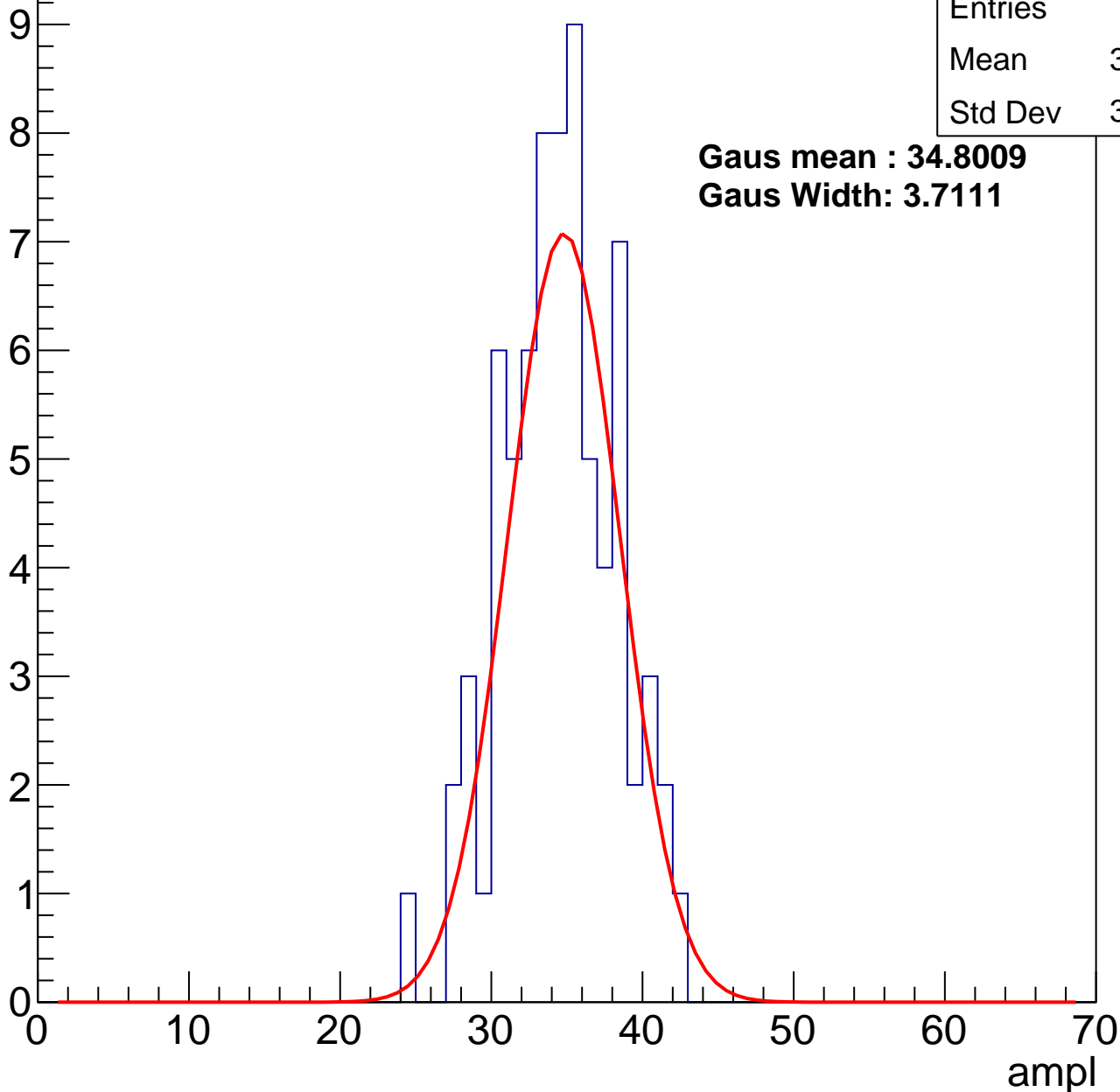
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	34.04
Std Dev	3.703

Gaus mean : 34.8009

Gaus Width: 3.7111



B1L102S, U18-ch54, adc2

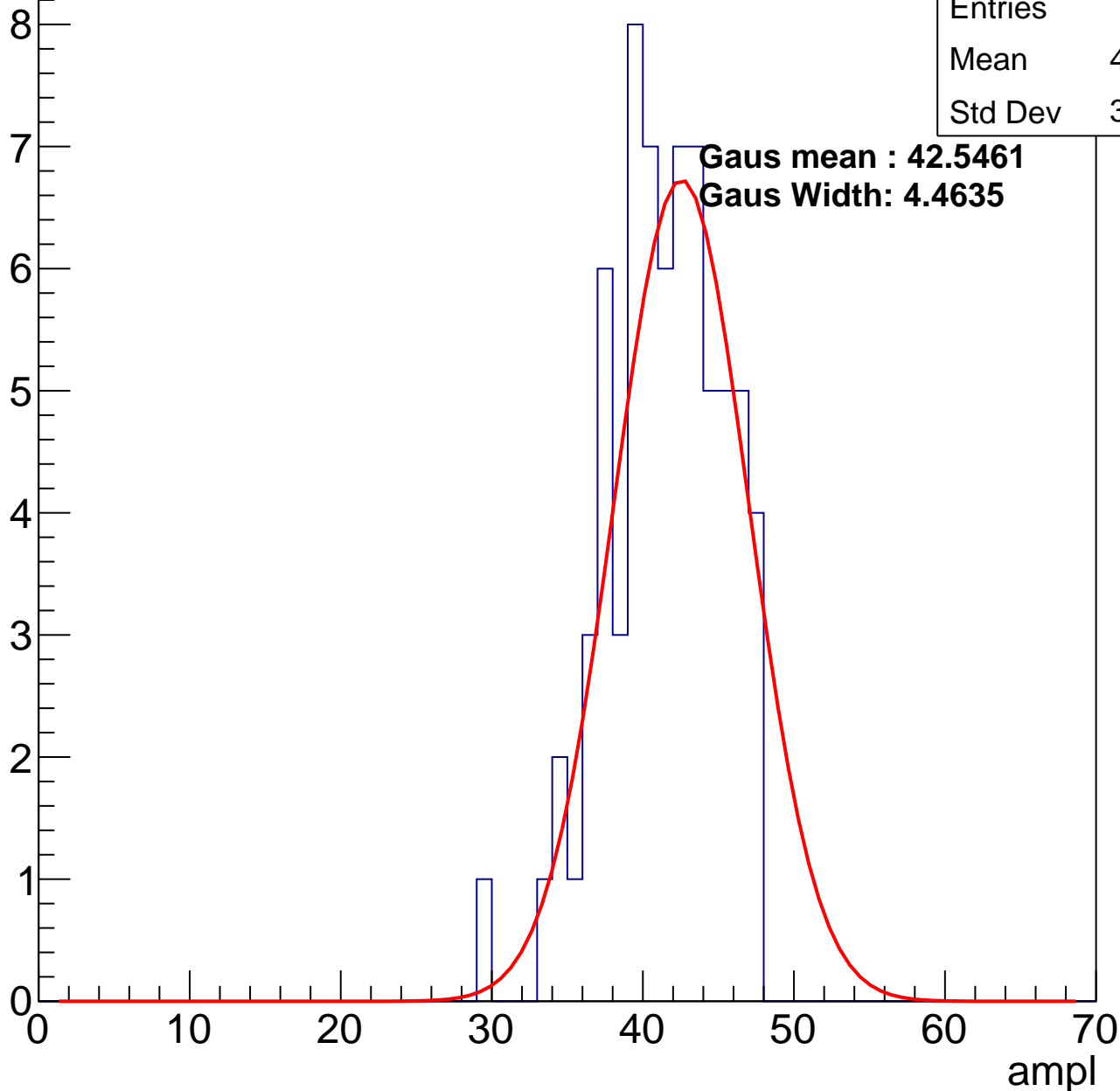
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	40.92
Std Dev	3.767

Gaus mean : 42.5461

Gaus Width: 4.4635

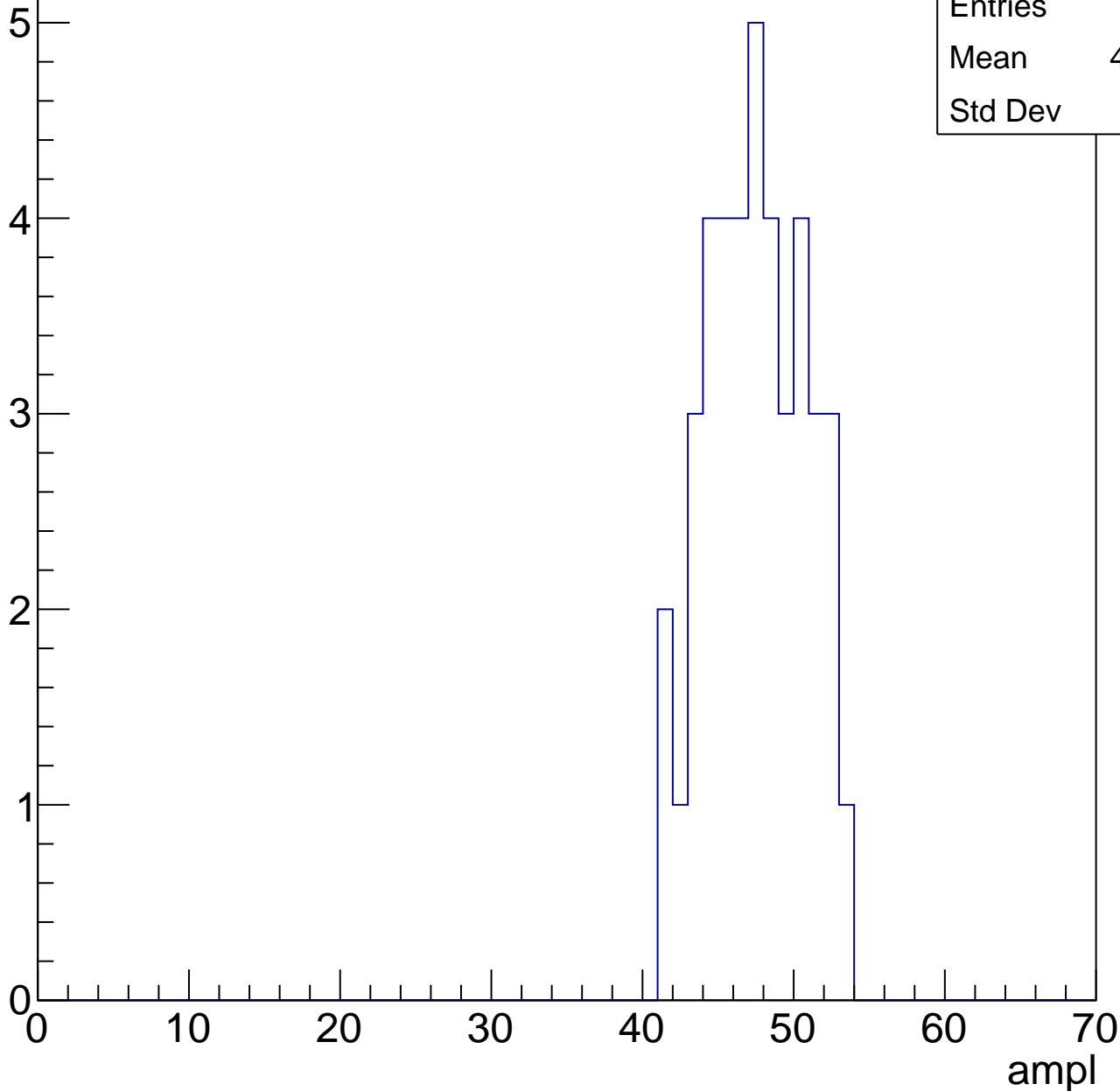


B1L102S, U18-ch54, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	47.05
Std Dev	3.17

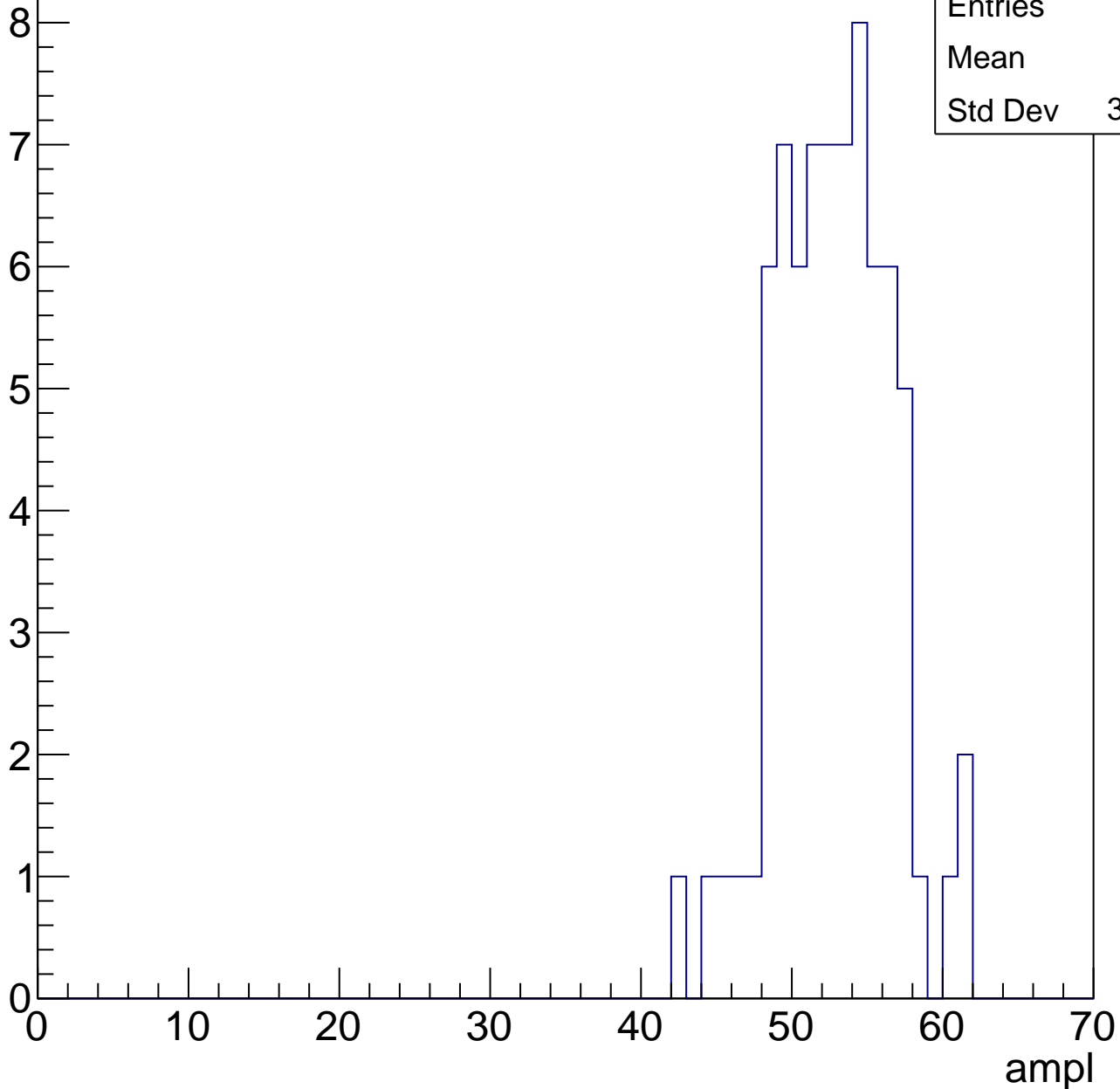


B1L102S, U18-ch54, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	52.3
Std Dev	3.737

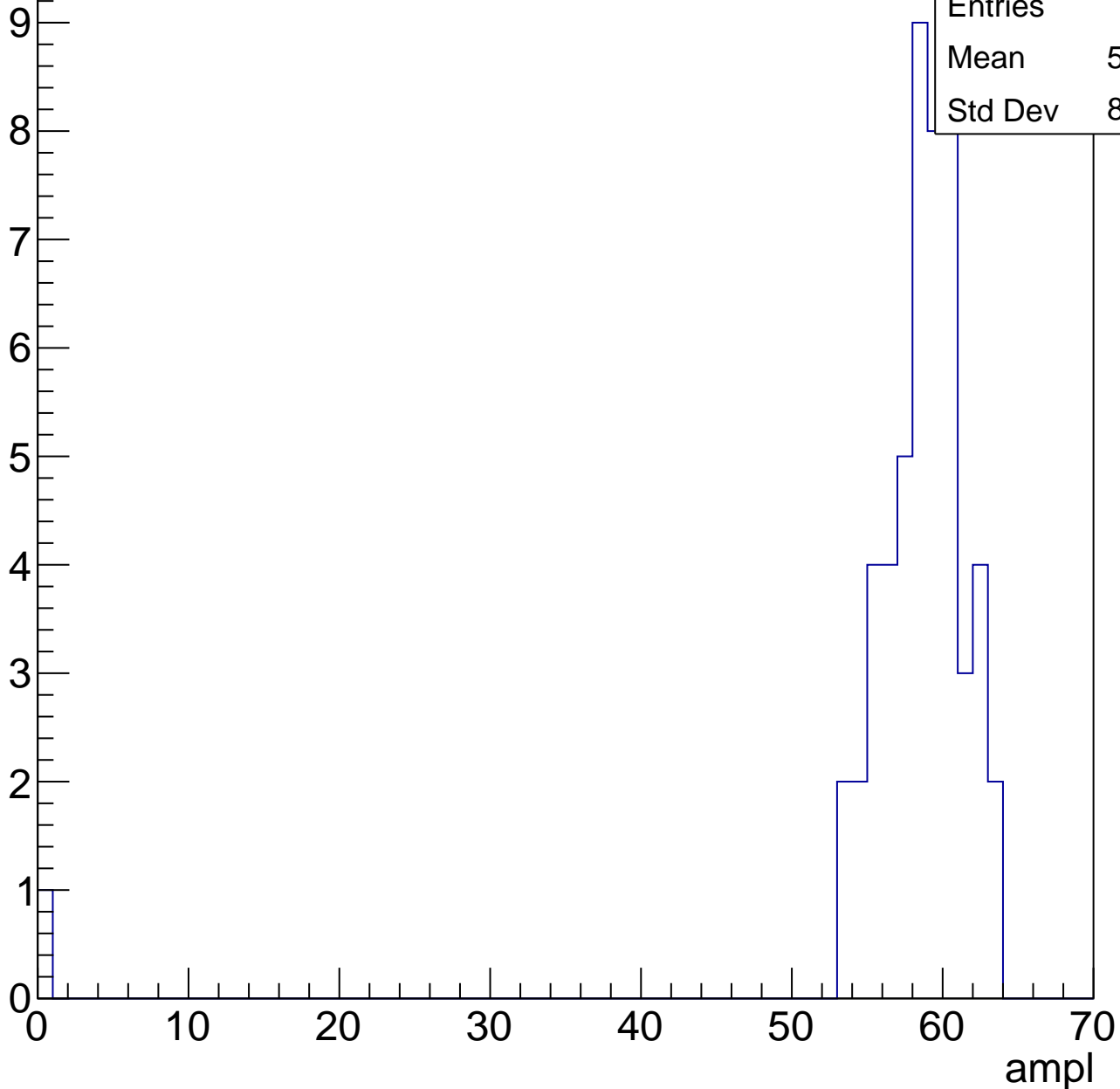


B1L102S, U18-ch54, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	57.19
Std Dev	8.378

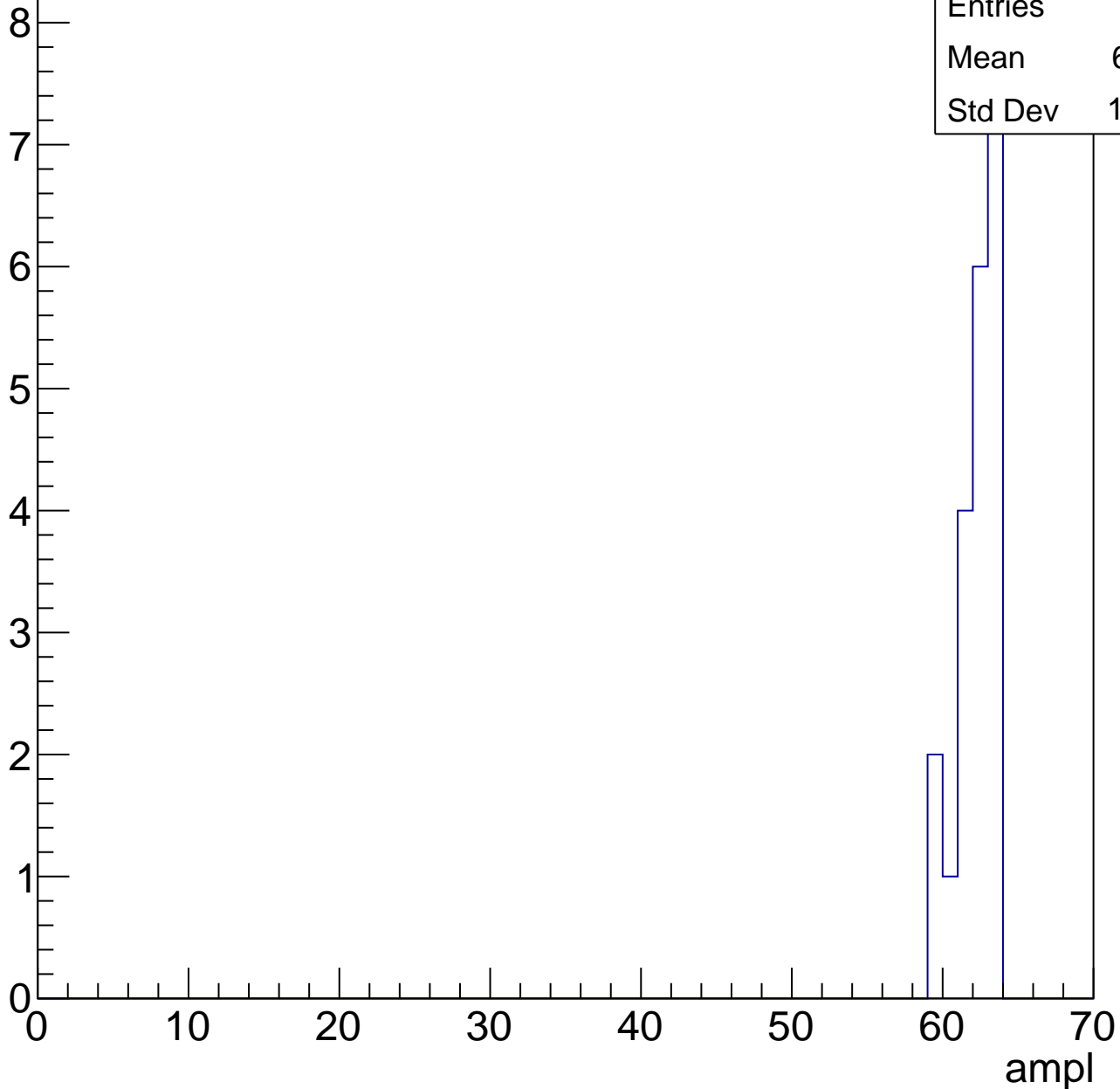


B1L102S, U18-ch54, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	61.81
Std Dev	1.258



B1L102S, U18-ch54, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch55, adc0

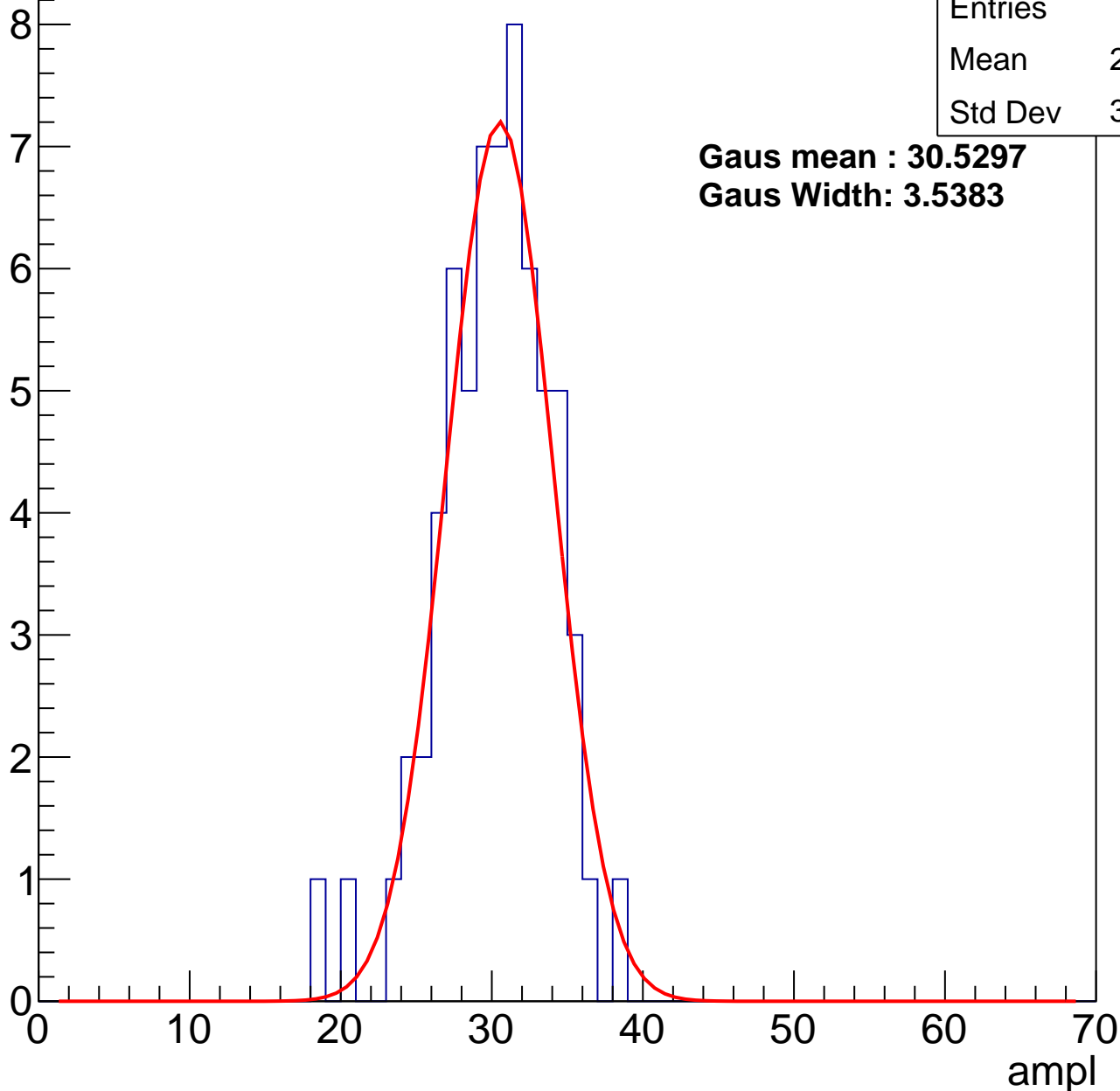
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	29.72
Std Dev	3.694

Gaus mean : 30.5297

Gaus Width: 3.5383



B1L102S, U18-ch55, adc1

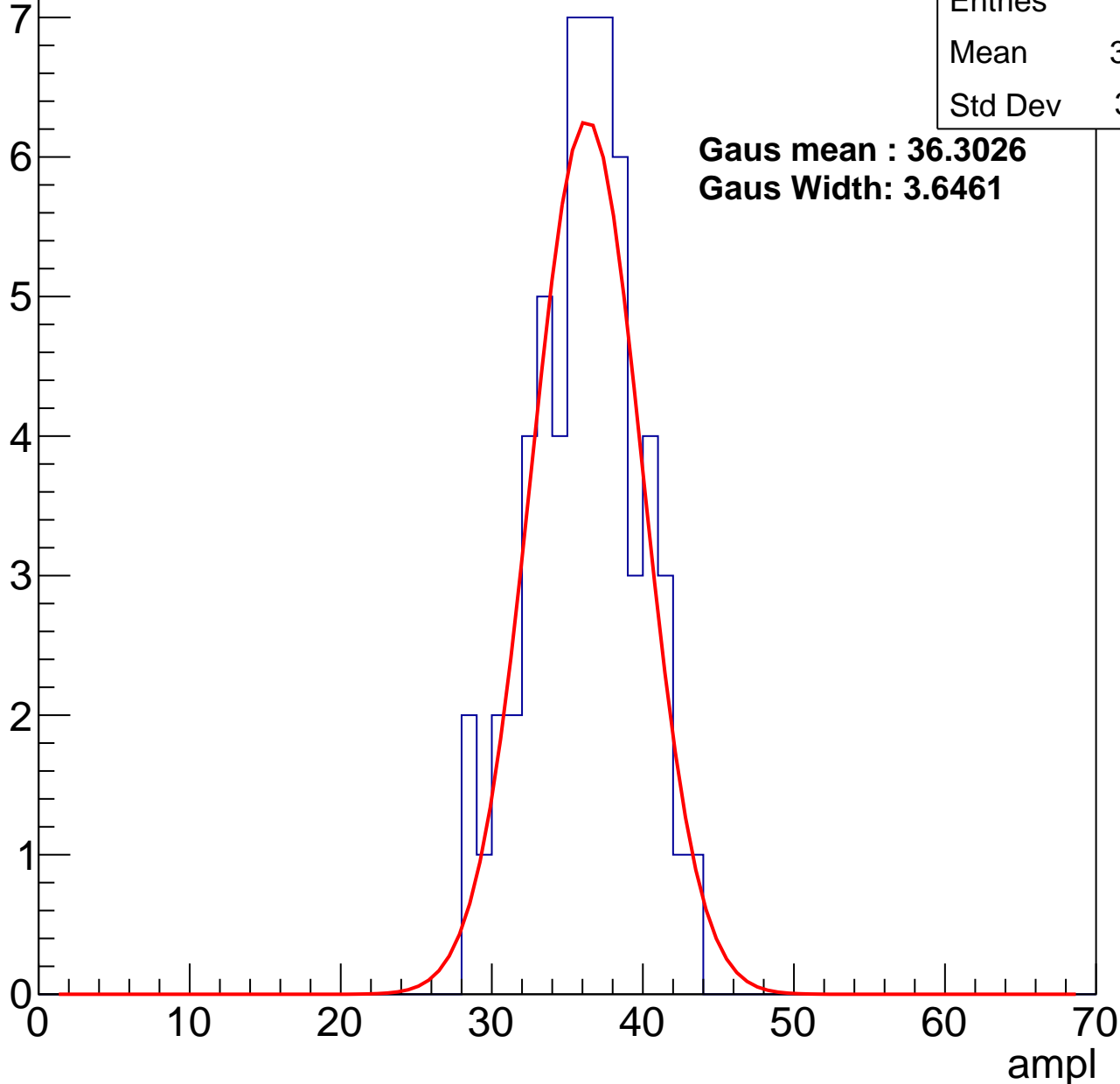
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	35.68
Std Dev	3.461

Gaus mean : 36.3026

Gaus Width: 3.6461



B1L102S, U18-ch55, adc2

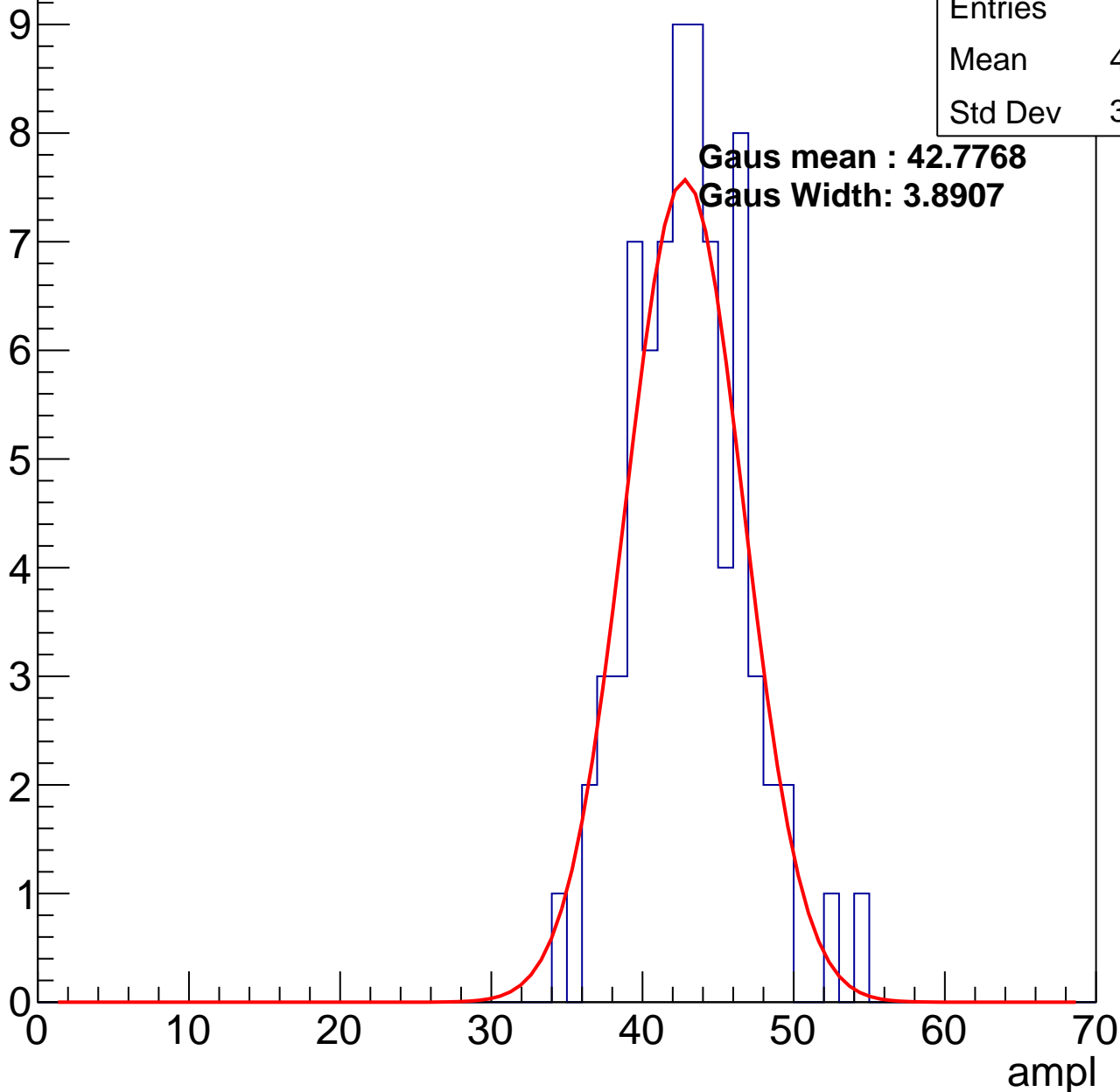
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	42.57
Std Dev	3.685

Gaus mean : 42.7768

Gaus Width: 3.8907

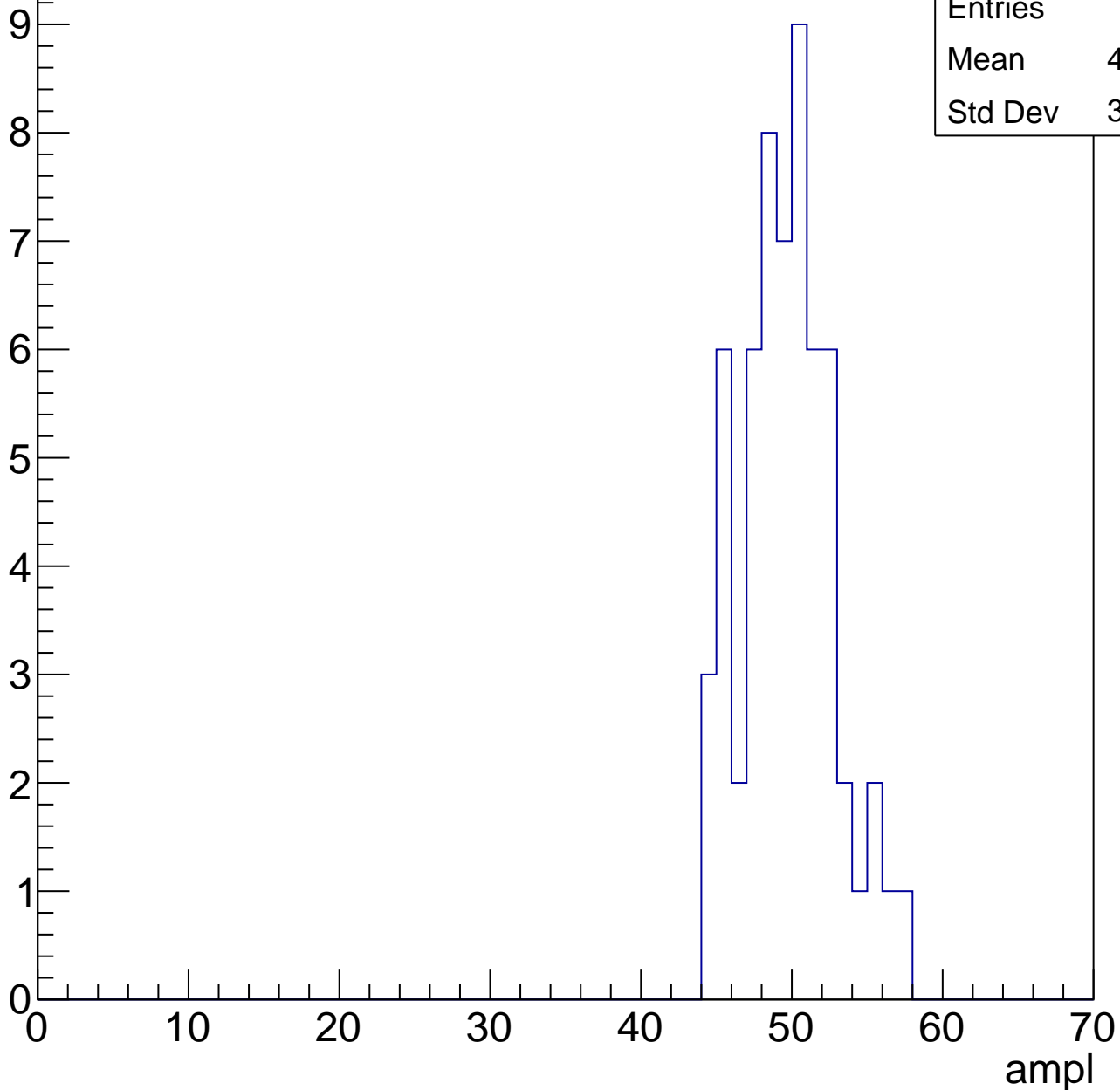


B1L102S, U18-ch55, adc3

calib_packv5_042523_0143.root, FC#11, port A2

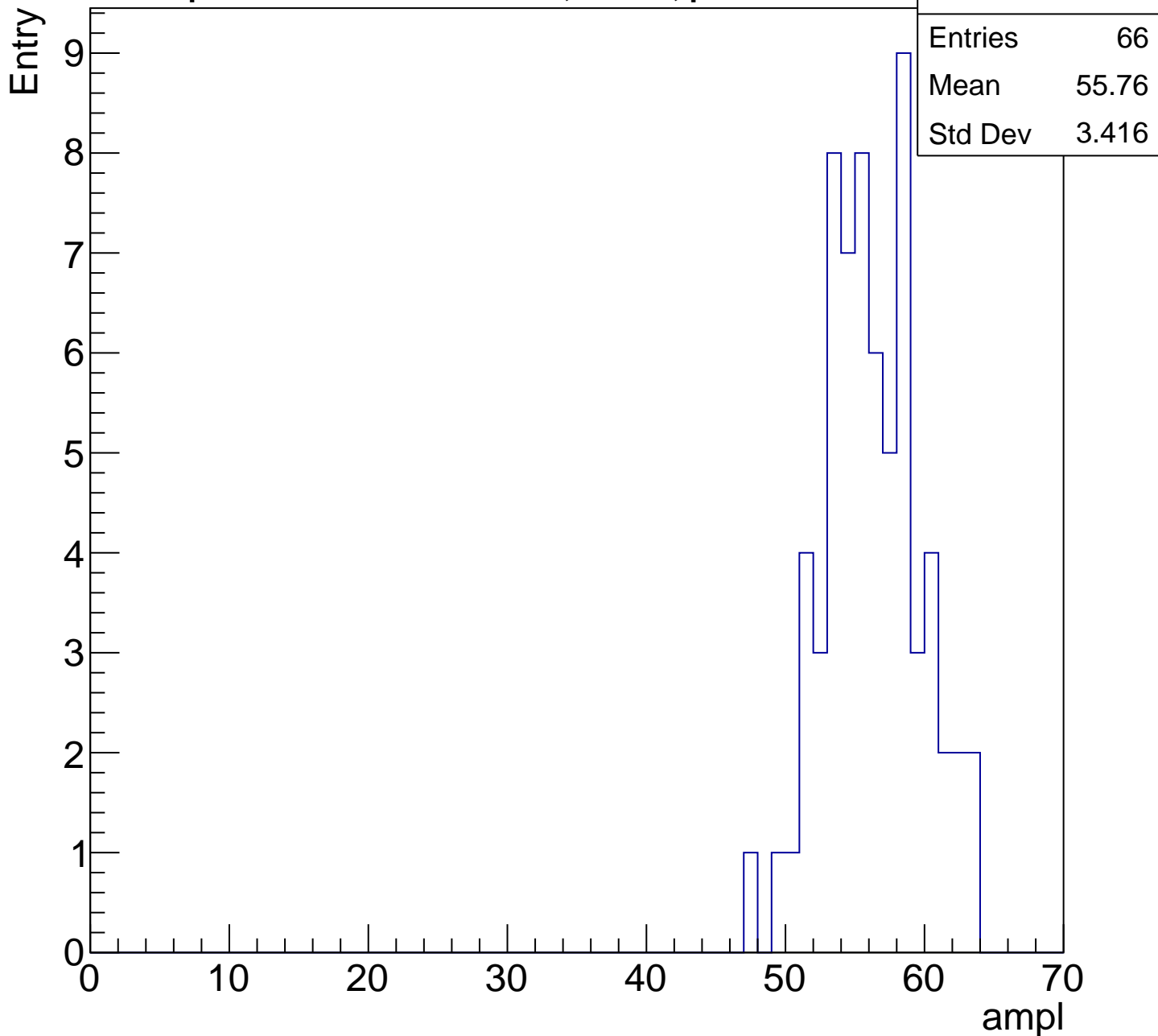
Entry

Entries	60
Mean	49.23
Std Dev	3.019



B1L102S, U18-ch55, adc4

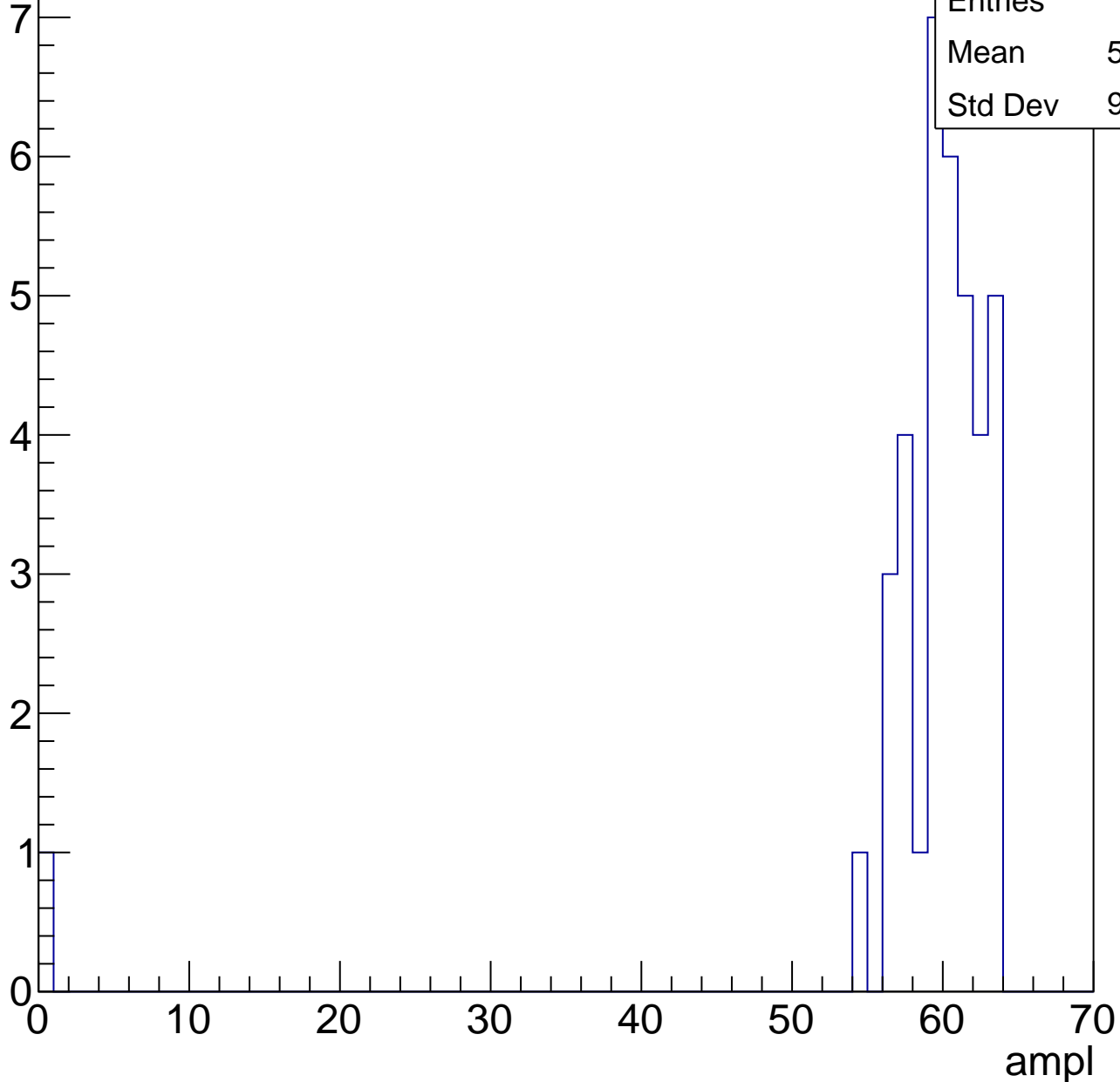
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U18-ch55, adc5

calib_packv5_042523_0143.root, FC#11, port A2

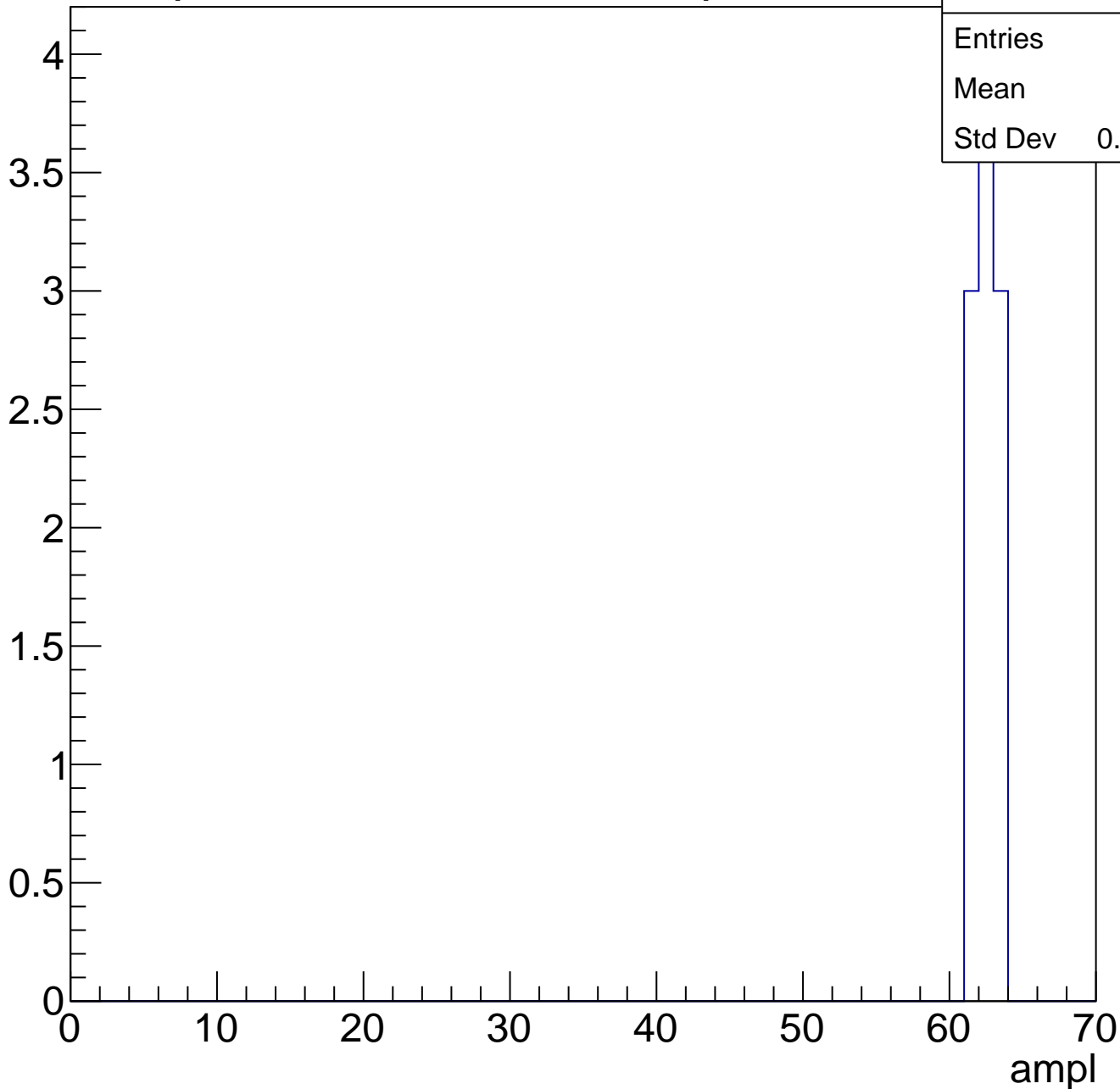
Entry



B1L102S, U18-ch55, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch55, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch56, adc0

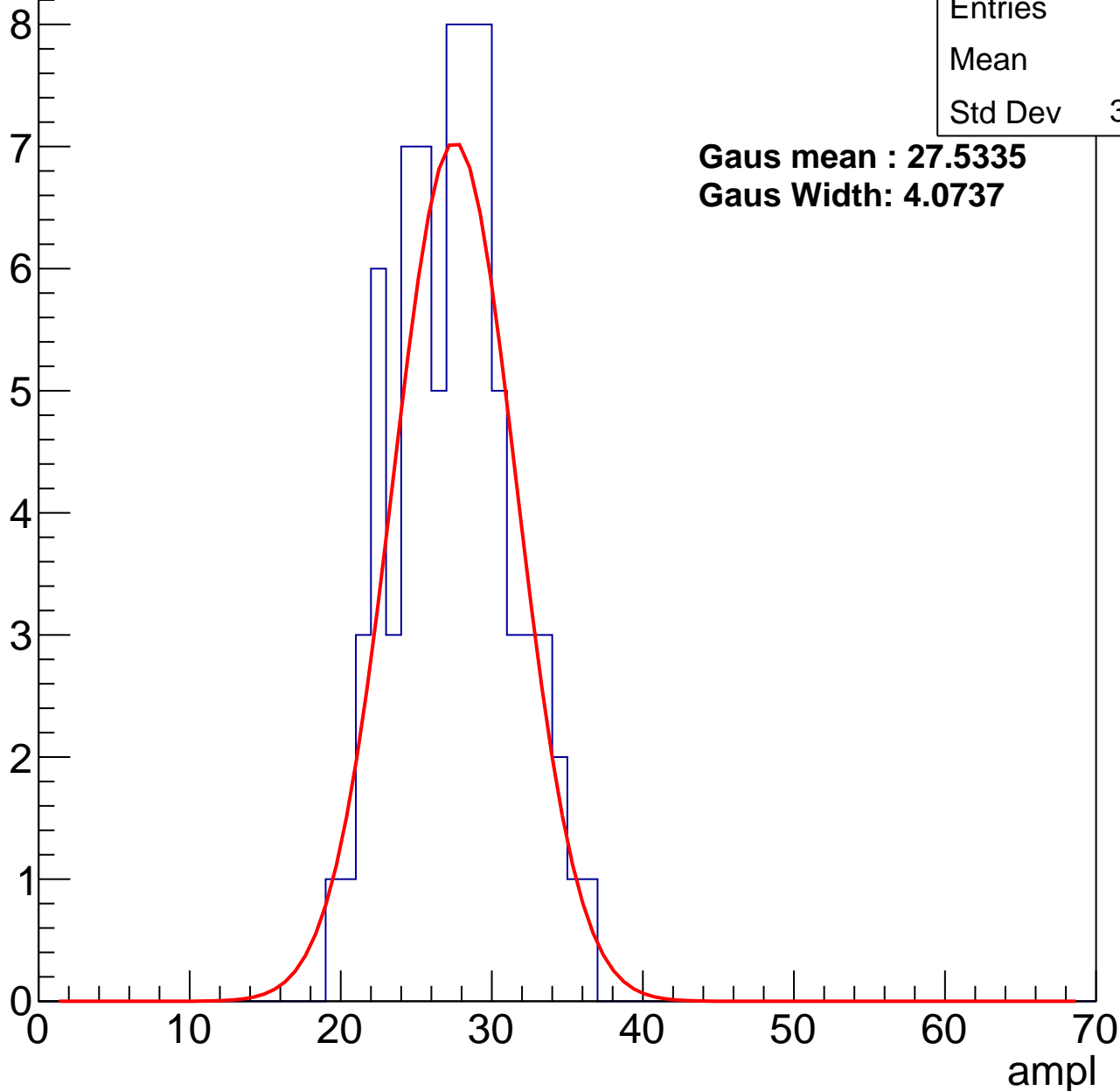
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	27
Std Dev	3.784

Gaus mean : 27.5335

Gaus Width: 4.0737



B1L102S, U18-ch56, adc1

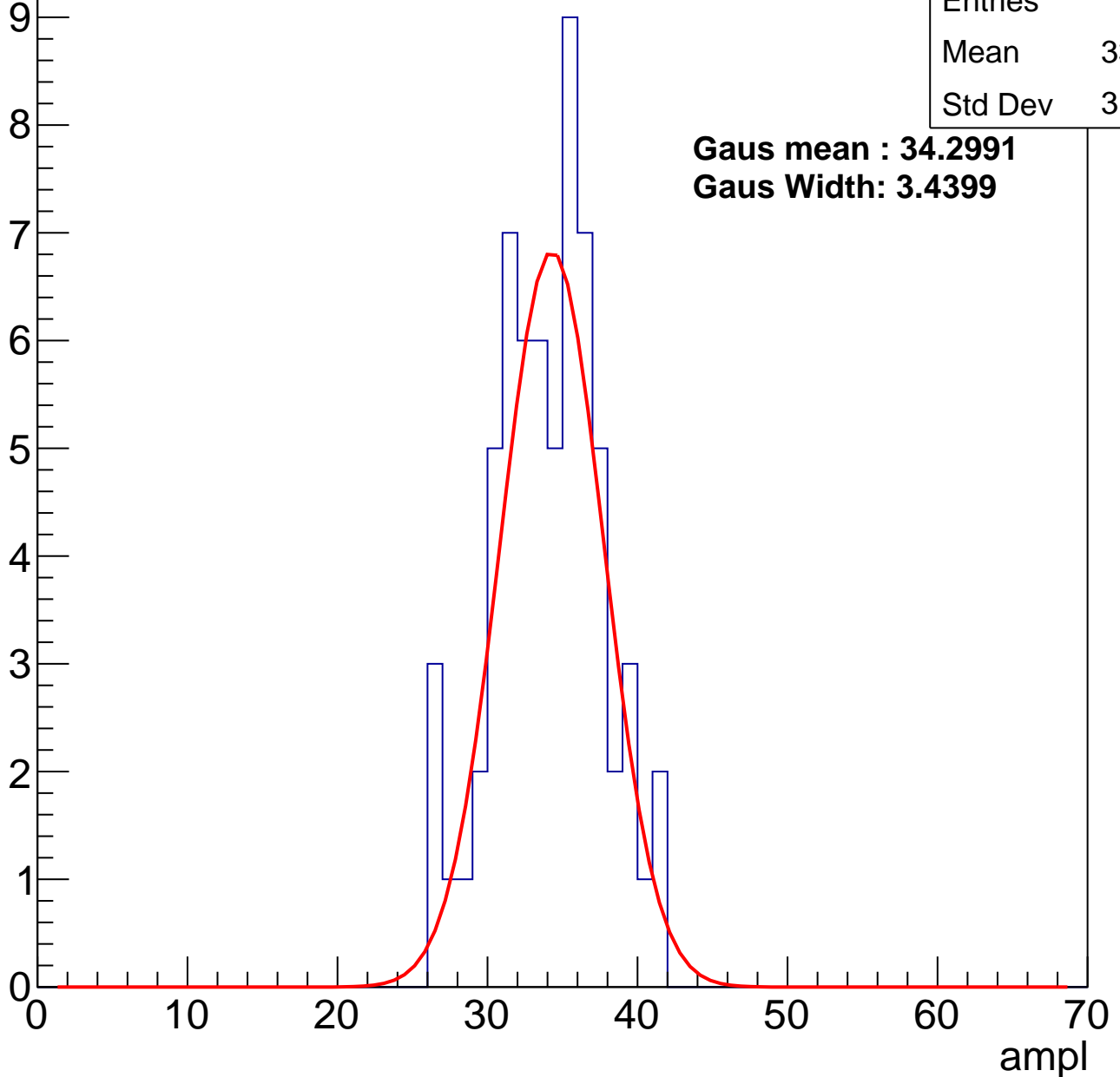
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	33.62
Std Dev	3.537

Gaus mean : 34.2991

Gaus Width: 3.4399



B1L102S, U18-ch56, adc2

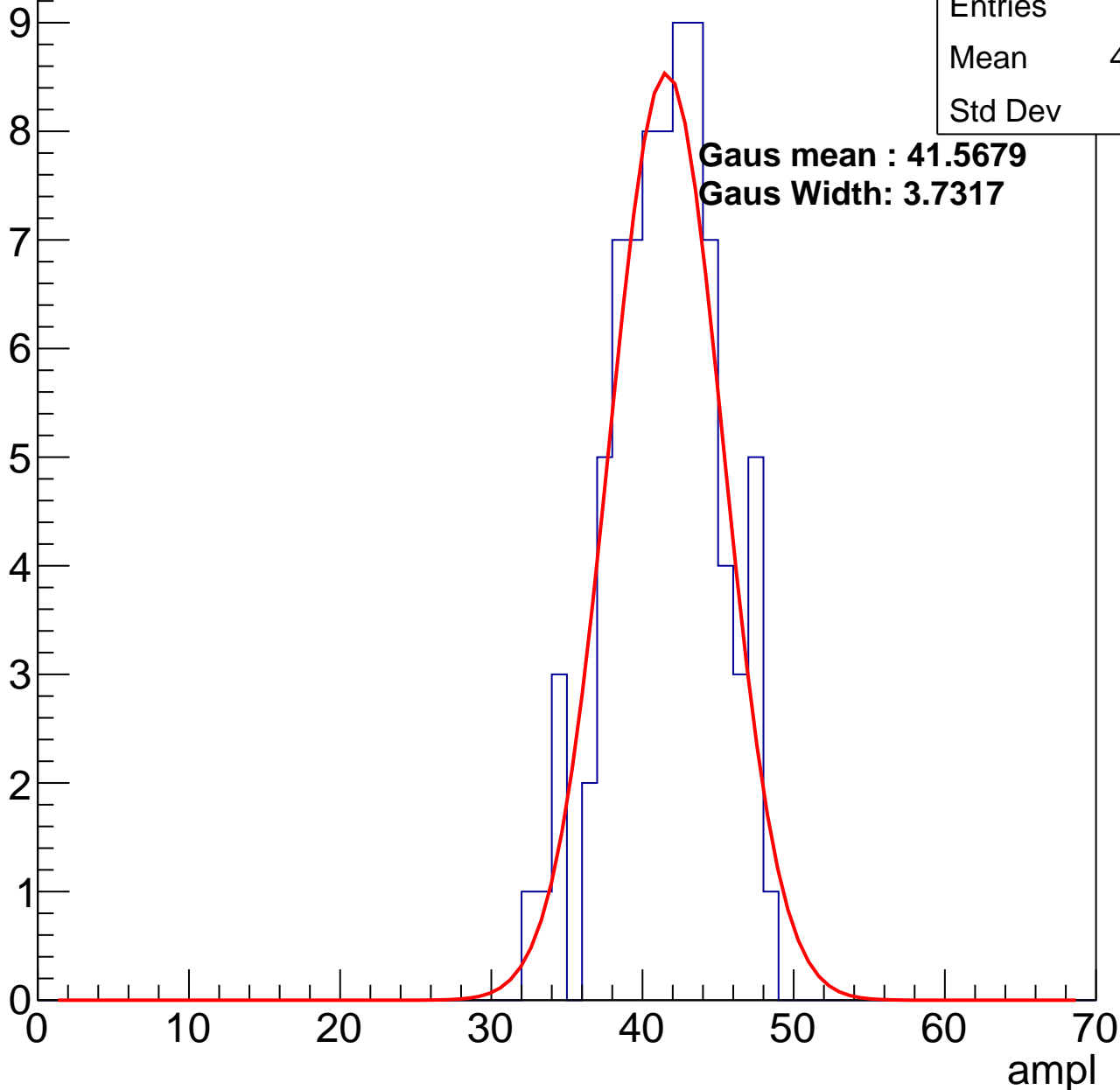
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.06
Std Dev	3.53

Gaus mean : 41.5679

Gaus Width: 3.7317

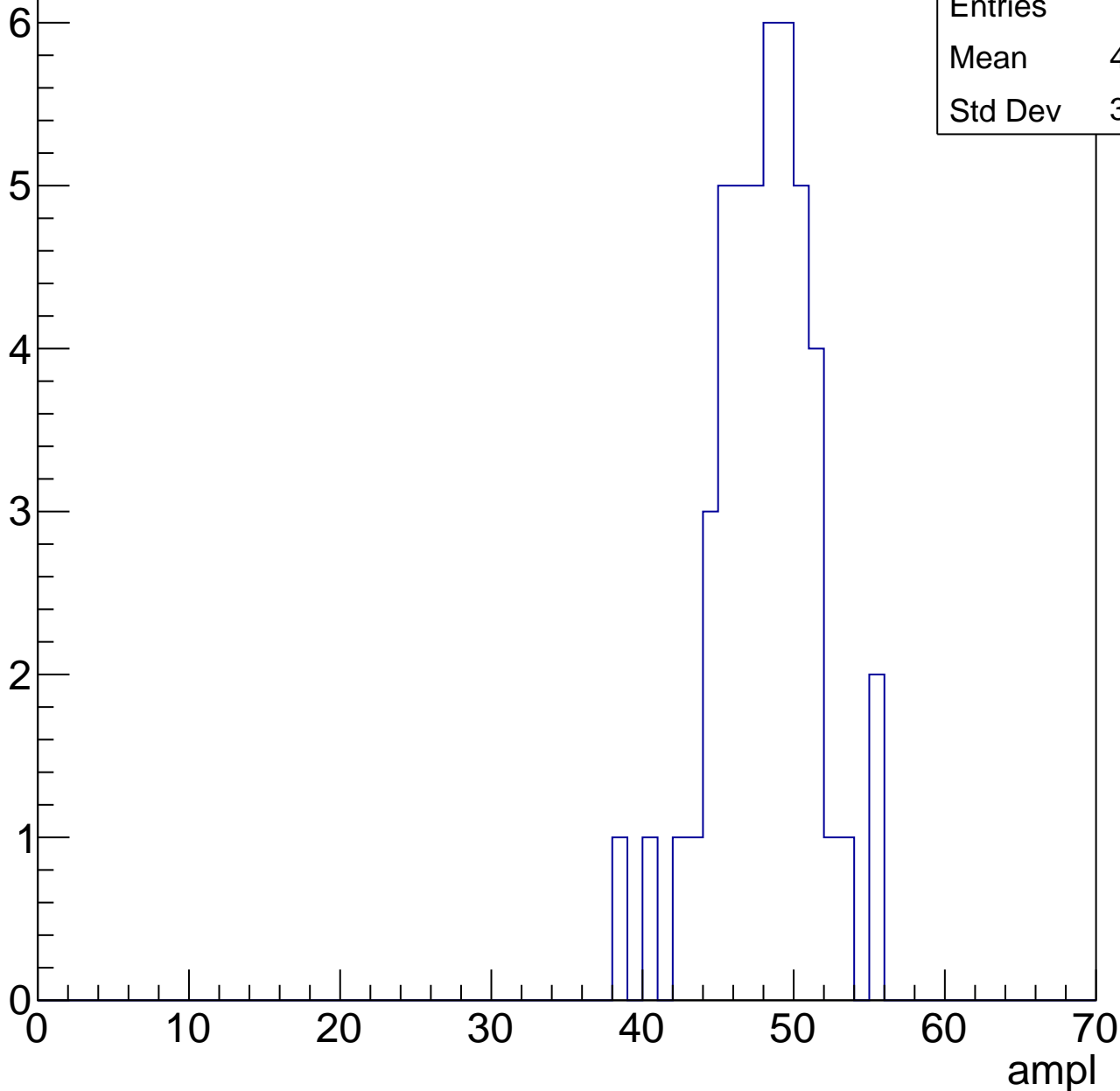


B1L102S, U18-ch56, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	47.57
Std Dev	3.375

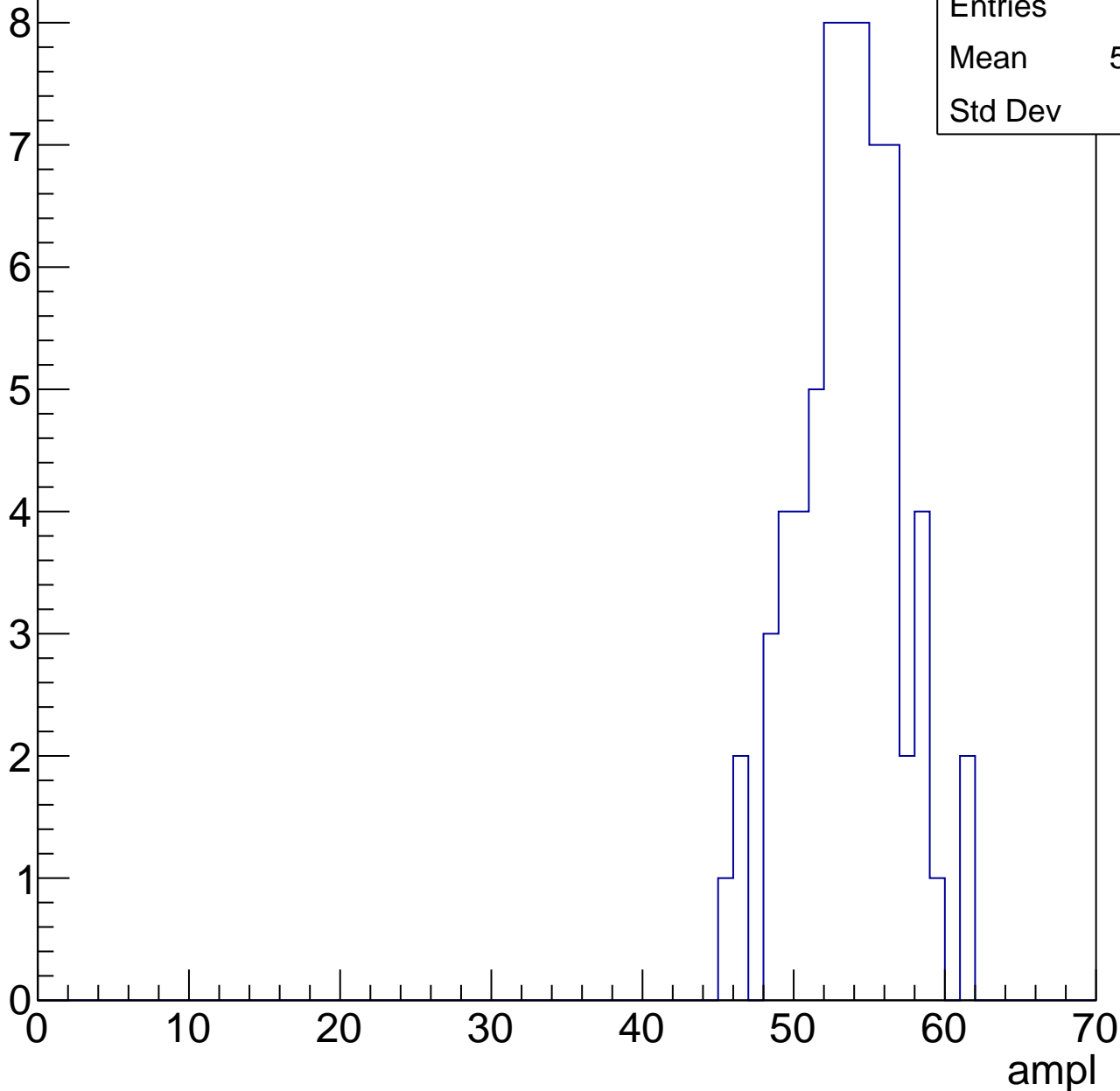


B1L102S, U18-ch56, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.15
Std Dev	3.39



B1L102S, U18-ch56, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 58.38

Std Dev 3.073

ampl

0

10

20

30

40

50

60

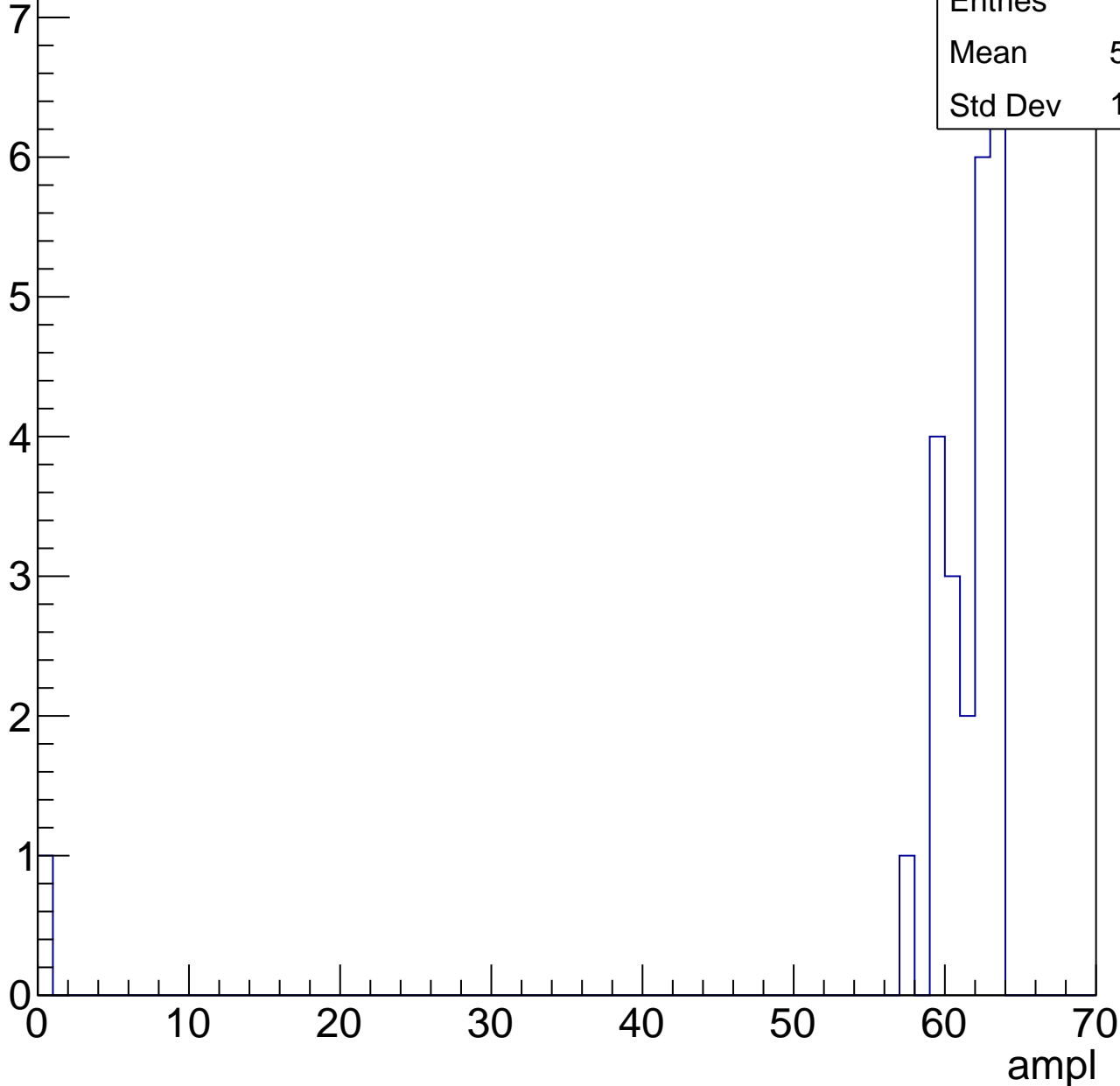
70

B1L102S, U18-ch56, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

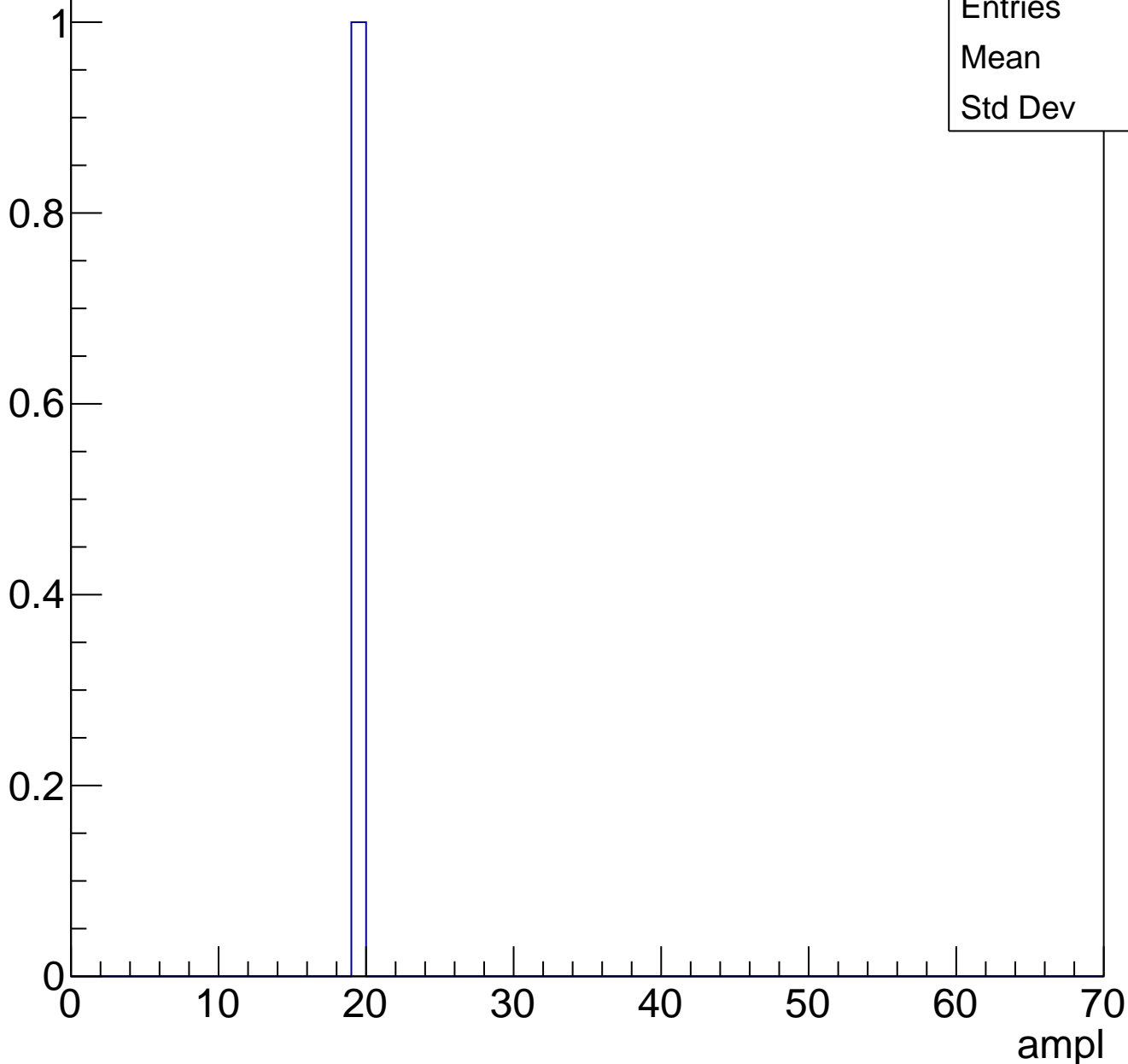
Entries	24
Mean	58.67
Std Dev	12.35



B1L102S, U18-ch56, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch57, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	80
Mean	29.51
Std Dev	4.806

Gaus mean : 30.6350

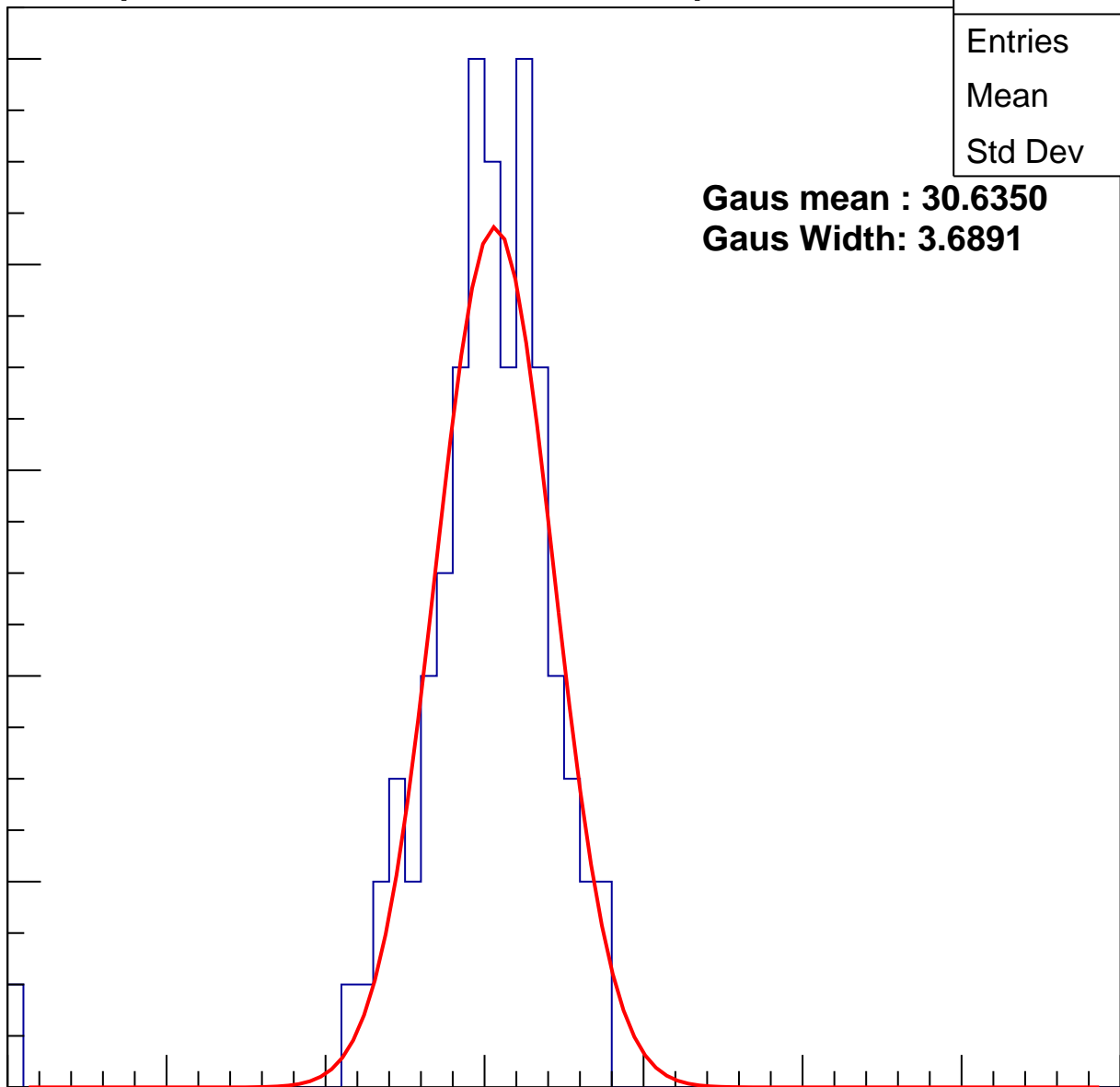
Gaus Width: 3.6891

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch57, adc1

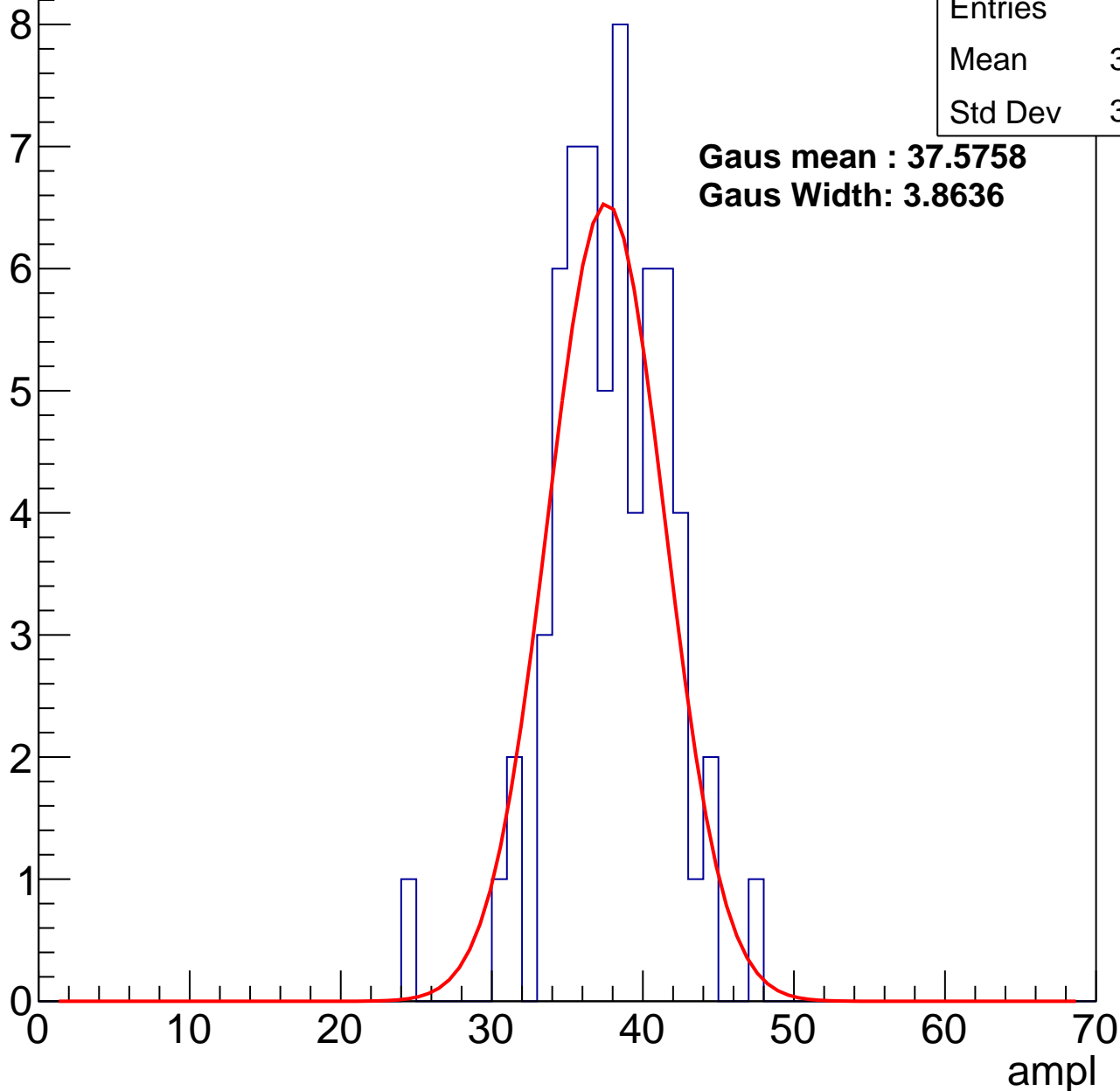
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	37.39
Std Dev	3.802

Gaus mean : 37.5758

Gaus Width: 3.8636

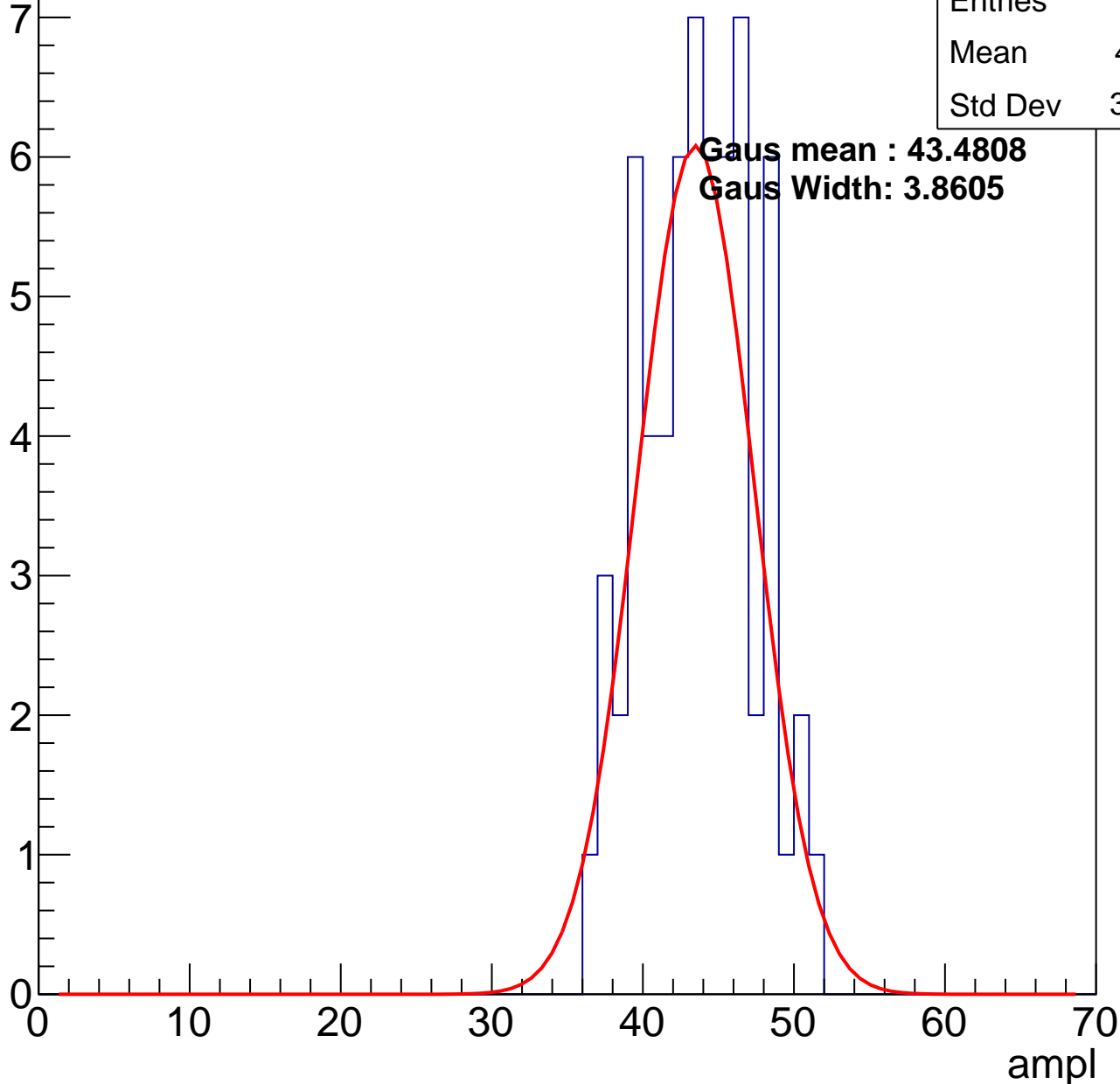


B1L102S, U18-ch57, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	43.31
Std Dev	3.596

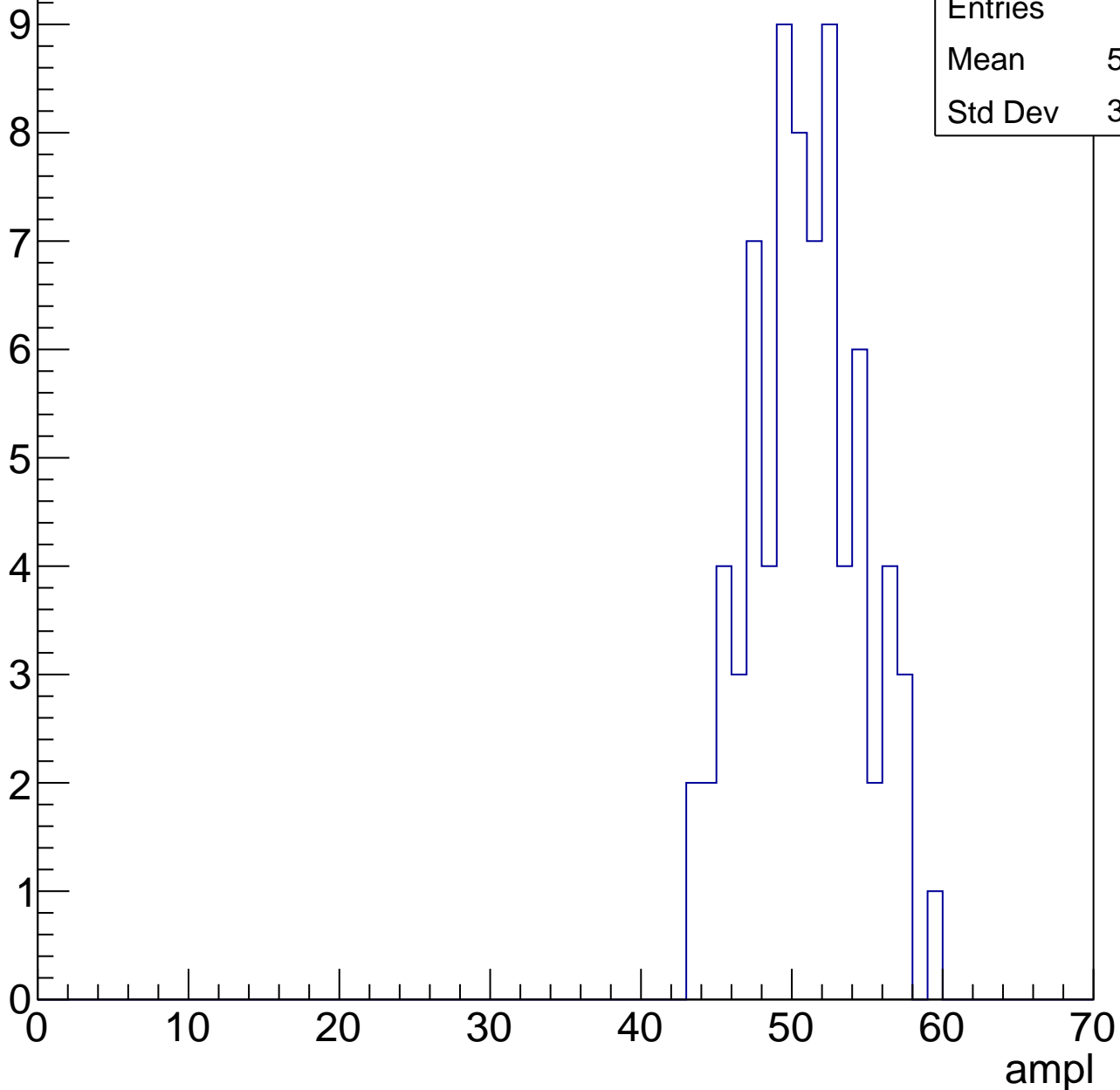


B1L102S, U18-ch57, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

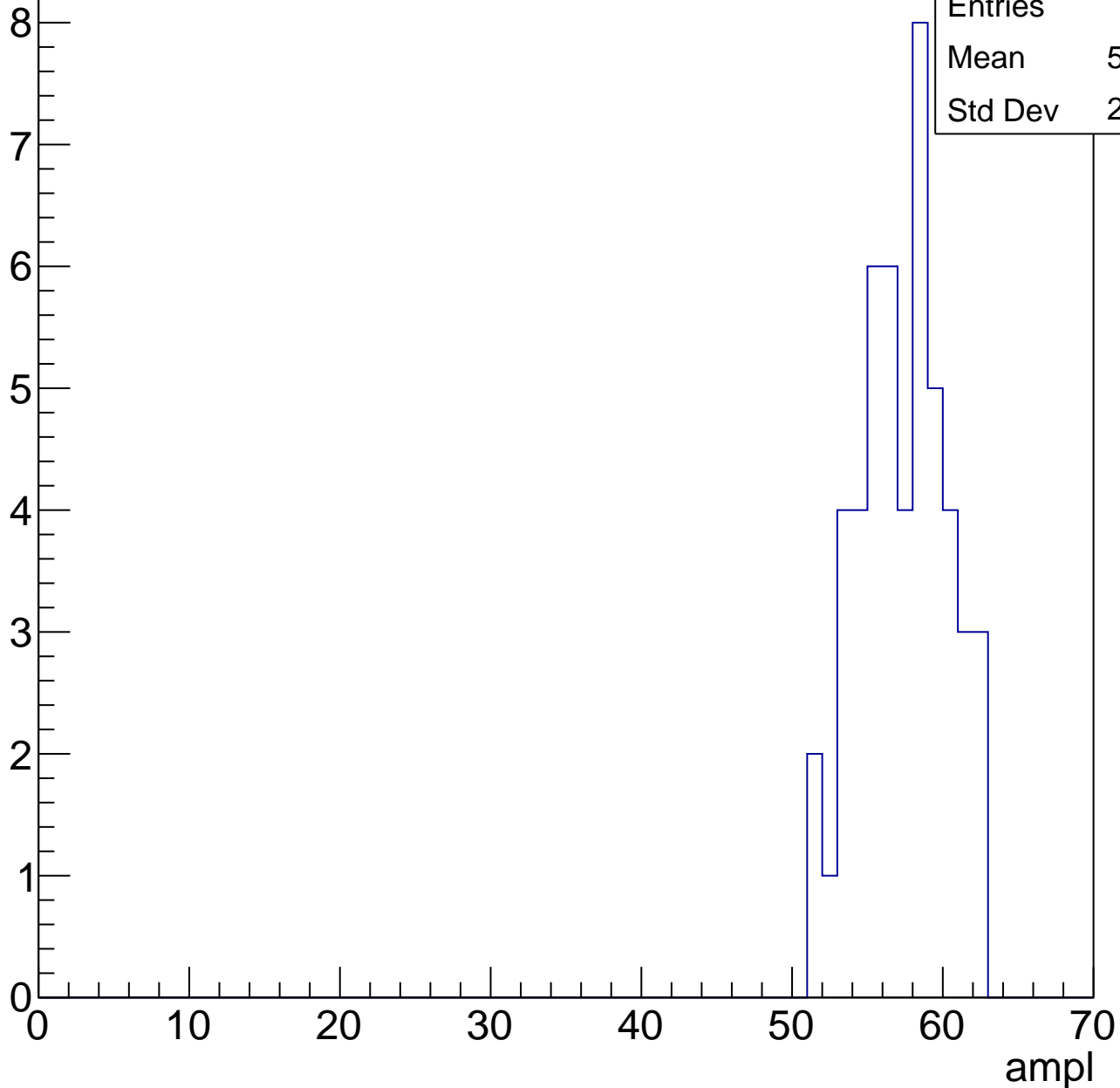
Entries	75
Mean	50.39
Std Dev	3.636



B1L102S, U18-ch57, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

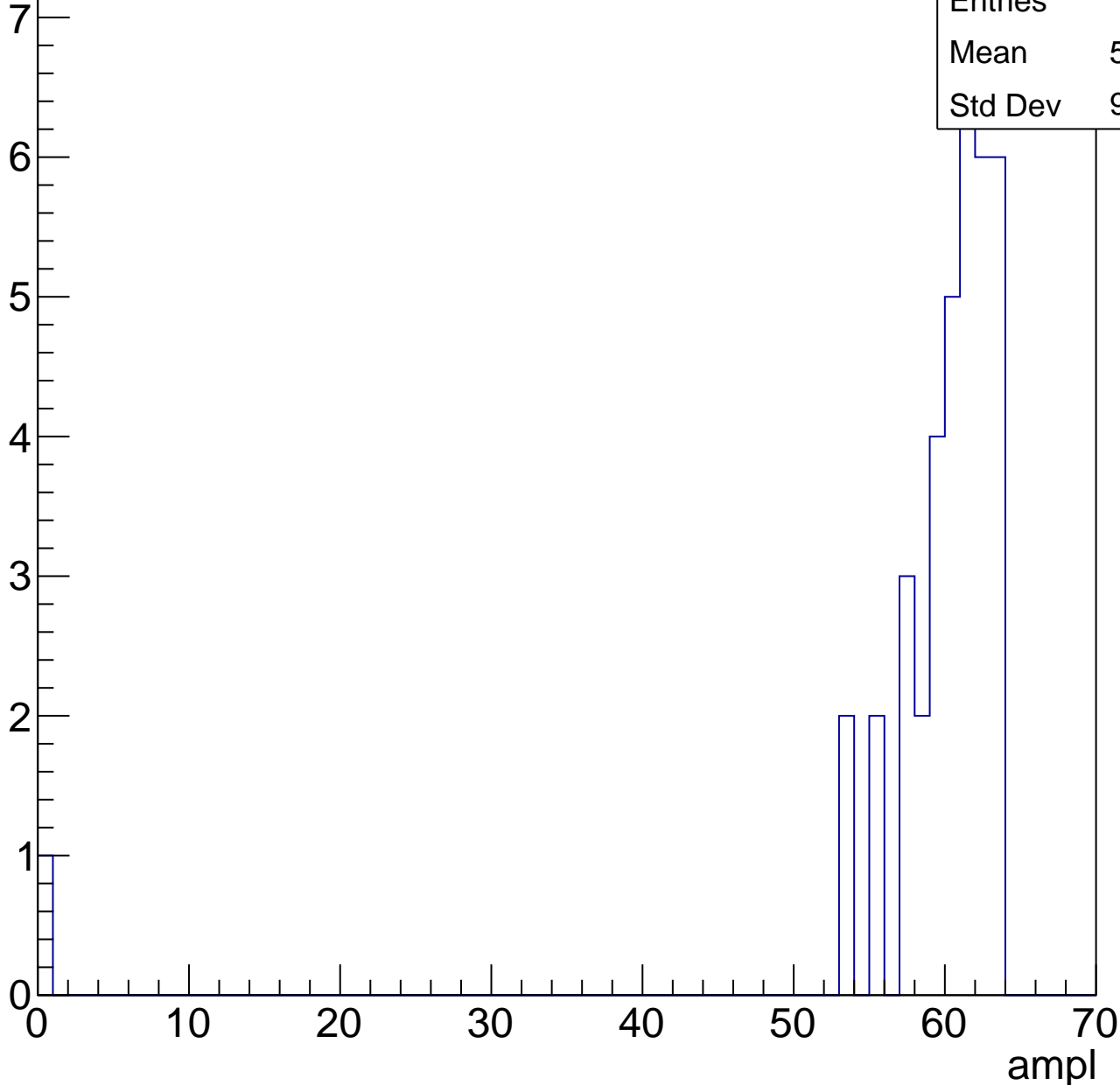


B1L102S, U18-ch57, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

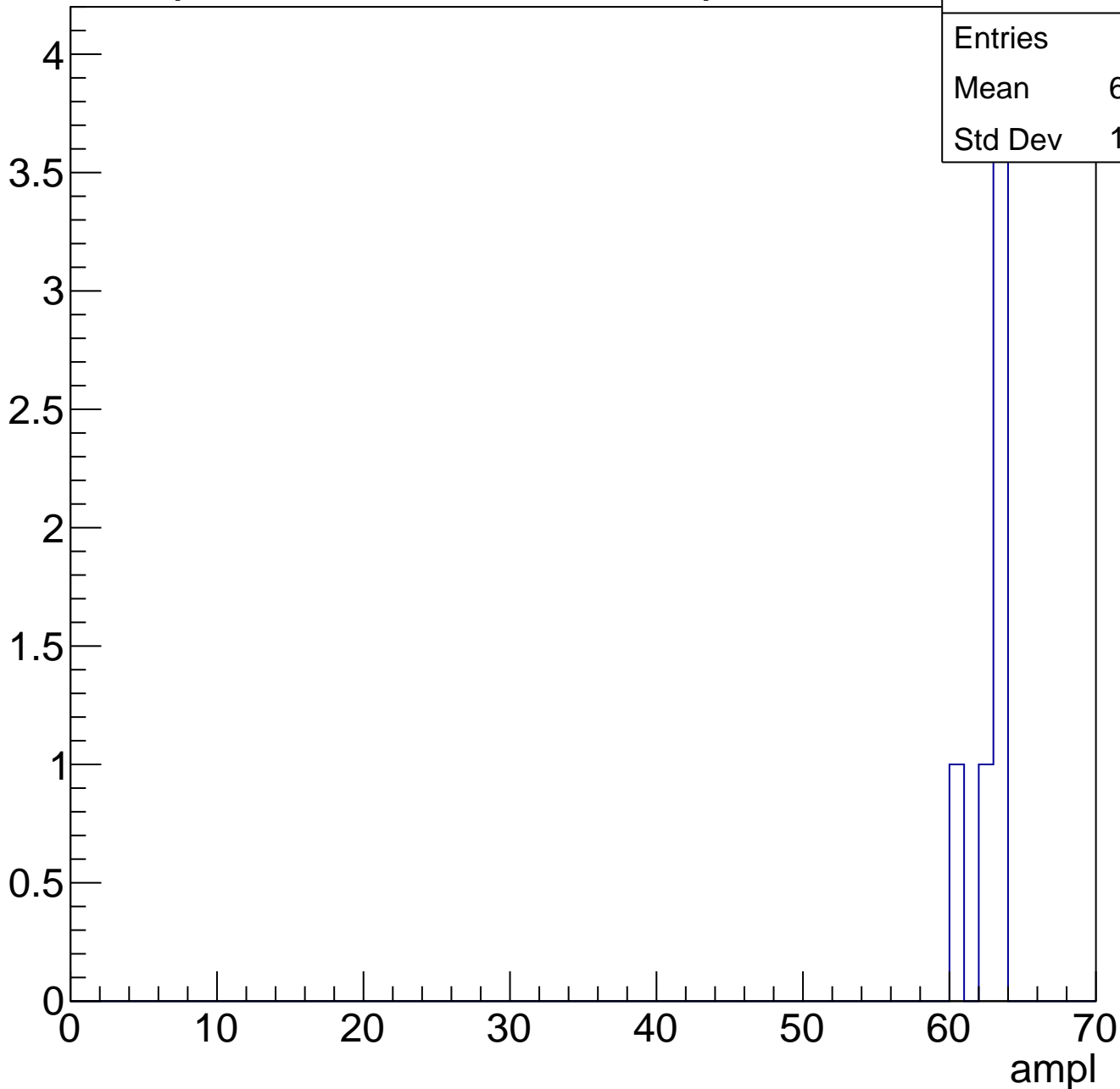
Entries	38
Mean	58.32
Std Dev	9.953



B1L102S, U18-ch57, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch57, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch58, adc0

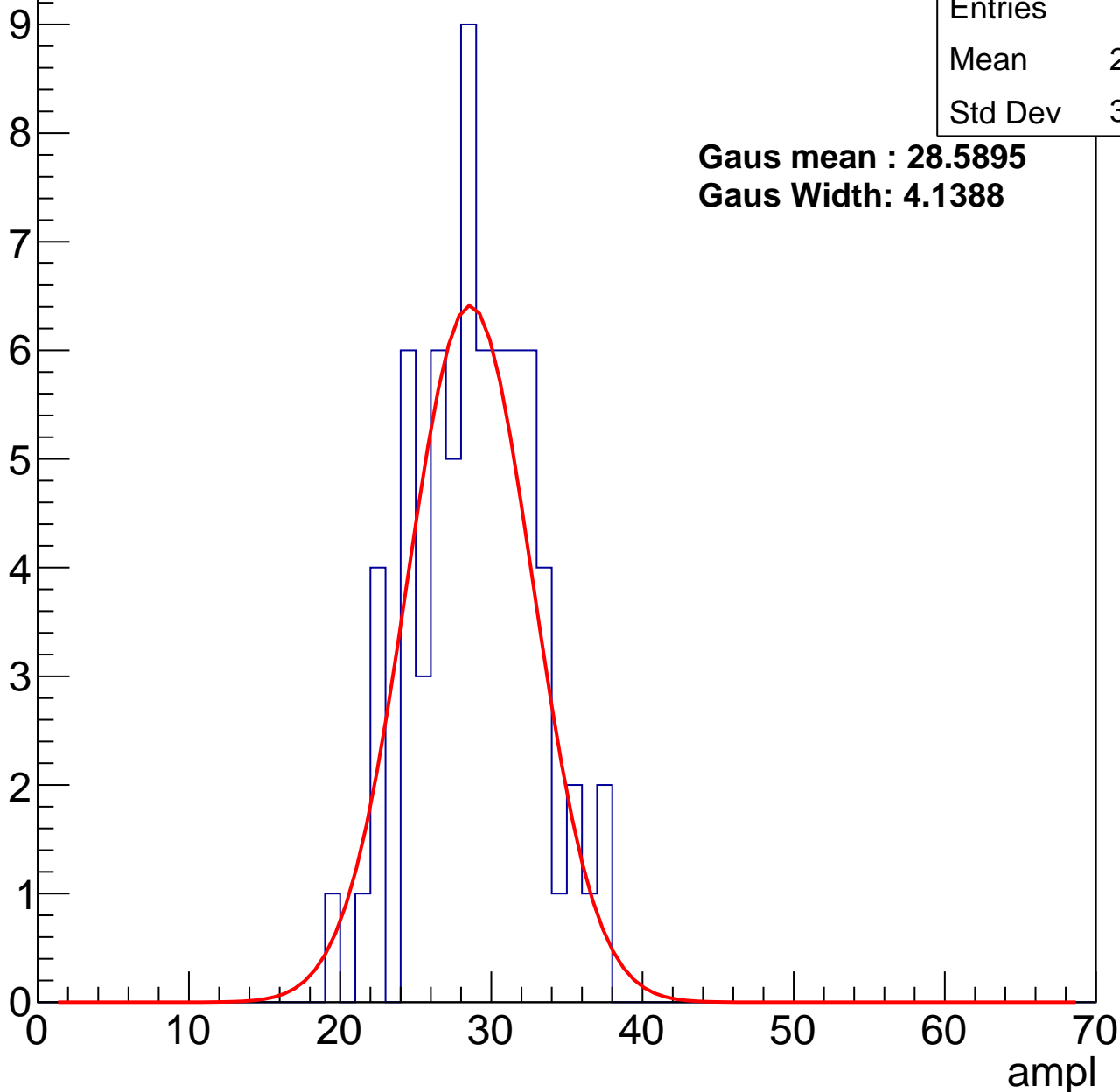
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.52
Std Dev	3.896

Gaus mean : 28.5895

Gaus Width: 4.1388



B1L102S, U18-ch58, adc1

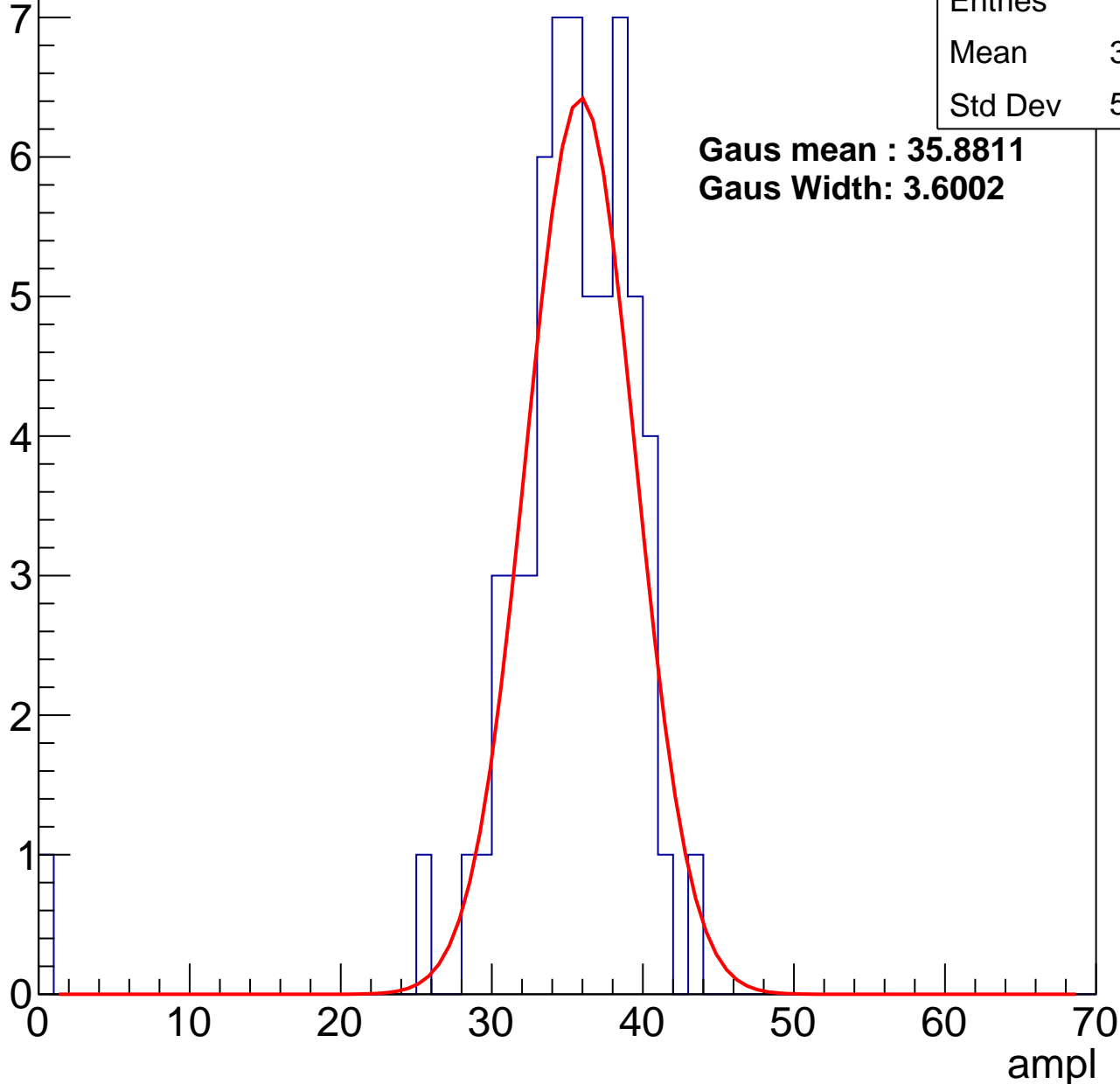
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.62
Std Dev	5.654

Gaus mean : 35.8811

Gaus Width: 3.6002



B1L102S, U18-ch58, adc2

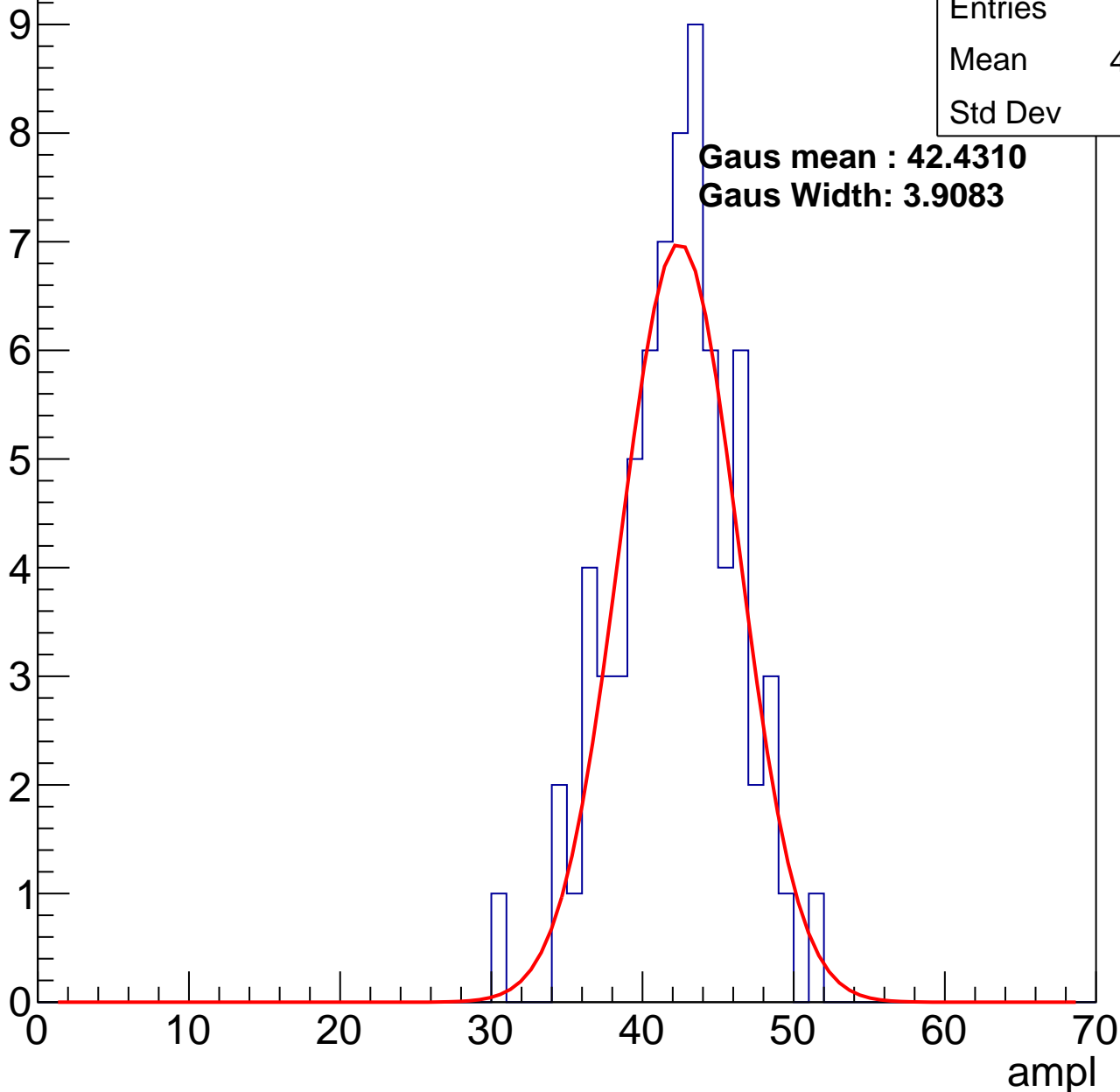
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.74
Std Dev	3.93

Gaus mean : 42.4310

Gaus Width: 3.9083



B1L102S, U18-ch58, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	68
Mean	48.71
Std Dev	3.25

Entry

10

8

6

4

2

0

0

10

20

30

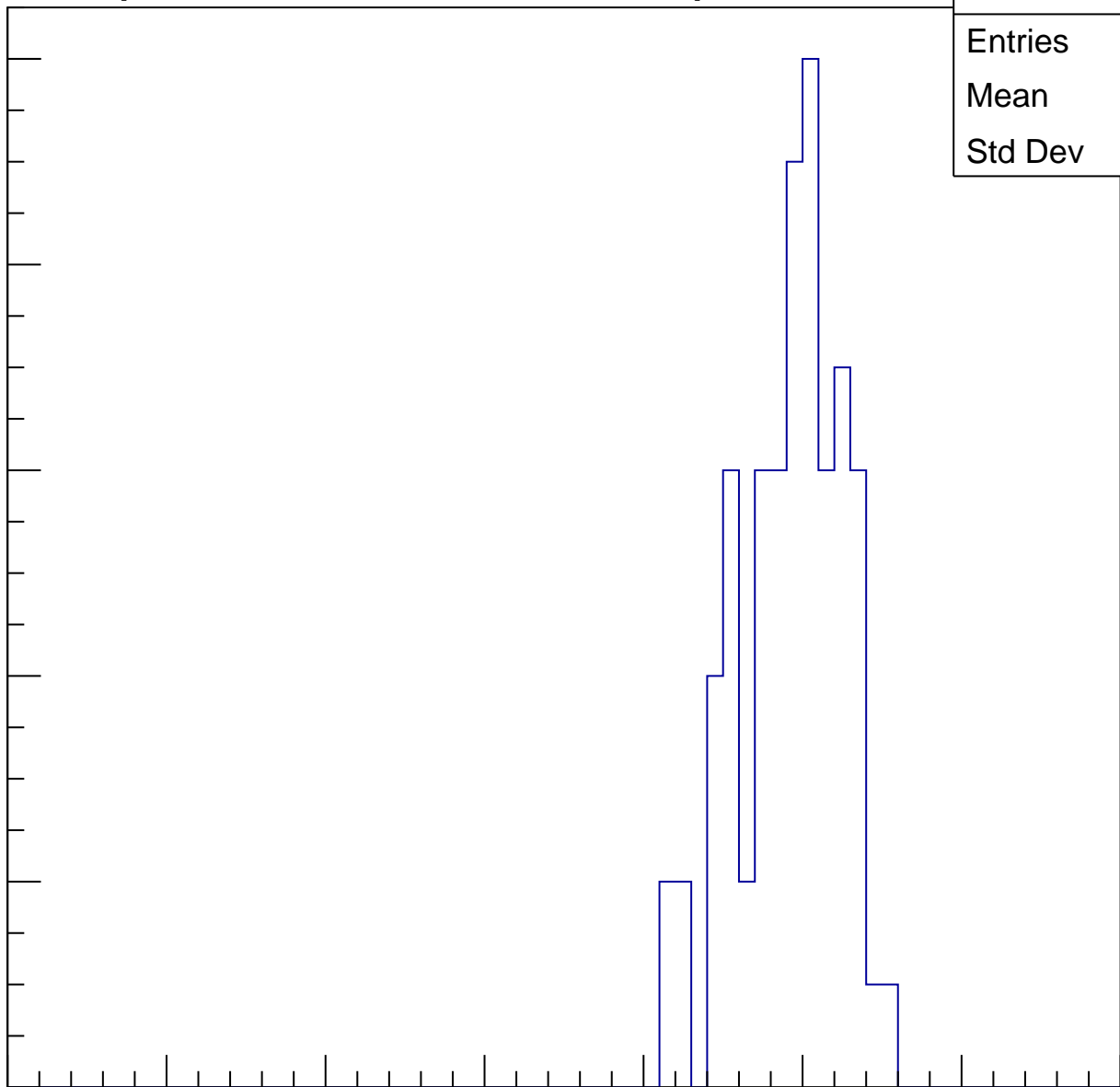
40

50

60

70

ampl

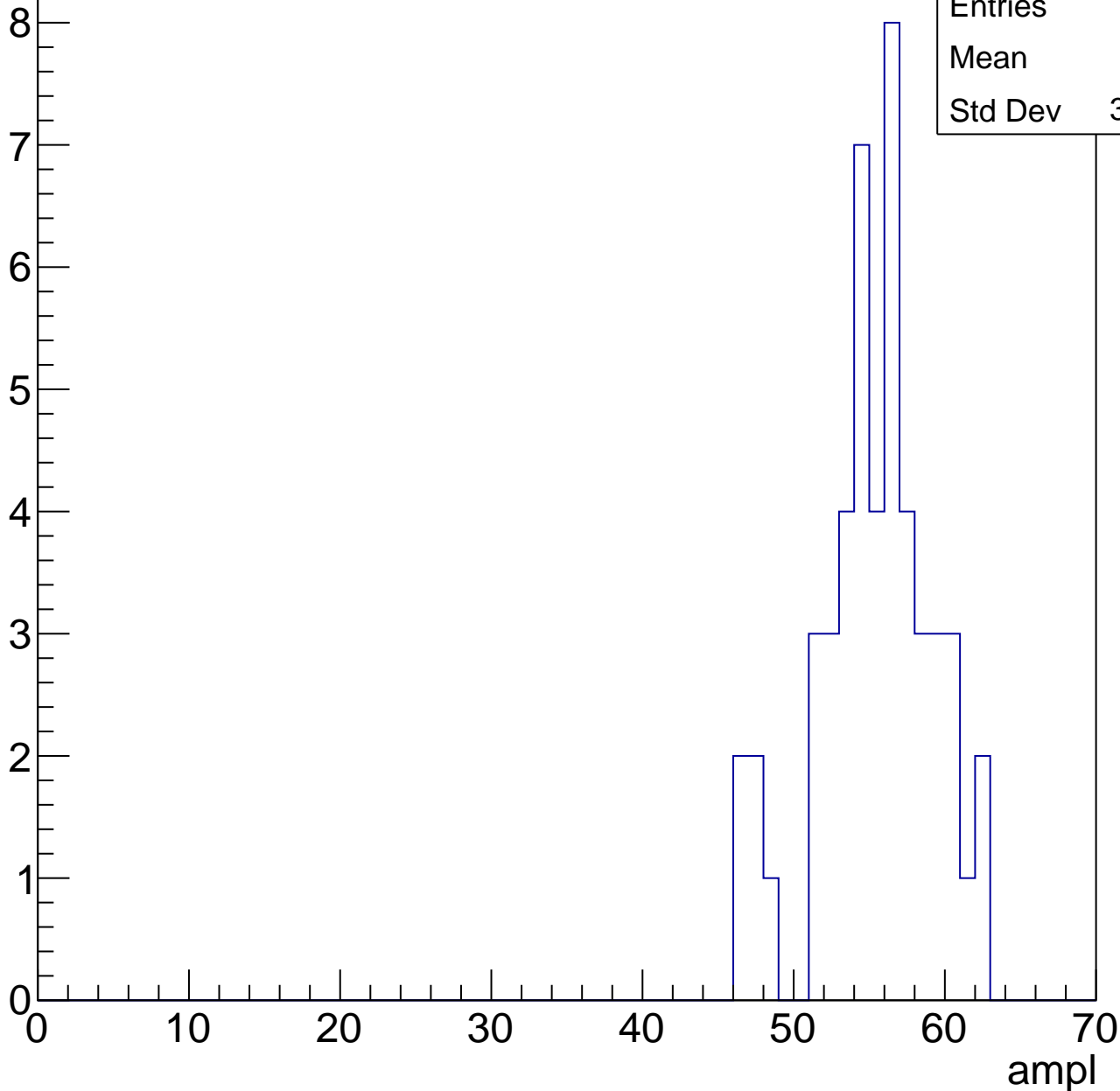


B1L102S, U18-ch58, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.9
Std Dev	3.859

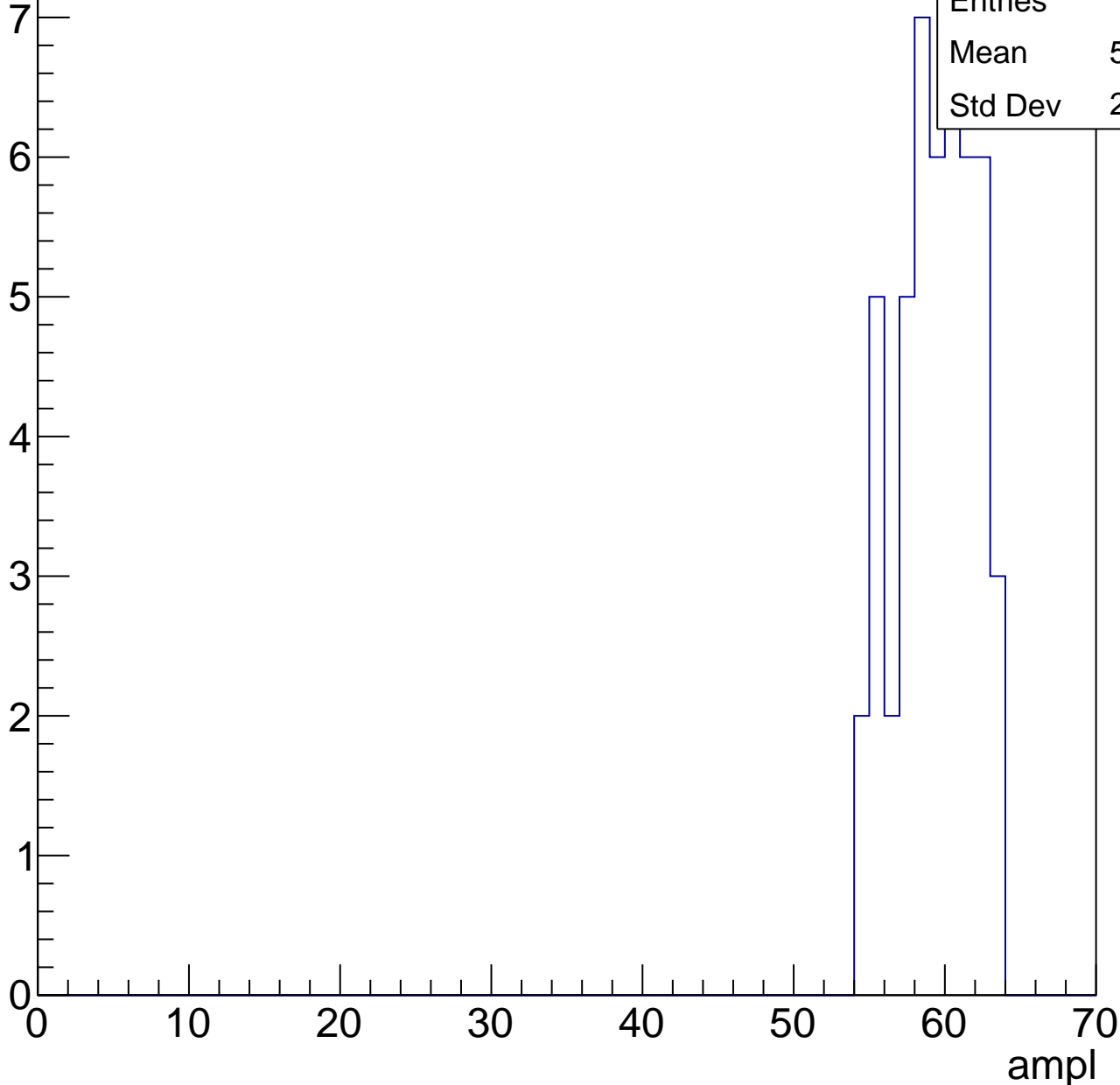


B1L102S, U18-ch58, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	58.92
Std Dev	2.506

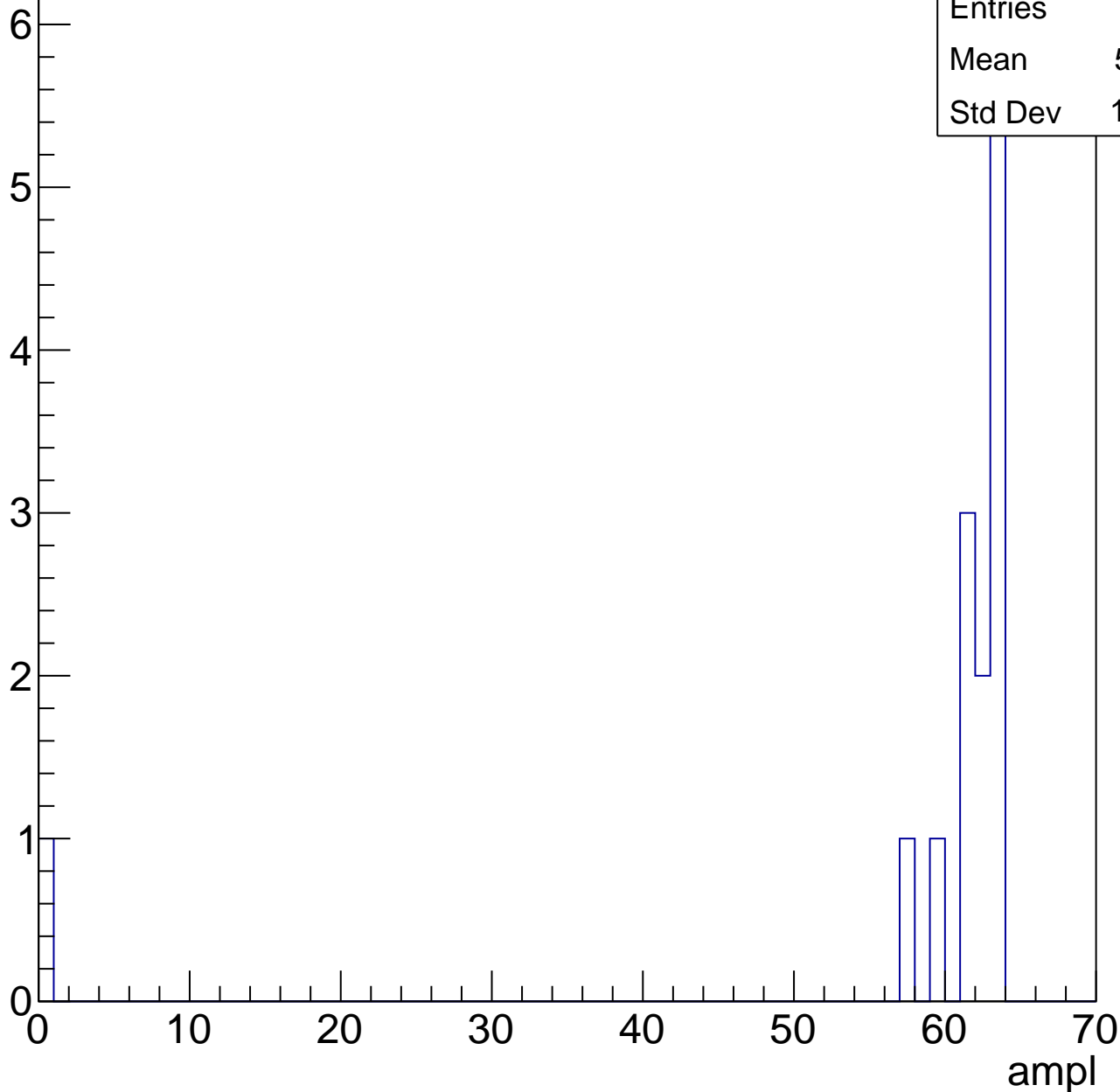


B1L102S, U18-ch58, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.21
Std Dev	15.96



B1L102S, U18-ch58, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch59, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	90
Mean	27.04
Std Dev	4.887

Gaus mean : 27.8025

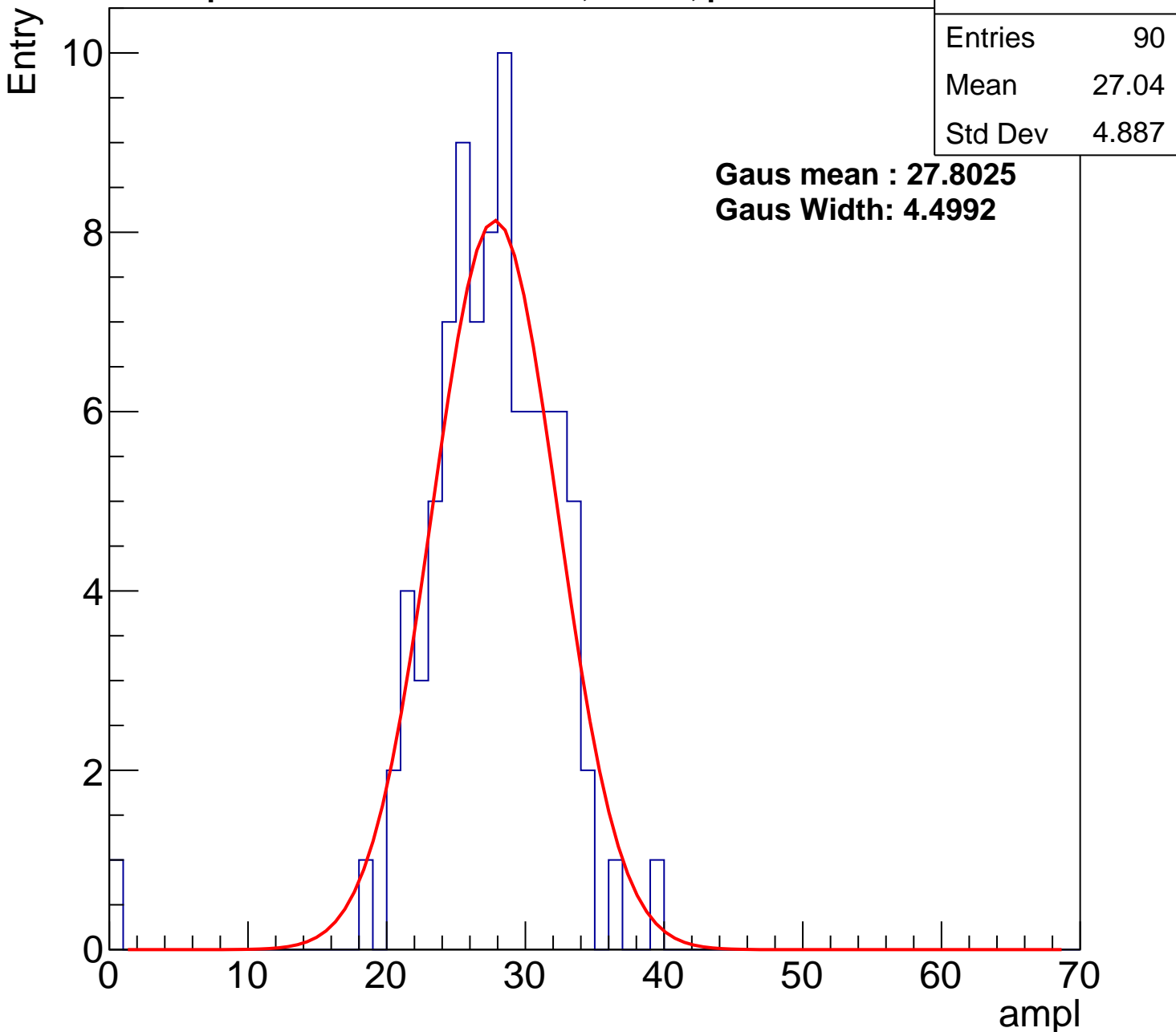
Gaus Width: 4.4992

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch59, adc1

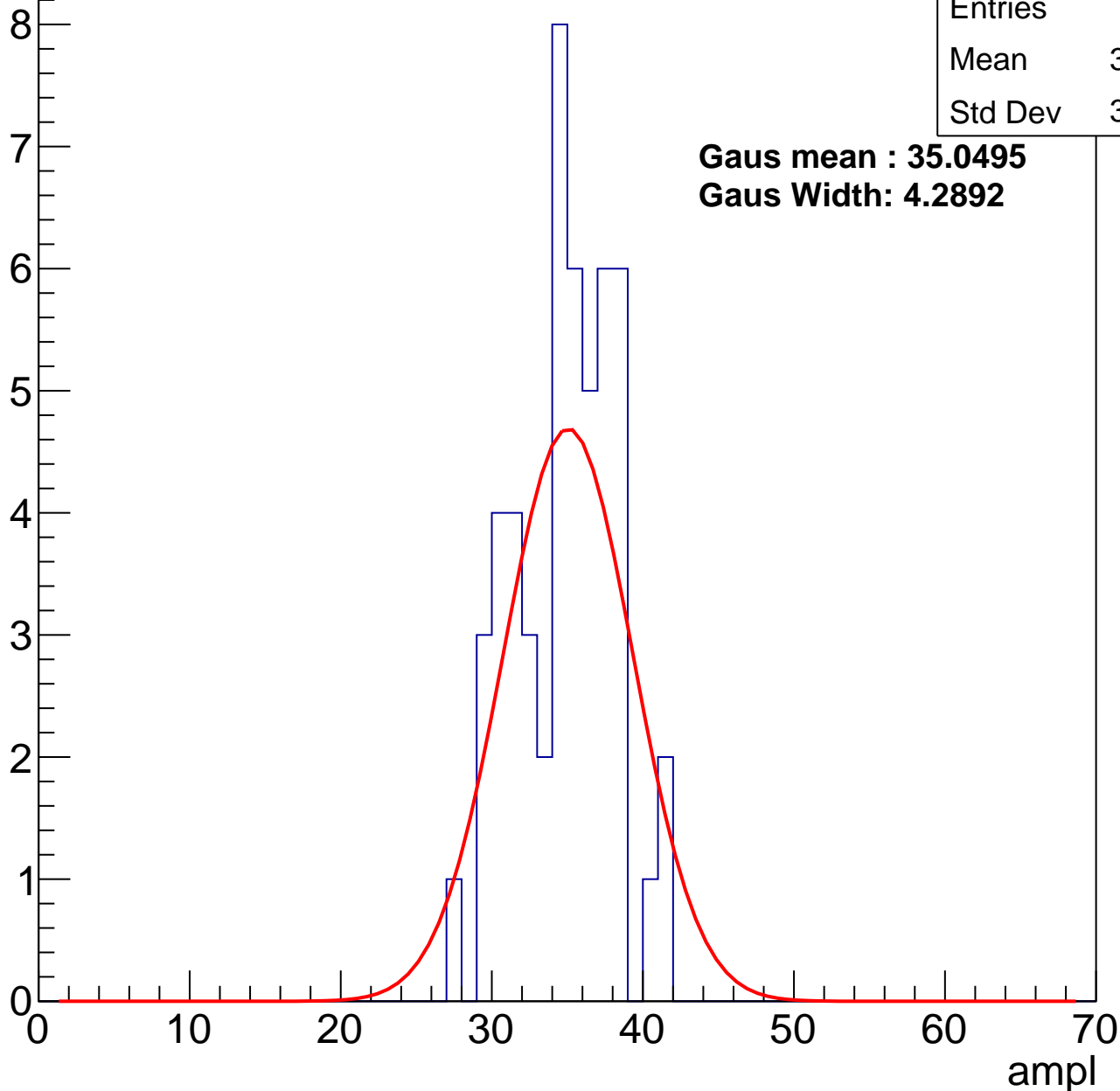
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	34.39
Std Dev	3.254

Gaus mean : 35.0495

Gaus Width: 4.2892



B1L102S, U18-ch59, adc2

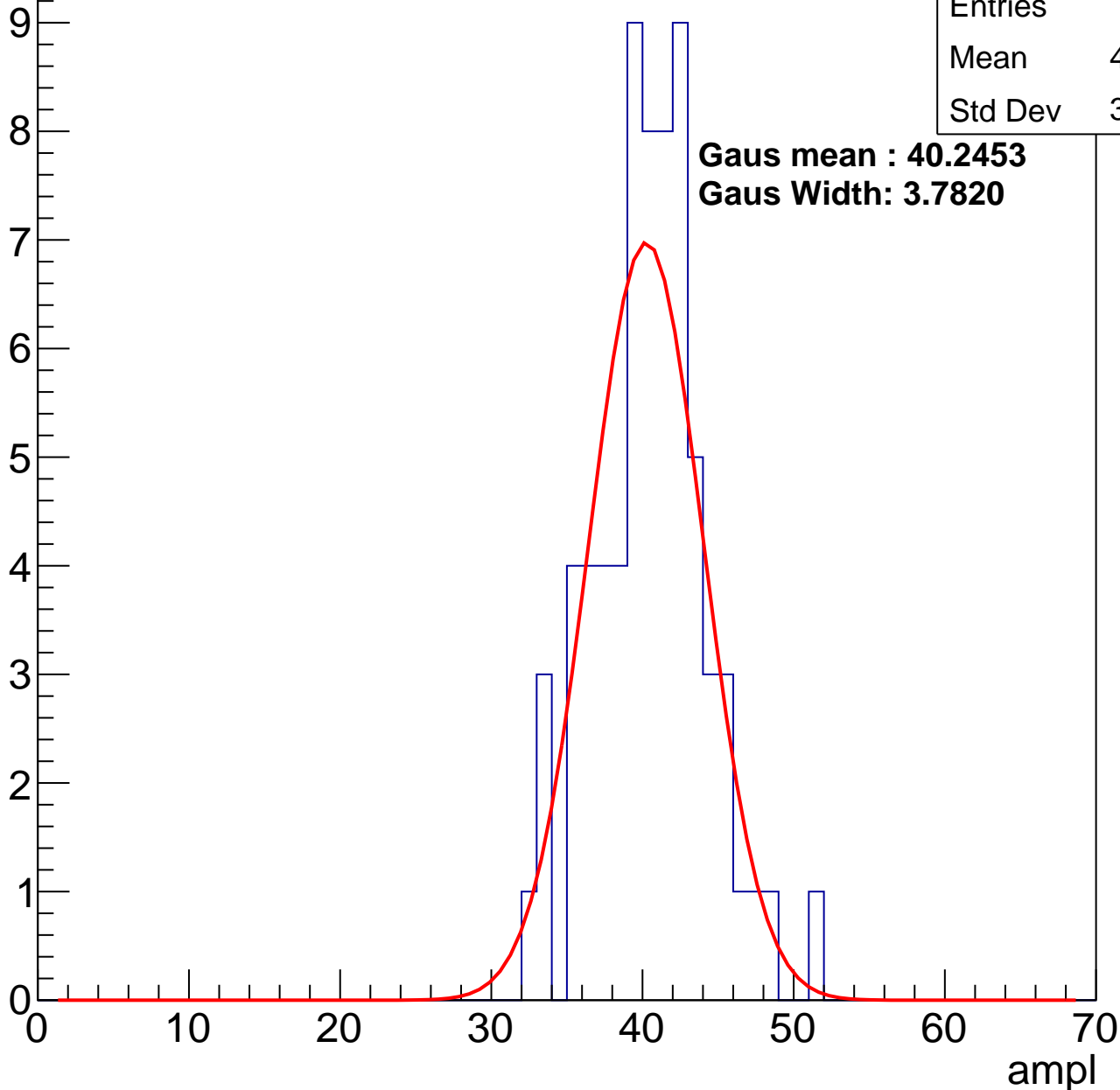
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	40.09
Std Dev	3.646

Gaus mean : 40.2453

Gaus Width: 3.7820

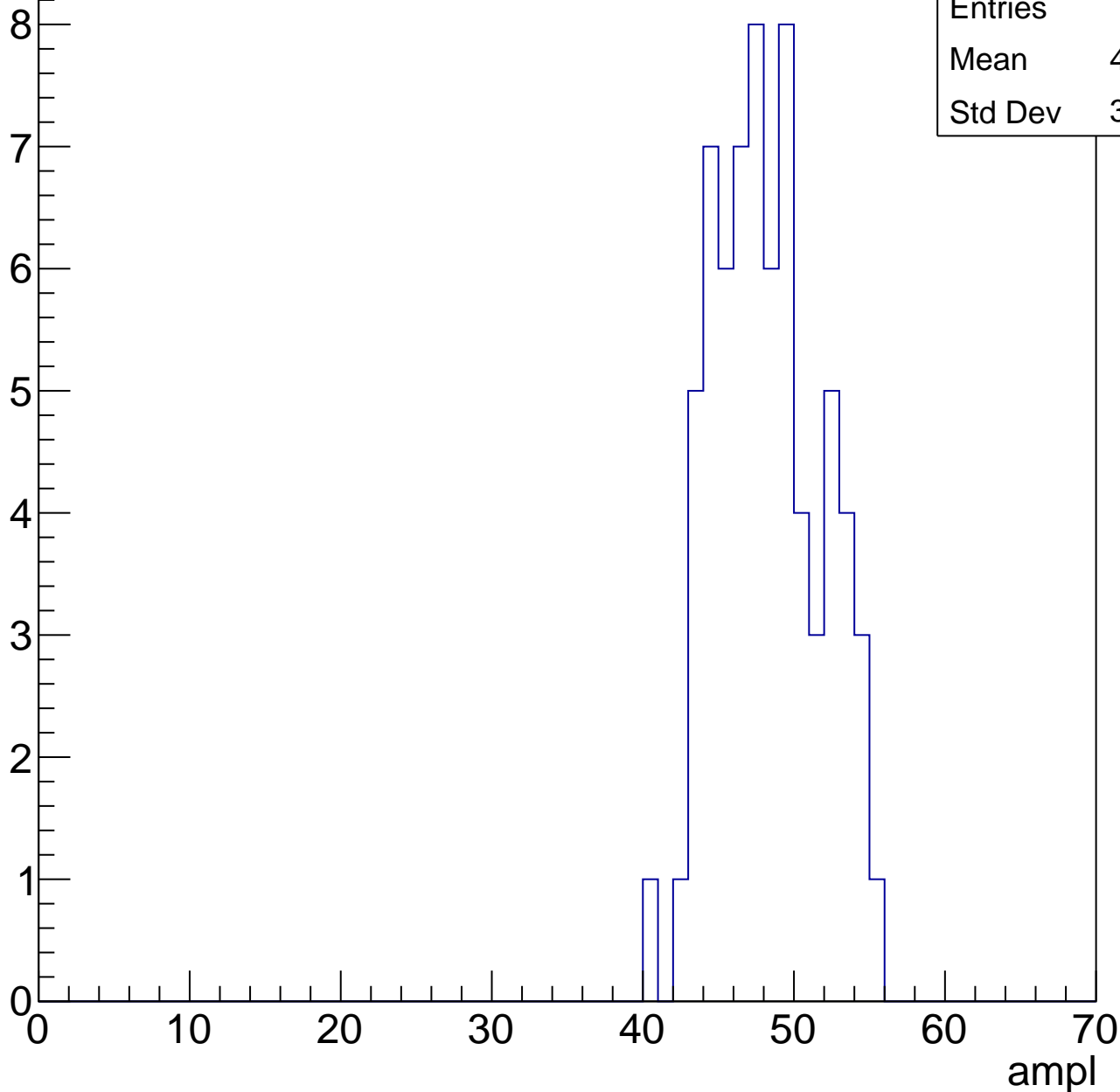


B1L102S, U18-ch59, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.75
Std Dev	3.428

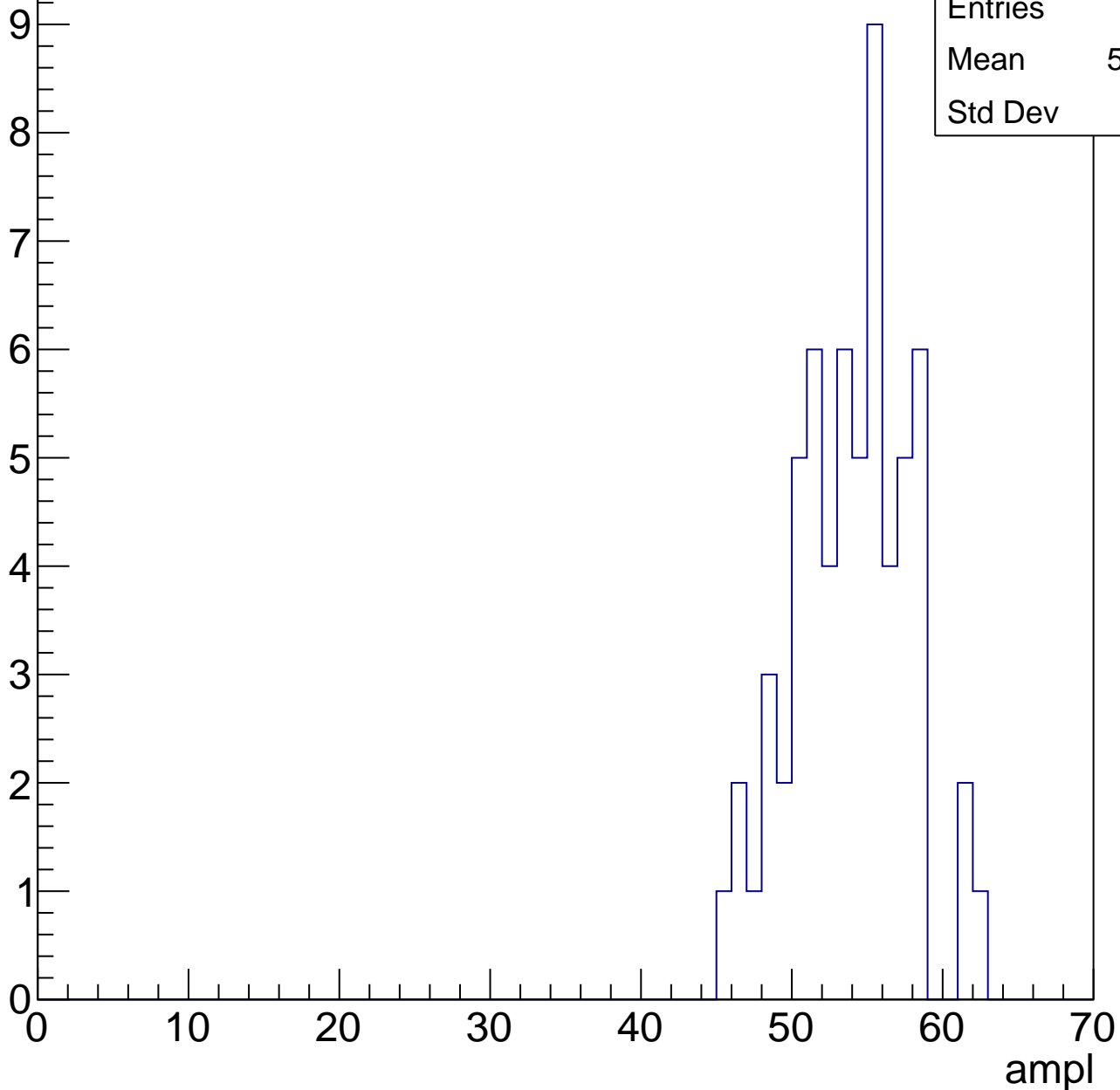


B1L102S, U18-ch59, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	53.45
Std Dev	3.77

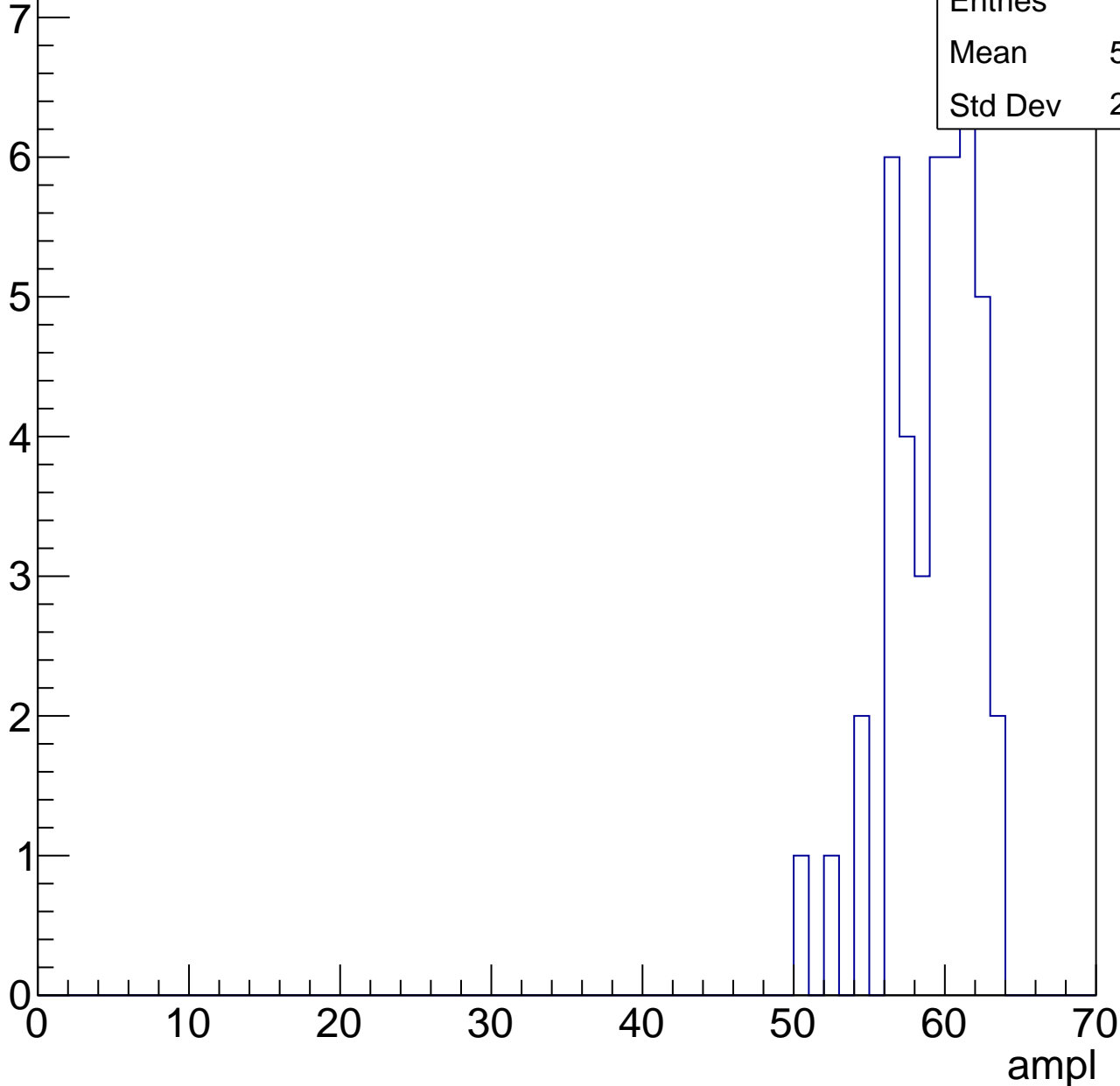


B1L102S, U18-ch59, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.72
Std Dev	2.896

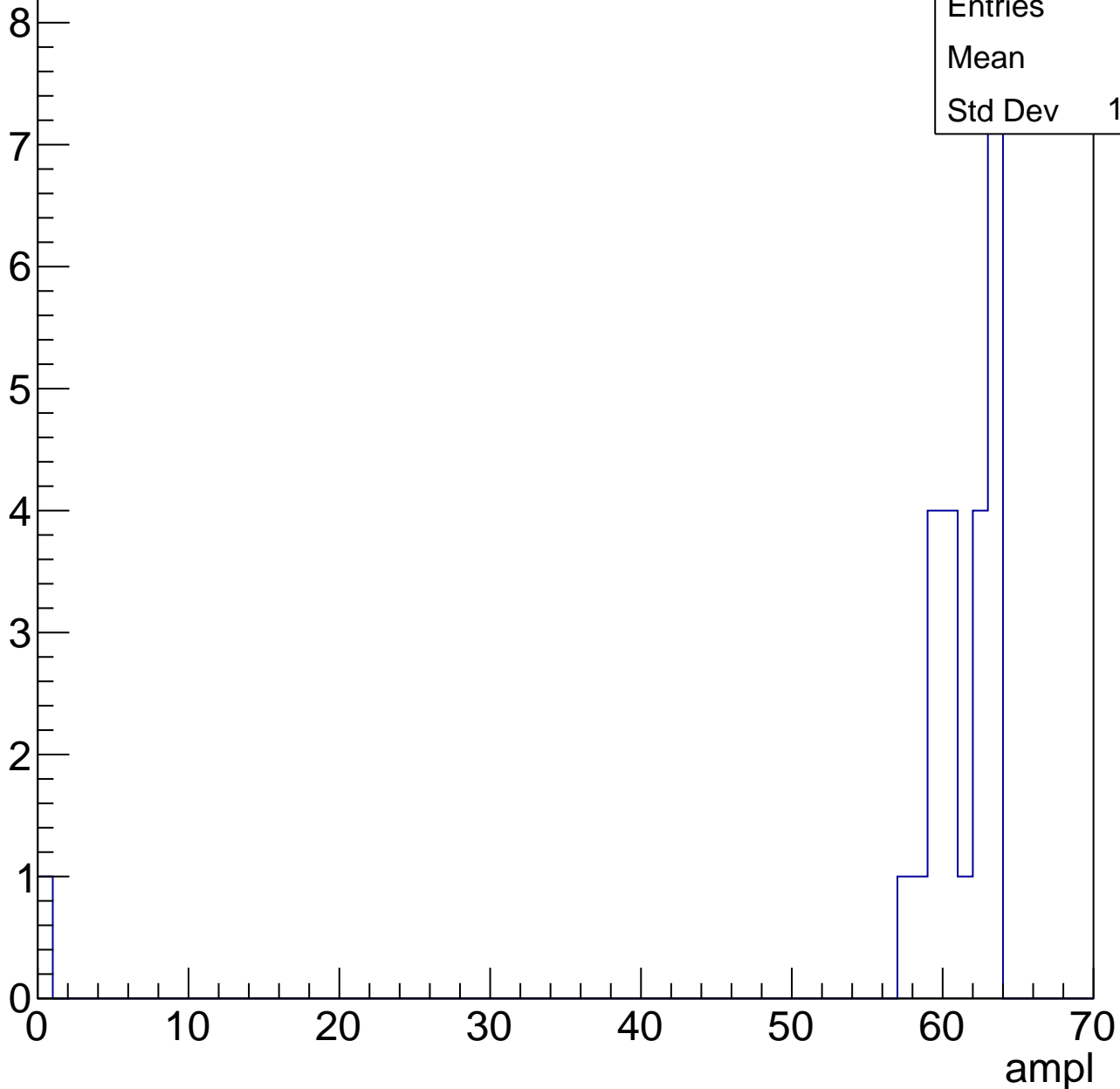


B1L102S, U18-ch59, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	24
Mean	58.5
Std Dev	12.34



B1L102S, U18-ch59, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch60, adc0

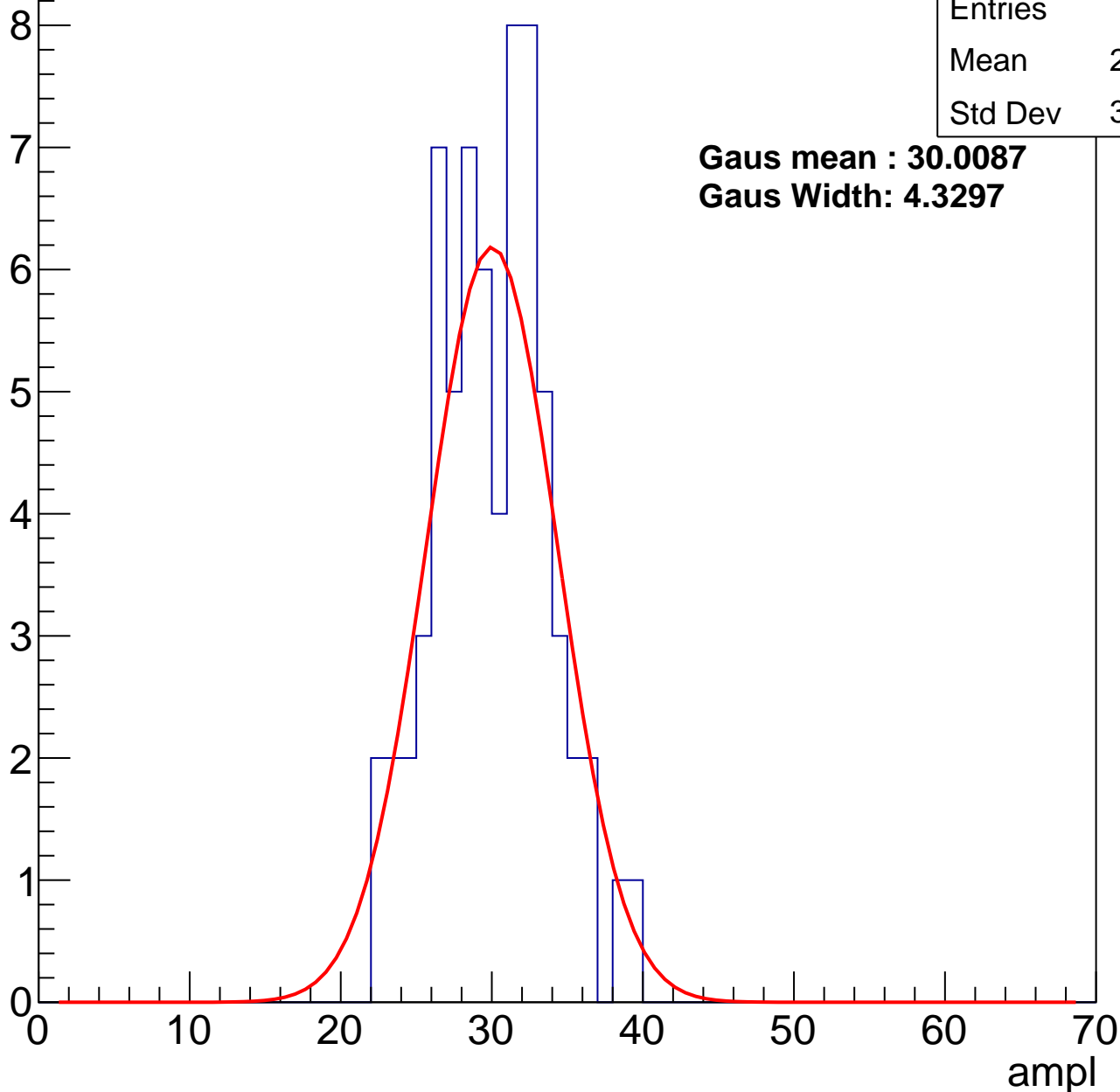
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	29.56
Std Dev	3.739

Gaus mean : 30.0087

Gaus Width: 4.3297



B1L102S, U18-ch60, adc1

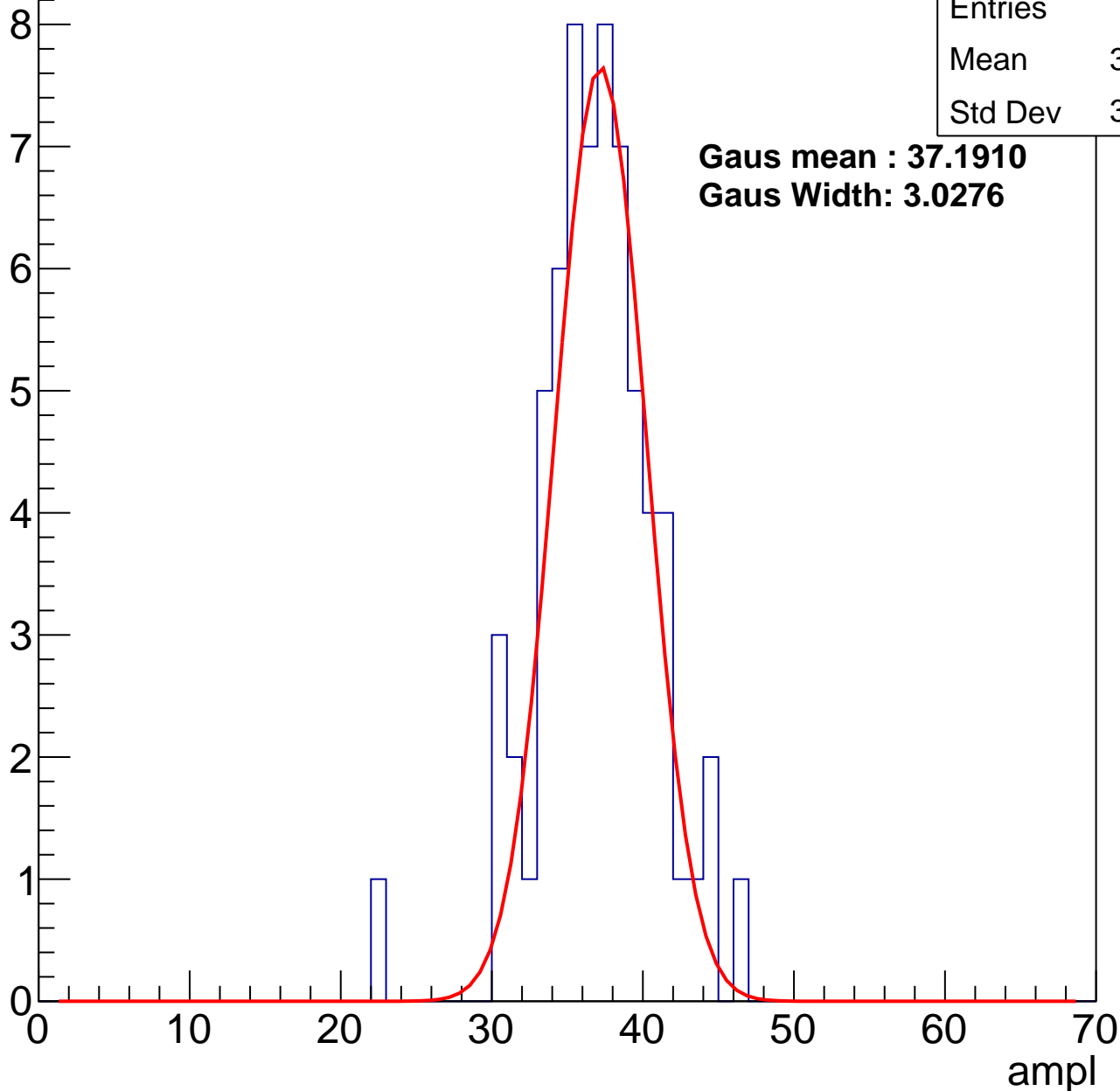
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	36.47
Std Dev	3.874

Gaus mean : 37.1910

Gaus Width: 3.0276



B1L102S, U18-ch60, adc2

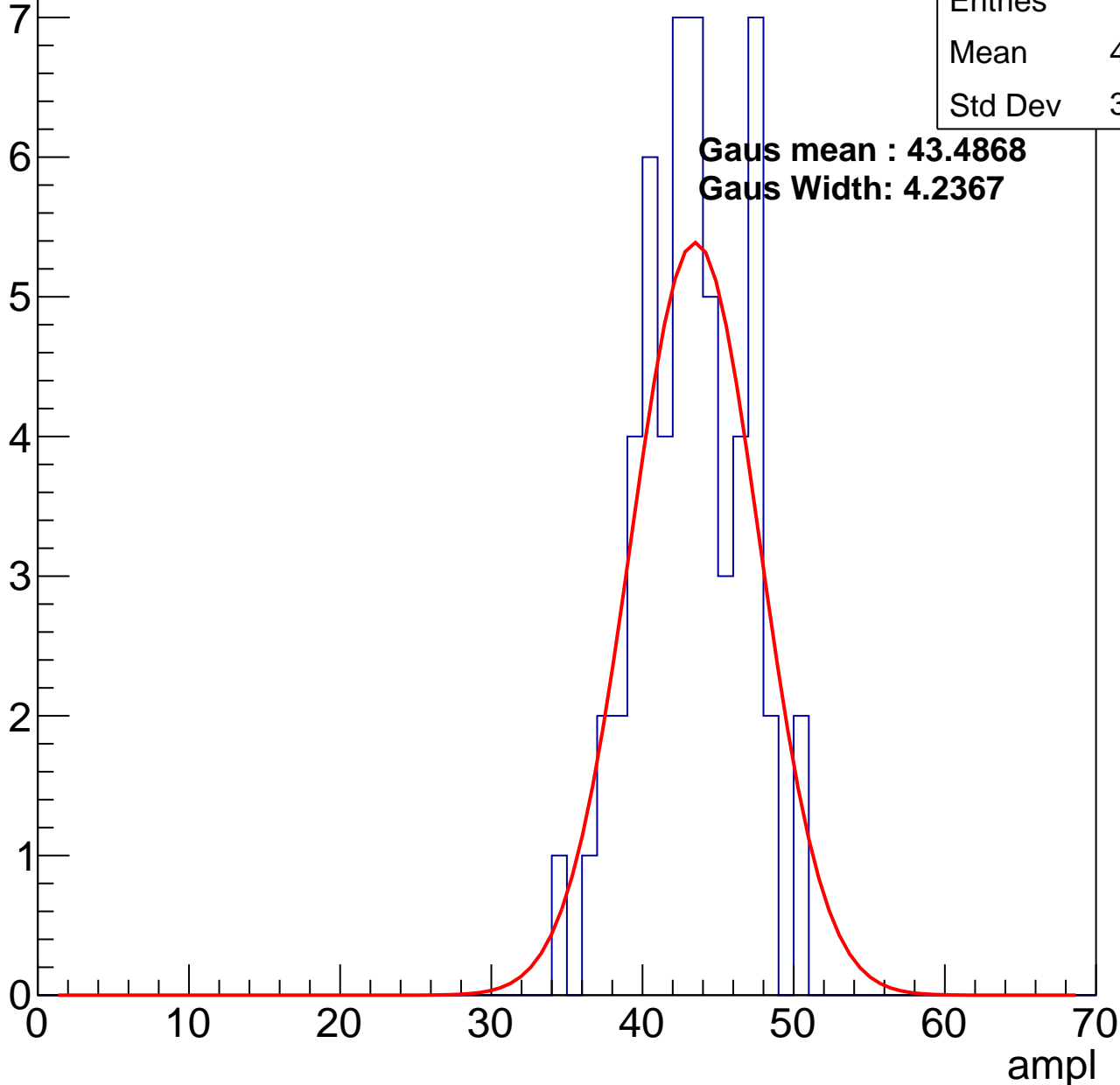
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	42.79
Std Dev	3.513

Gaus mean : 43.4868

Gaus Width: 4.2367

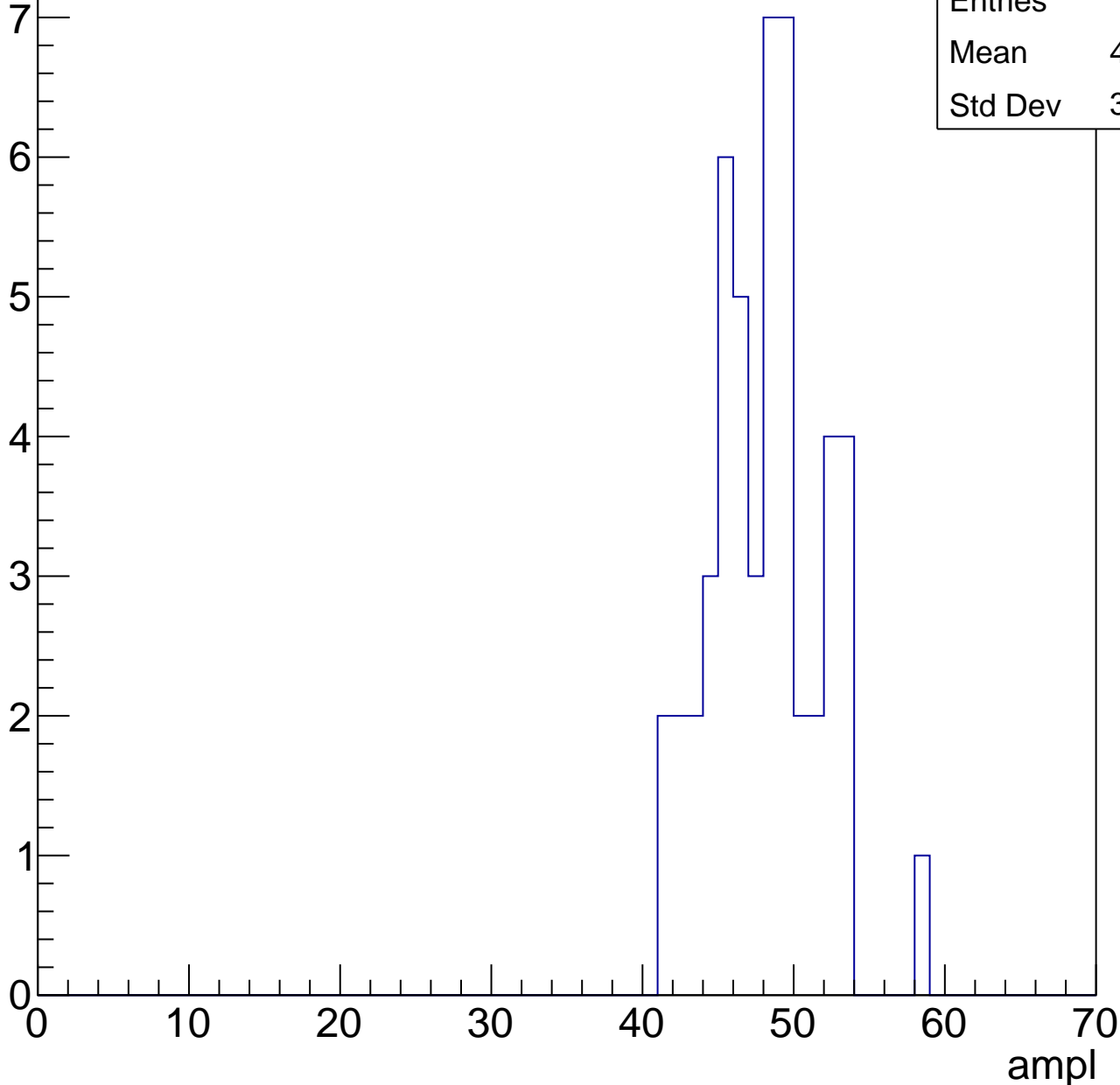


B1L102S, U18-ch60, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	47.68
Std Dev	3.569

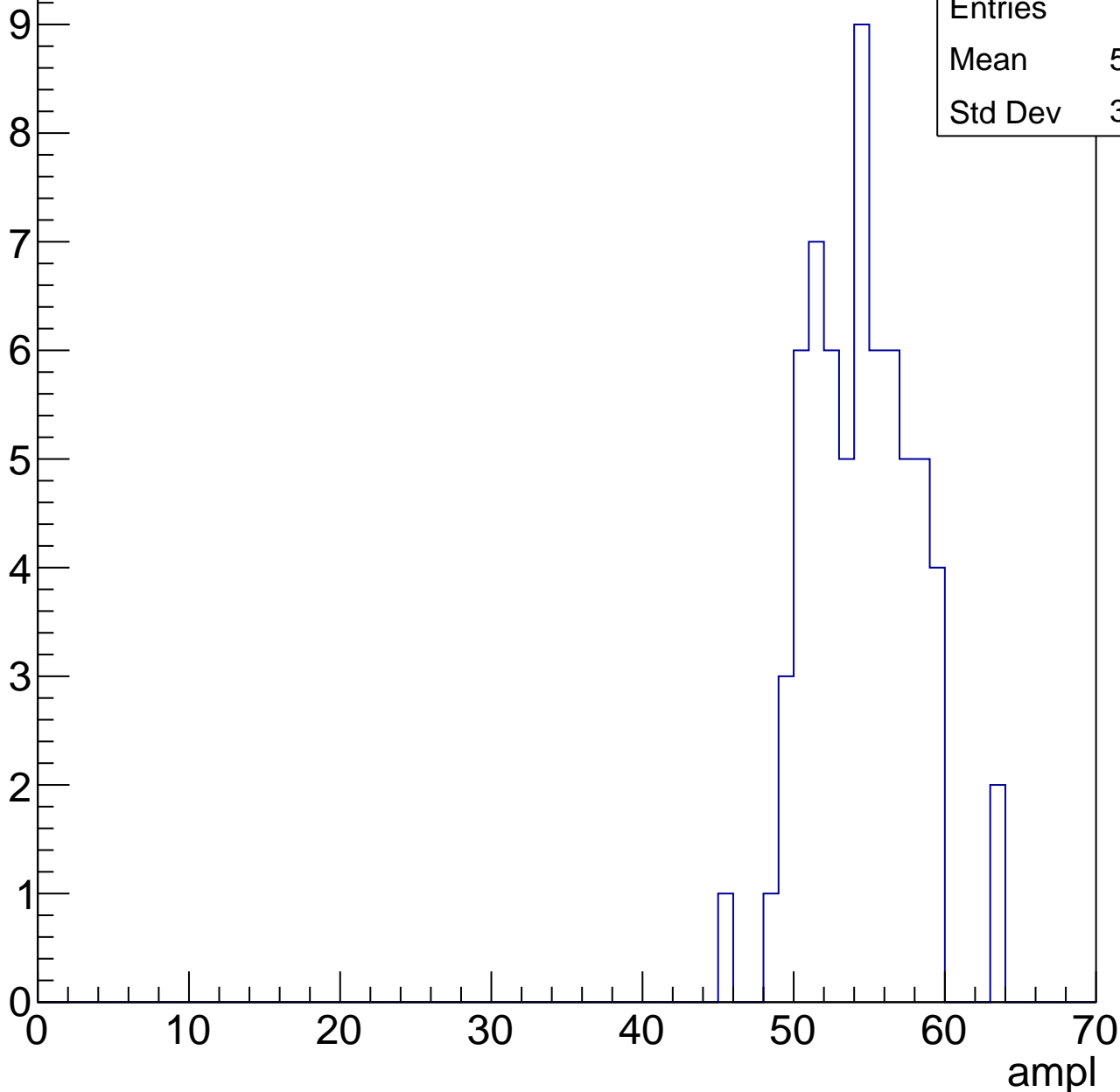


B1L102S, U18-ch60, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.98
Std Dev	3.475



B1L102S, U18-ch60, adc5

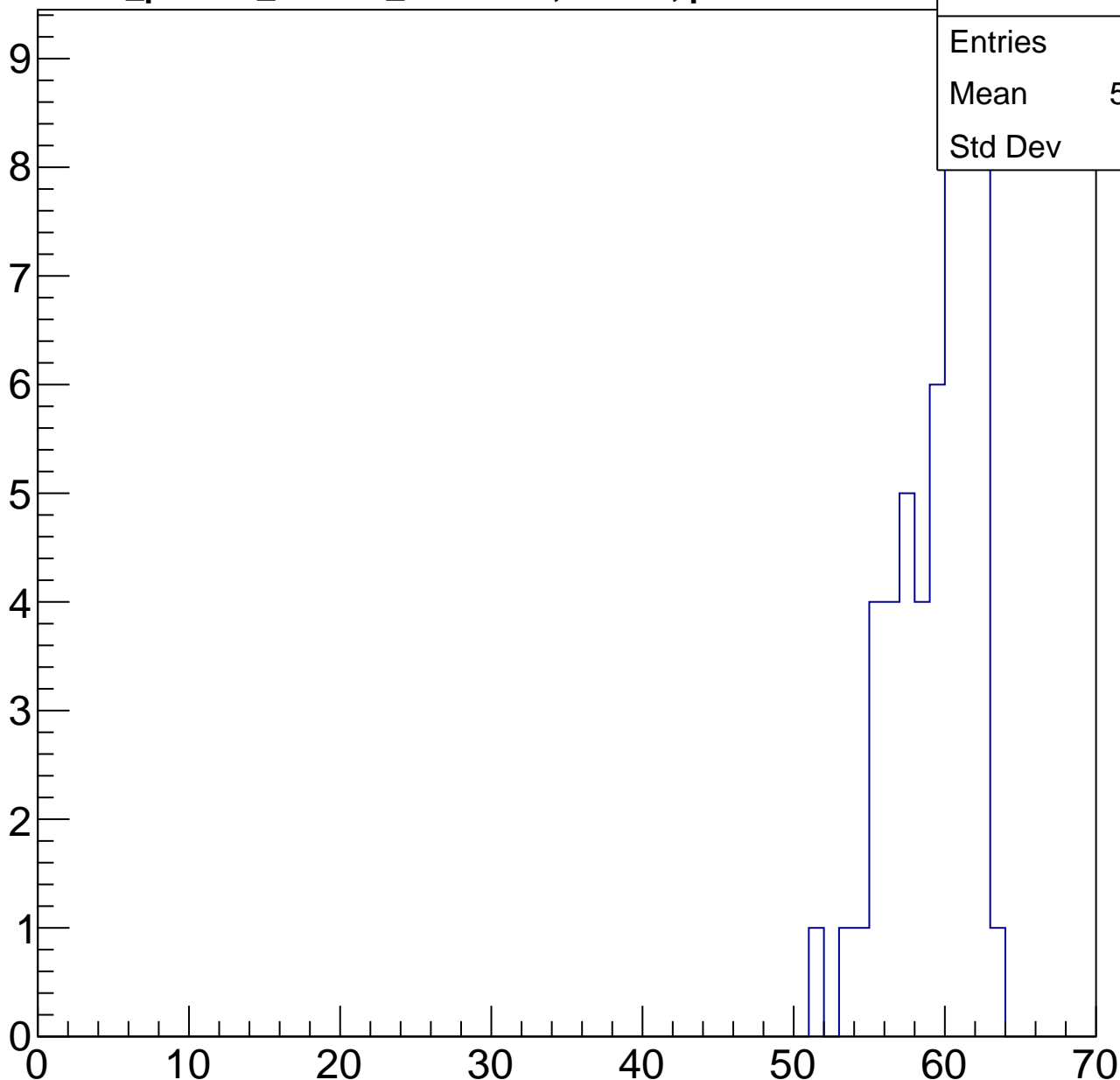
calib_packv5_042523_0143.root, FC#11, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.94
Std Dev	2.69

ampl

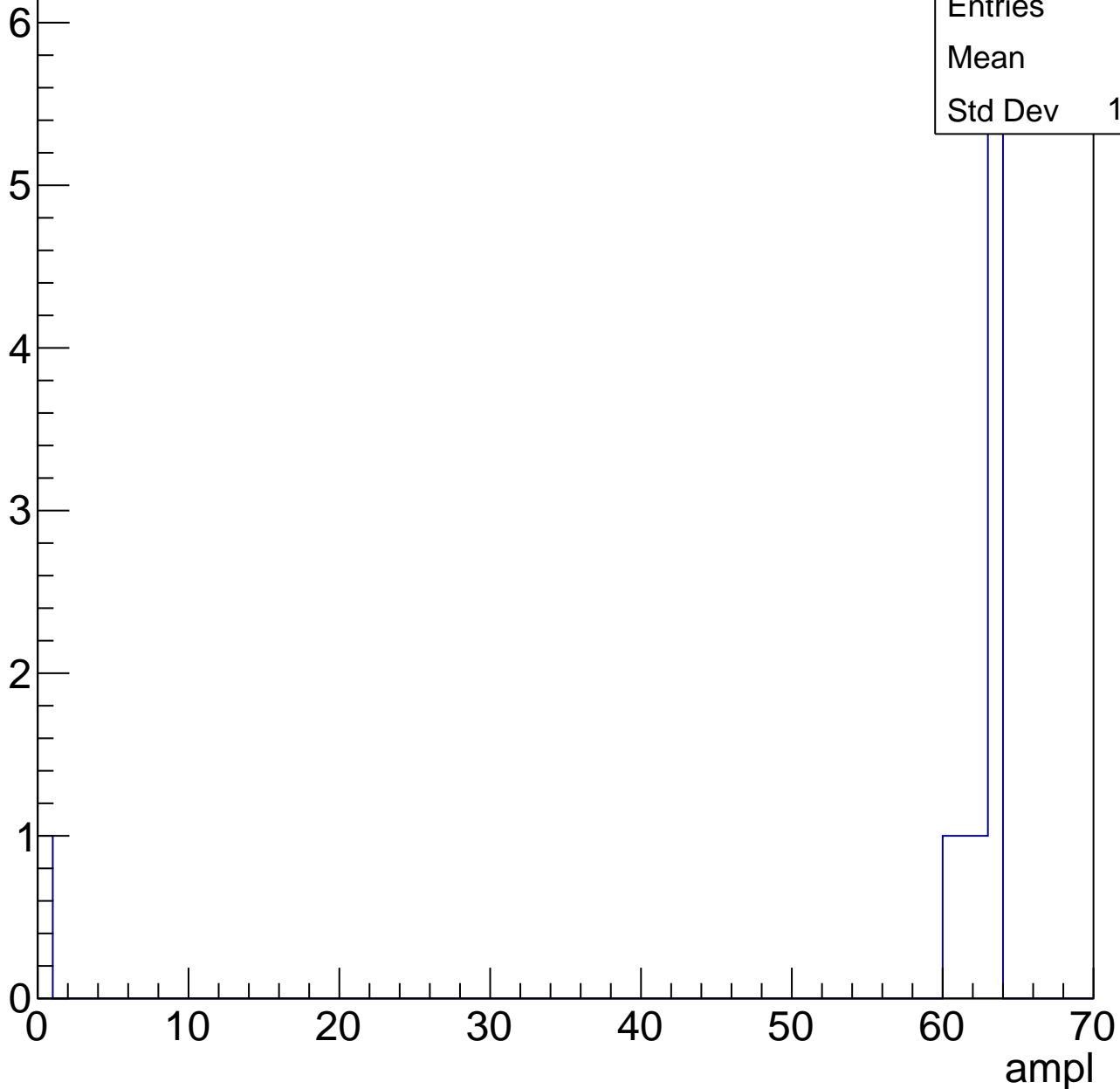


B1L102S, U18-ch60, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	56.1
Std Dev	18.73



B1L102S, U18-ch60, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch61, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	29.22
Std Dev	3.559

Gaus mean : 29.0932

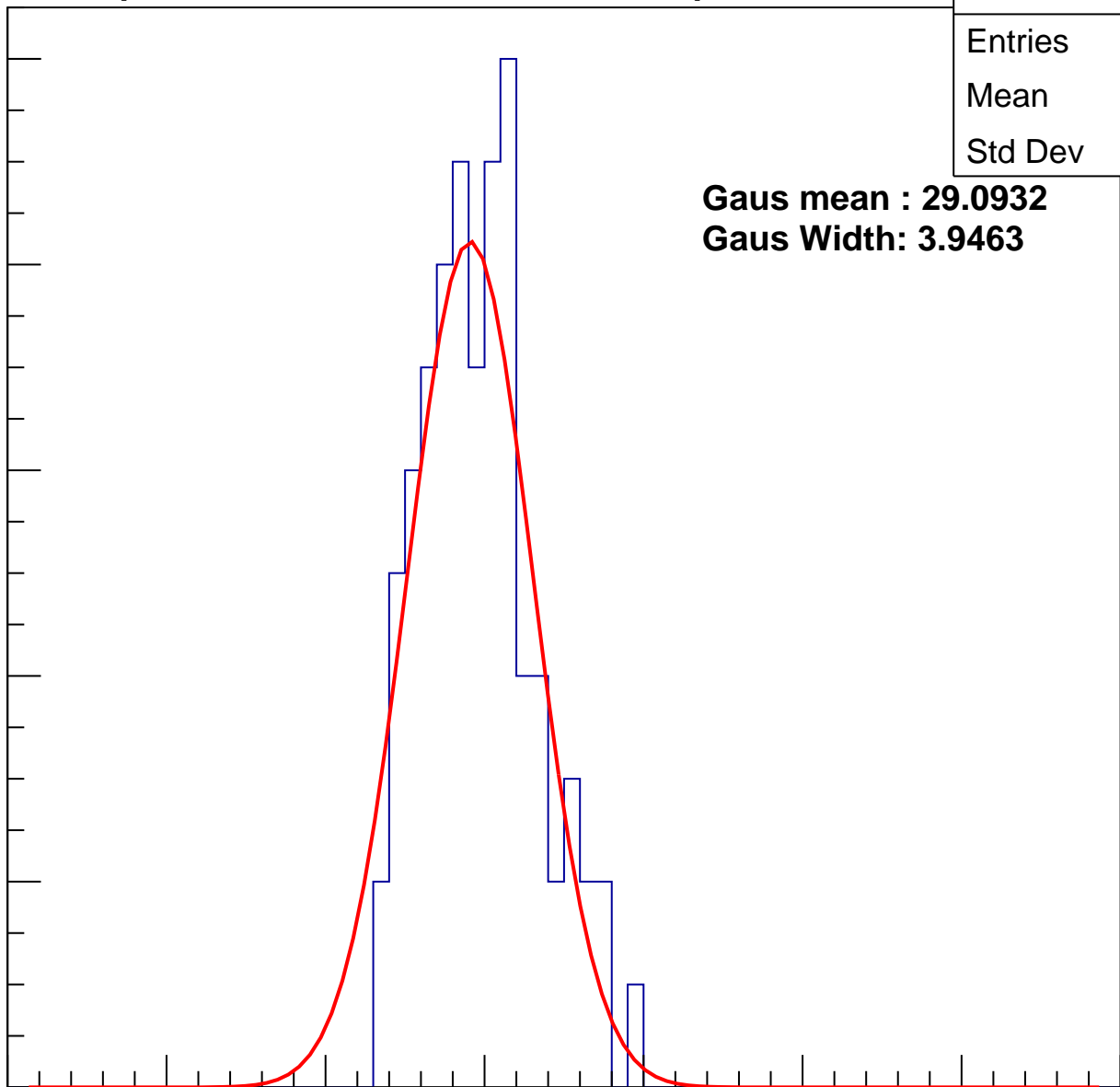
Gaus Width: 3.9463

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch61, adc1

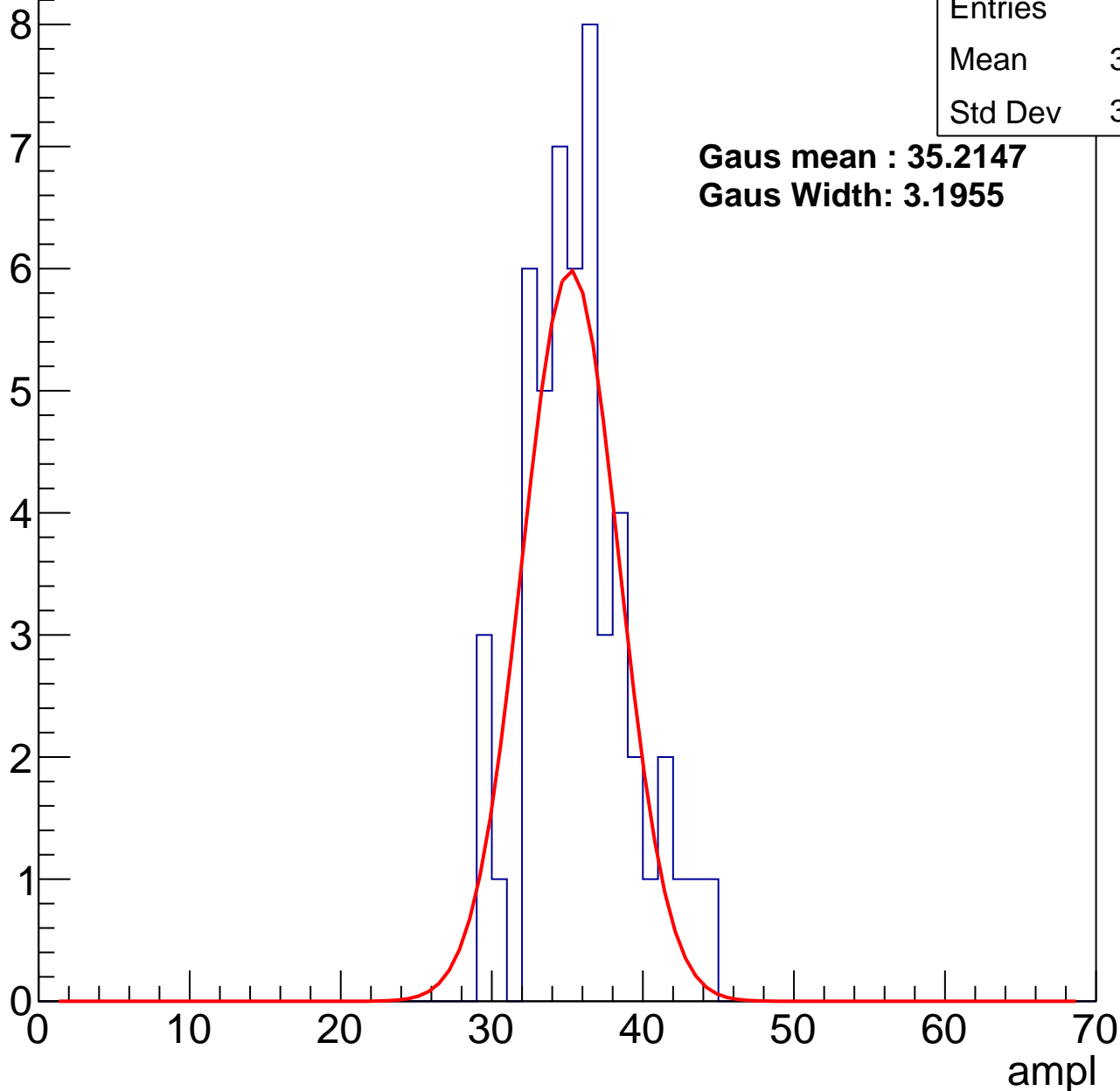
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	35.33
Std Dev	3.394

Gaus mean : 35.2147

Gaus Width: 3.1955



B1L102S, U18-ch61, adc2

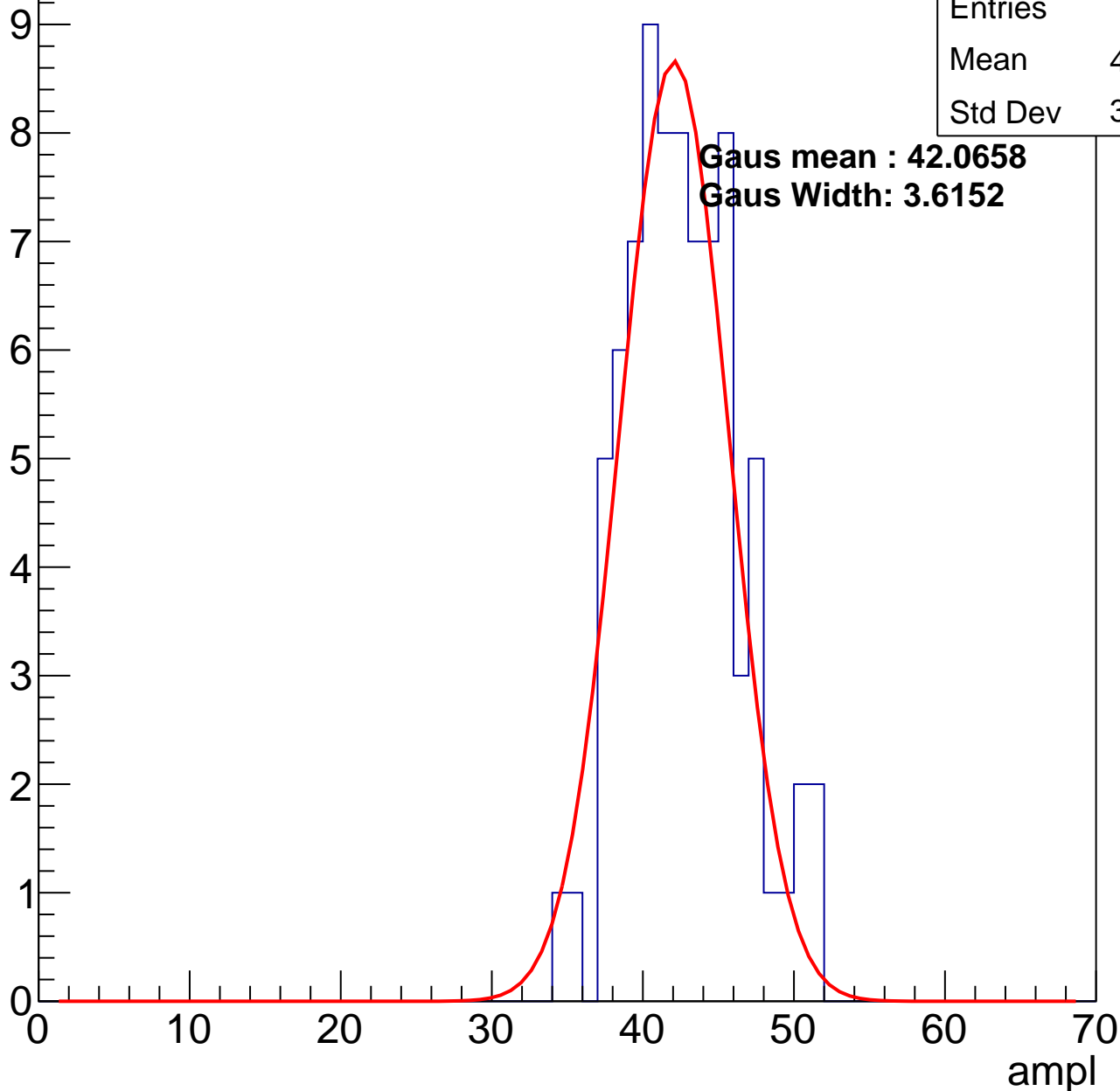
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.22
Std Dev	3.672

Gaus mean : 42.0658

Gaus Width: 3.6152

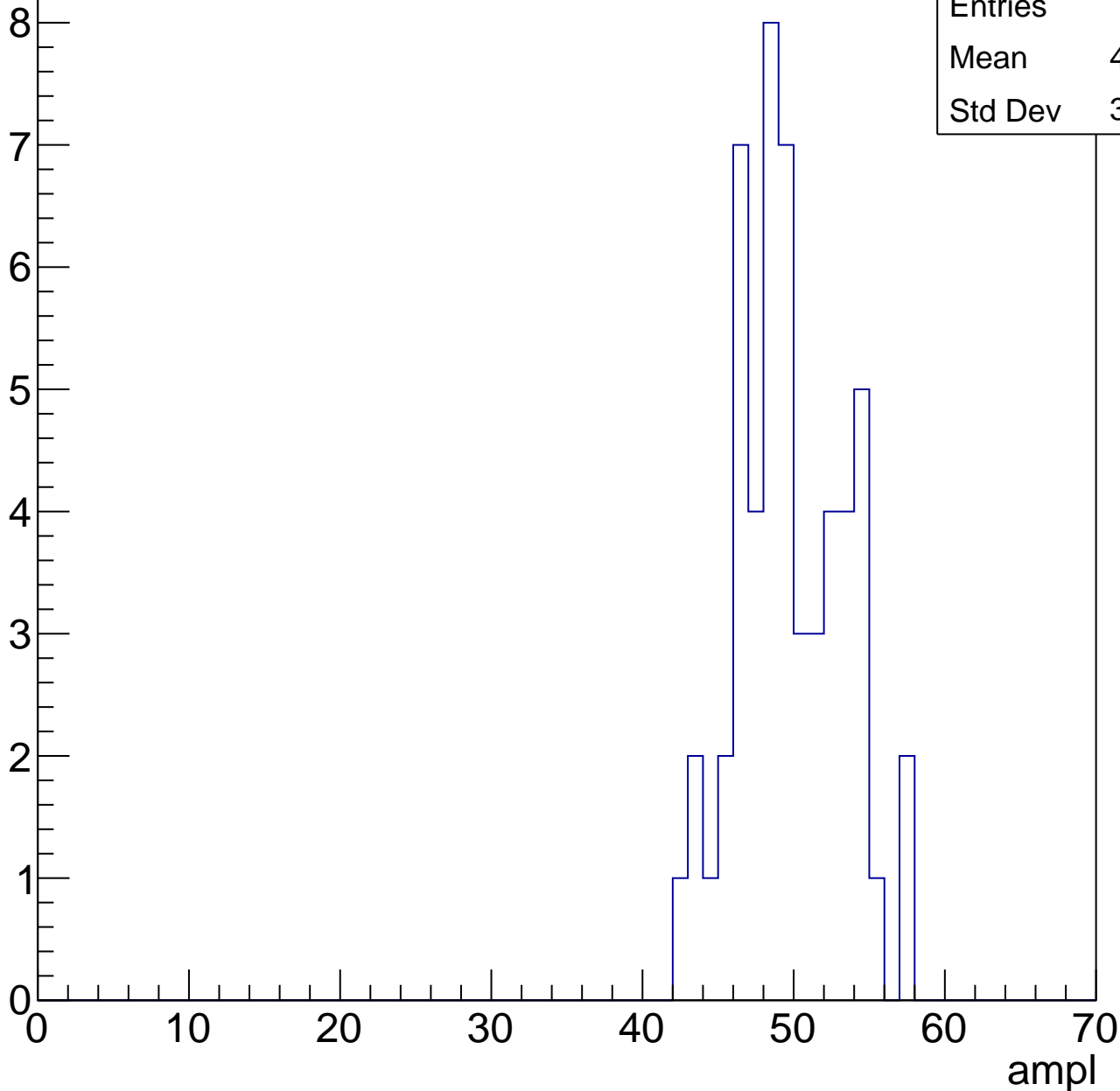


B1L102S, U18-ch61, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	49.28
Std Dev	3.509

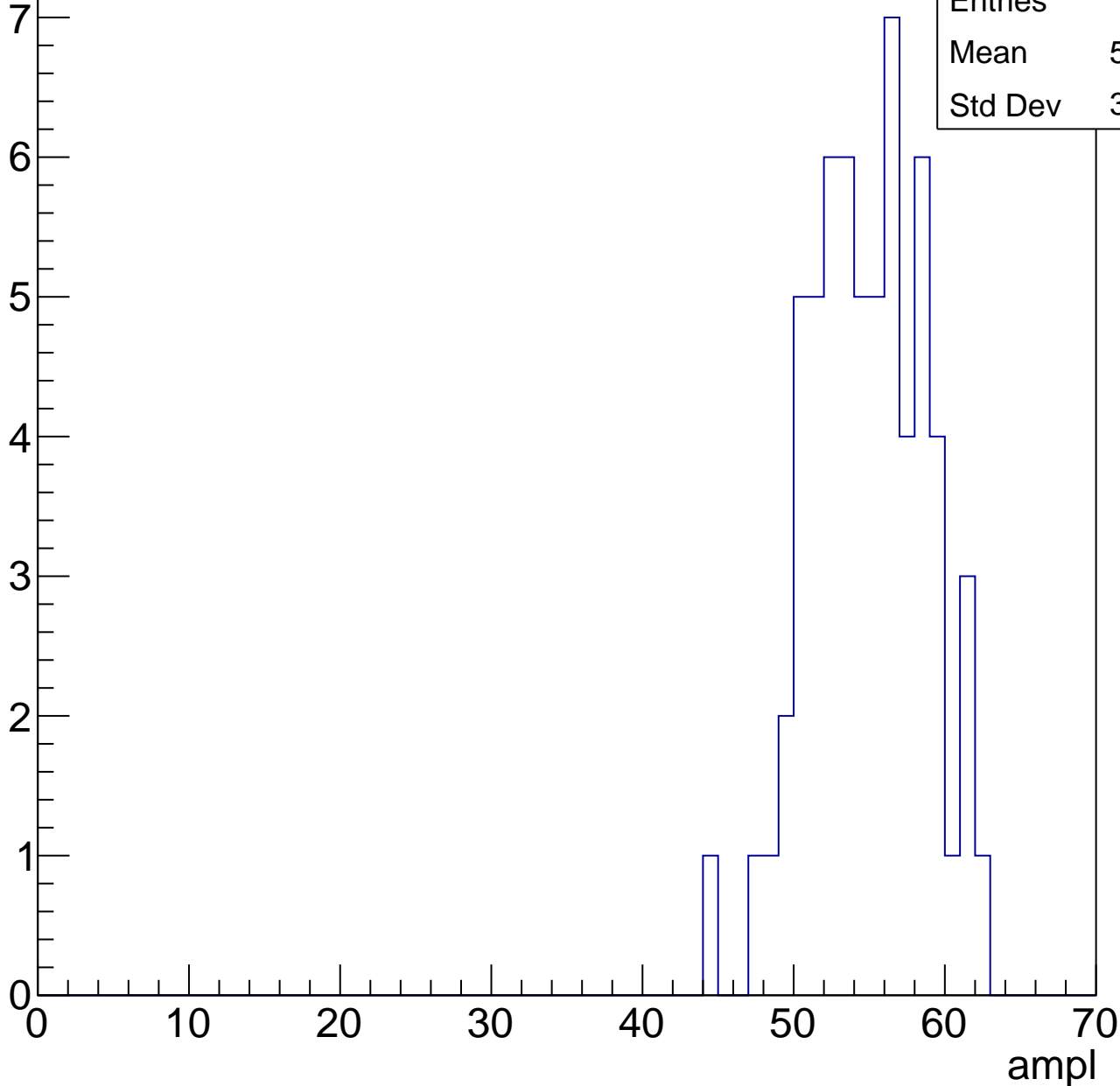


B1L102S, U18-ch61, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.38
Std Dev	3.765



B1L102S, U18-ch61, adc5

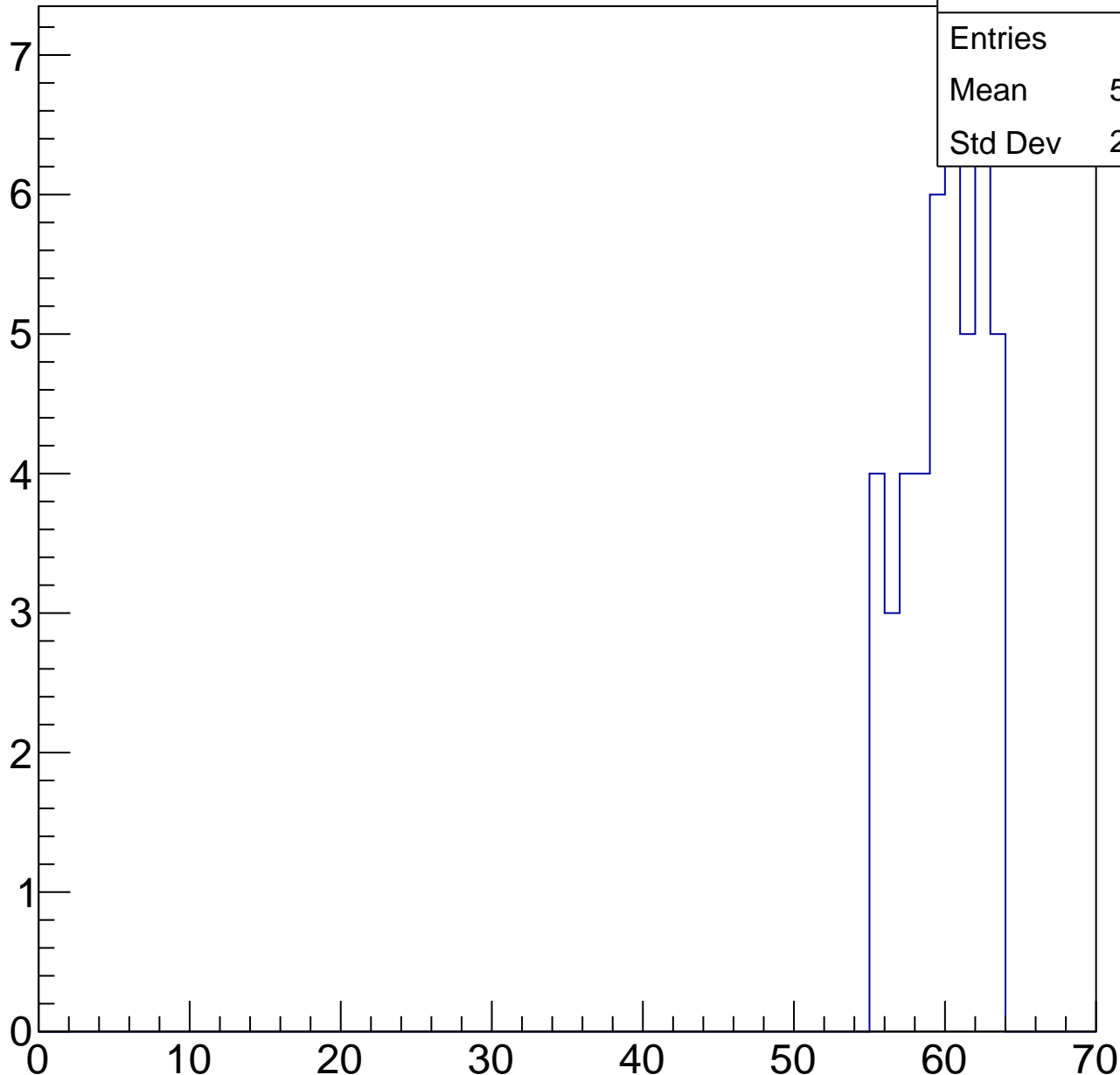
calib_packv5_042523_0143.root, FC#11, port A2

Entry

7
6
5
4
3
2
1
0

Entries	45
Mean	59.47
Std Dev	2.455

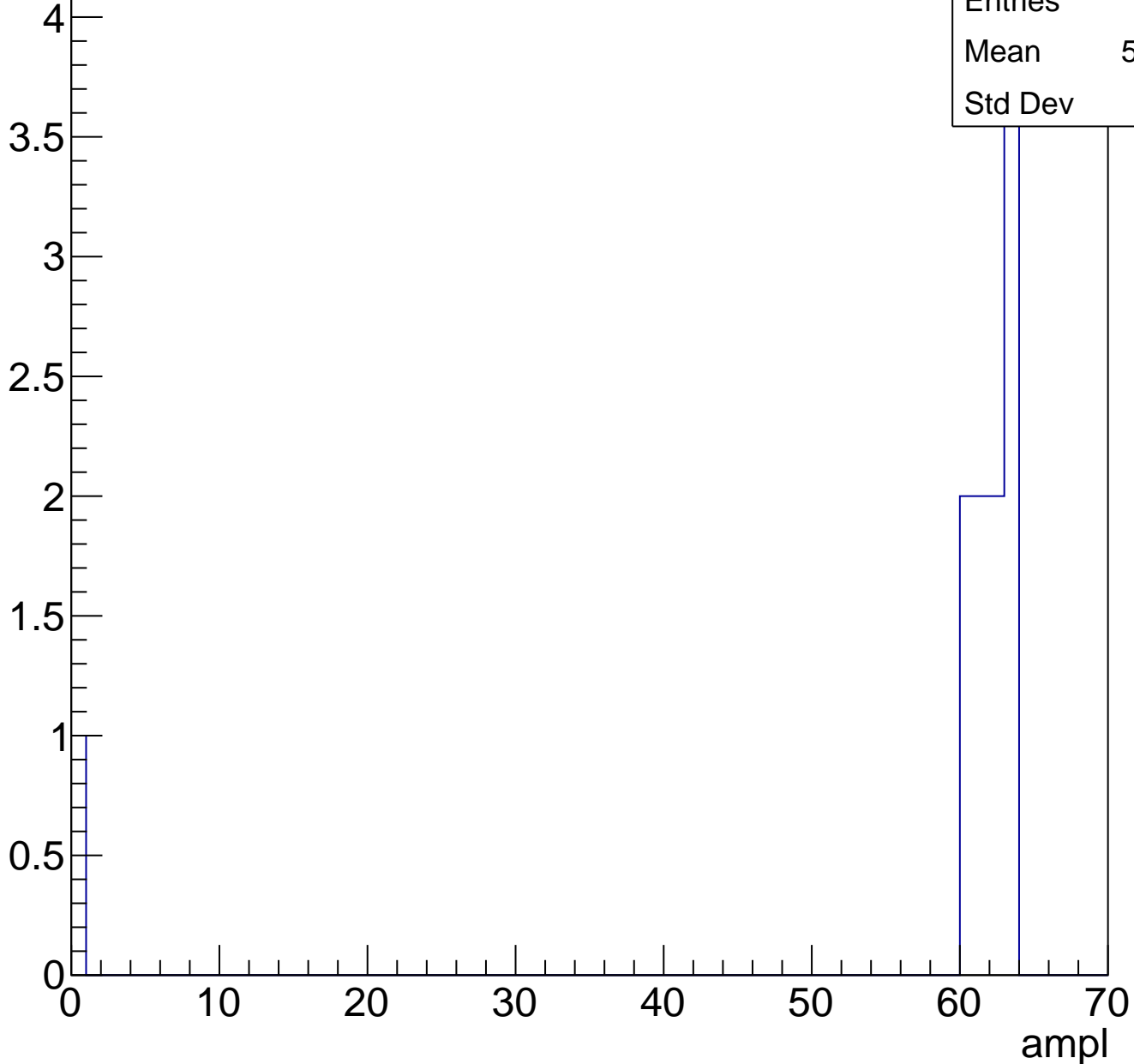
ampl



B1L102S, U18-ch61, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

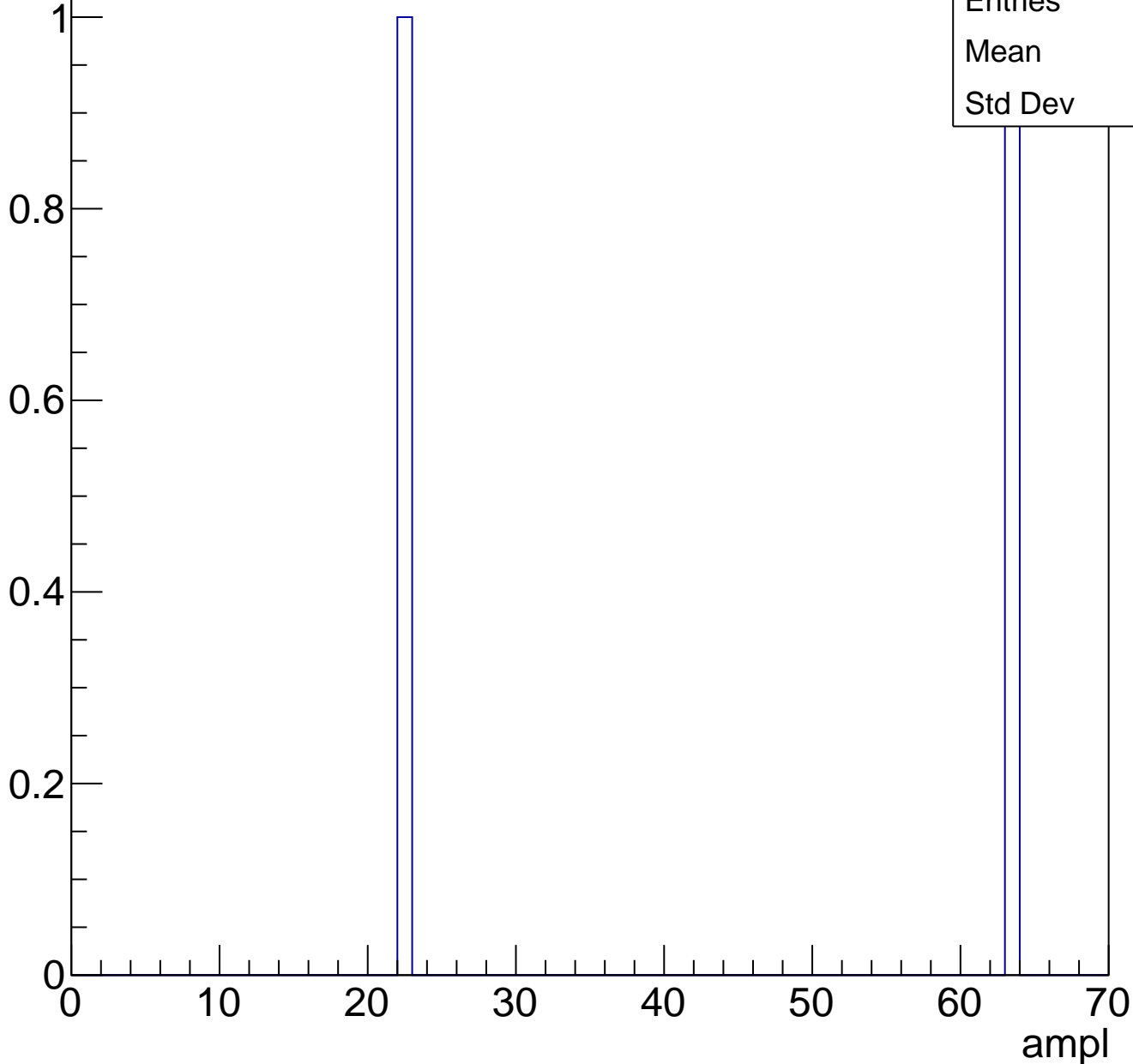


Entries	11
Mean	56.18
Std Dev	17.8

B1L102S, U18-ch61, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch62, adc0

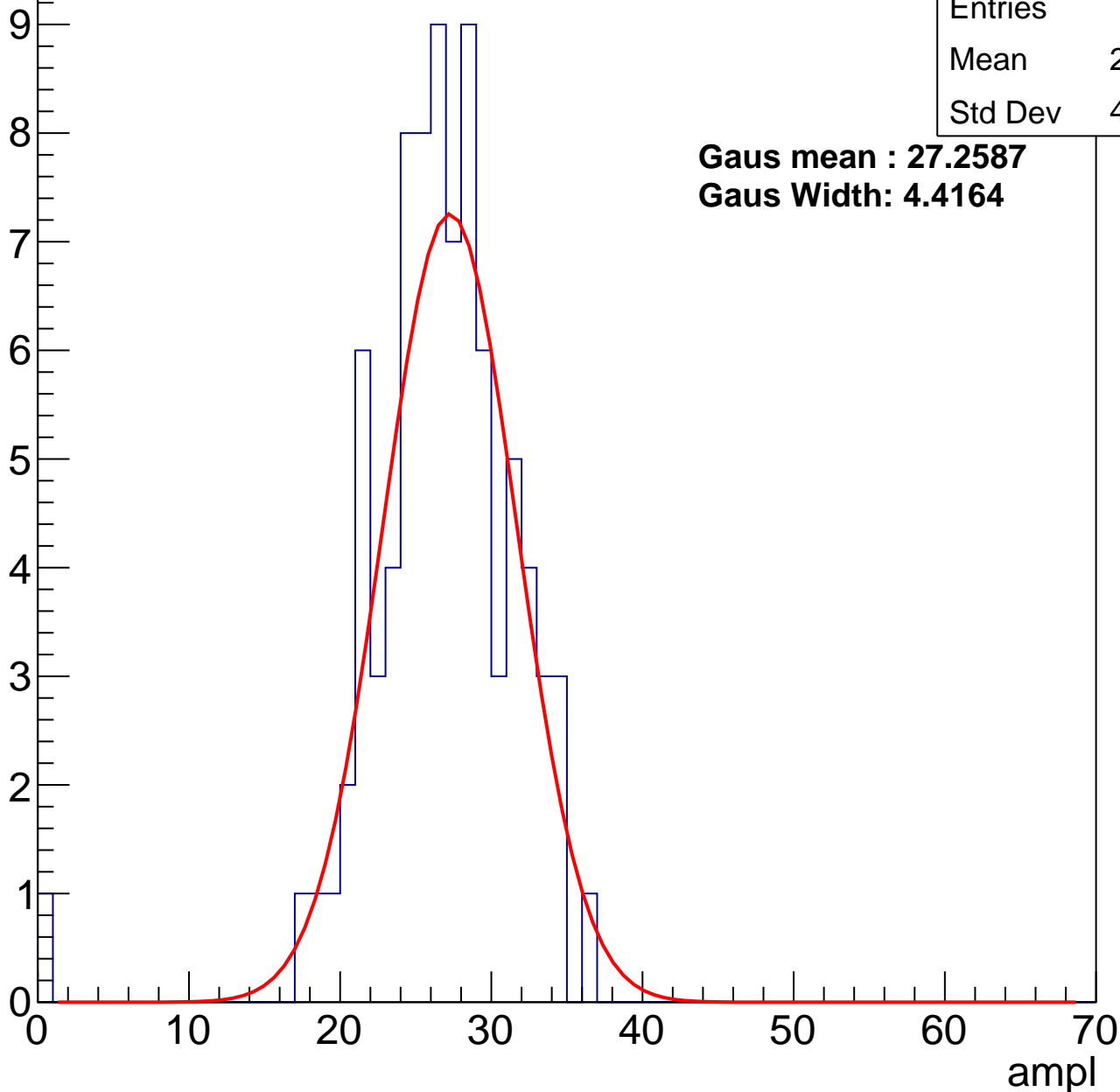
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	26.22
Std Dev	4.929

Gaus mean : 27.2587

Gaus Width: 4.4164



B1L102S, U18-ch62, adc1

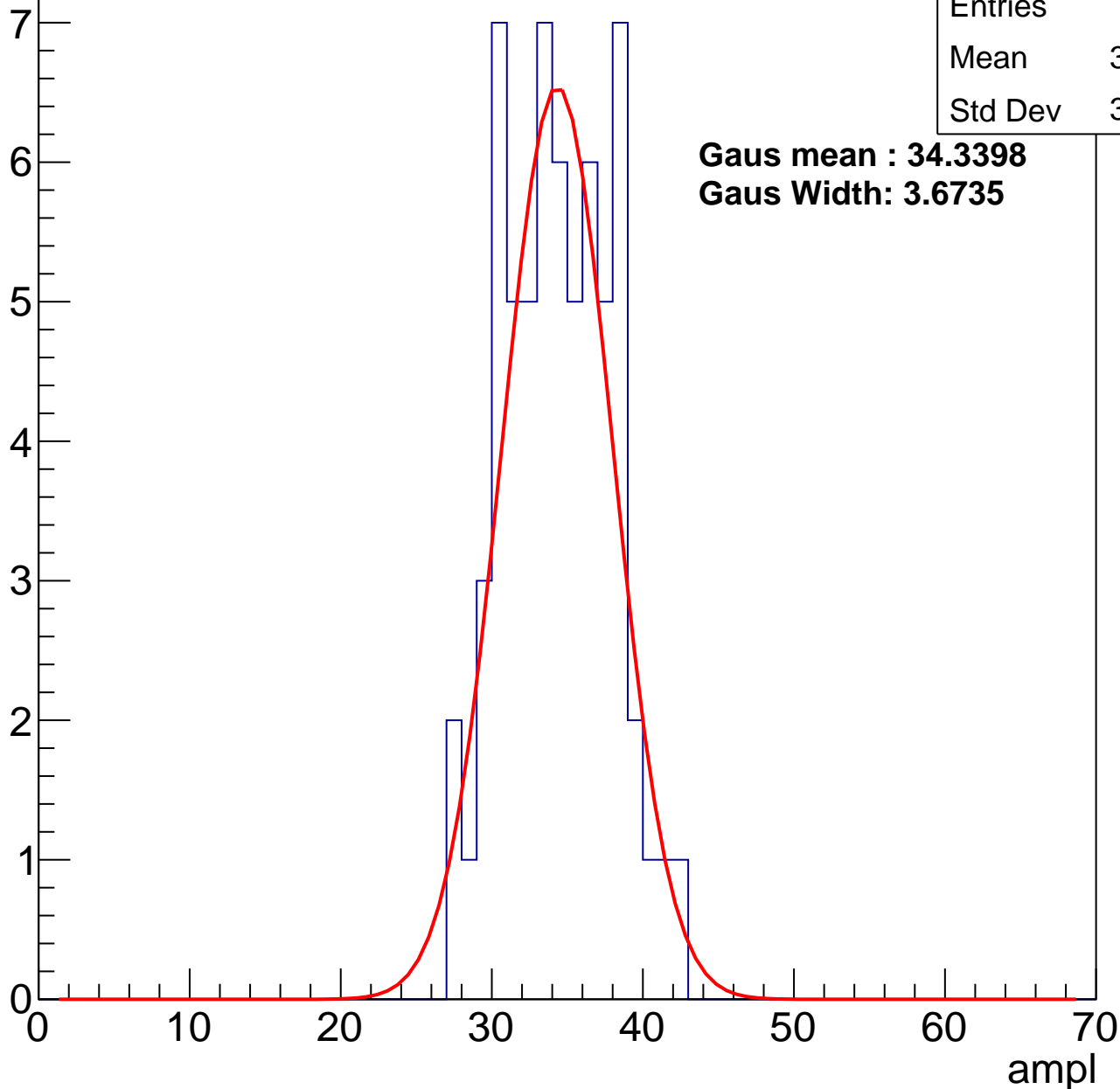
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	33.94
Std Dev	3.486

Gaus mean : 34.3398

Gaus Width: 3.6735



B1L102S, U18-ch62, adc2

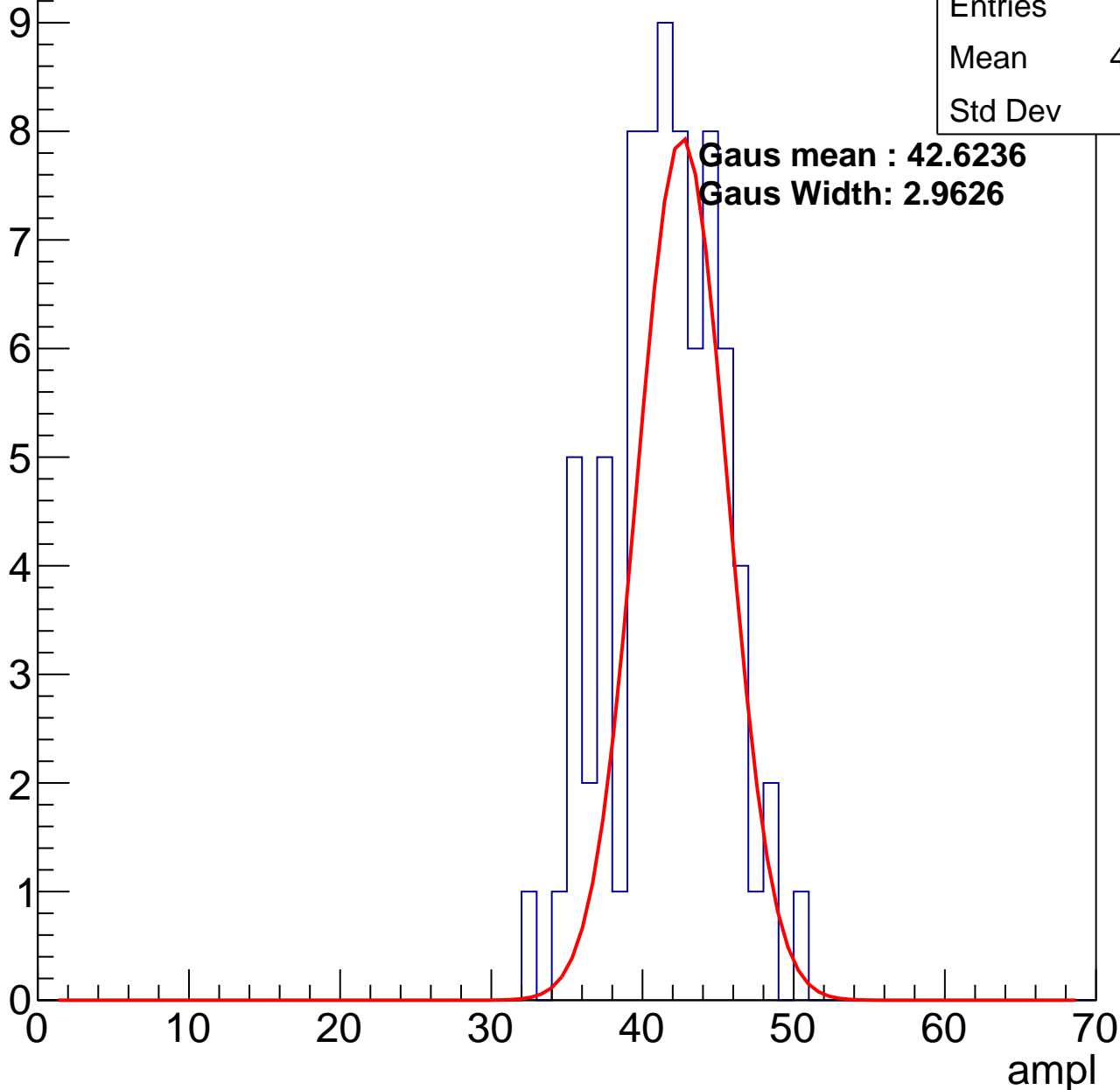
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	41.18
Std Dev	3.63

Gaus mean : 42.6236

Gaus Width: 2.9626

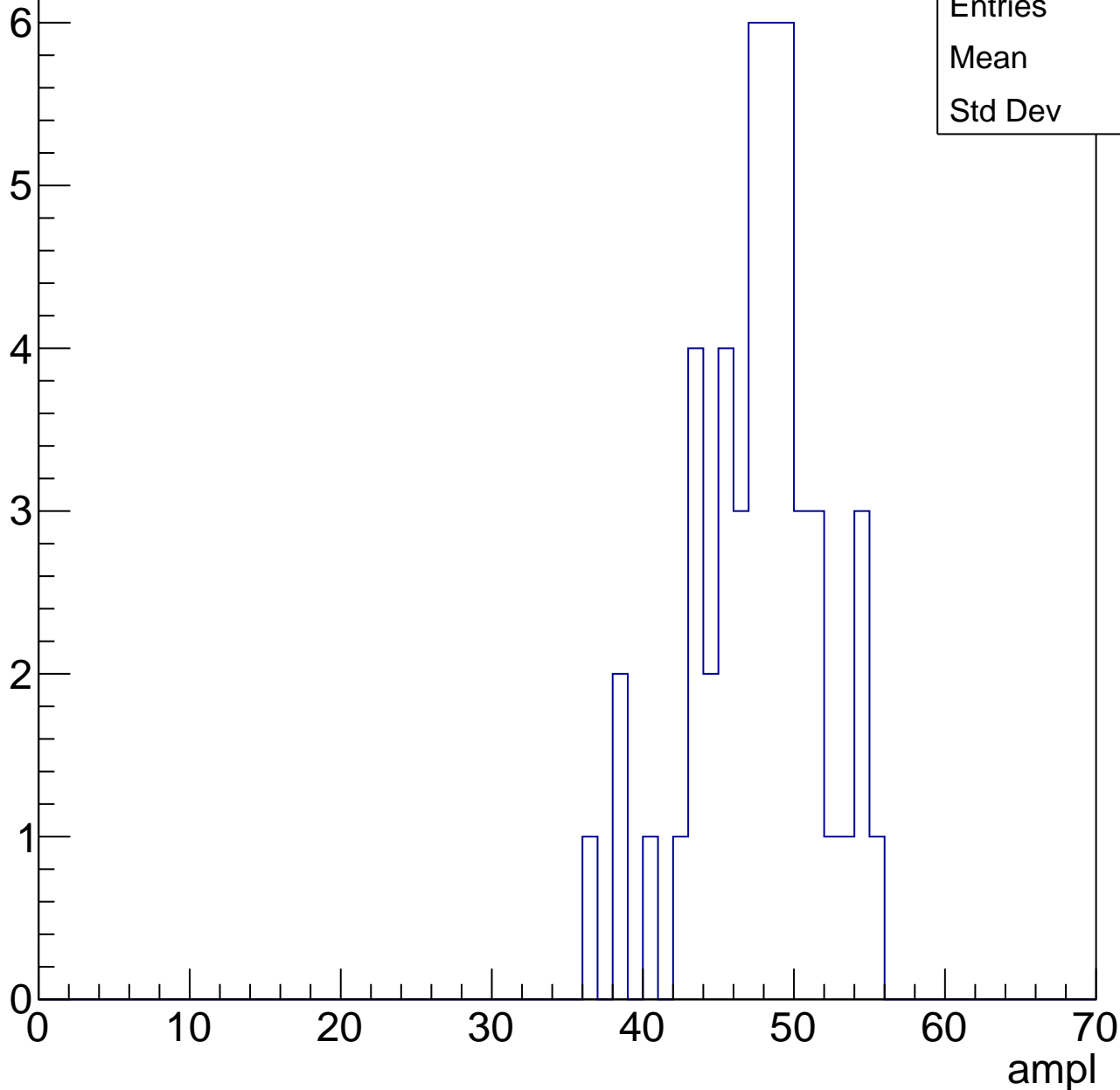


B1L102S, U18-ch62, adc3

calib_packv5_042523_0143.root, FC#11, port A2

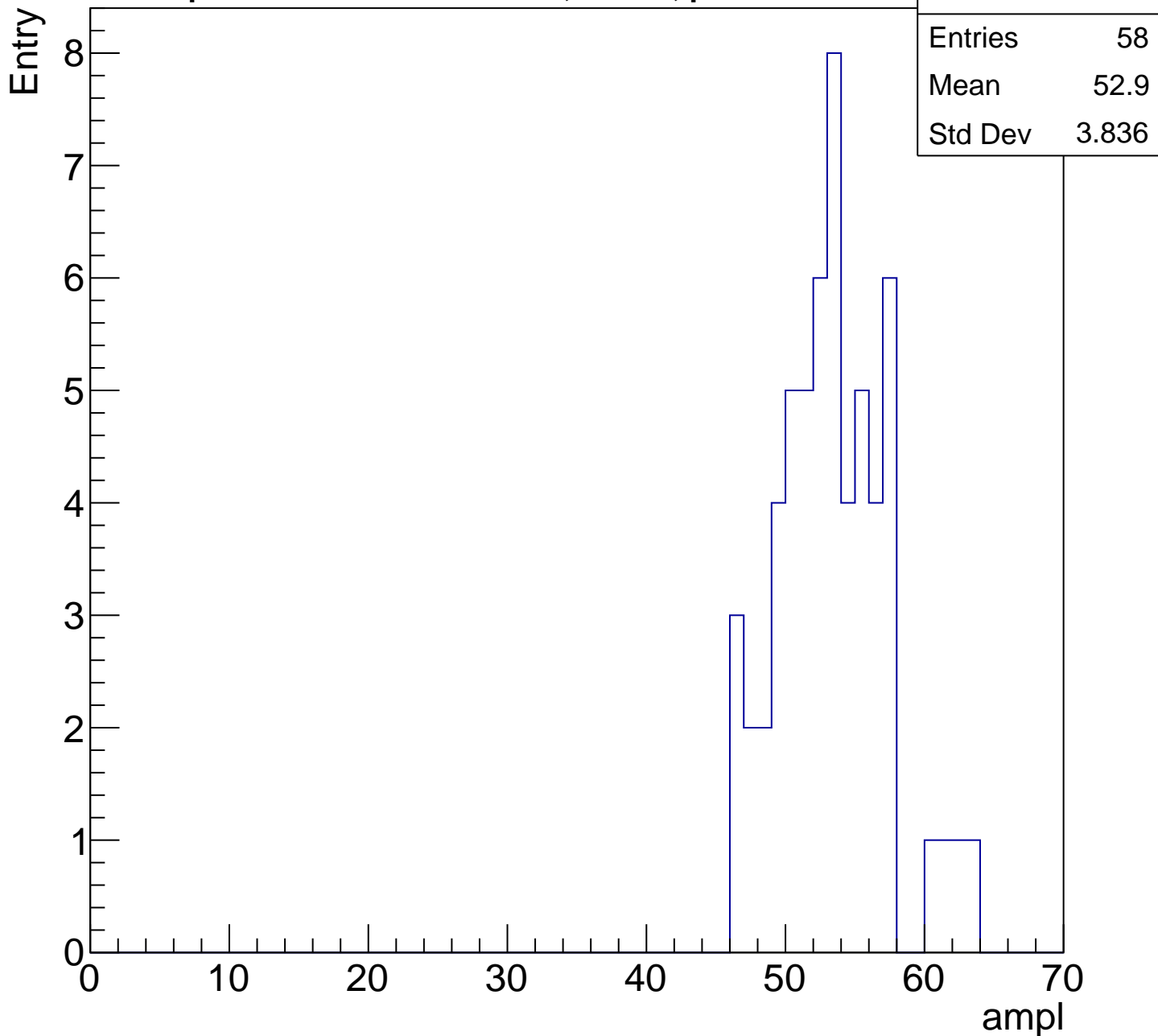
Entry

Entries	48
Mean	47.1
Std Dev	4.17



B1L102S, U18-ch62, adc4

calib_packv5_042523_0143.root, FC#11, port A2

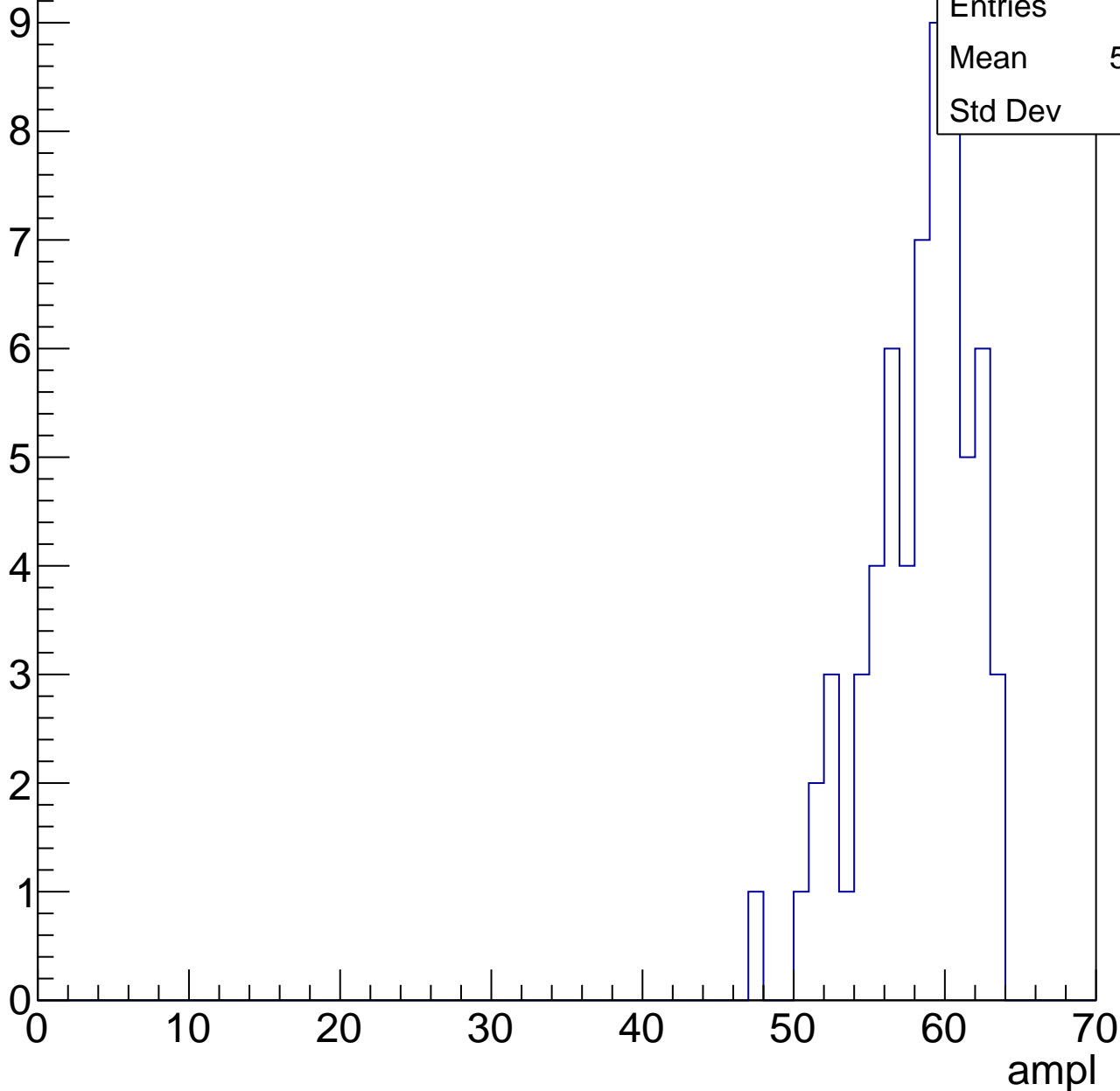


B1L102S, U18-ch62, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	57.73
Std Dev	3.52

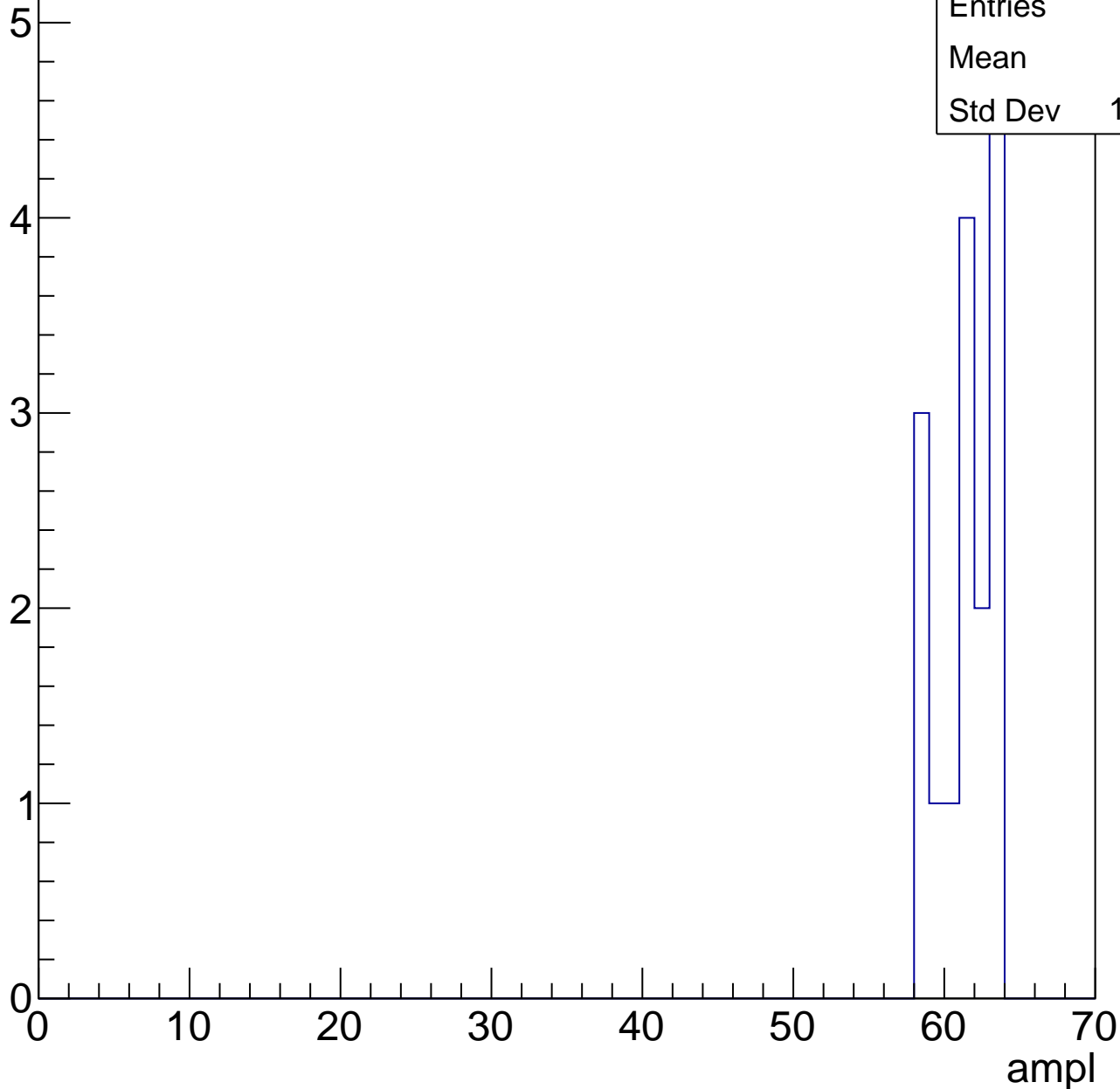


B1L102S, U18-ch62, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

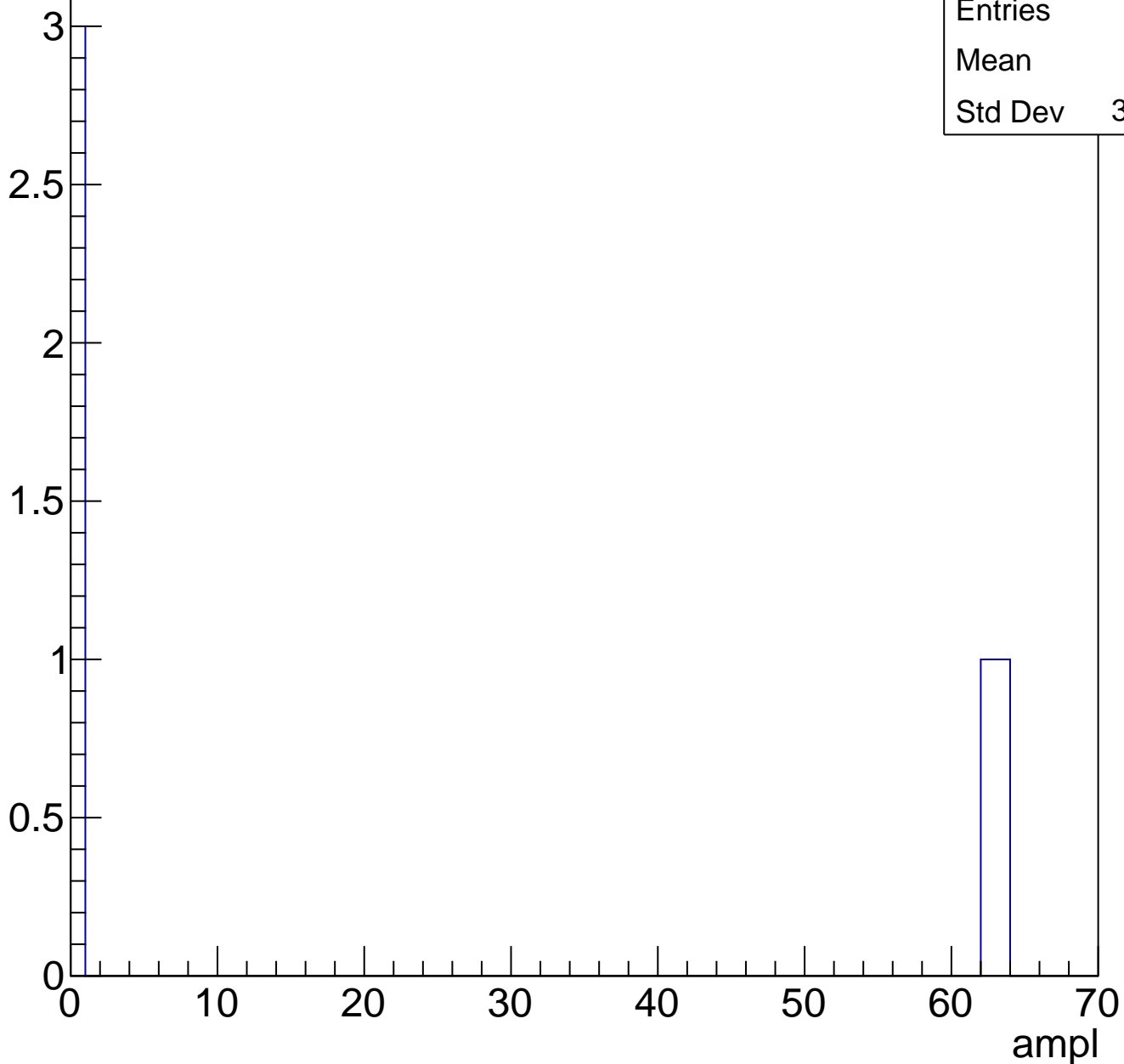
Entries	16
Mean	61
Std Dev	1.837



B1L102S, U18-ch62, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch63, adc0

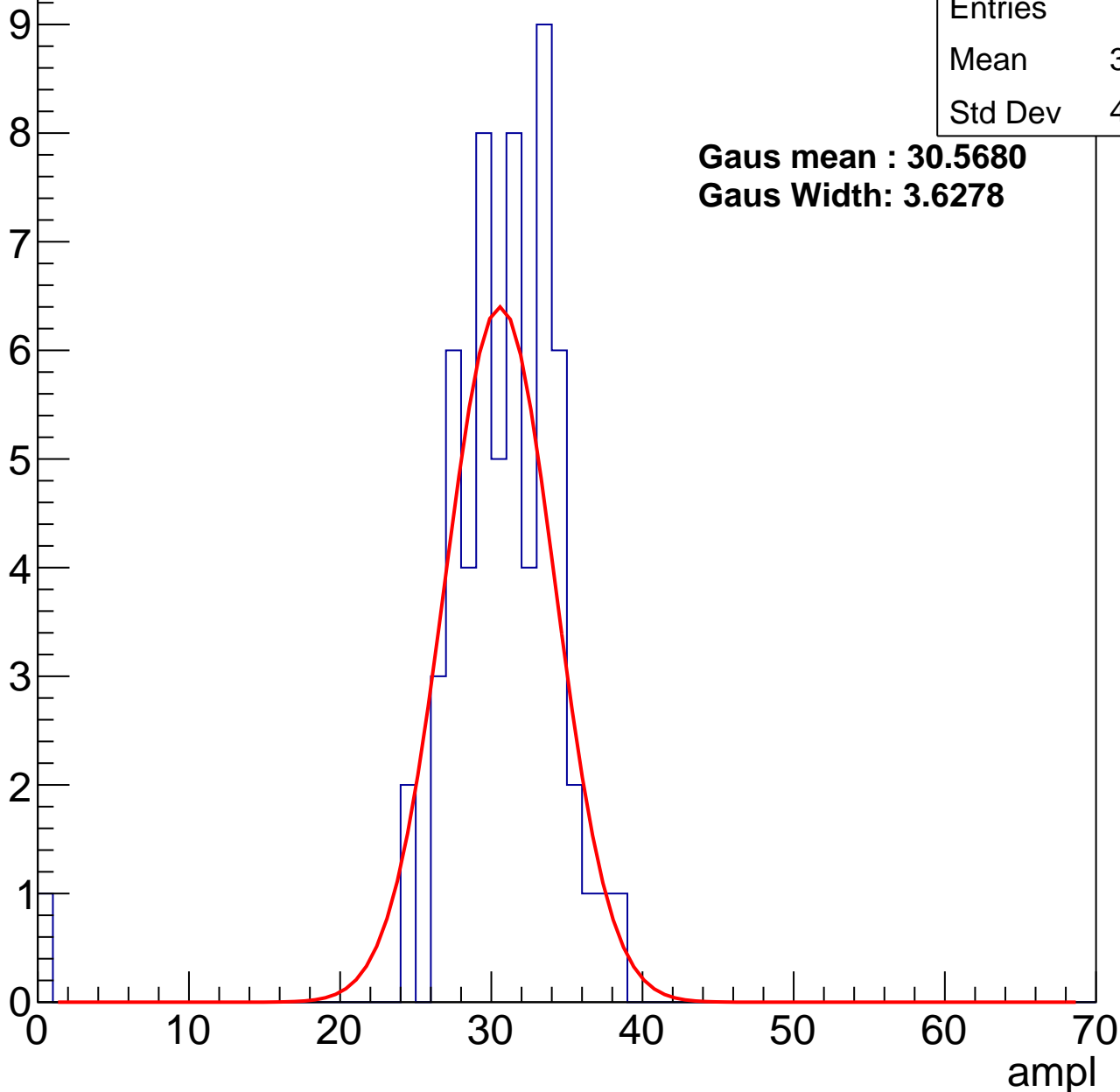
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	30.16
Std Dev	4.953

Gaus mean : 30.5680

Gaus Width: 3.6278



B1L102S, U18-ch63, adc1

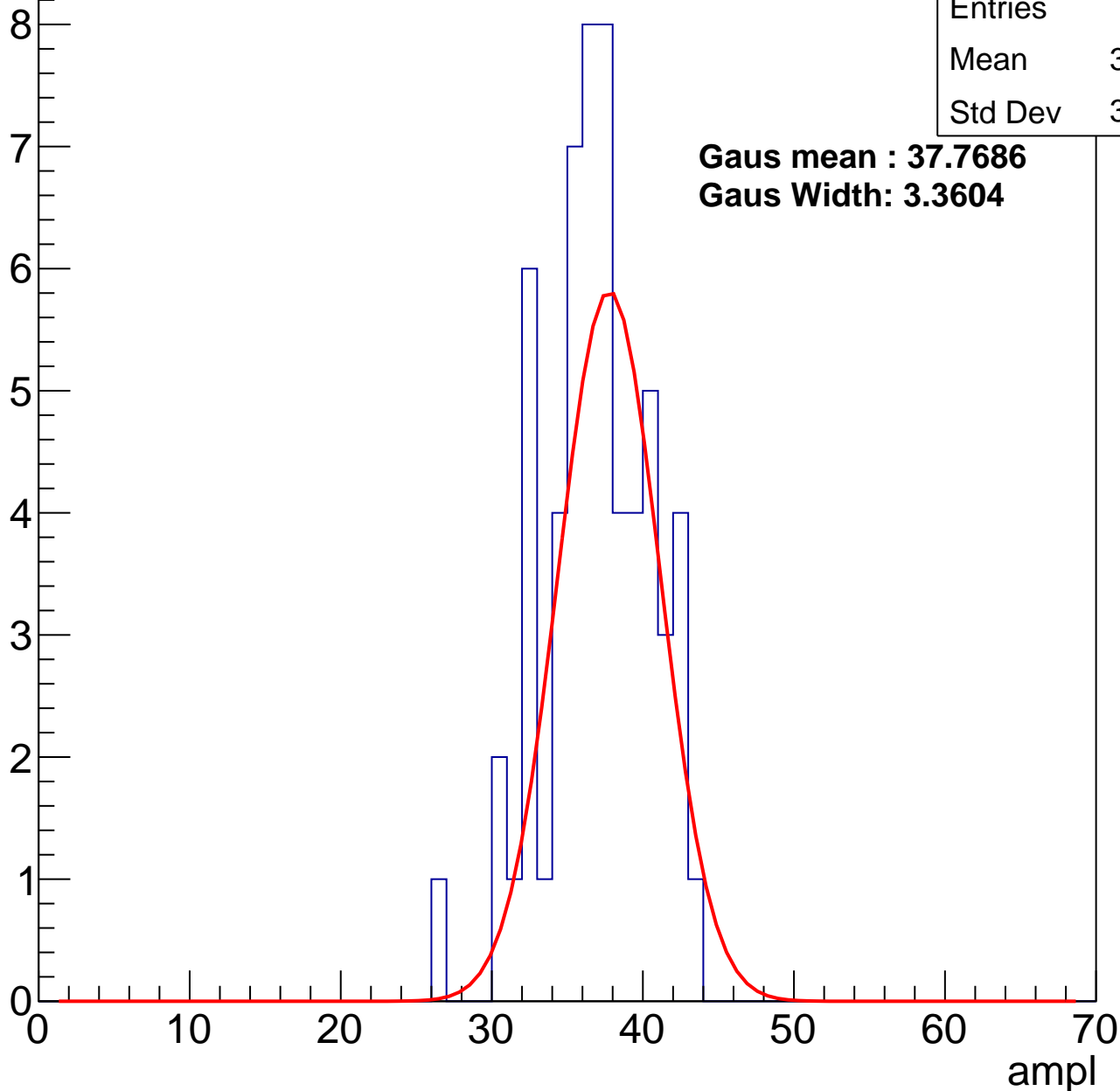
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	36.42
Std Dev	3.499

Gaus mean : 37.7686

Gaus Width: 3.3604



B1L102S, U18-ch63, adc2

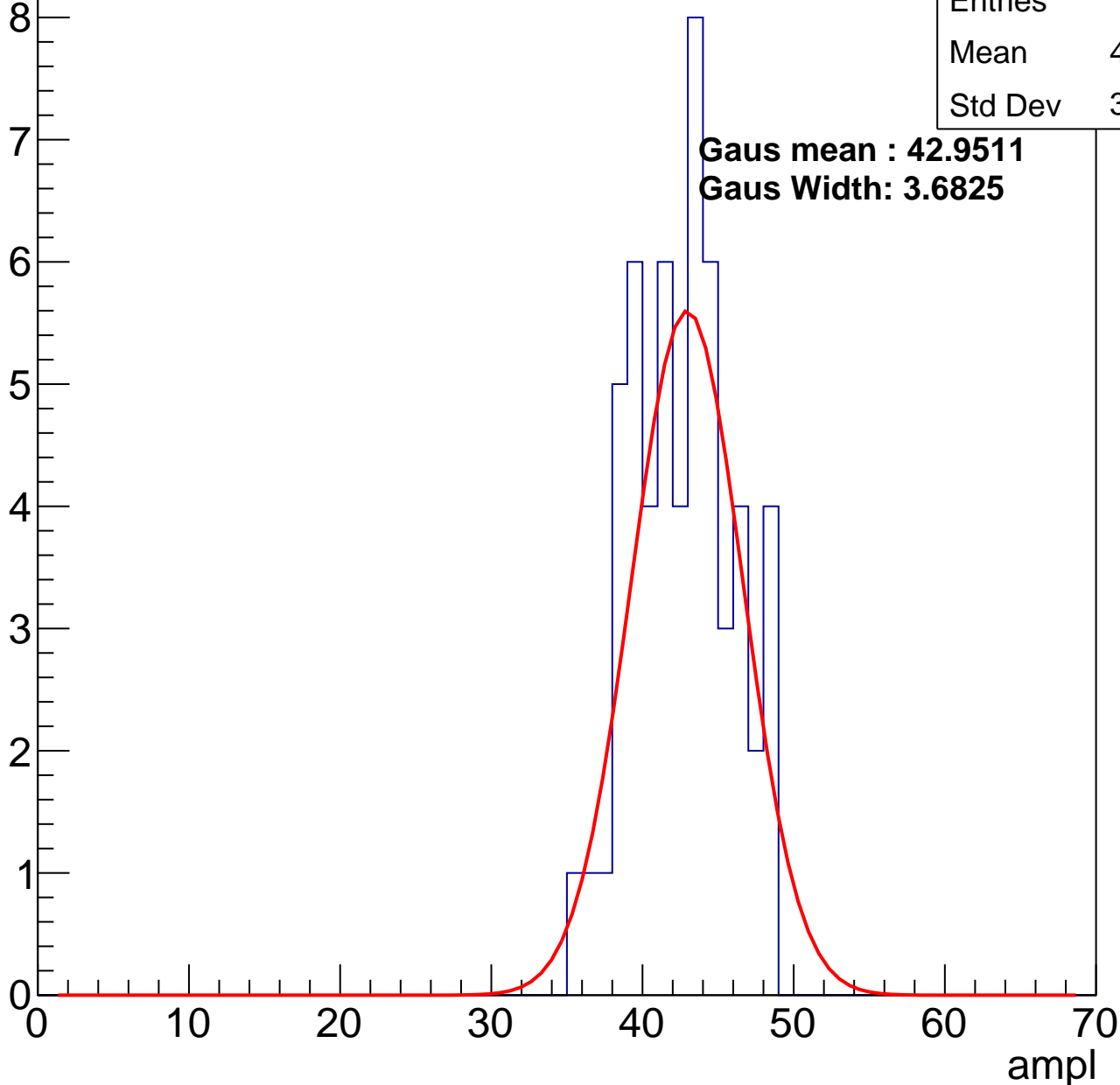
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	42.16
Std Dev	3.252

Gaus mean : 42.9511

Gaus Width: 3.6825

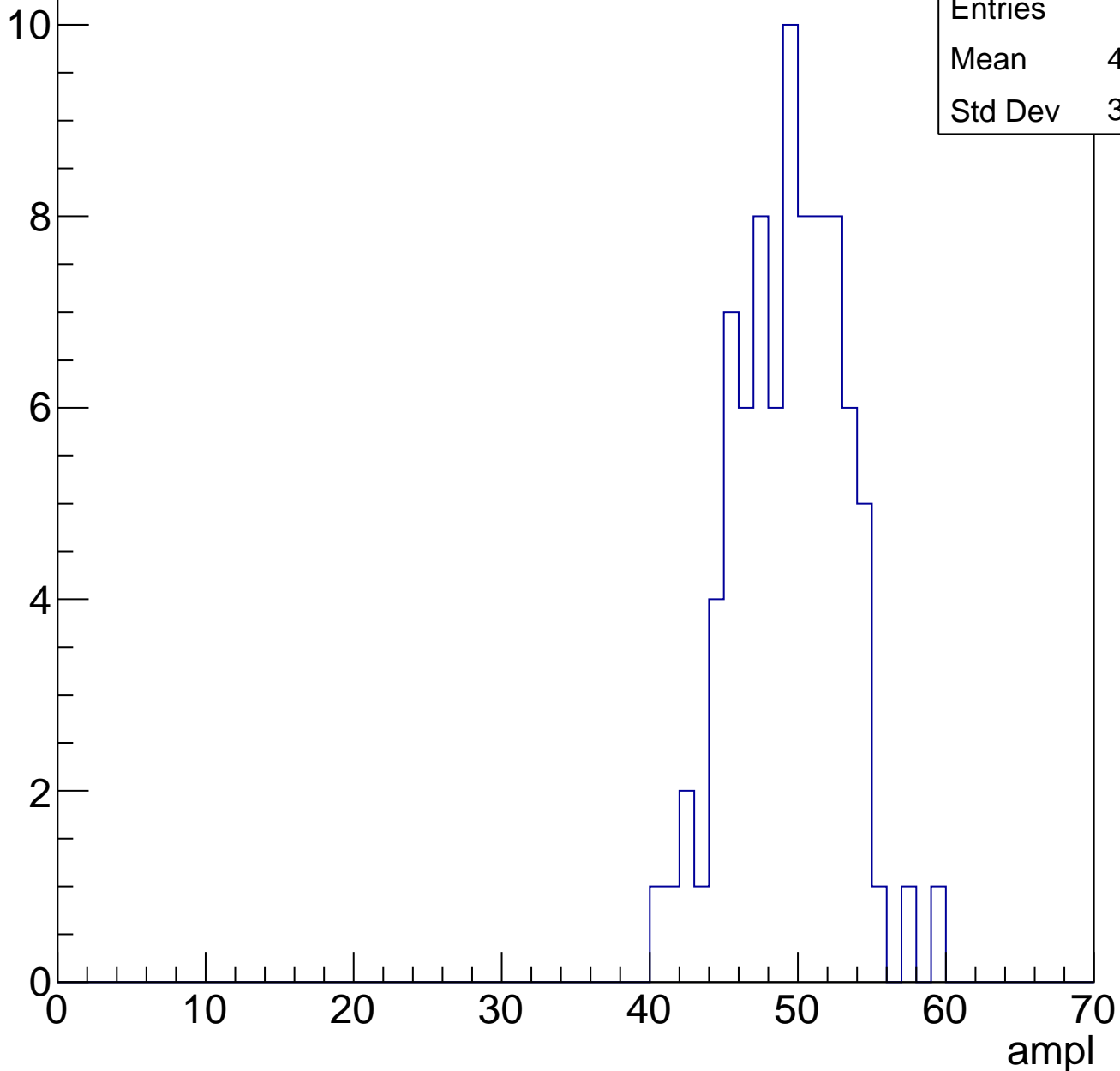


B1L102S, U18-ch63, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	84
Mean	48.95
Std Dev	3.645

Entry

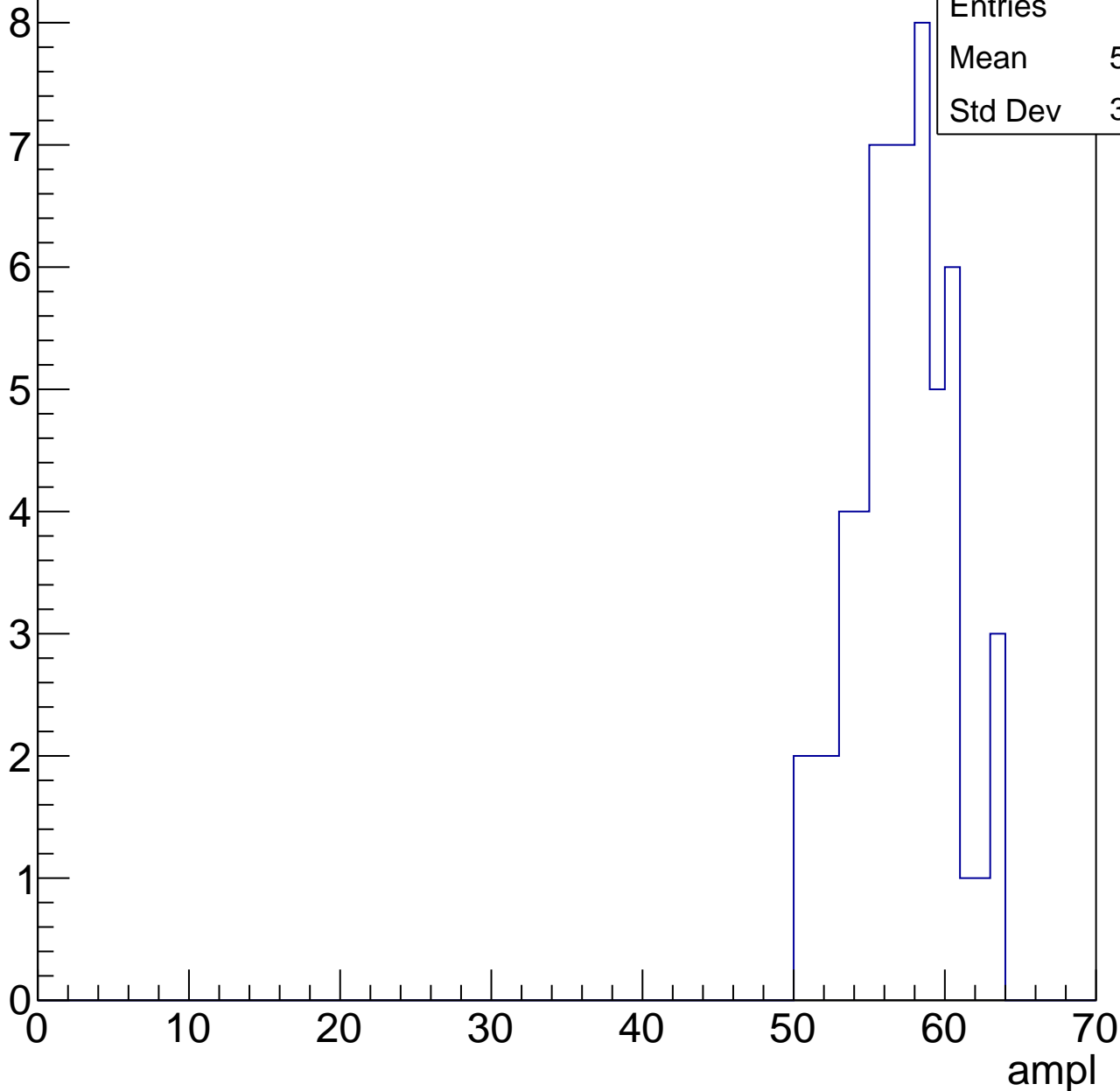


B1L102S, U18-ch63, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	56.63
Std Dev	3.129

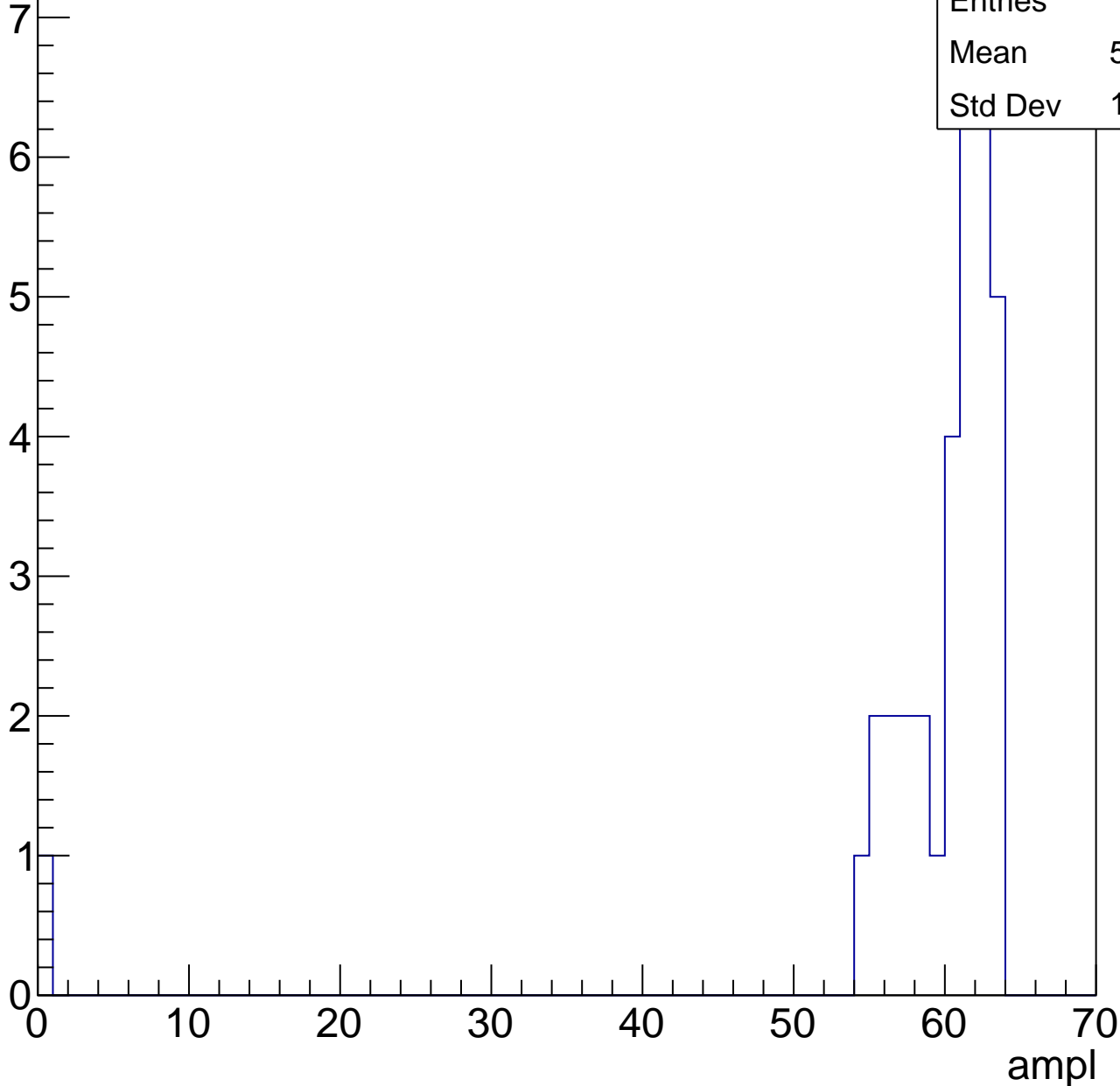


B1L102S, U18-ch63, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	34
Mean	58.26
Std Dev	10.46



B1L102S, U18-ch63, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

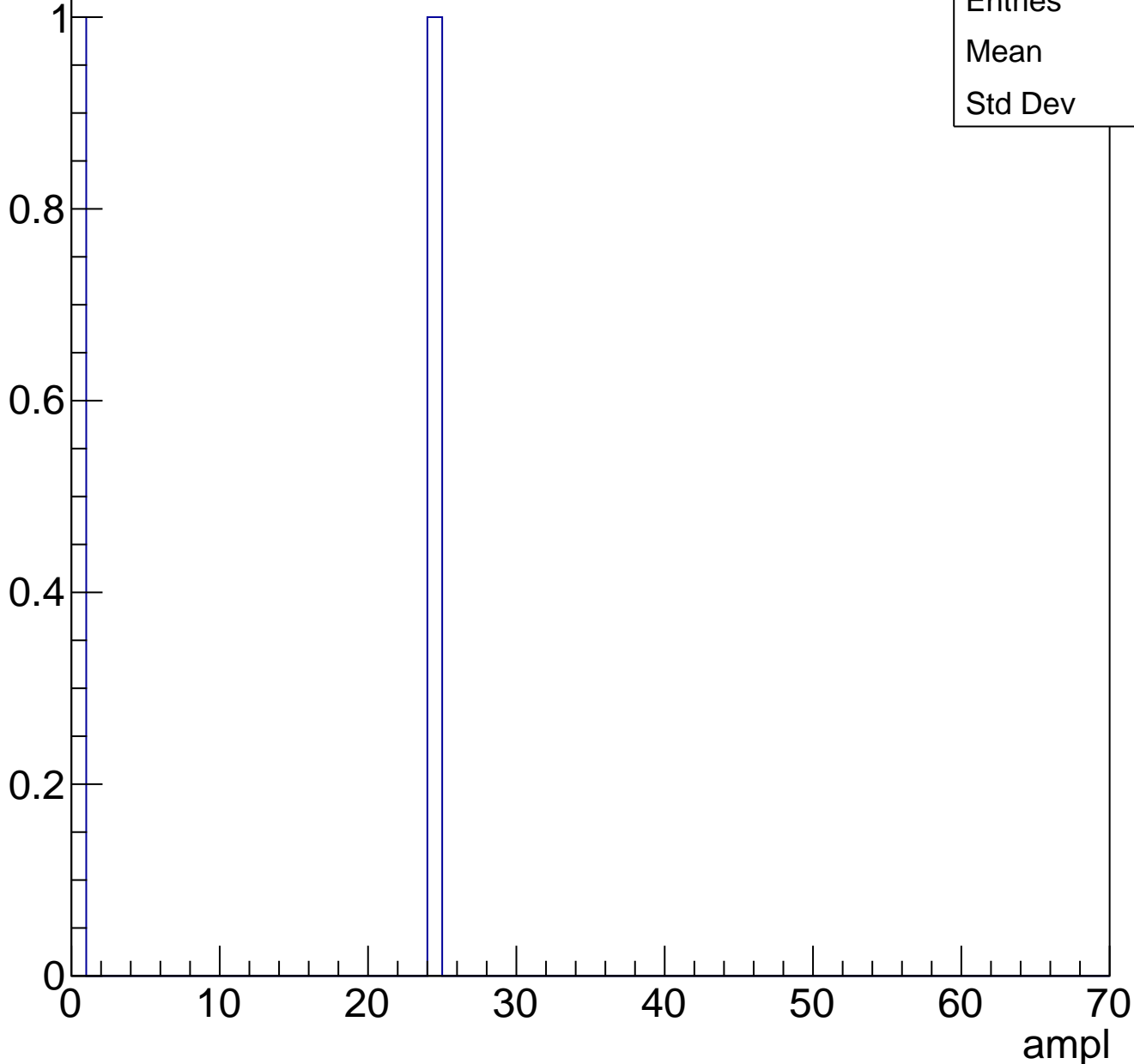
ampl

Entries	10
Mean	60.5
Std Dev	2.11

B1L102S, U18-ch63, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch64, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	27.19
Std Dev	4.663

Gaus mean : 27.8457

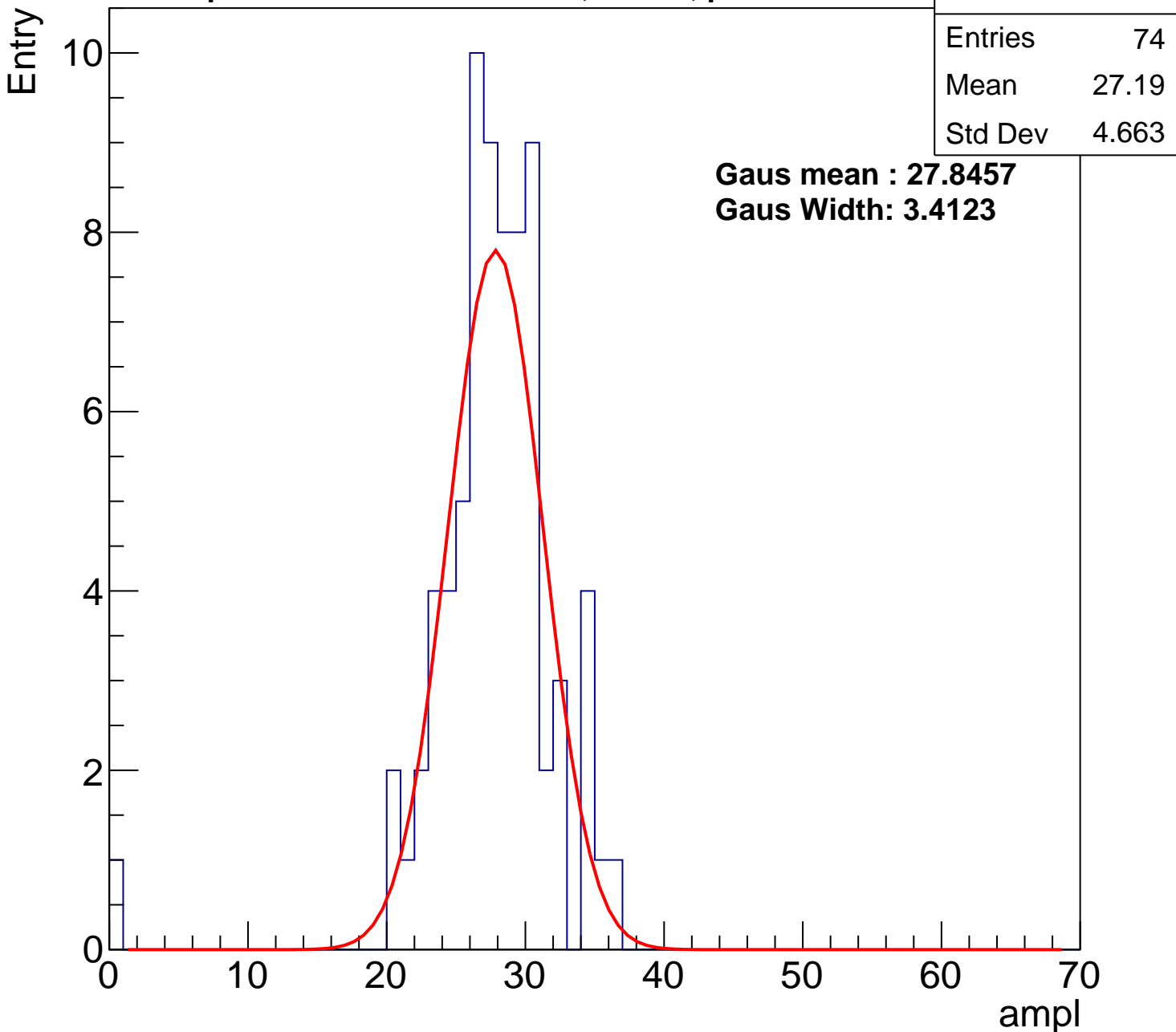
Gaus Width: 3.4123

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch64, adc1

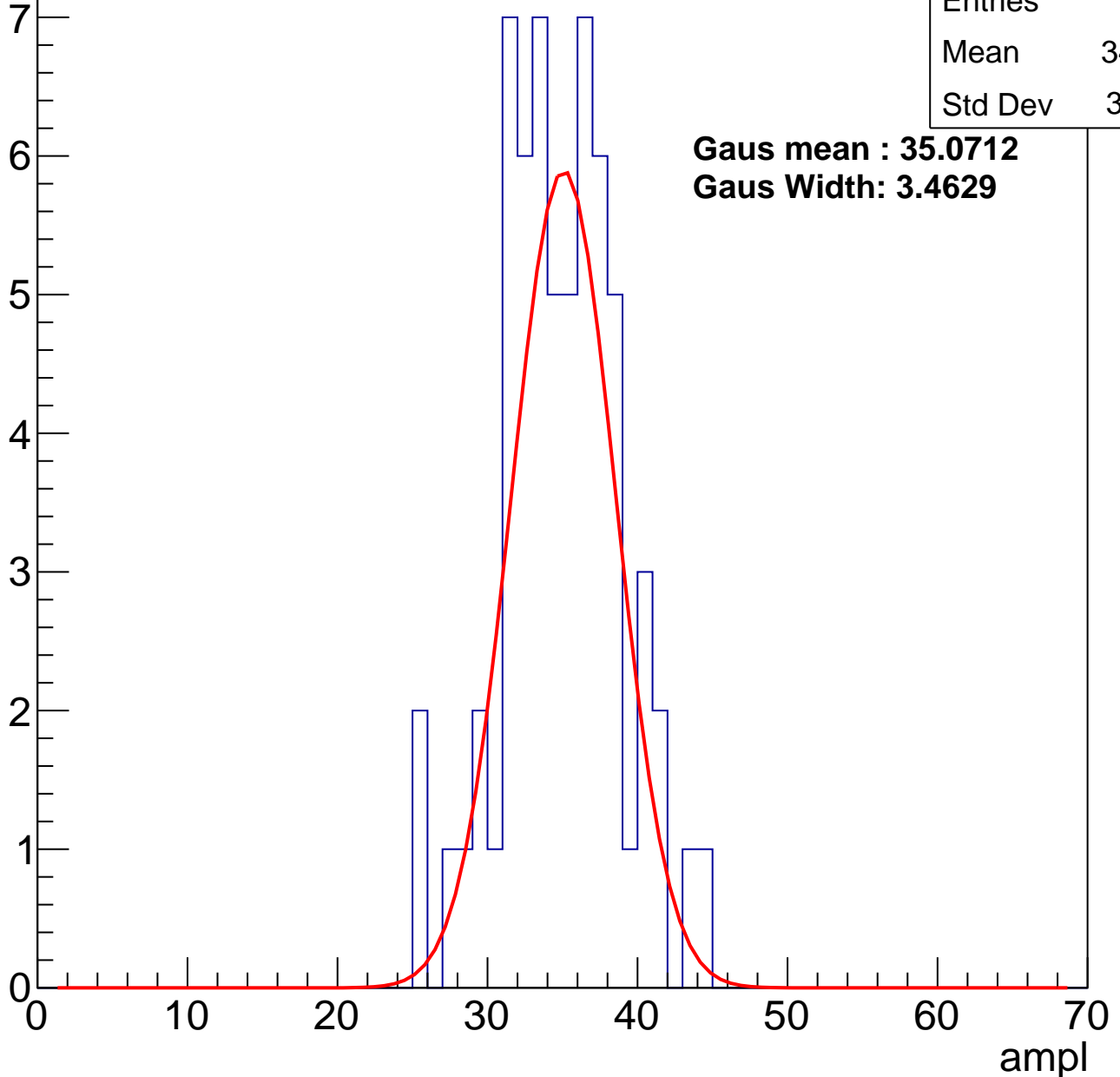
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	34.44
Std Dev	3.931

Gaus mean : 35.0712

Gaus Width: 3.4629



B1L102S, U18-ch64, adc2

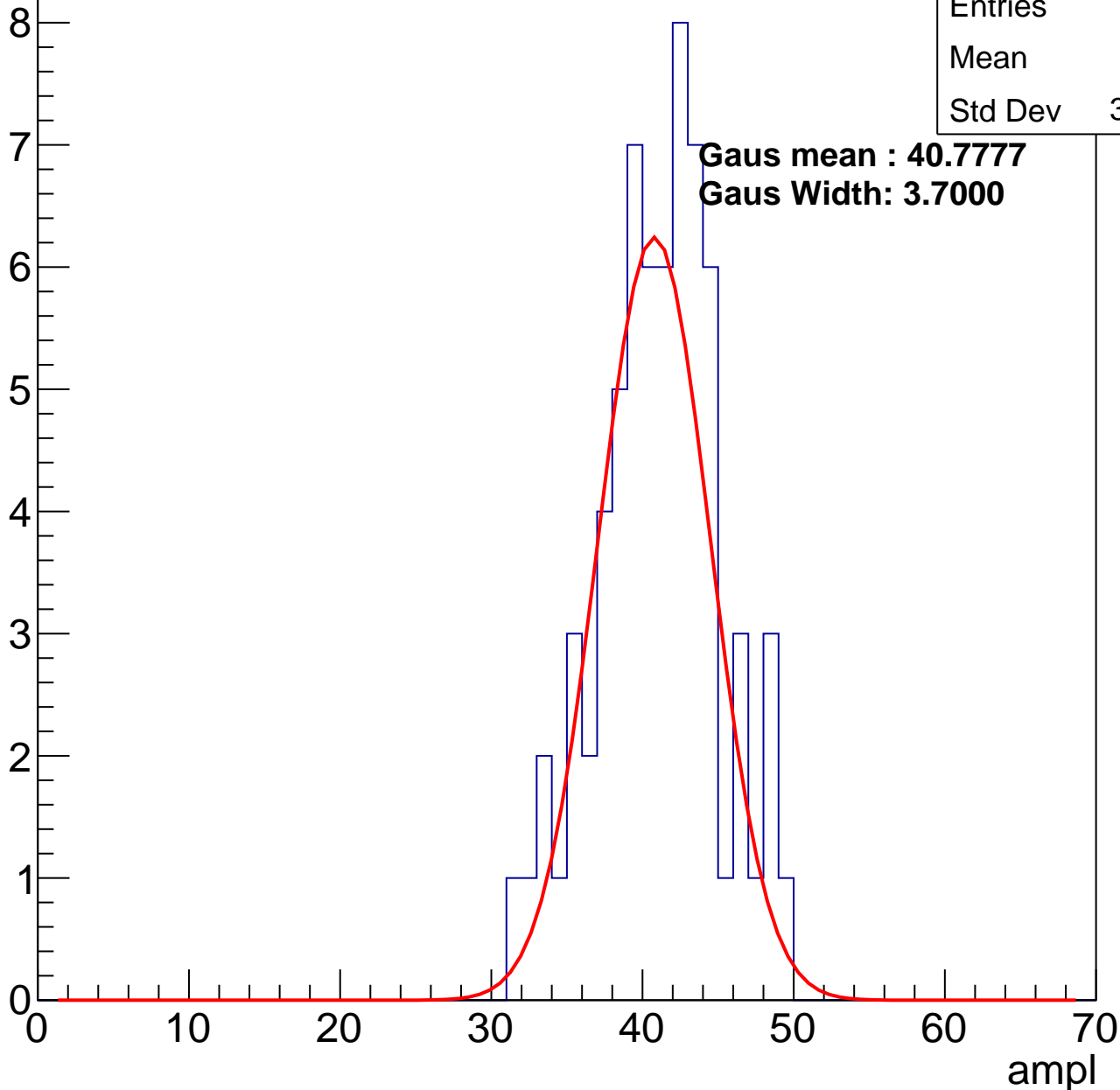
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	40.6
Std Dev	3.978

Gaus mean : 40.7777

Gaus Width: 3.7000

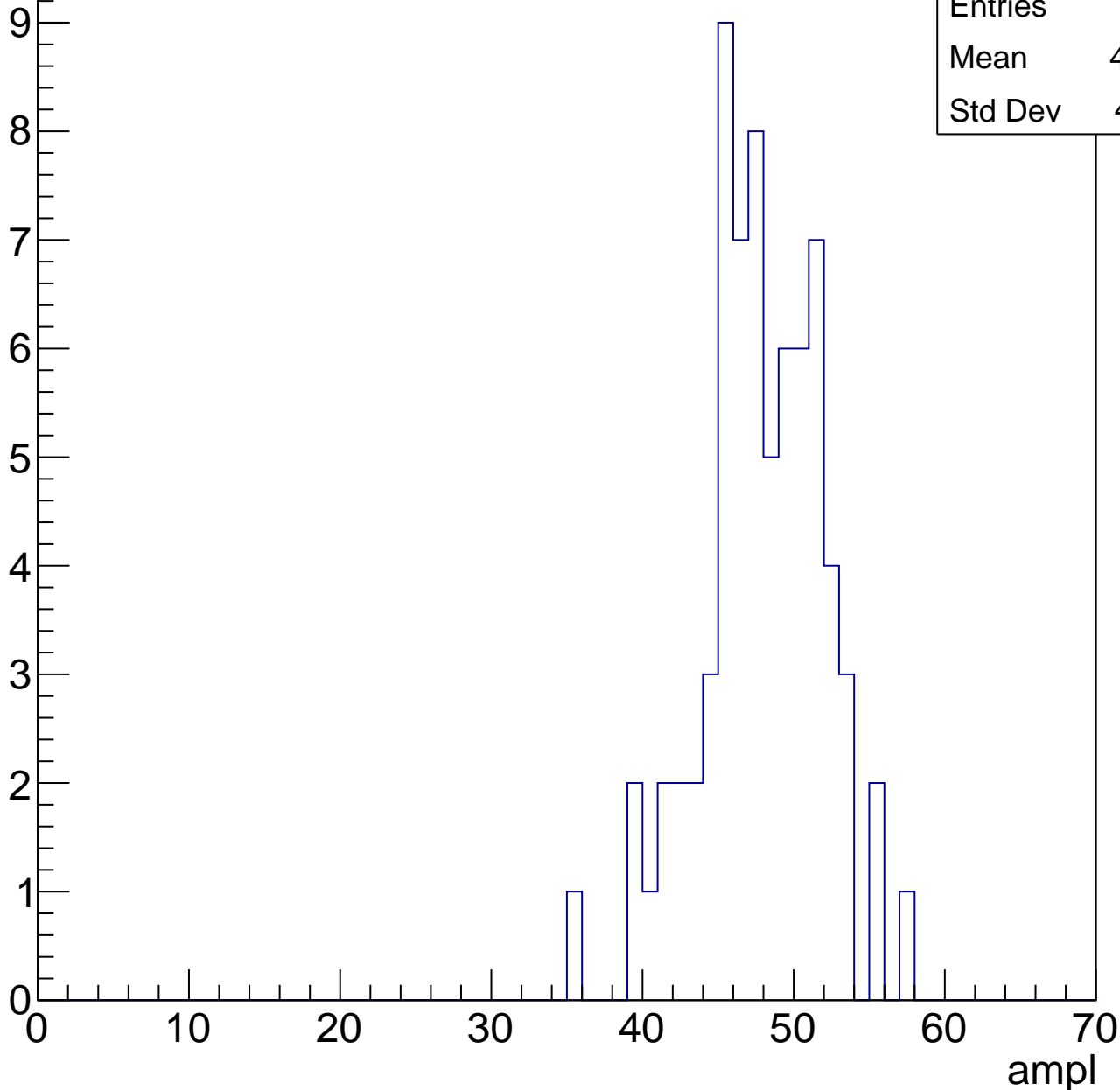


B1L102S, U18-ch64, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	47.39
Std Dev	4.051

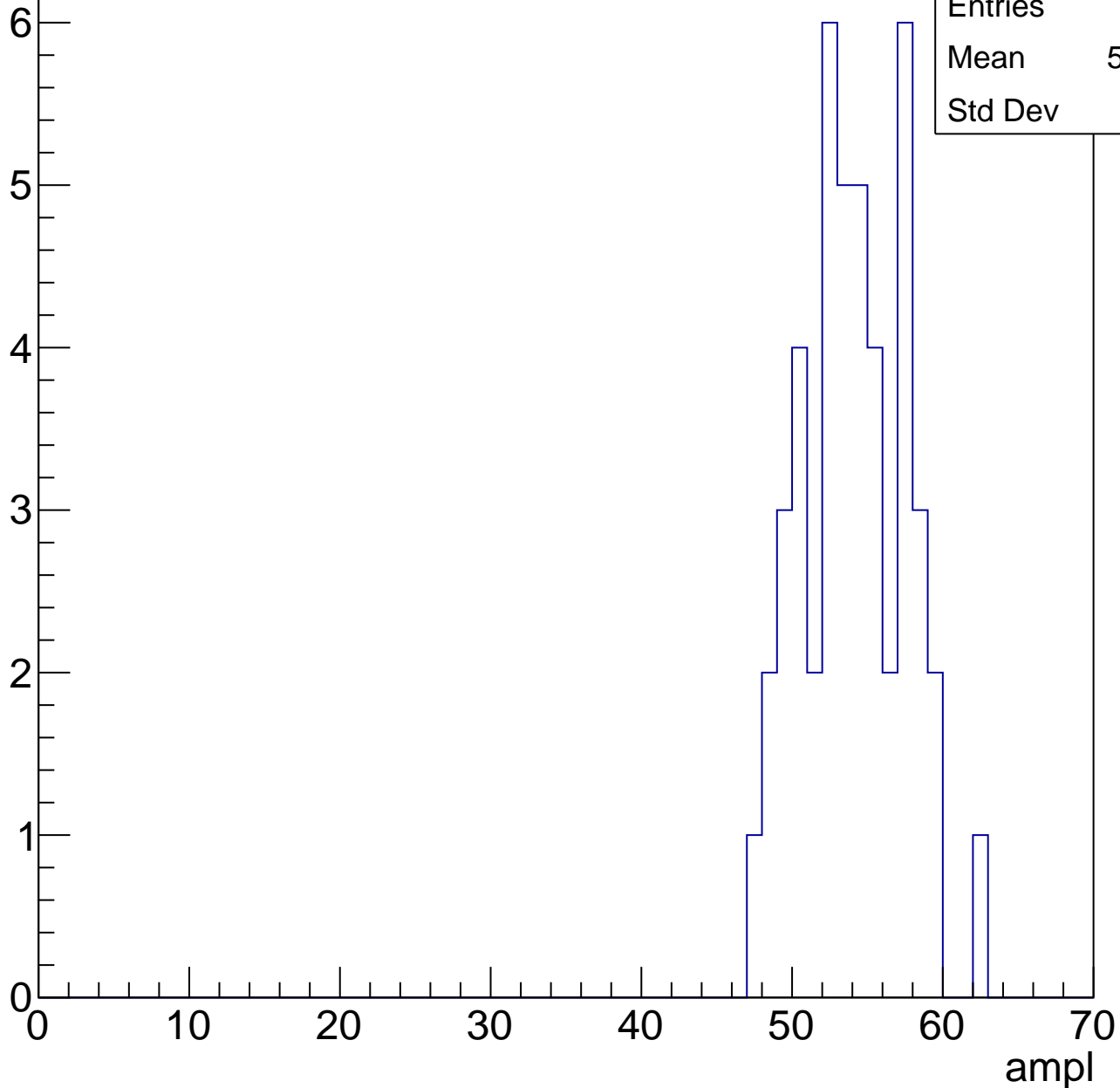


B1L102S, U18-ch64, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	53.63
Std Dev	3.39

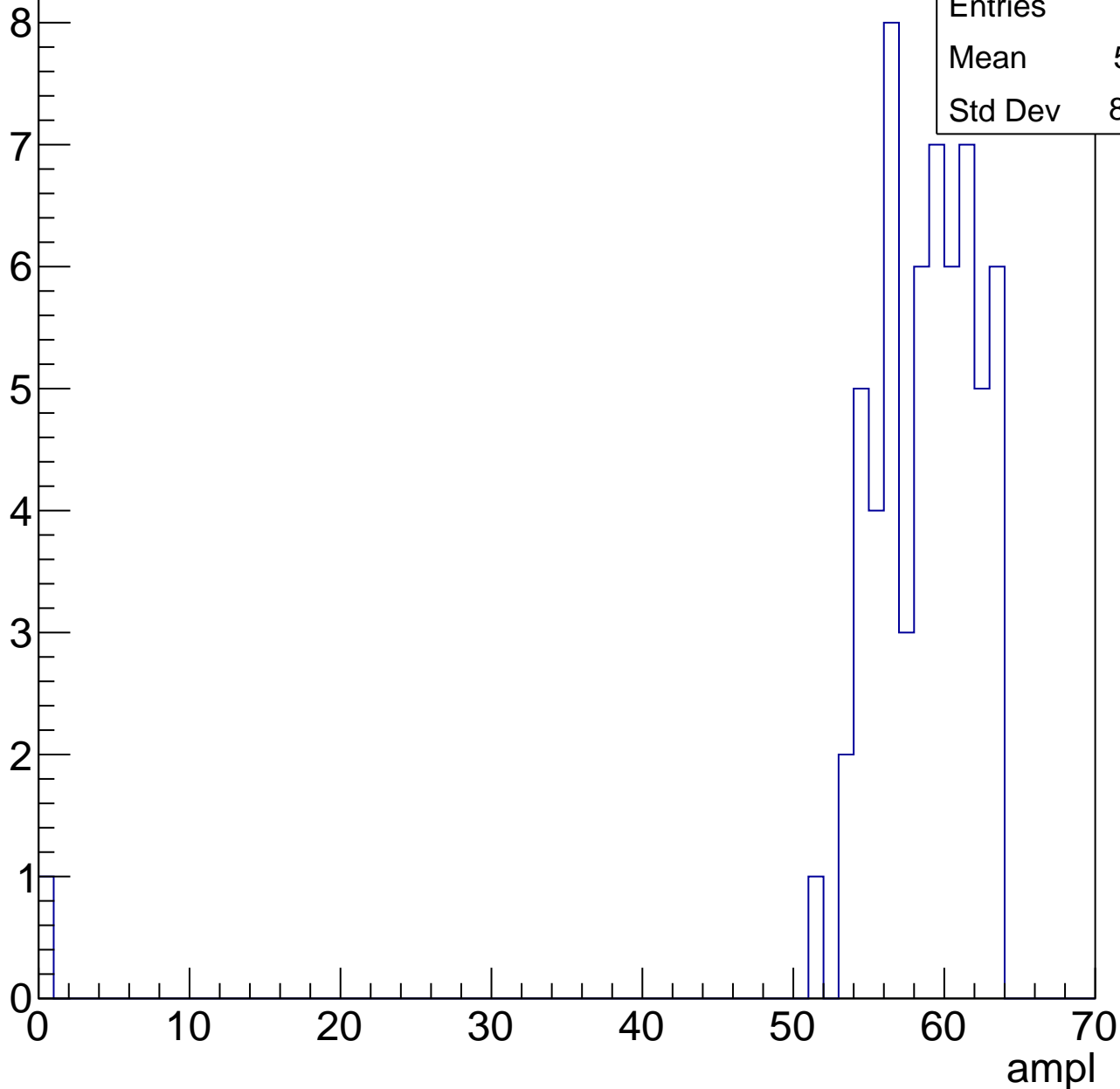


B1L102S, U18-ch64, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

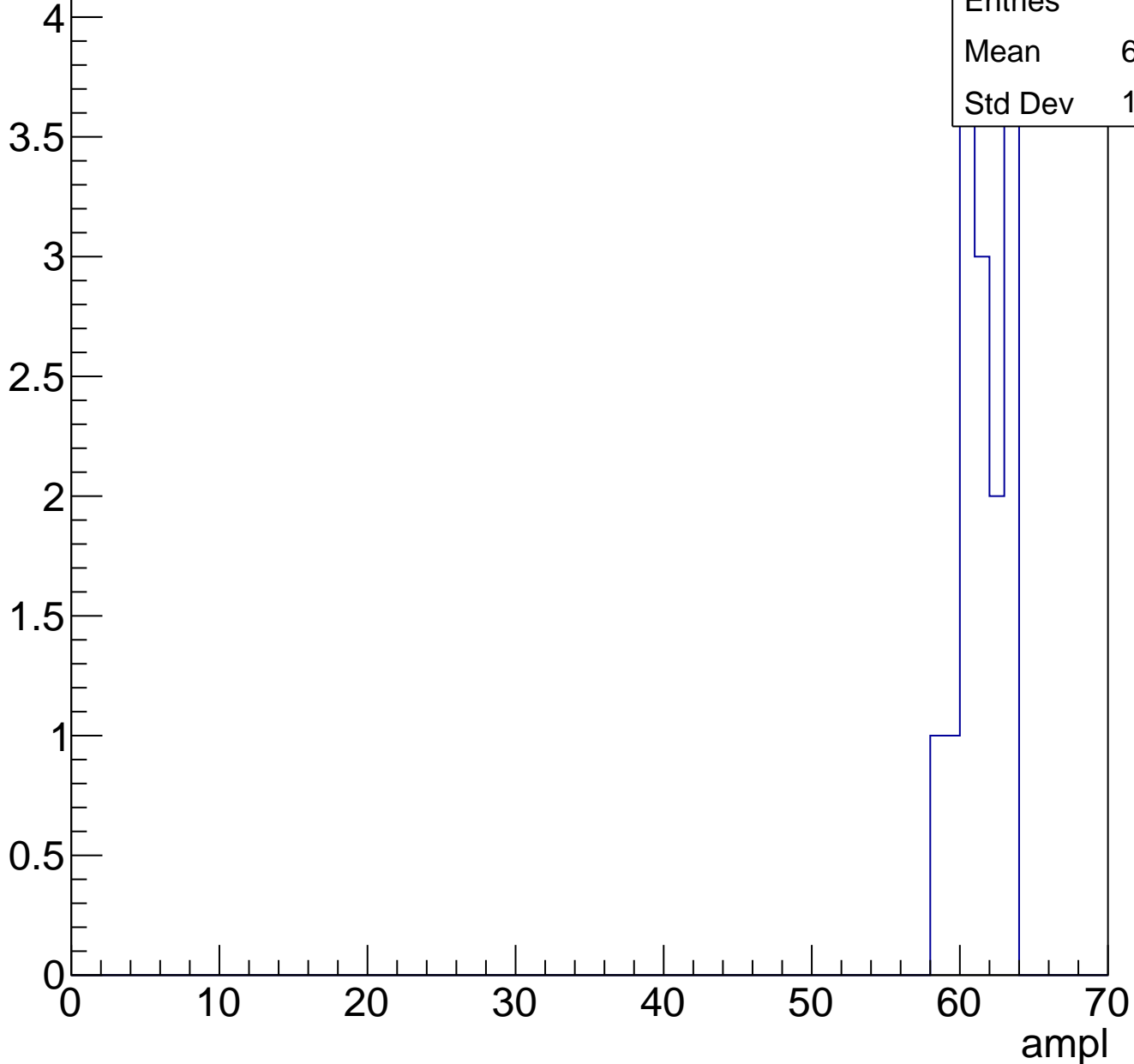
Entries	61
Mean	57.41
Std Dev	8.013



B1L102S, U18-ch64, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch64, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch65, adc0

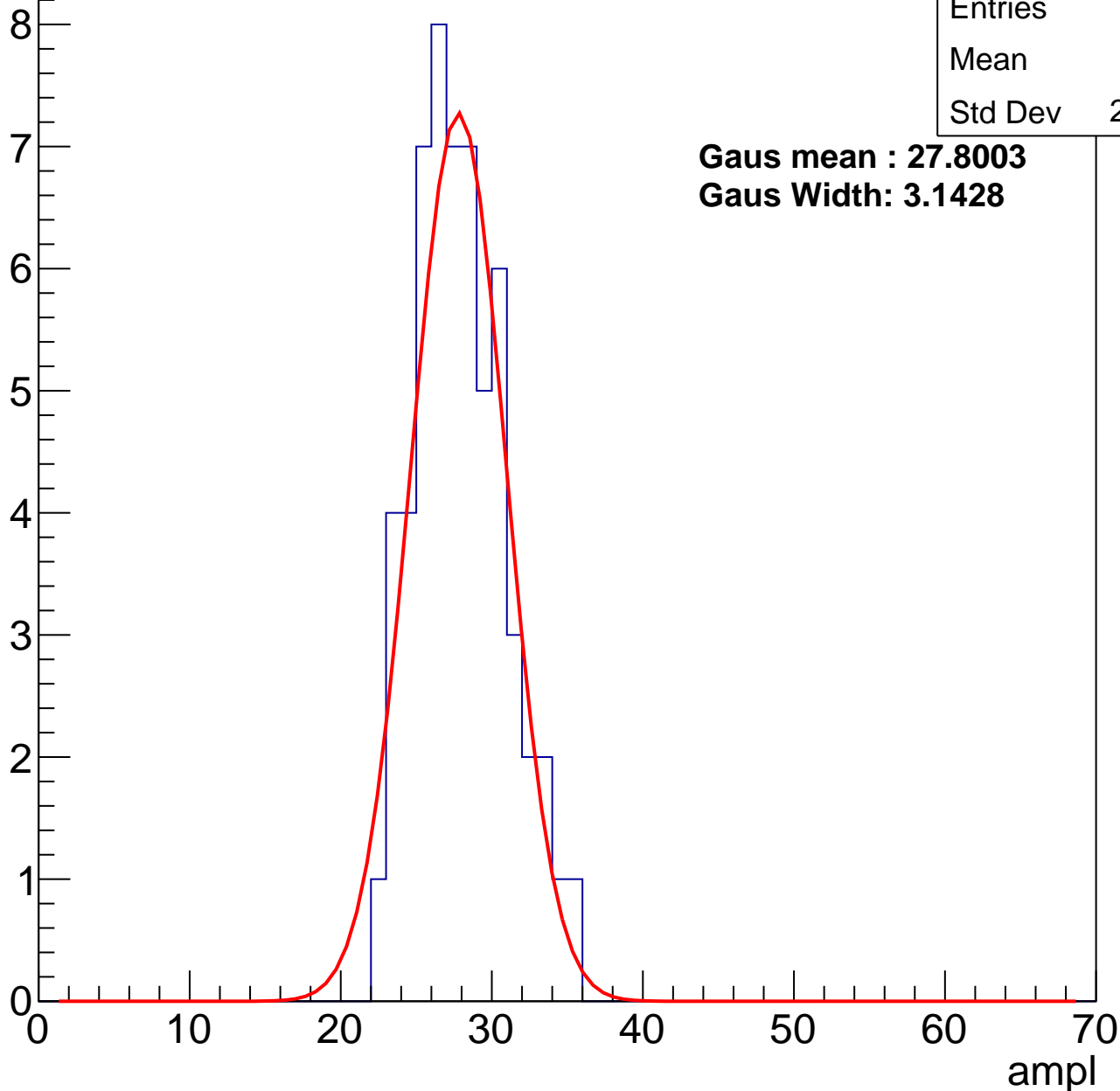
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.5
Std Dev	2.967

Gaus mean : 27.8003

Gaus Width: 3.1428



B1L102S, U18-ch65, adc1

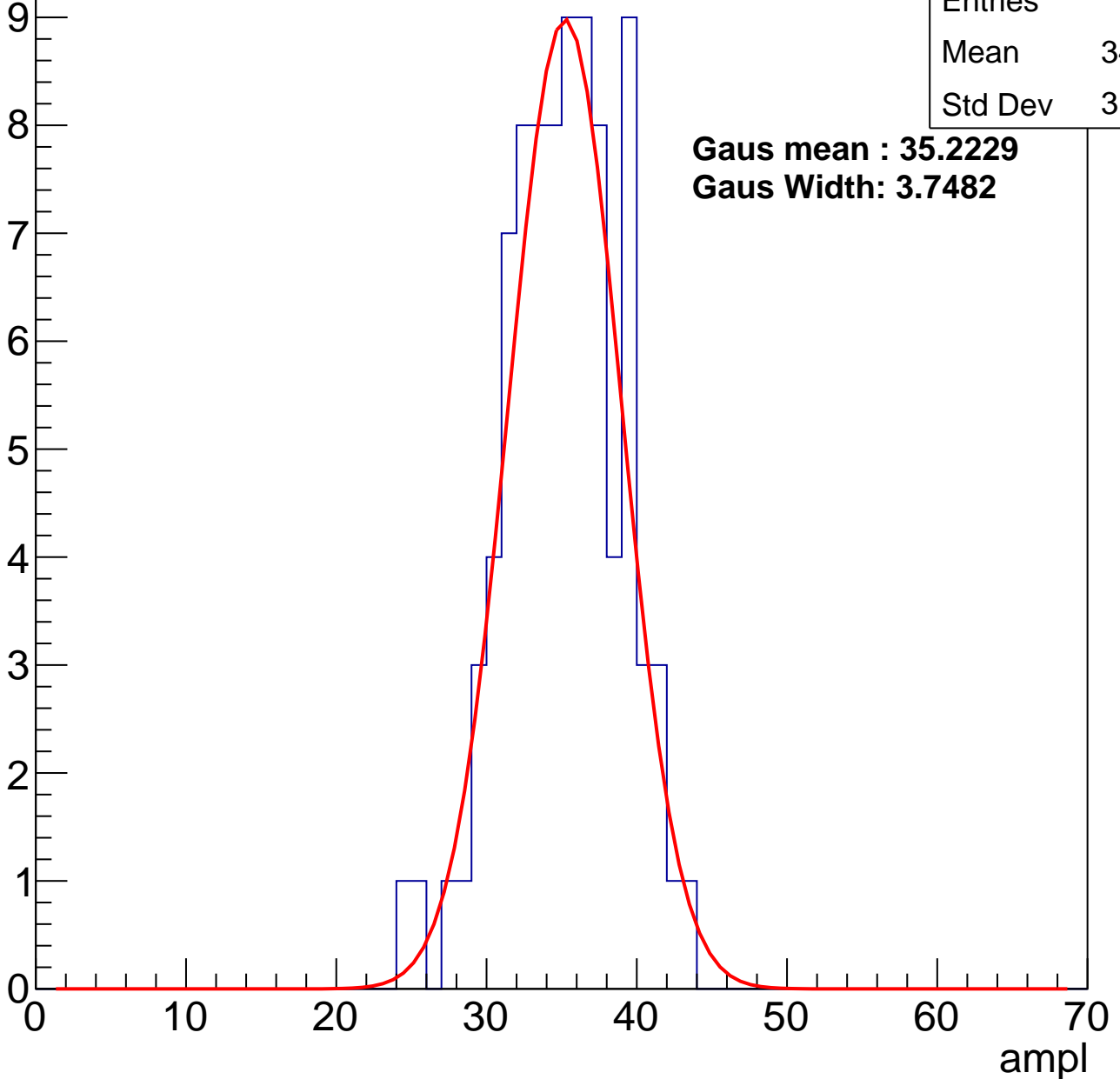
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	89
Mean	34.67
Std Dev	3.783

Gaus mean : 35.2229

Gaus Width: 3.7482



B1L102S, U18-ch65, adc2

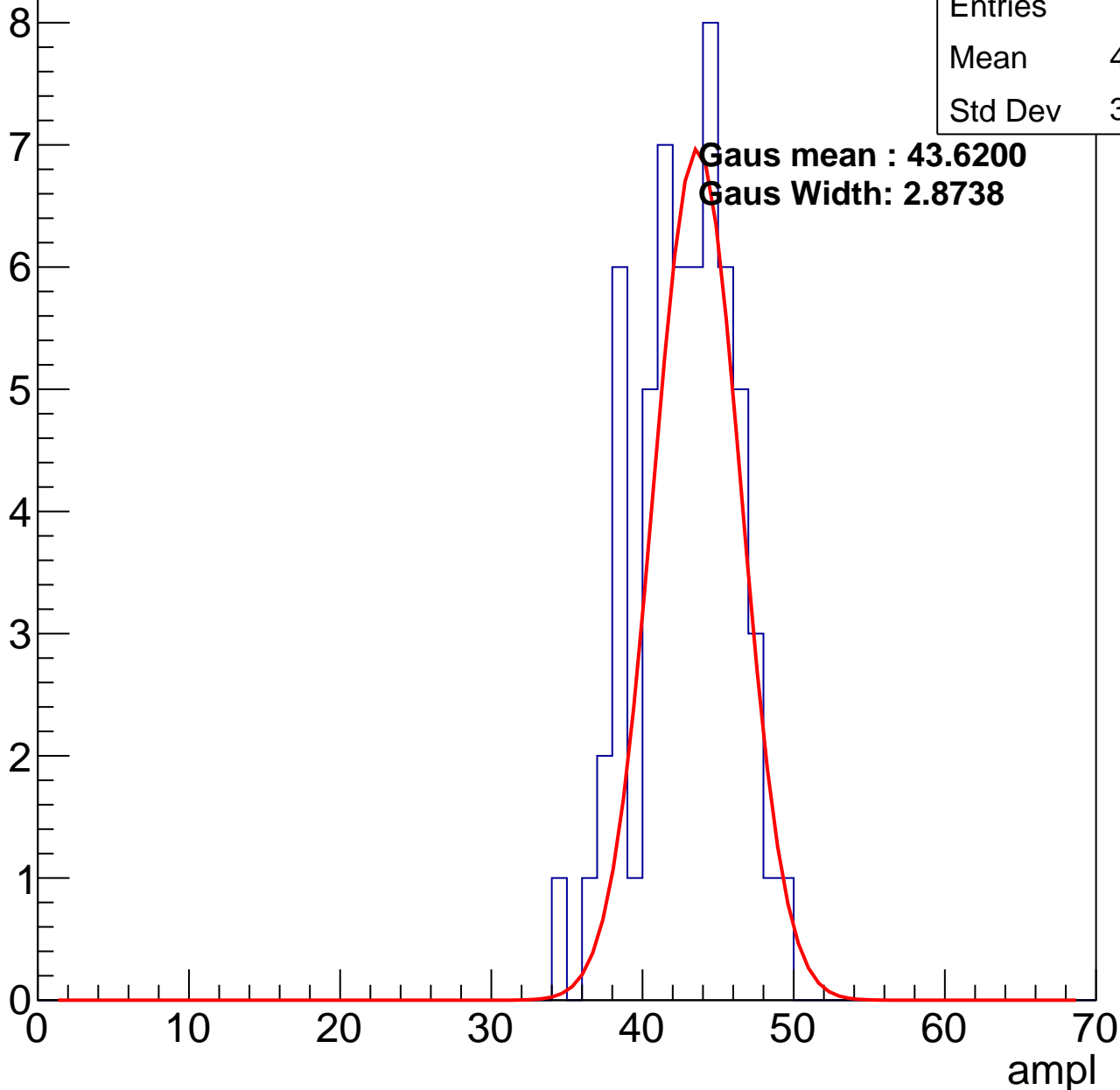
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.34
Std Dev	3.208

Gaus mean : 43.6200

Gaus Width: 2.8738

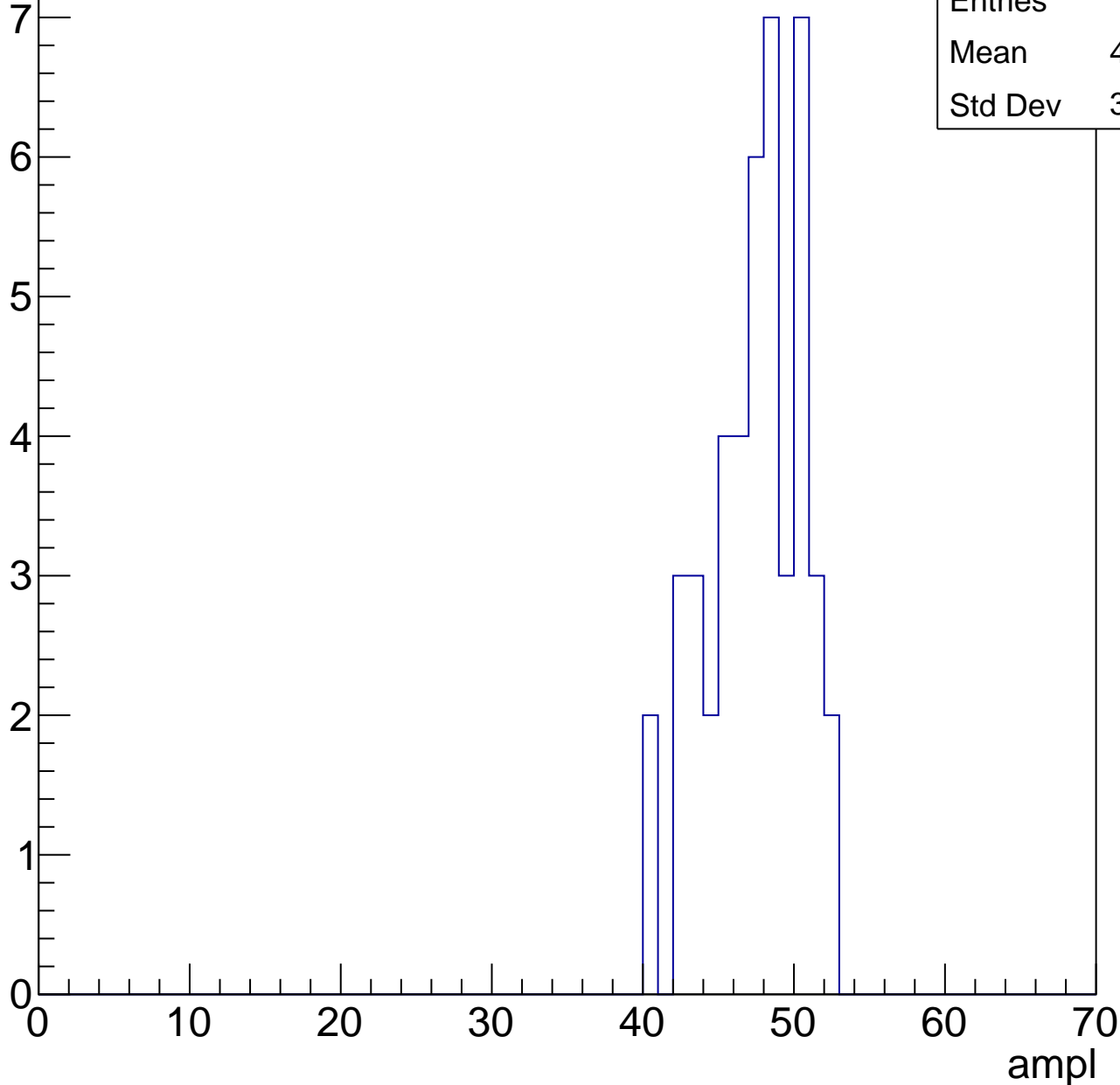


B1L102S, U18-ch65, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	46.93
Std Dev	3.089

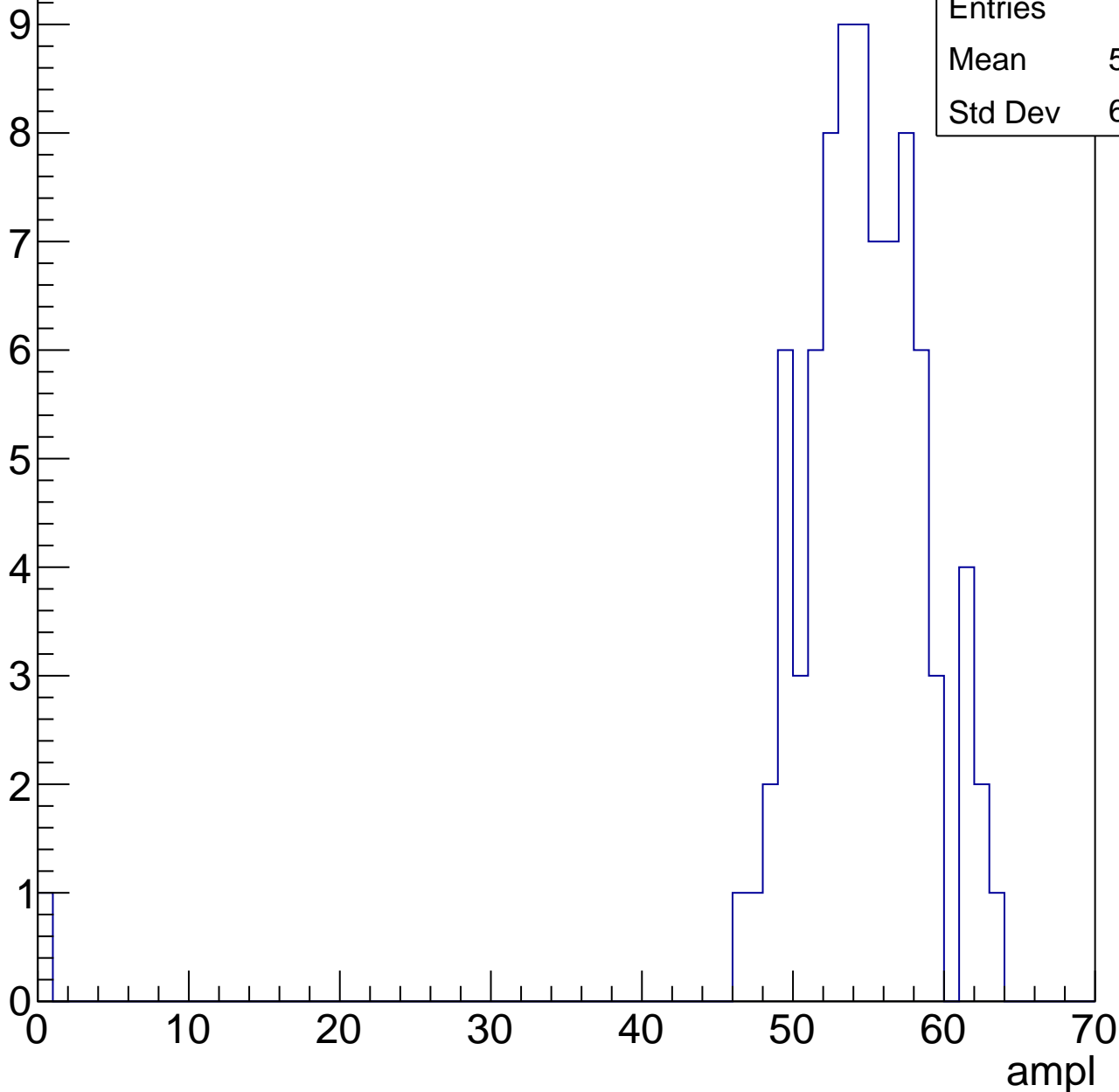


B1L102S, U18-ch65, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	53.65
Std Dev	6.958

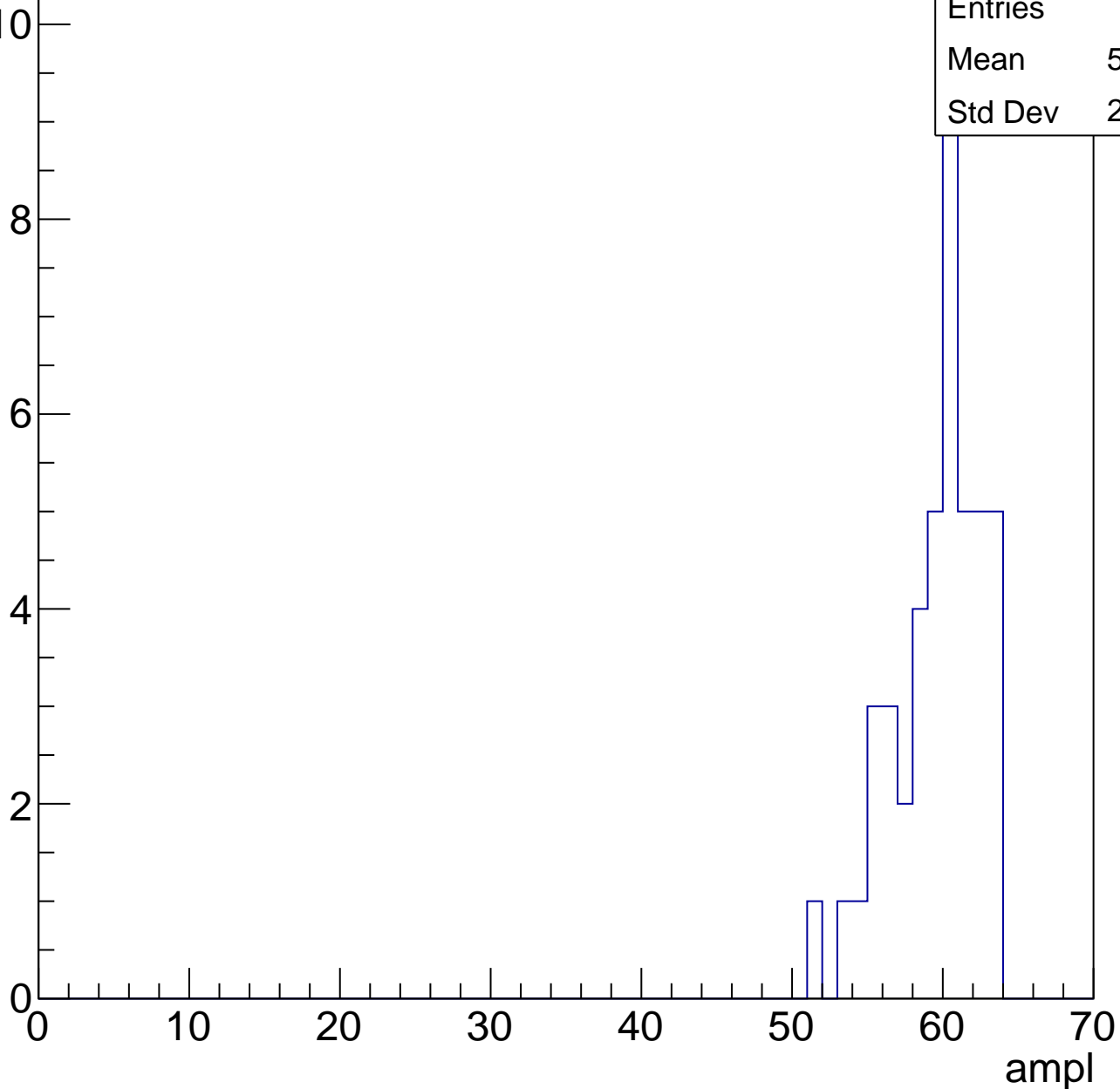


B1L102S, U18-ch65, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.16
Std Dev	2.852



B1L102S, U18-ch65, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

9

Mean

61.56

Std Dev

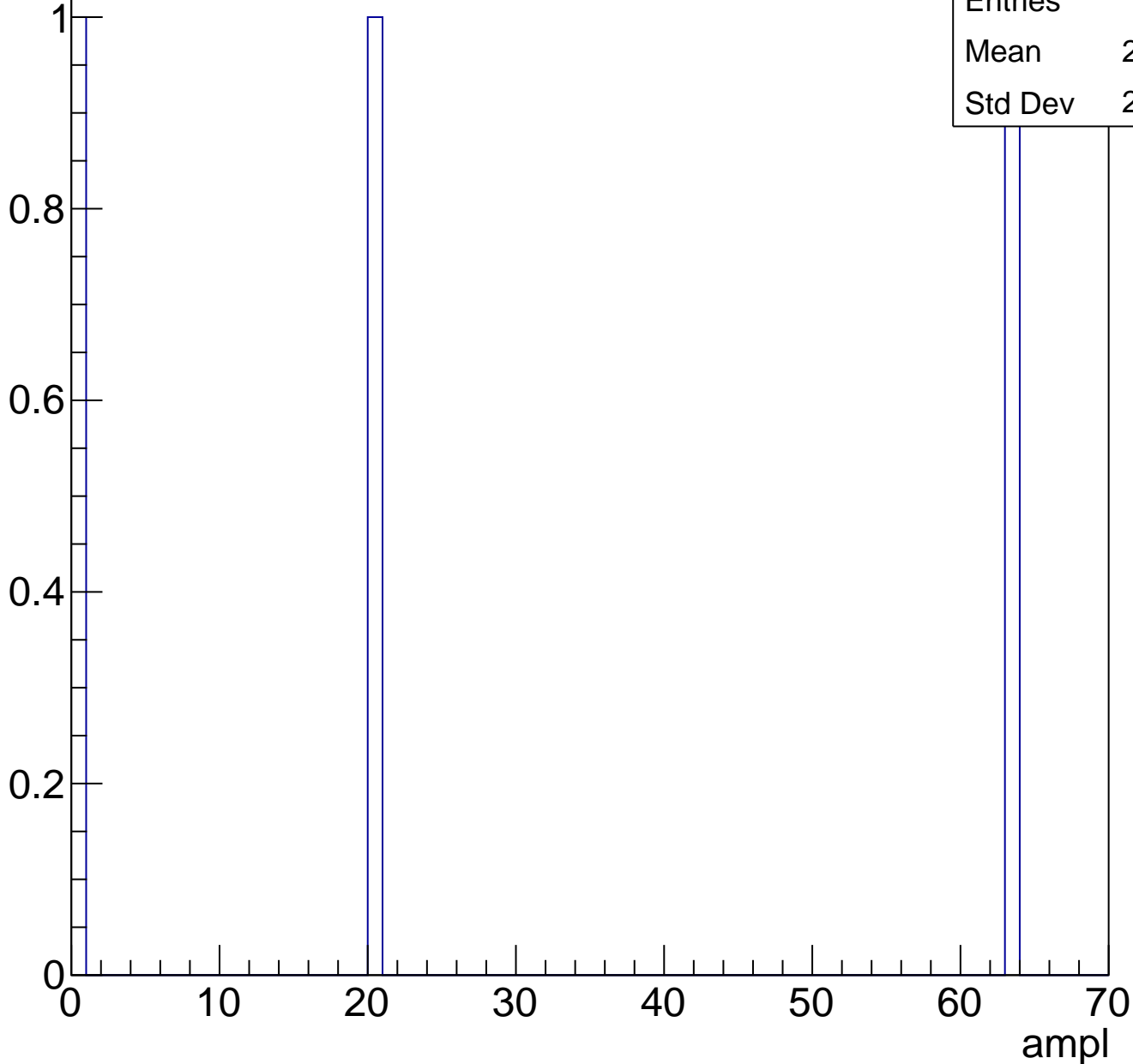
1.499

Entries	9
Mean	61.56
Std Dev	1.499

B1L102S, U18-ch65, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27.67
Std Dev	26.28

B1L102S, U18-ch66, adc0

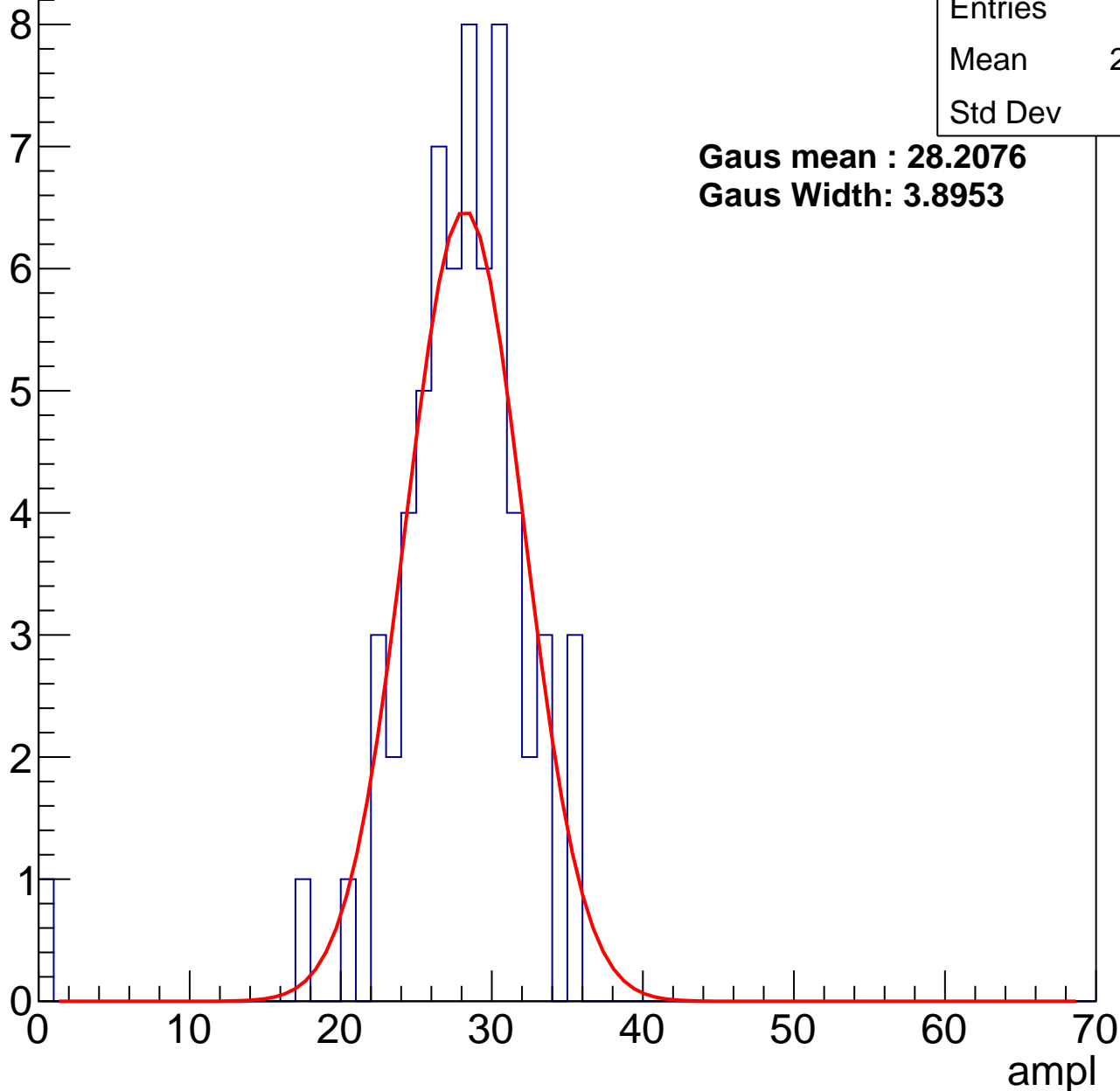
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	27.25
Std Dev	4.94

Gaus mean : 28.2076

Gaus Width: 3.8953



B1L102S, U18-ch66, adc1

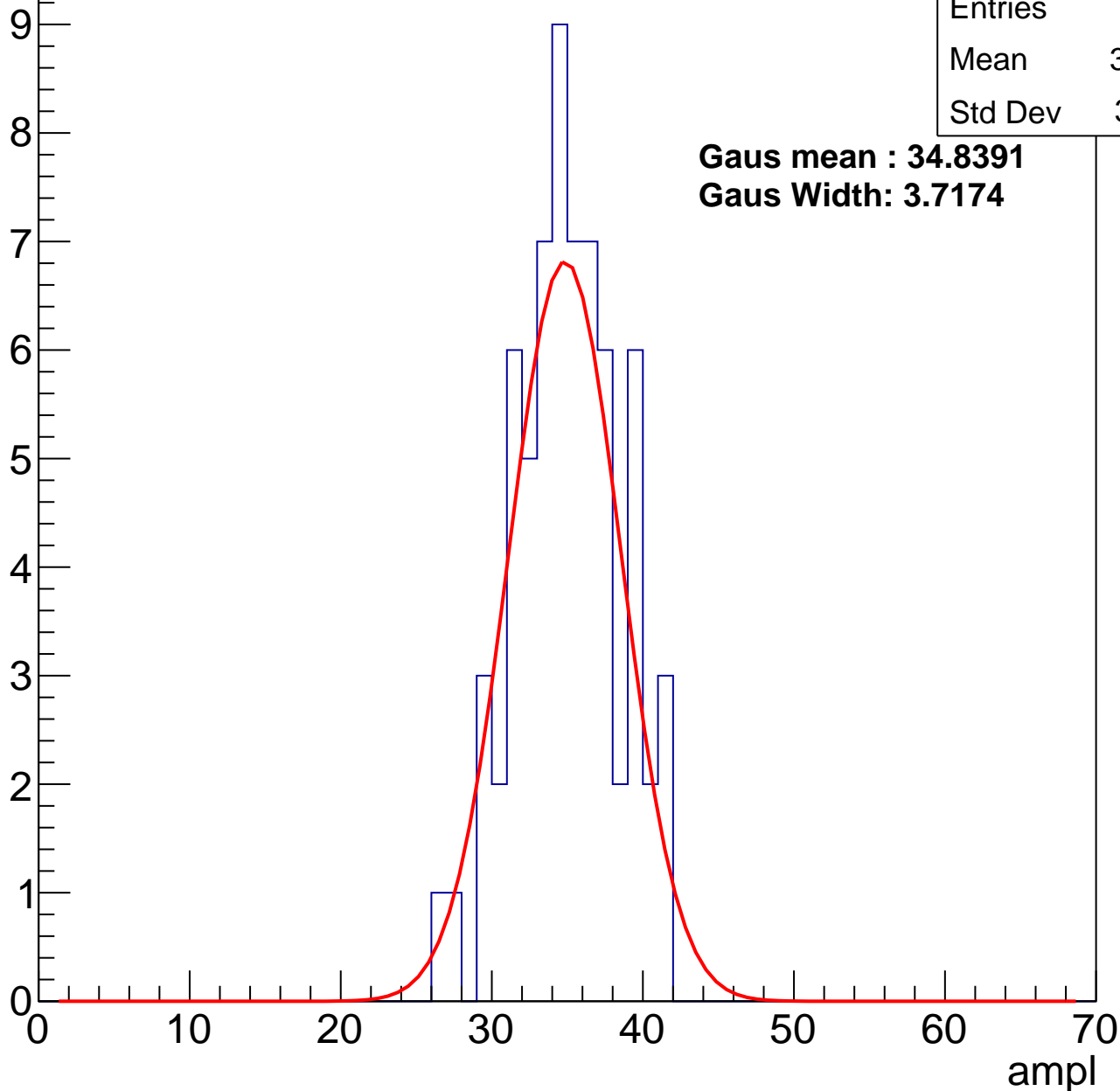
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.55
Std Dev	3.391

Gaus mean : 34.8391

Gaus Width: 3.7174



B1L102S, U18-ch66, adc2

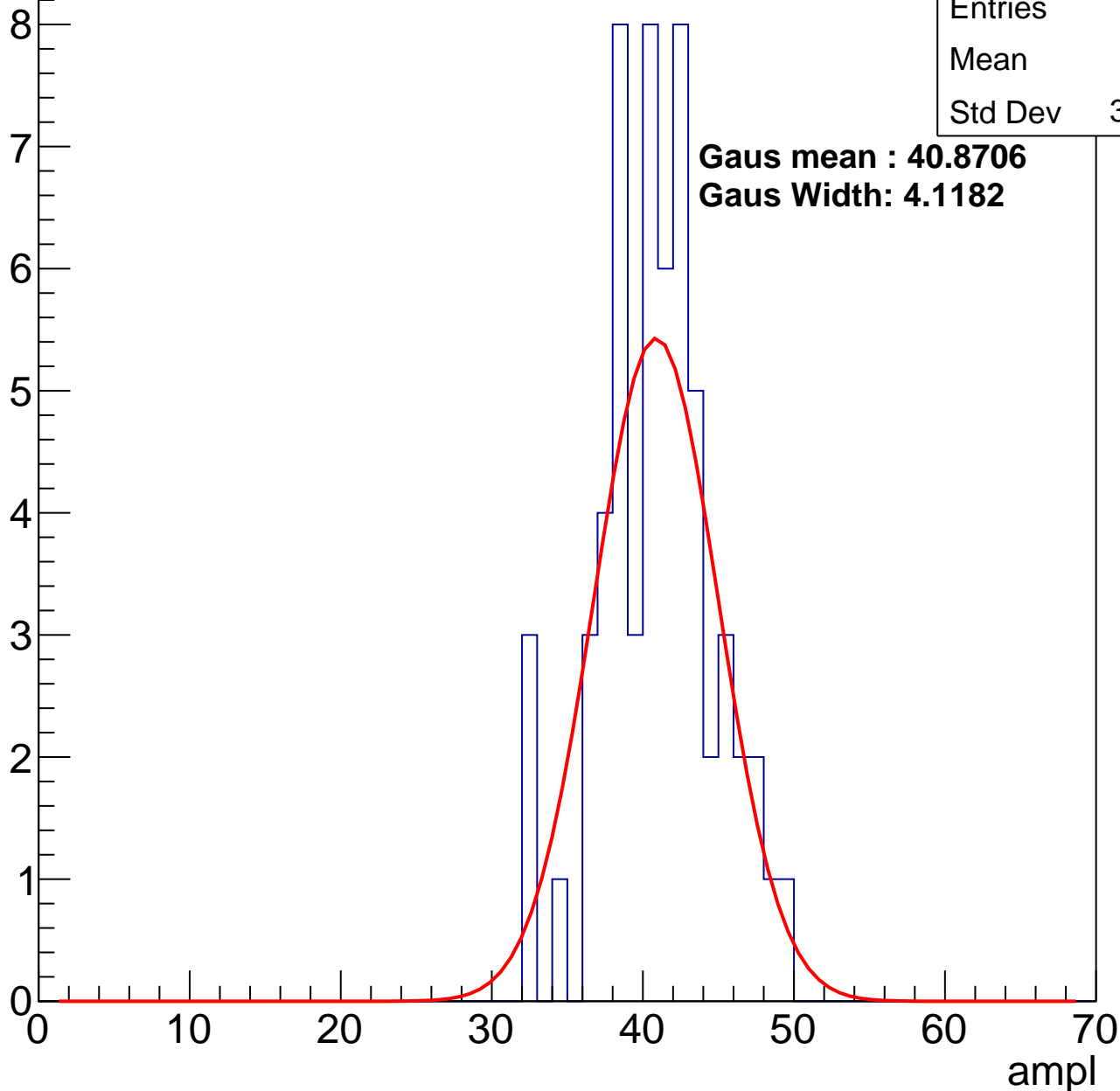
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	40.5
Std Dev	3.735

Gaus mean : 40.8706

Gaus Width: 4.1182

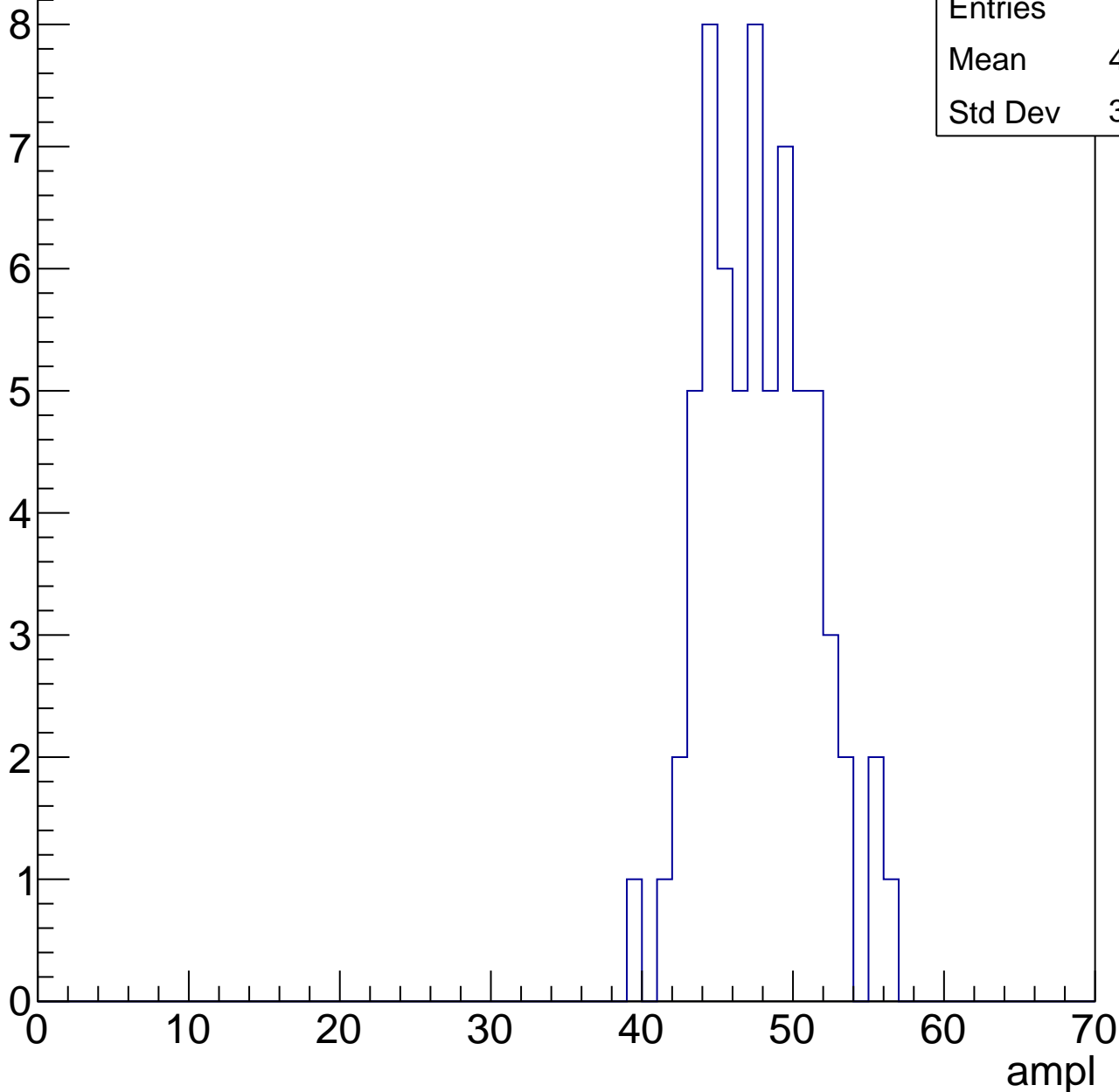


B1L102S, U18-ch66, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	47.32
Std Dev	3.564

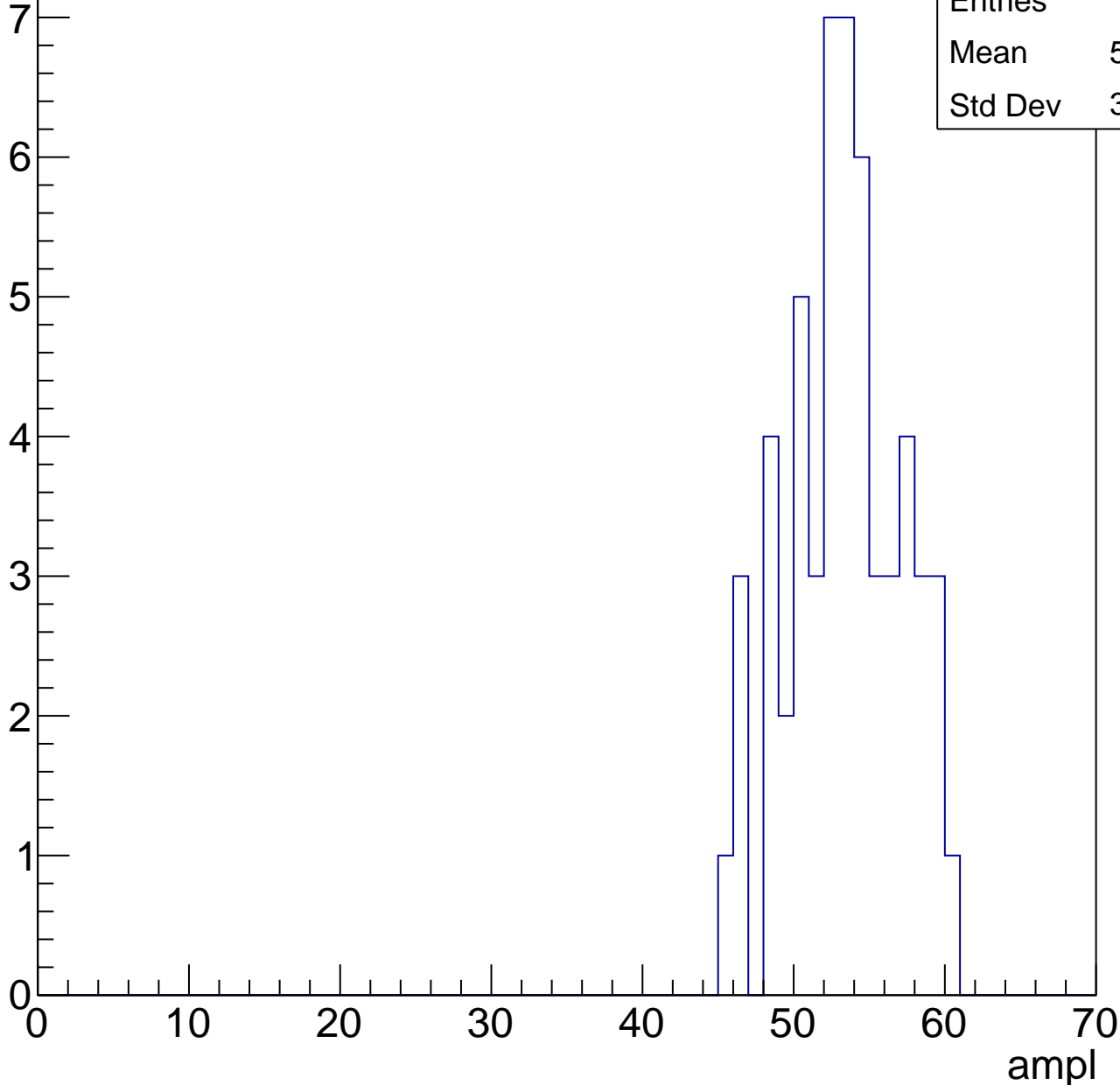


B1L102S, U18-ch66, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	52.85
Std Dev	3.685

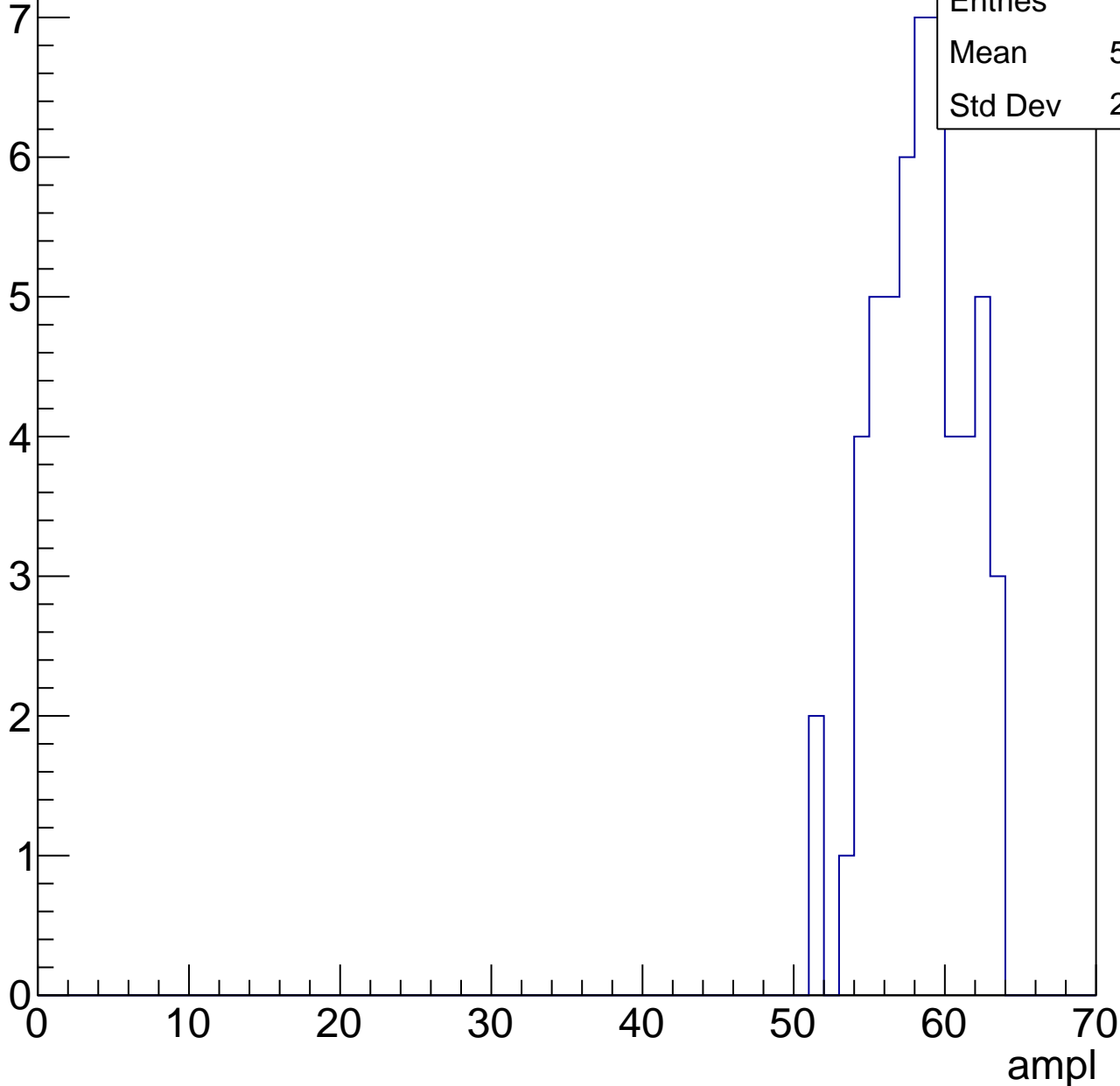


B1L102S, U18-ch66, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	57.92
Std Dev	2.983

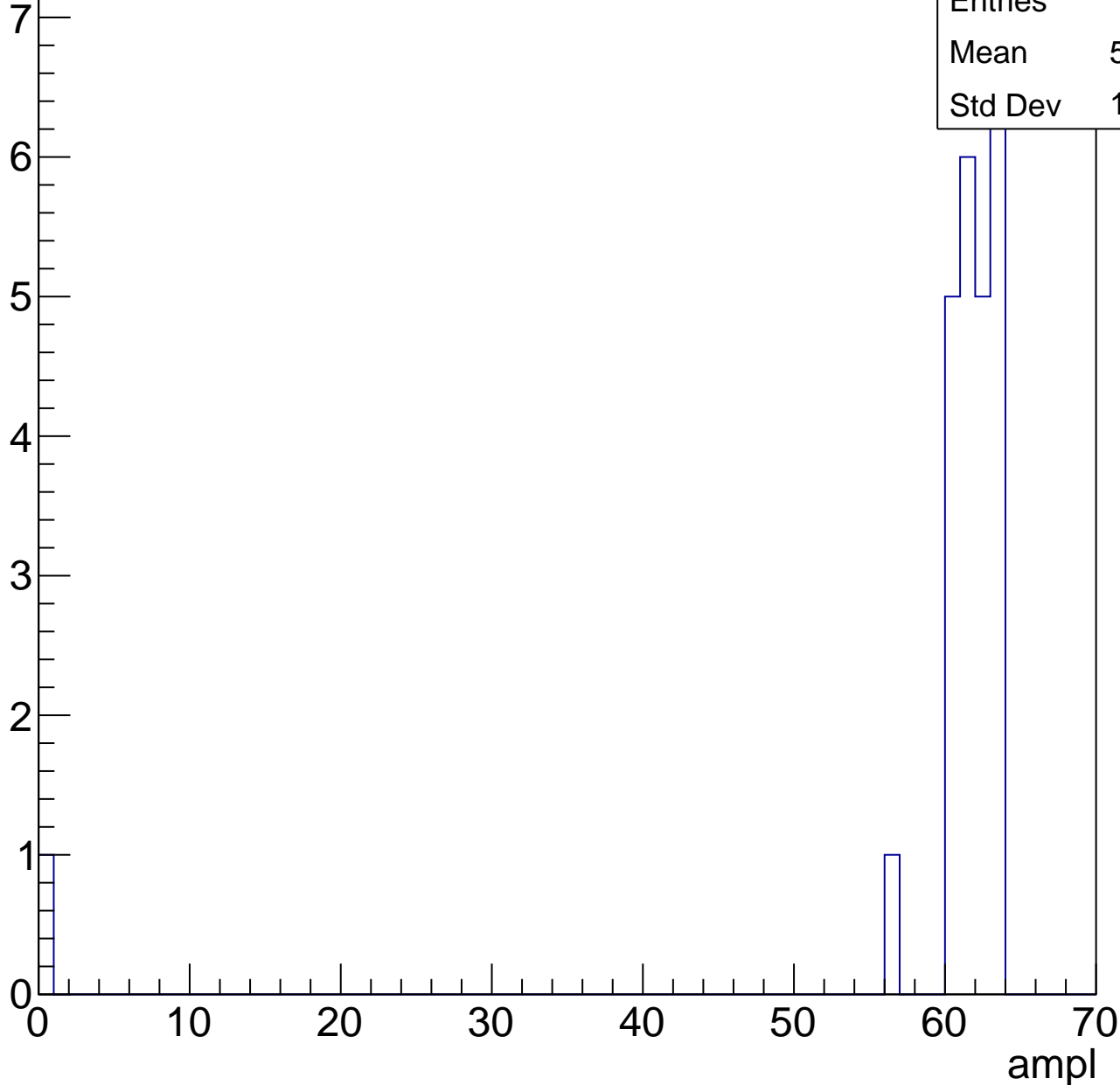


B1L102S, U18-ch66, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.92
Std Dev	12.13



B1L102S, U18-ch66, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch67, adc0

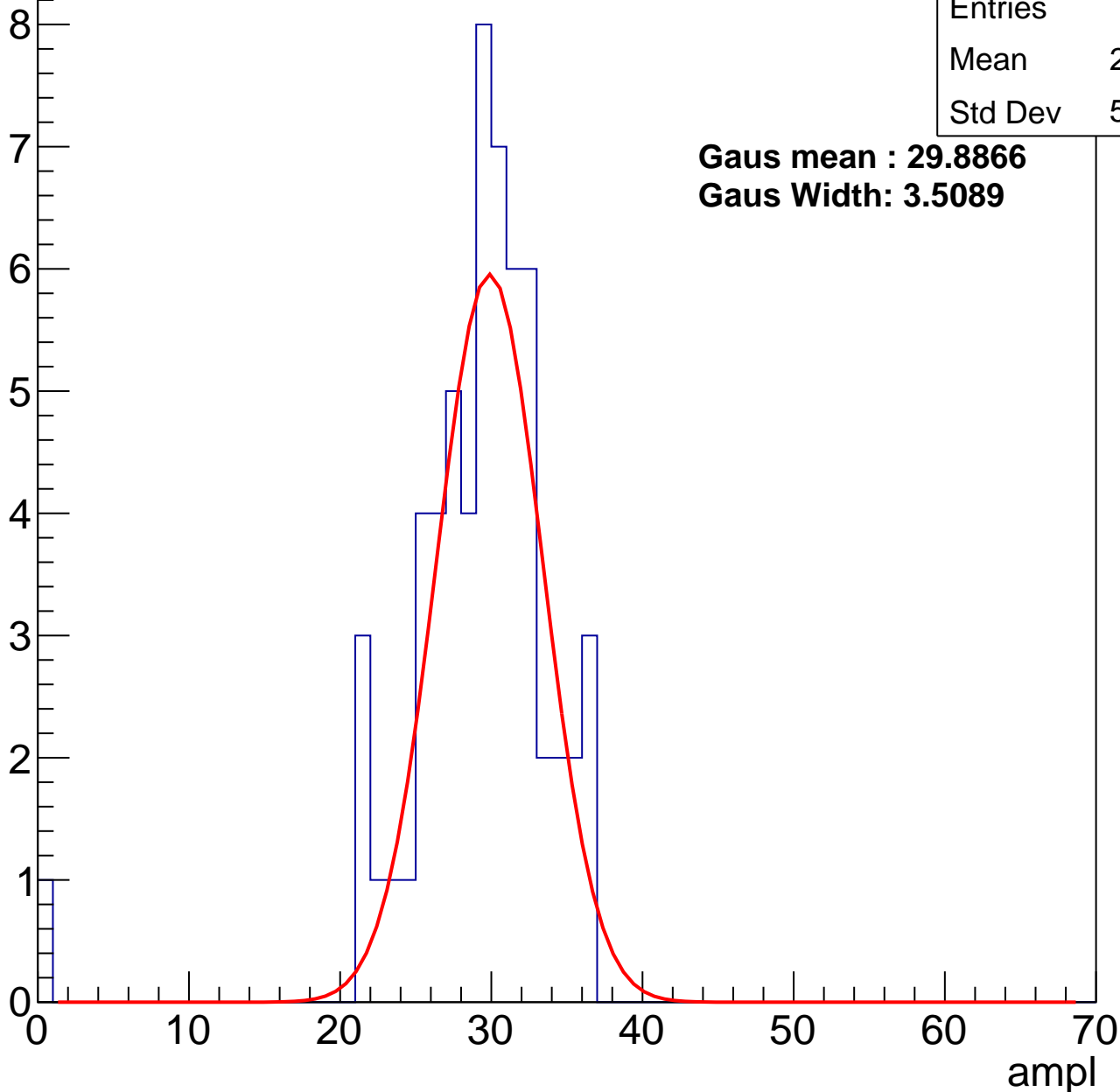
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	28.58
Std Dev	5.232

Gaus mean : 29.8866

Gaus Width: 3.5089



B1L102S, U18-ch67, adc1

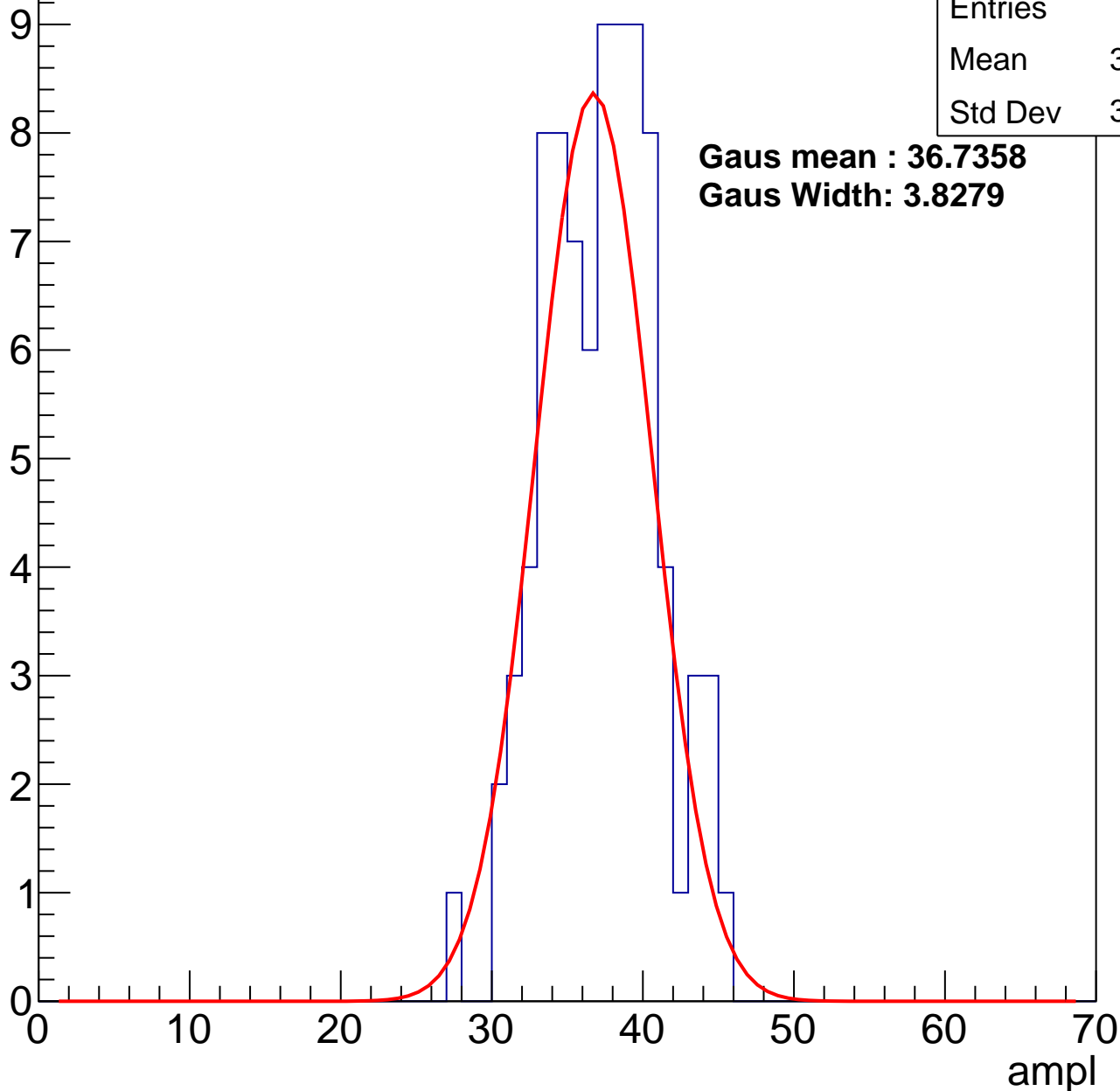
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	86
Mean	36.78
Std Dev	3.674

Gaus mean : 36.7358

Gaus Width: 3.8279



B1L102S, U18-ch67, adc2

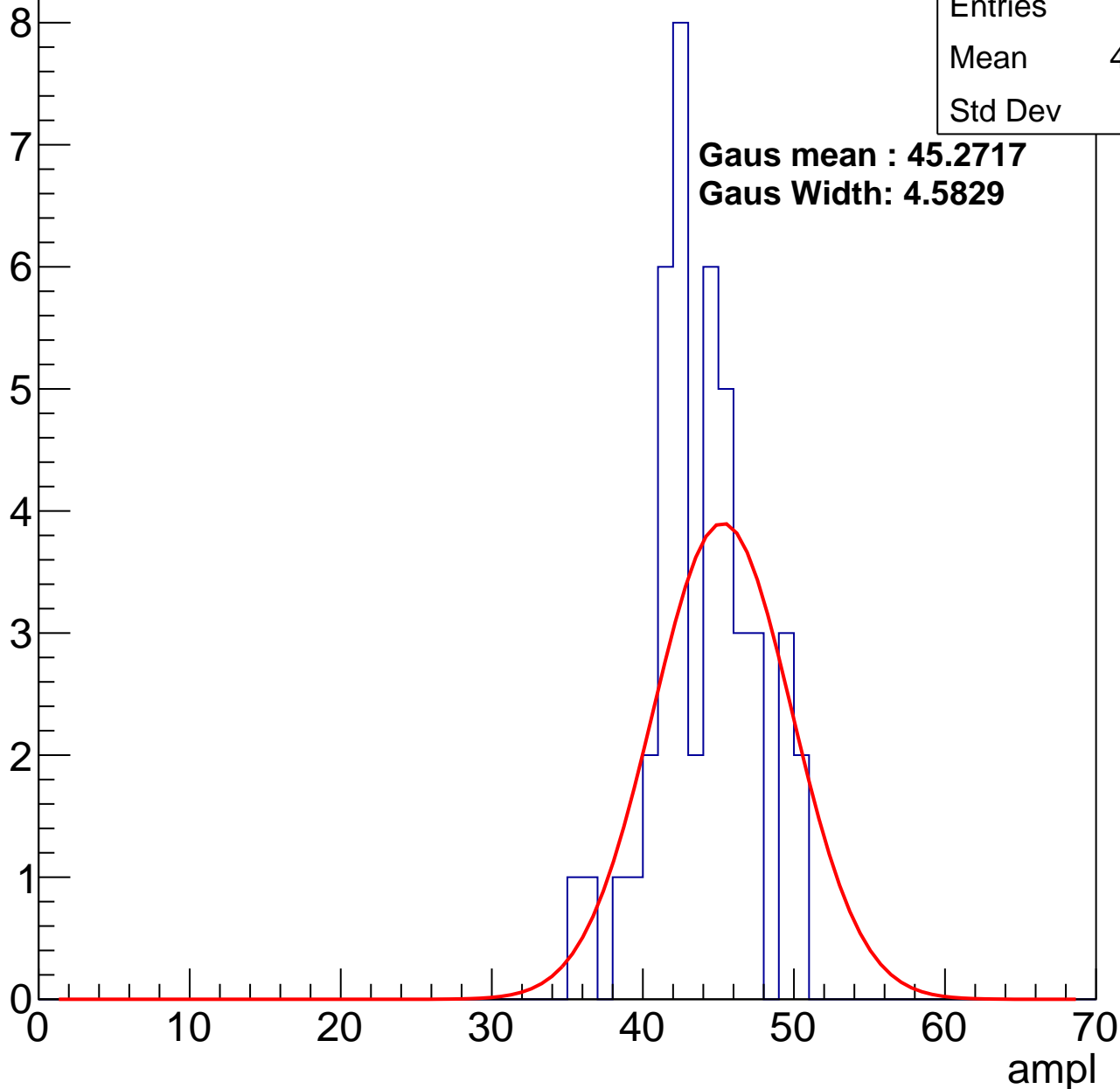
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	43.43
Std Dev	3.38

Gaus mean : 45.2717

Gaus Width: 4.5829

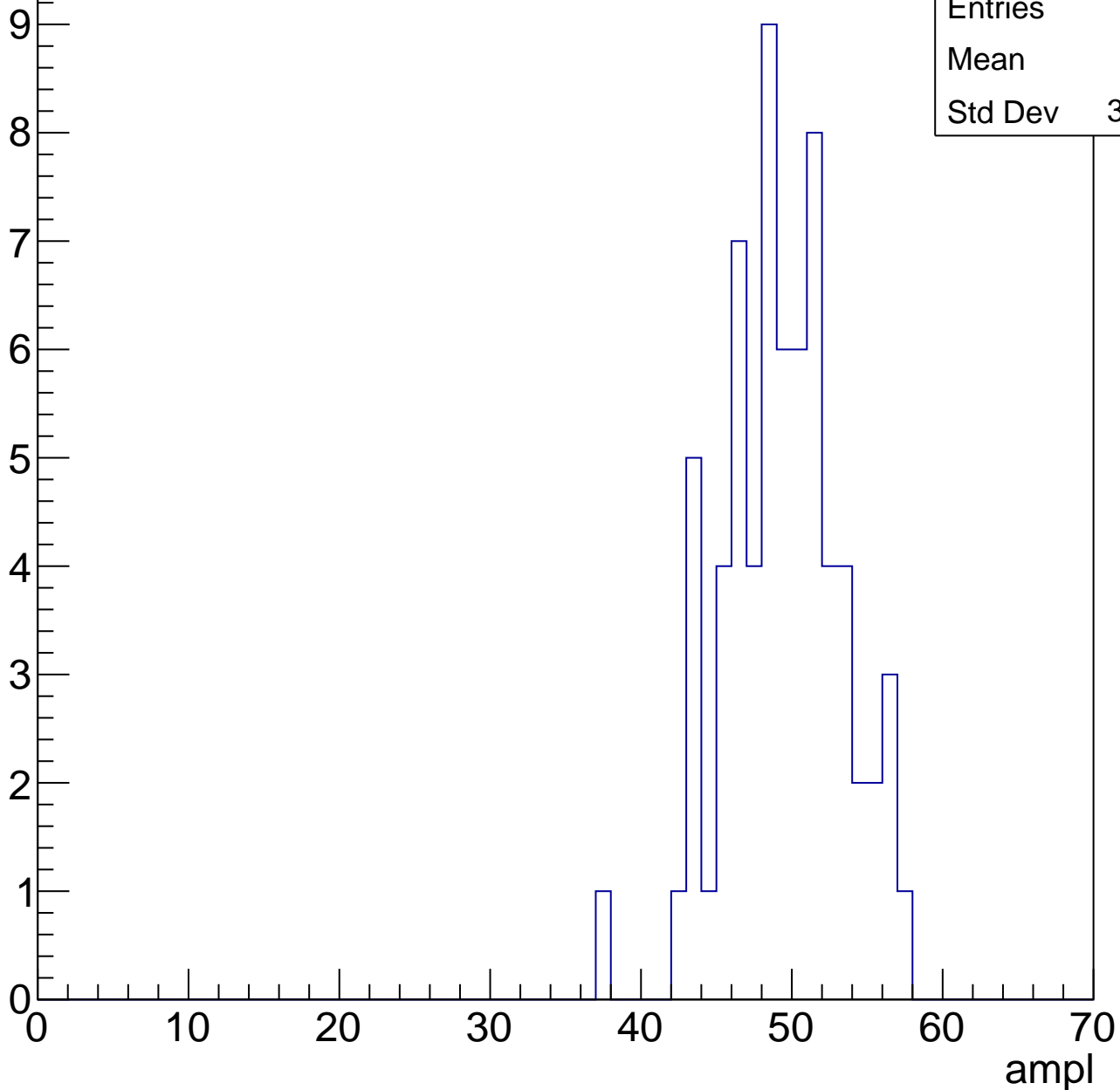


B1L102S, U18-ch67, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.9
Std Dev	3.874

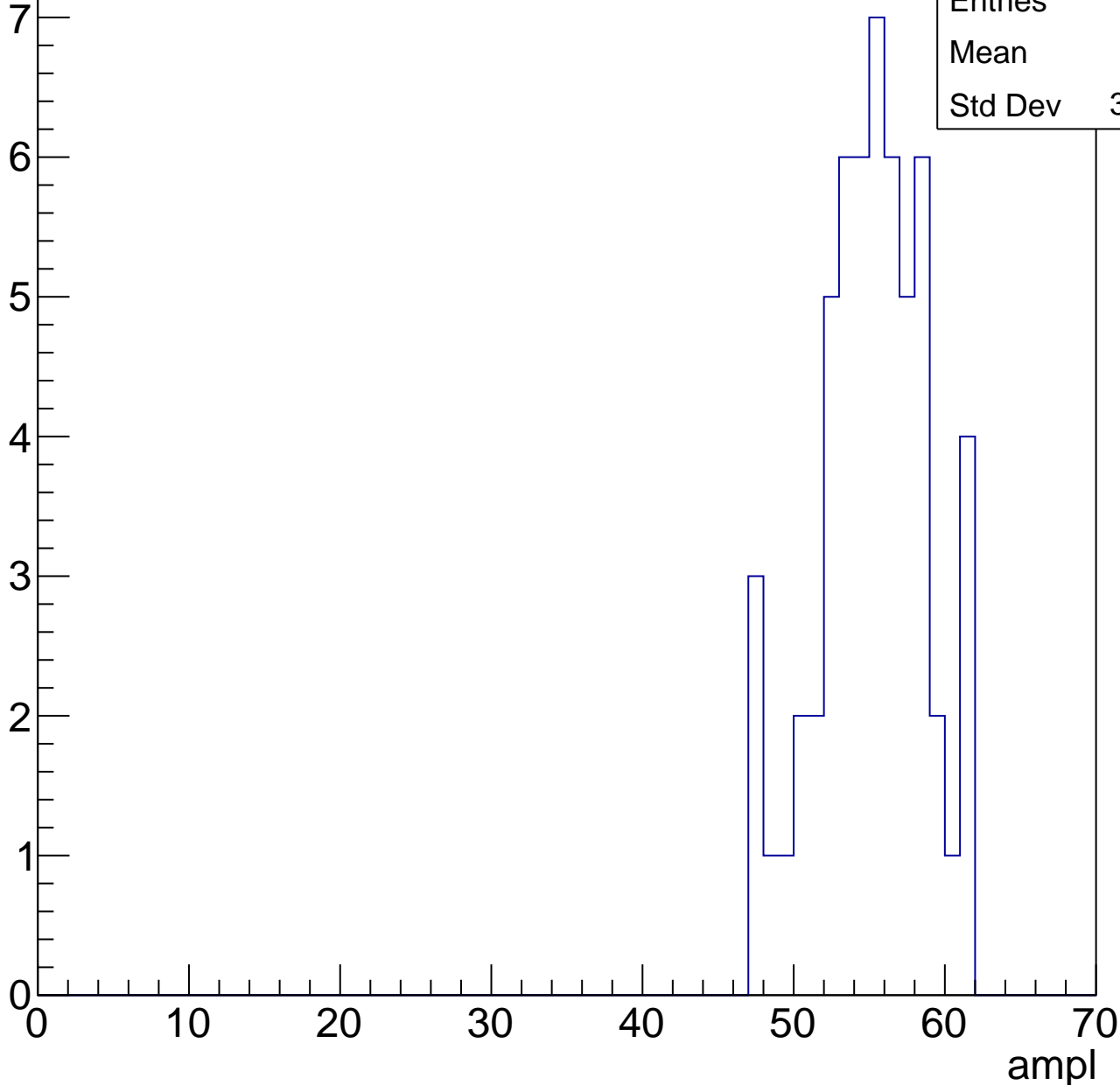


B1L102S, U18-ch67, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	54.7
Std Dev	3.529

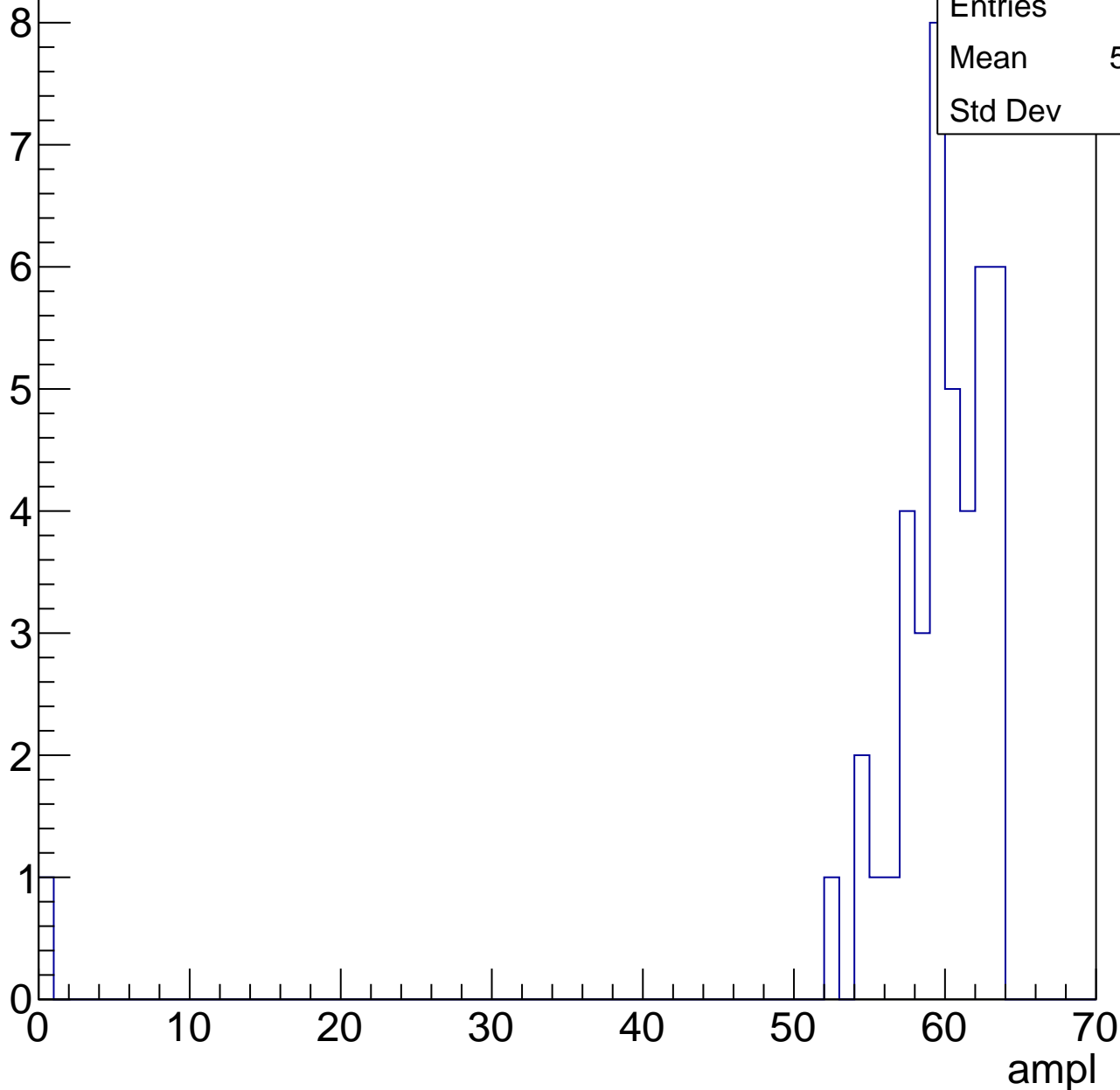


B1L102S, U18-ch67, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

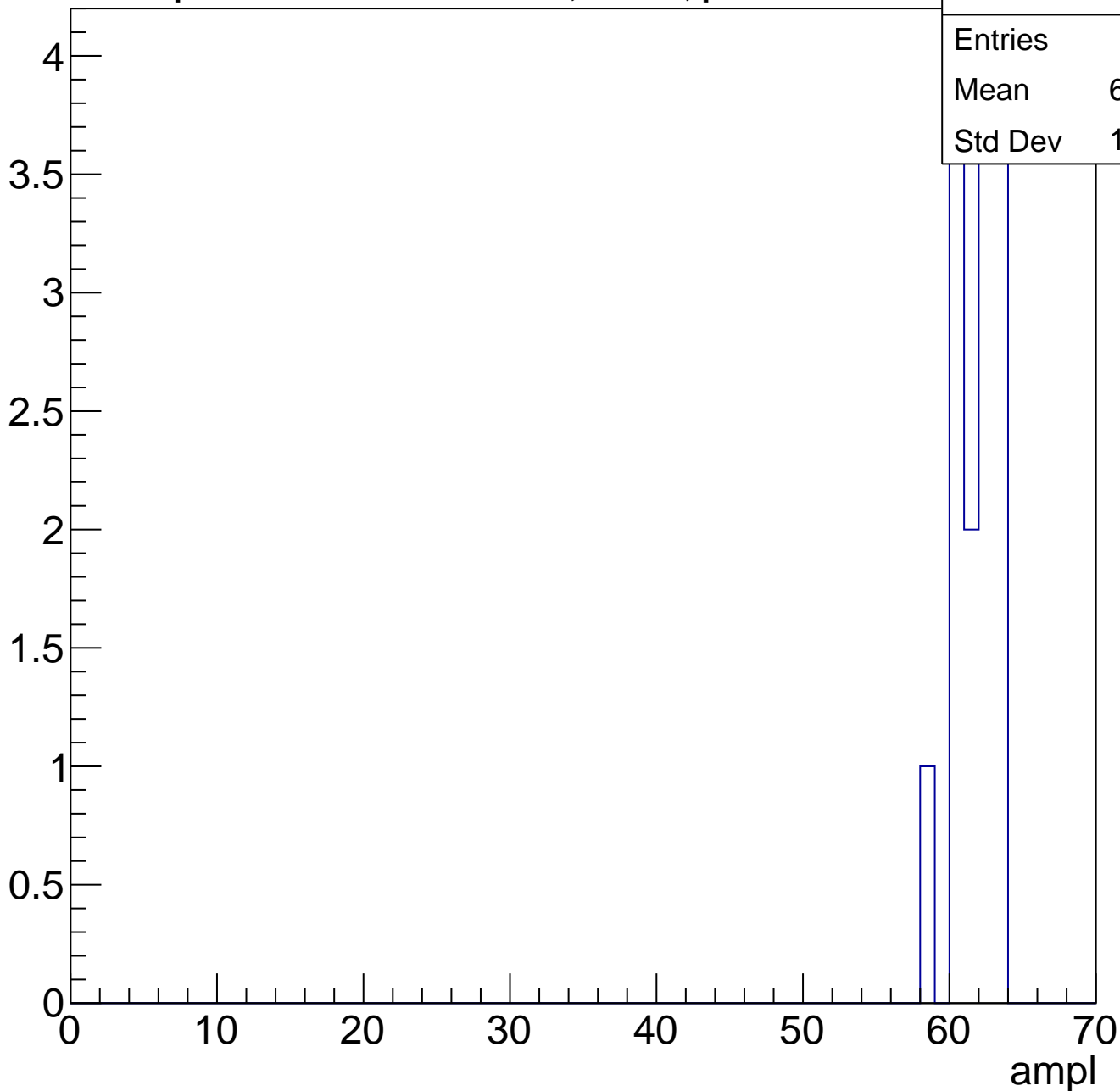
Entries	42
Mean	58.07
Std Dev	9.46



B1L102S, U18-ch67, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch67, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch68, adc0

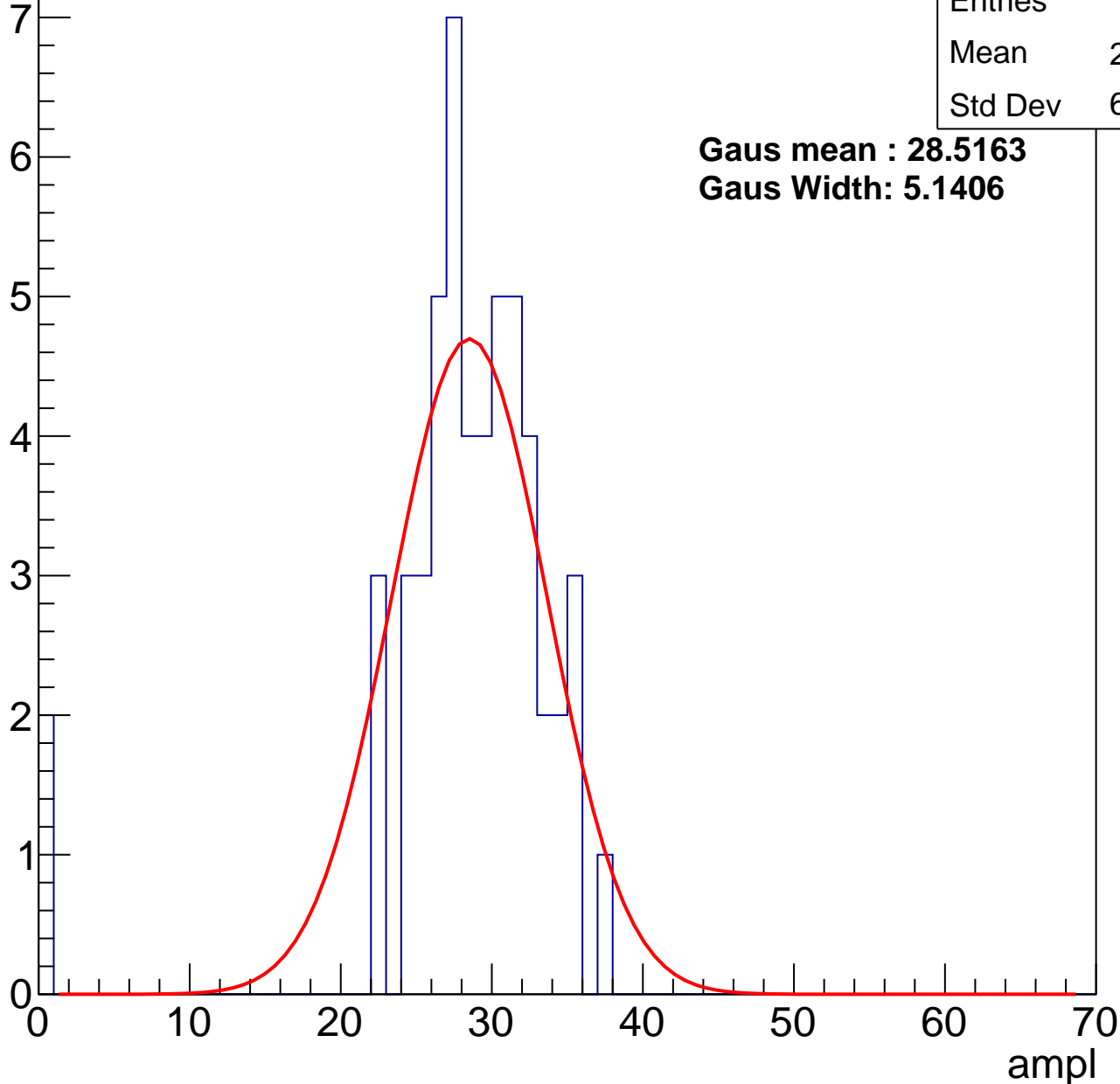
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	27.72
Std Dev	6.525

Gaus mean : 28.5163

Gaus Width: 5.1406



B1L102S, U18-ch68, adc1

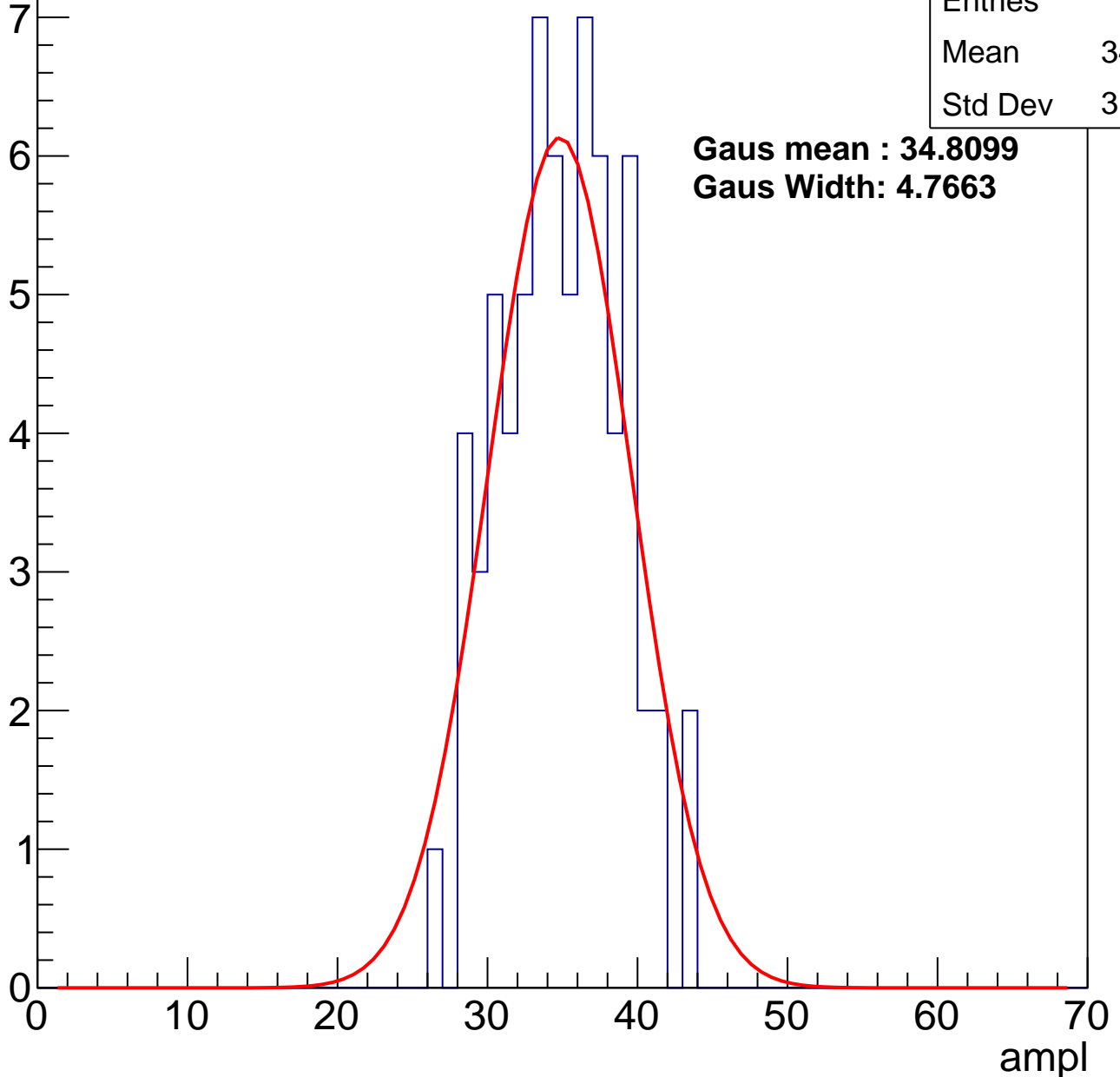
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.45
Std Dev	3.892

Gaus mean : 34.8099

Gaus Width: 4.7663



B1L102S, U18-ch68, adc2

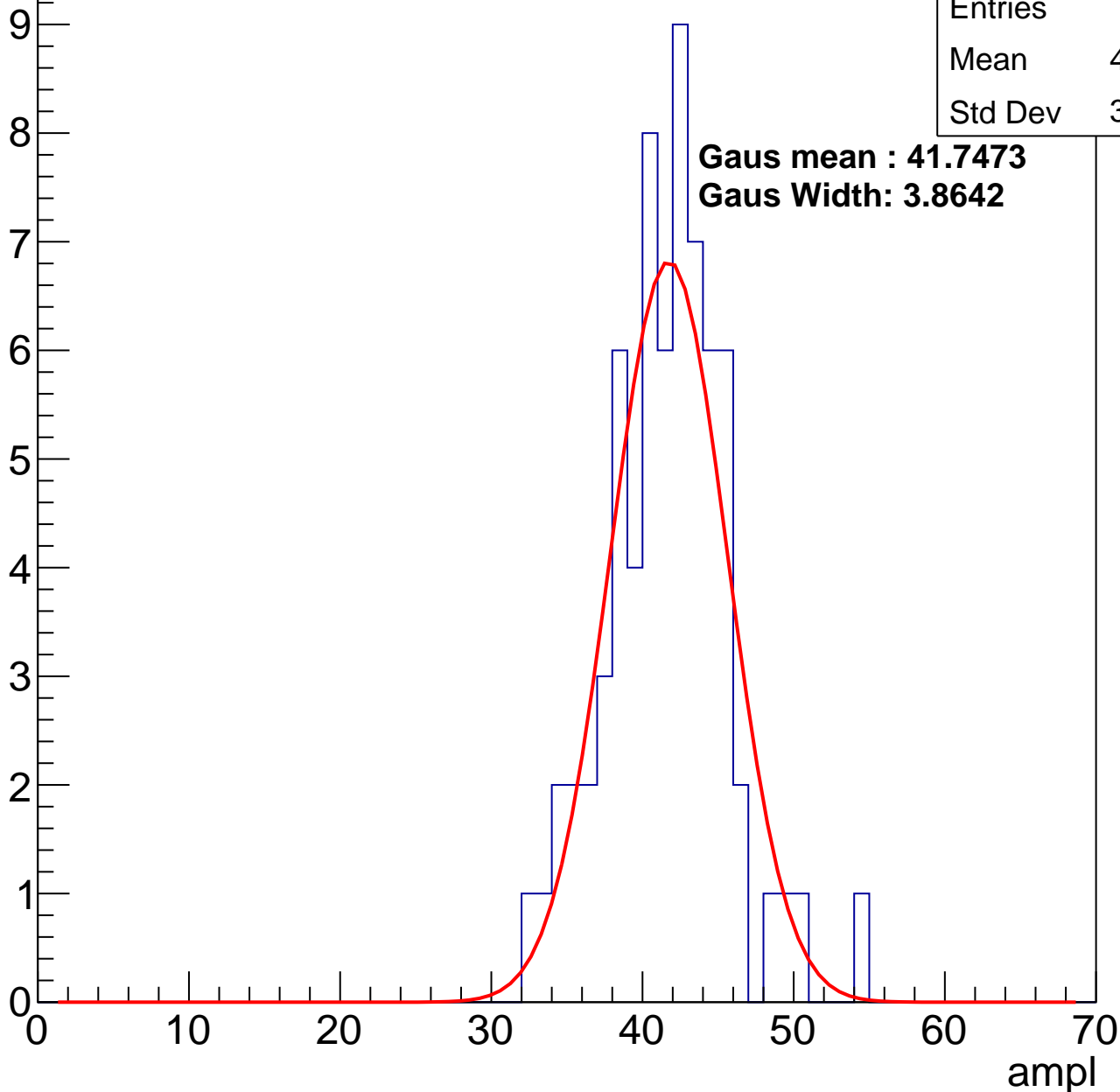
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.19
Std Dev	3.972

Gaus mean : 41.7473

Gaus Width: 3.8642

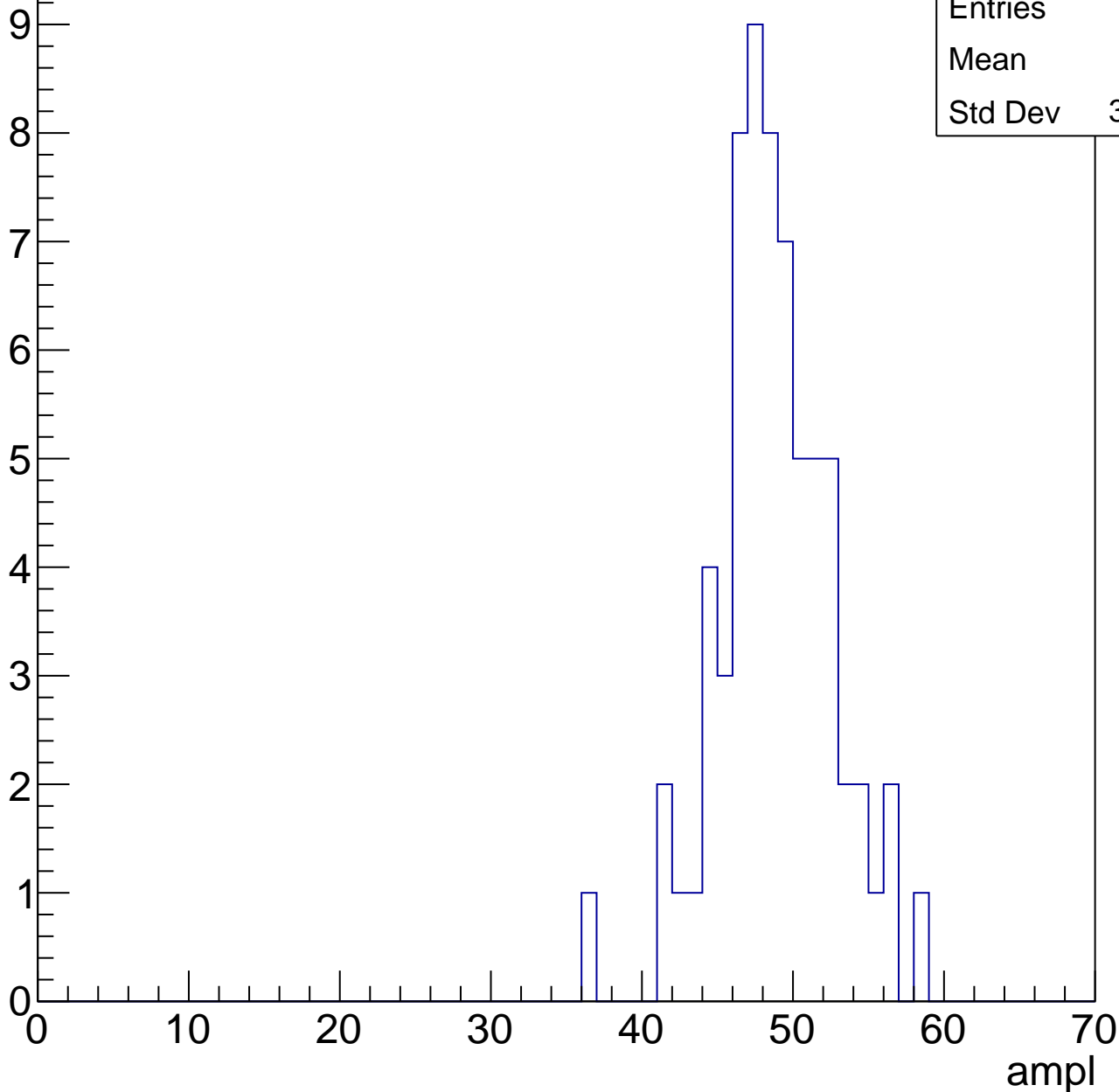


B1L102S, U18-ch68, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

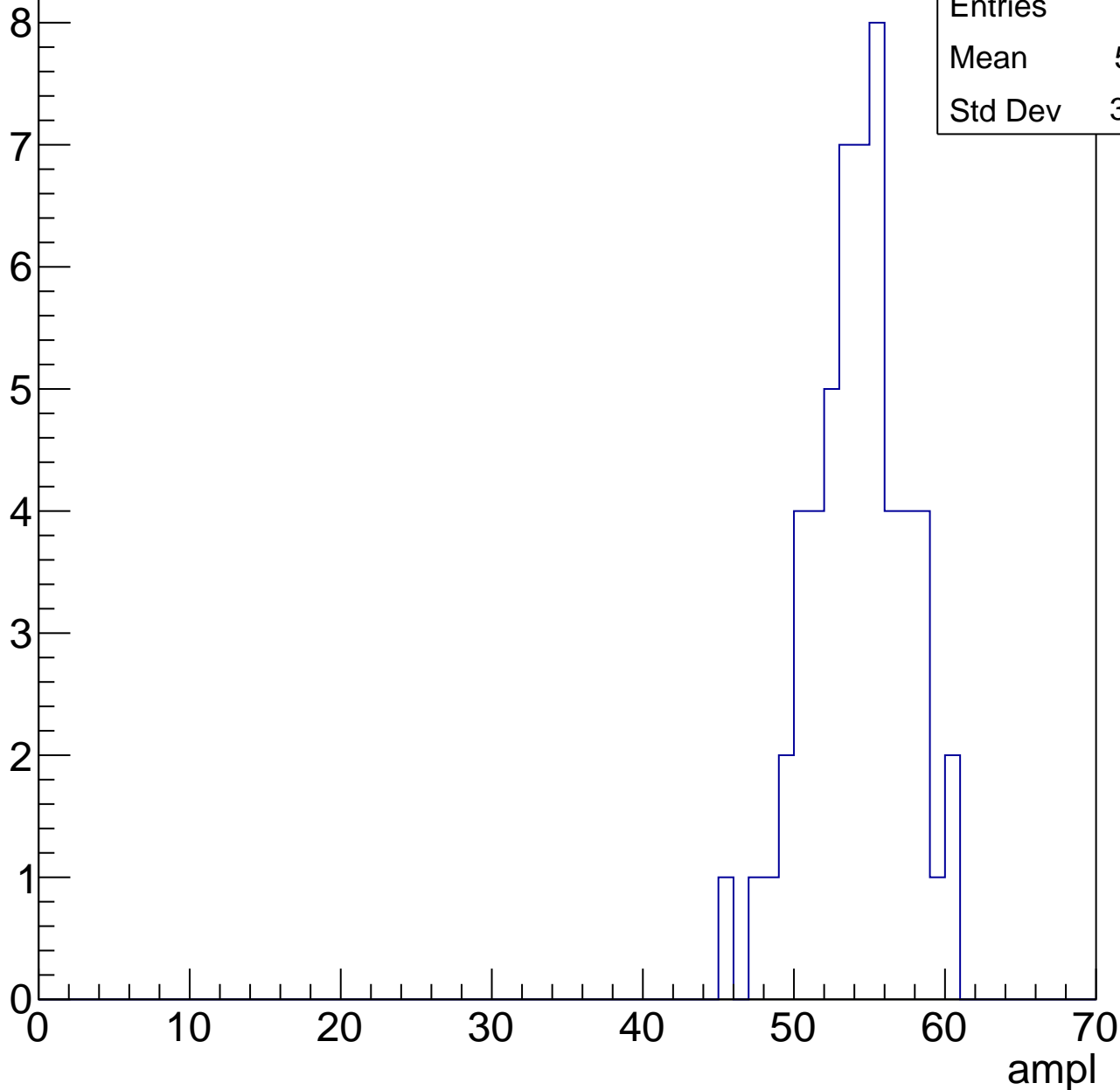
Entries	67
Mean	48.3
Std Dev	3.825



B1L102S, U18-ch68, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry



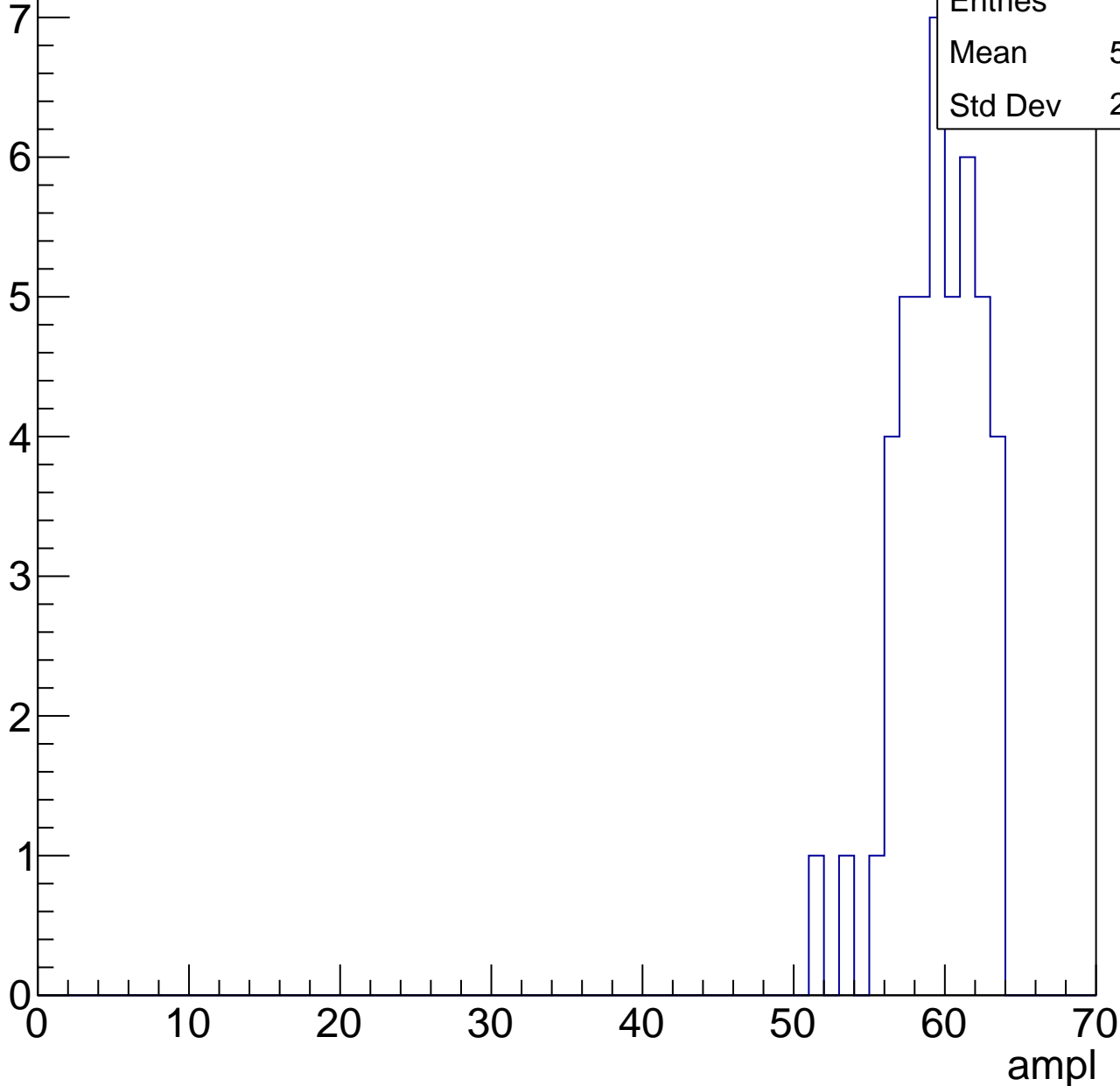
Entries	55
Mean	53.71
Std Dev	3.189

B1L102S, U18-ch68, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	59.07
Std Dev	2.675

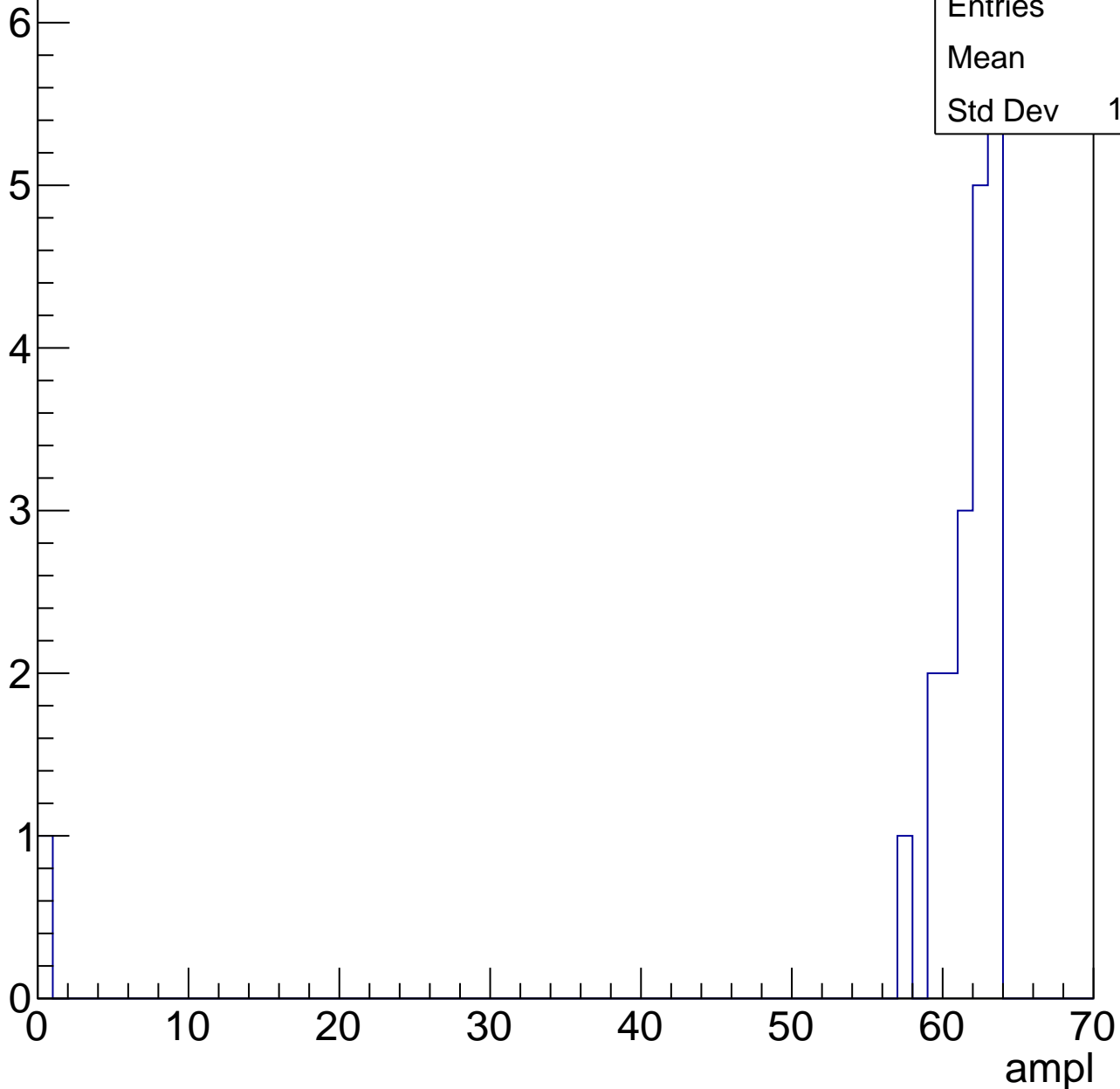


B1L102S, U18-ch68, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

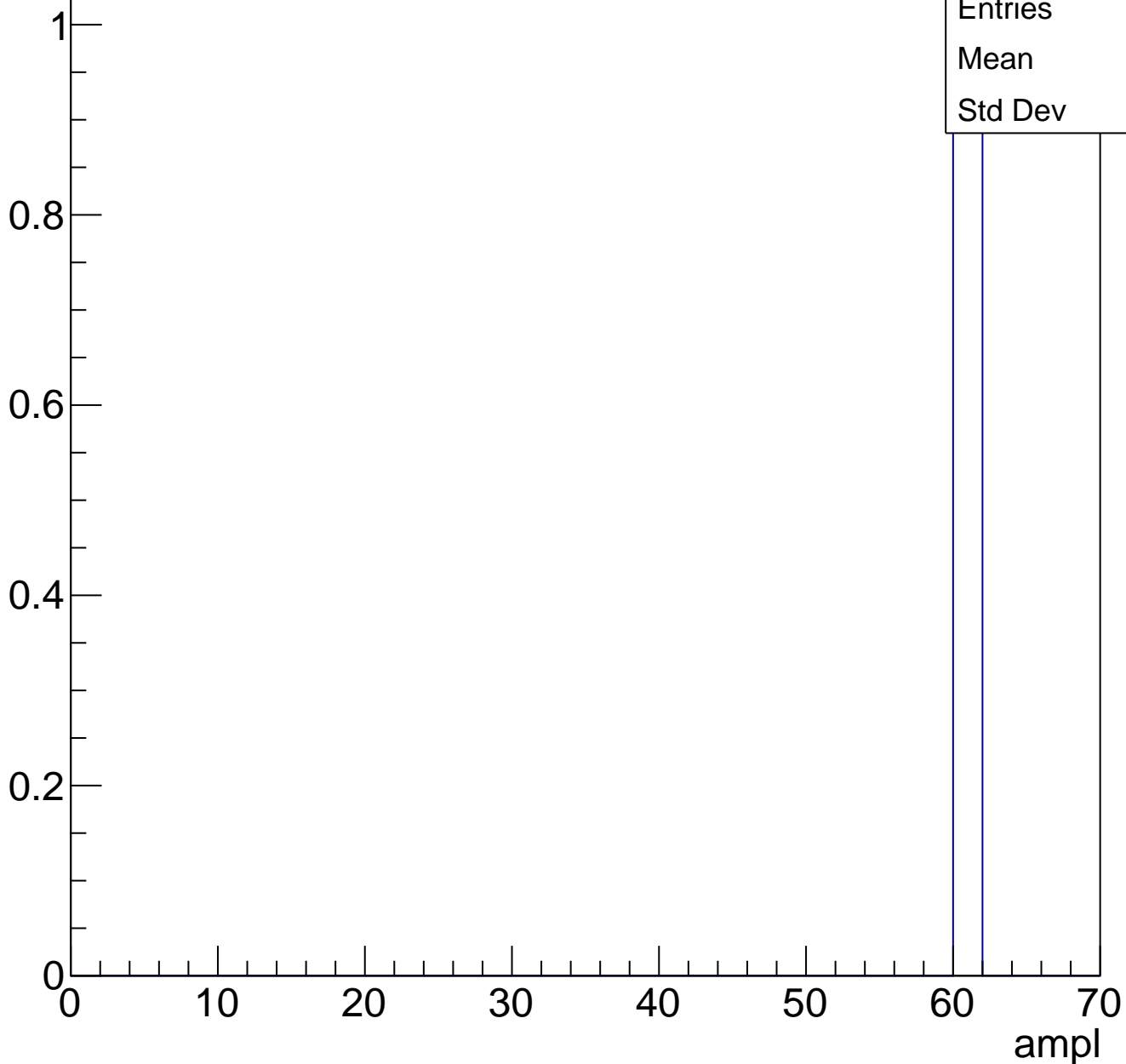
Entries	20
Mean	58.3
Std Dev	13.47



B1L102S, U18-ch68, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch69, adc0

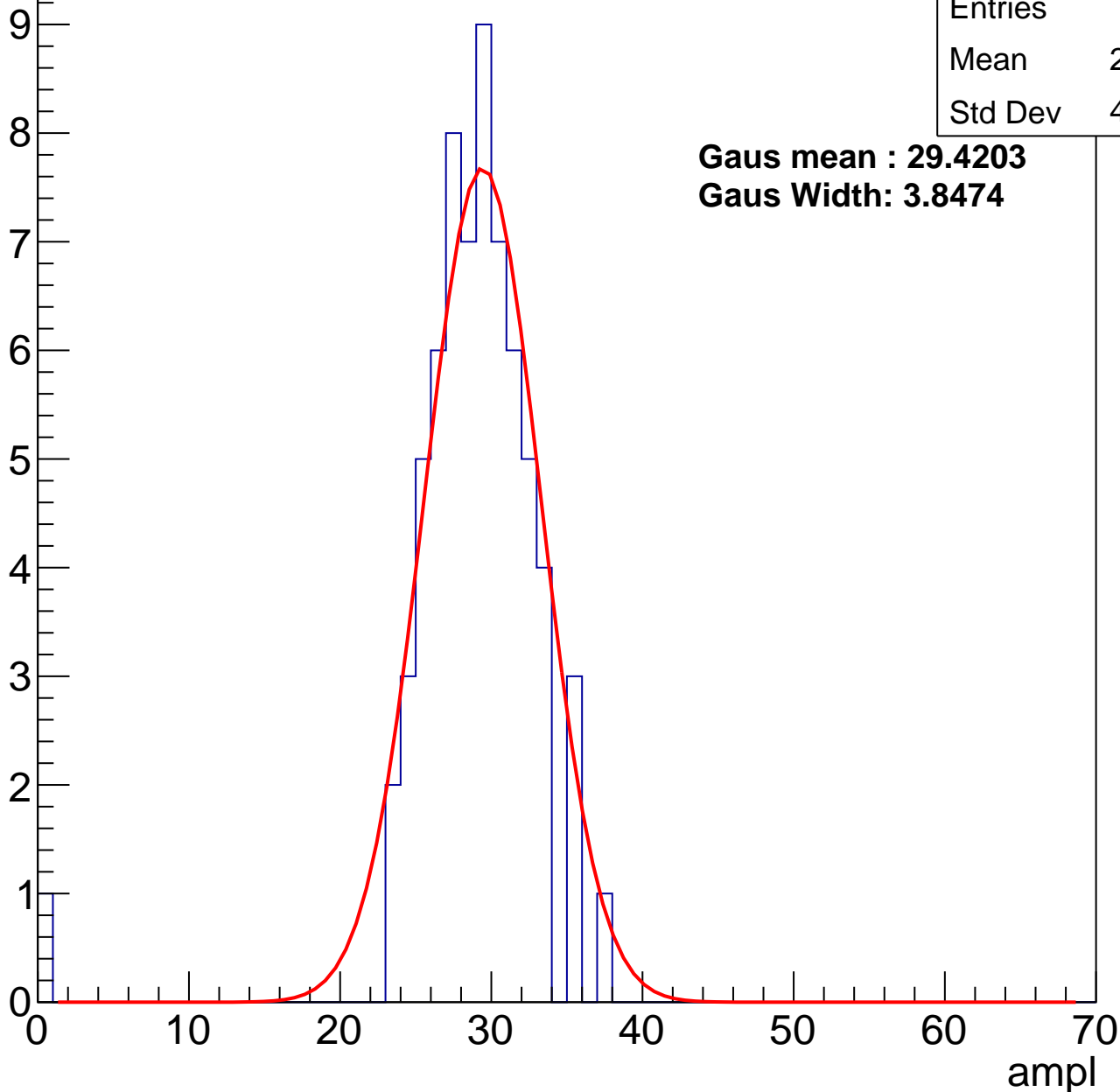
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	28.39
Std Dev	4.649

Gaus mean : 29.4203

Gaus Width: 3.8474



B1L102S, U18-ch69, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	69
Mean	35.67
Std Dev	3.537

Gaus mean : 35.6455

Gaus Width: 3.4267

Entry

10

8

6

4

2

0

0

10

20

30

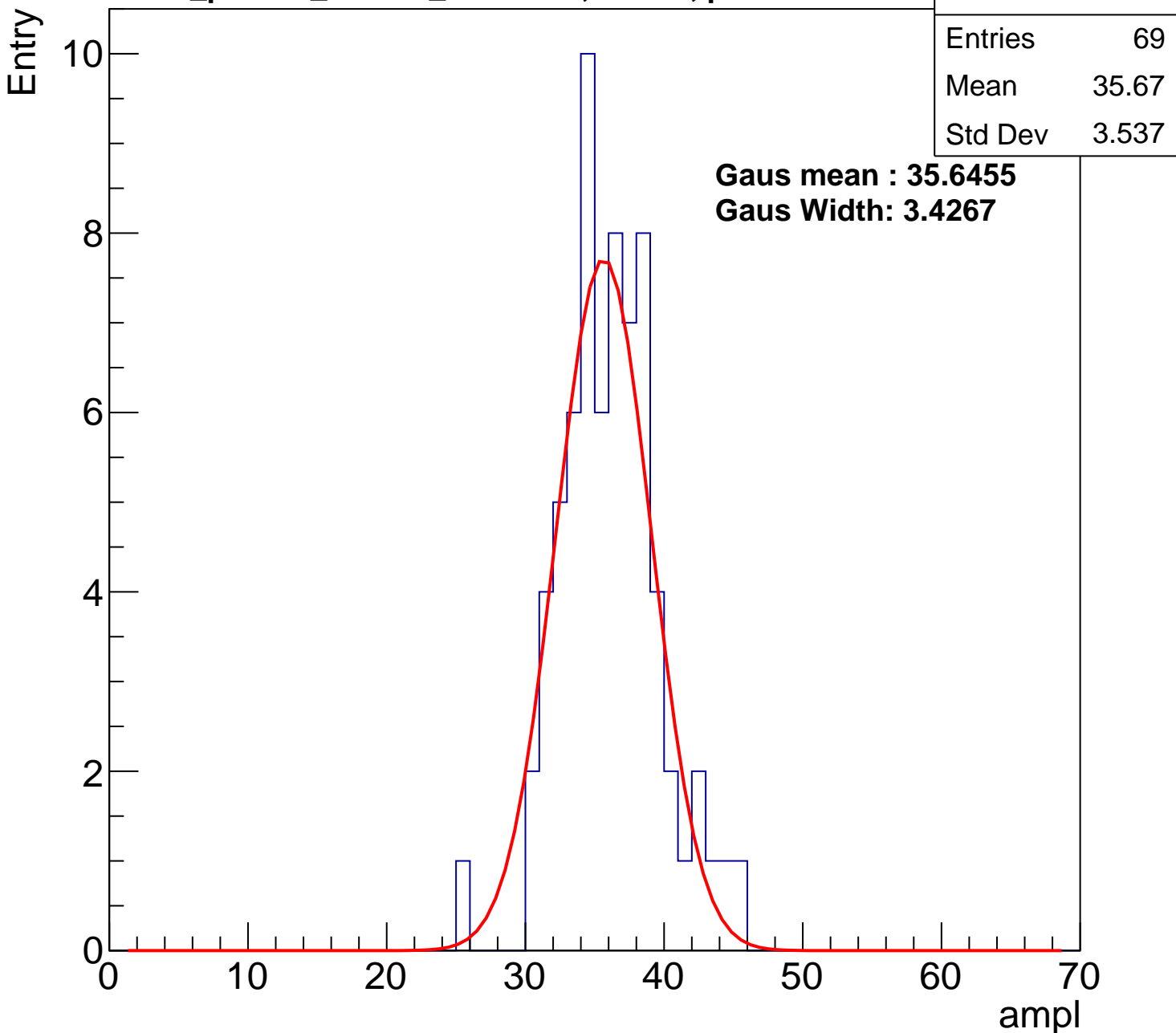
40

50

60

70

ampl



B1L102S, U18-ch69, adc2

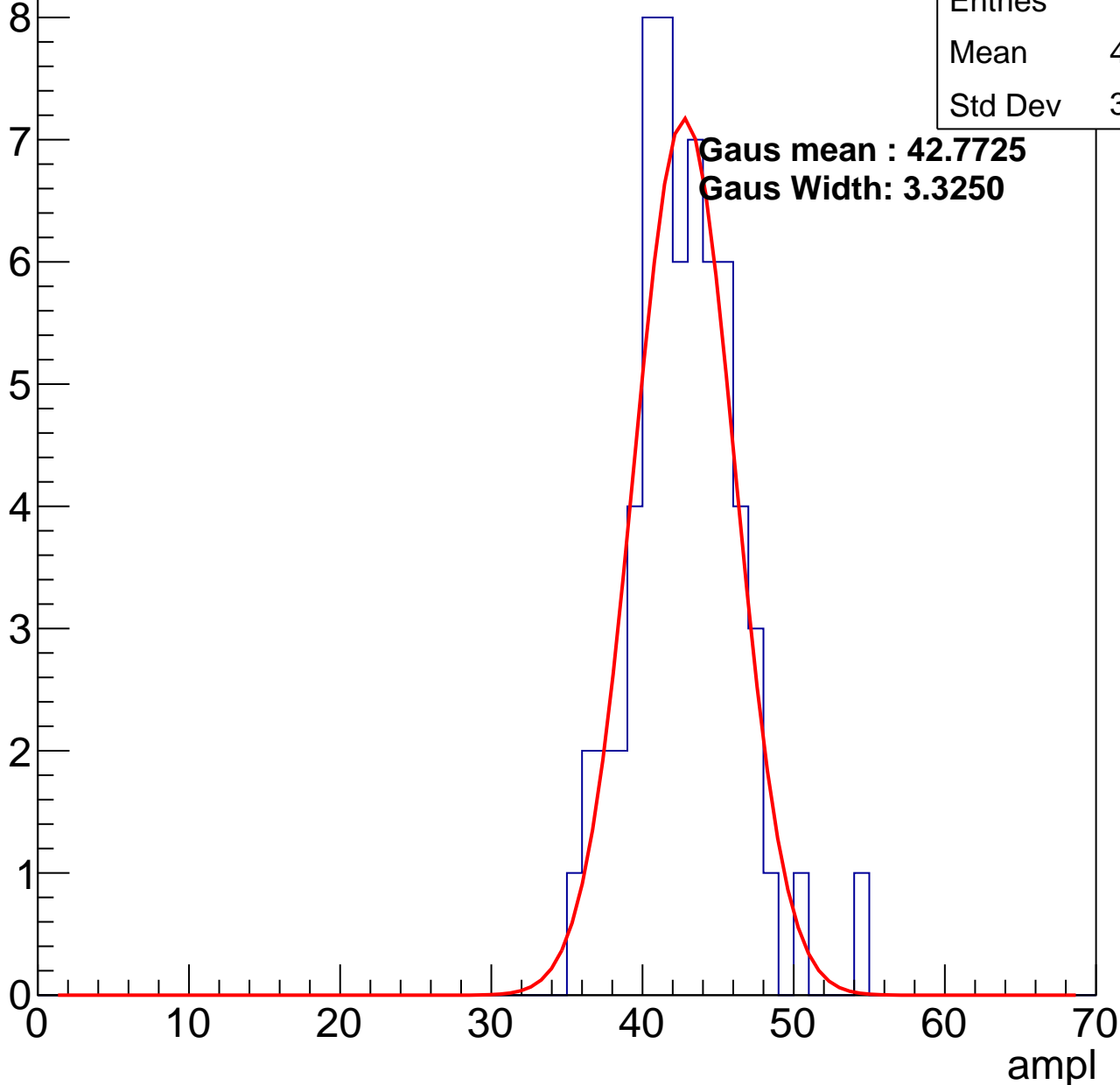
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.34
Std Dev	3.455

Gaus mean : 42.7725

Gaus Width: 3.3250



B1L102S, U18-ch69, adc3

calib_packv5_042523_0143.root, FC#11, port A2

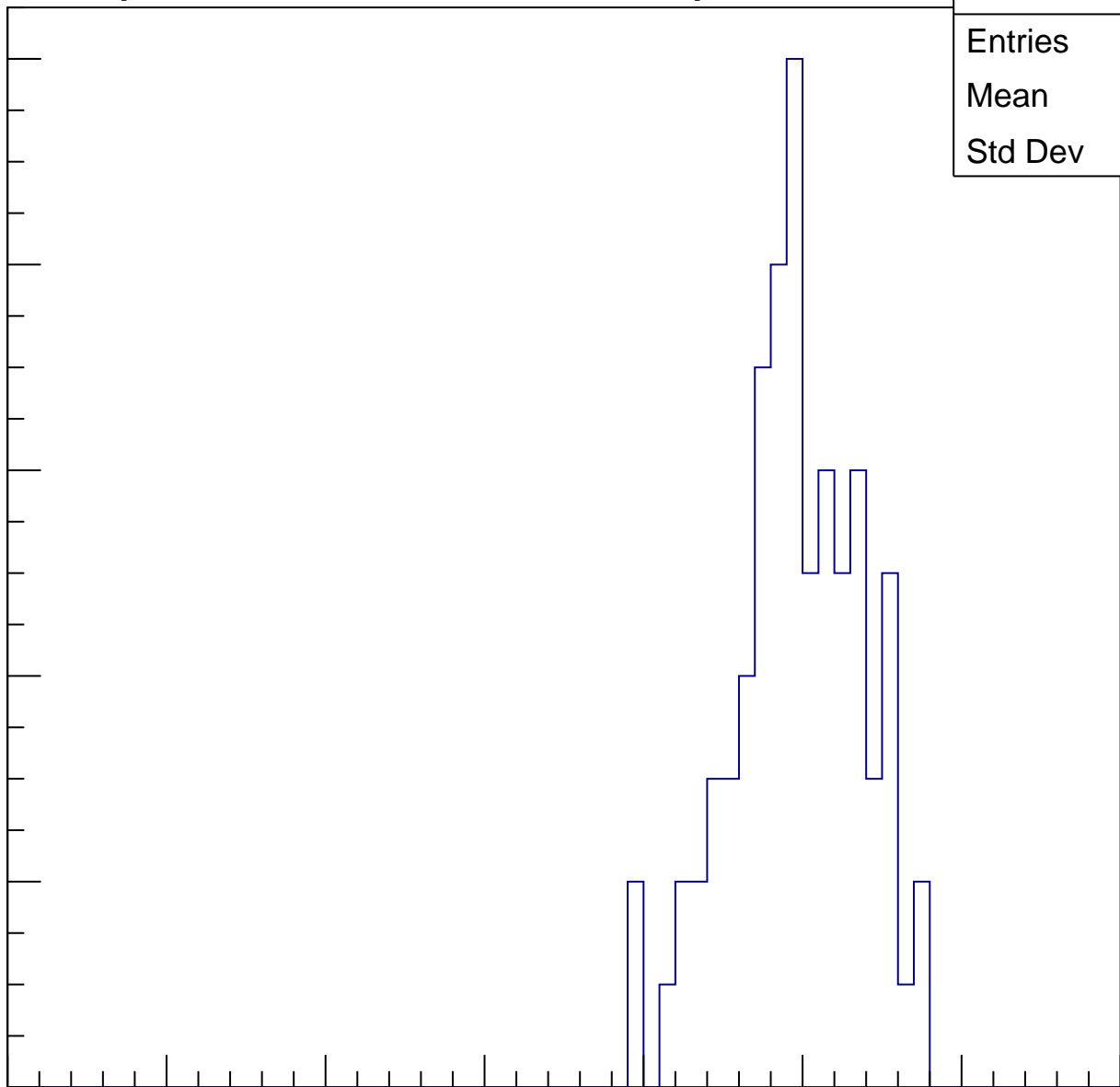
Entries	75
Mean	49.12
Std Dev	4.05

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

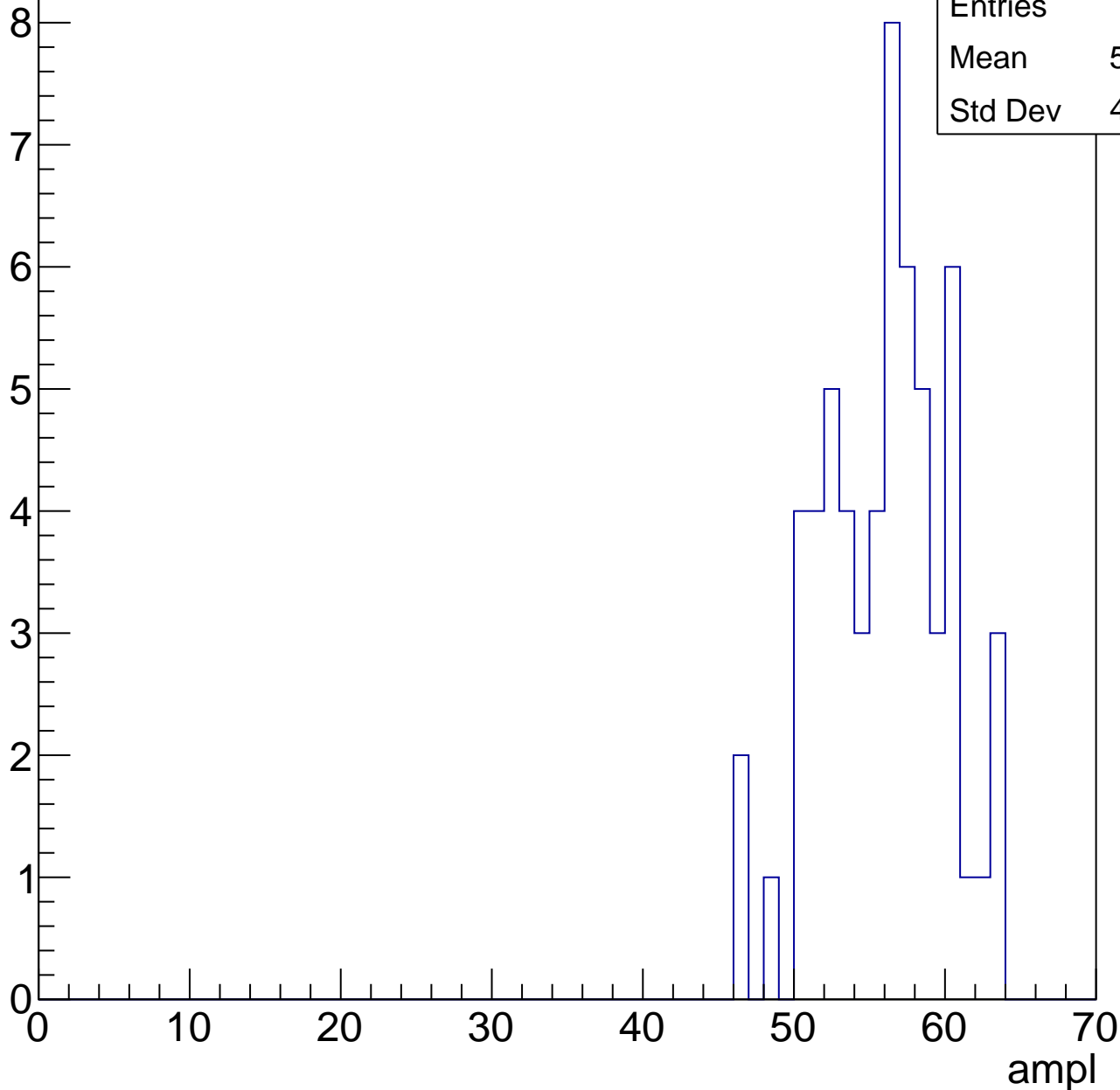


B1L102S, U18-ch69, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	55.45
Std Dev	4.039



B1L102S, U18-ch69, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

7

6

5

4

3

2

1

0

Entries	40
Mean	59.75
Std Dev	2.662

ampl

0

10

20

30

40

50

60

70

B1L102S, U18-ch69, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

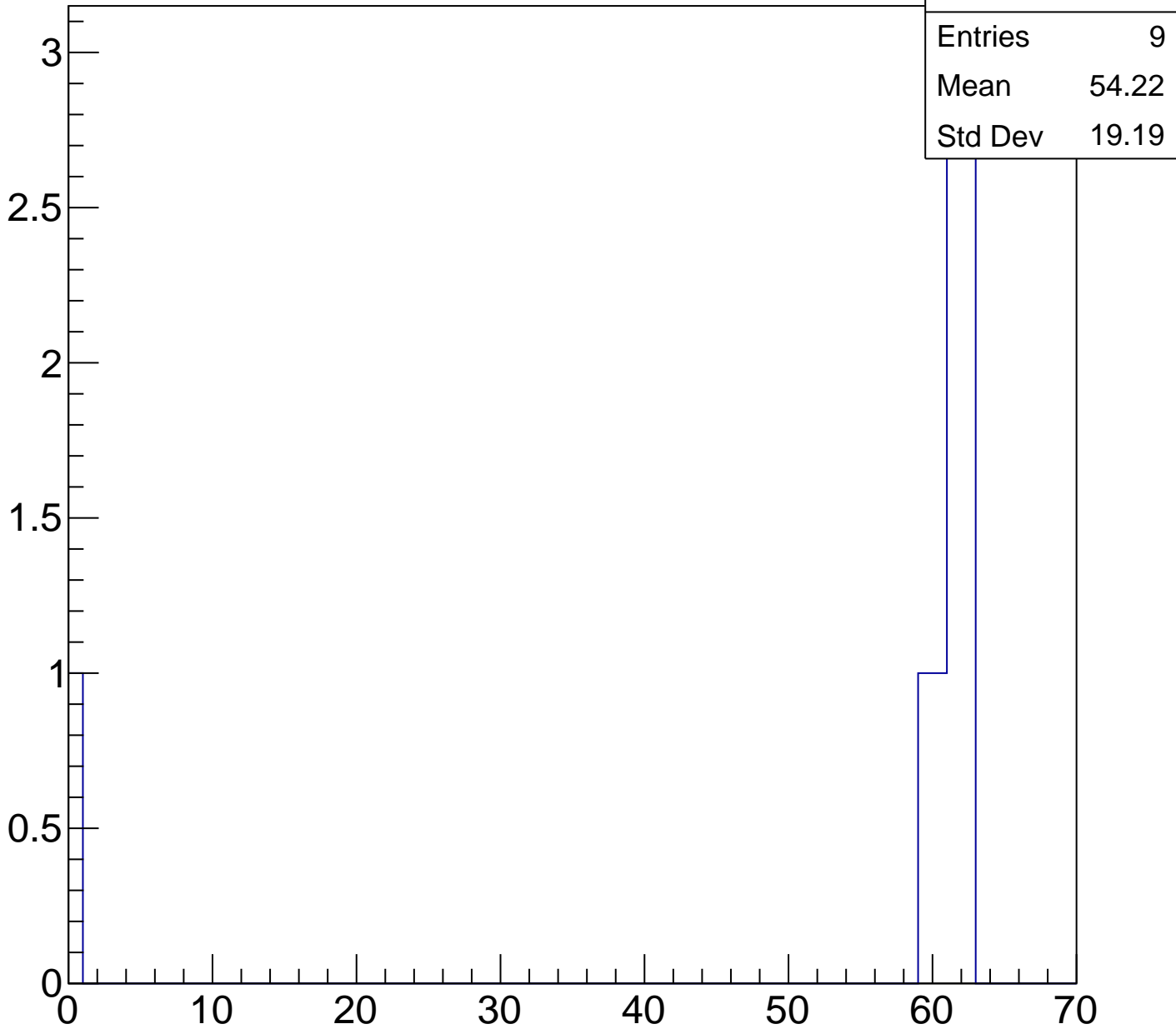
1

0.5

0

ampl

Entries	9
Mean	54.22
Std Dev	19.19



B1L102S, U18-ch69, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L102S, U18-ch70, adc0

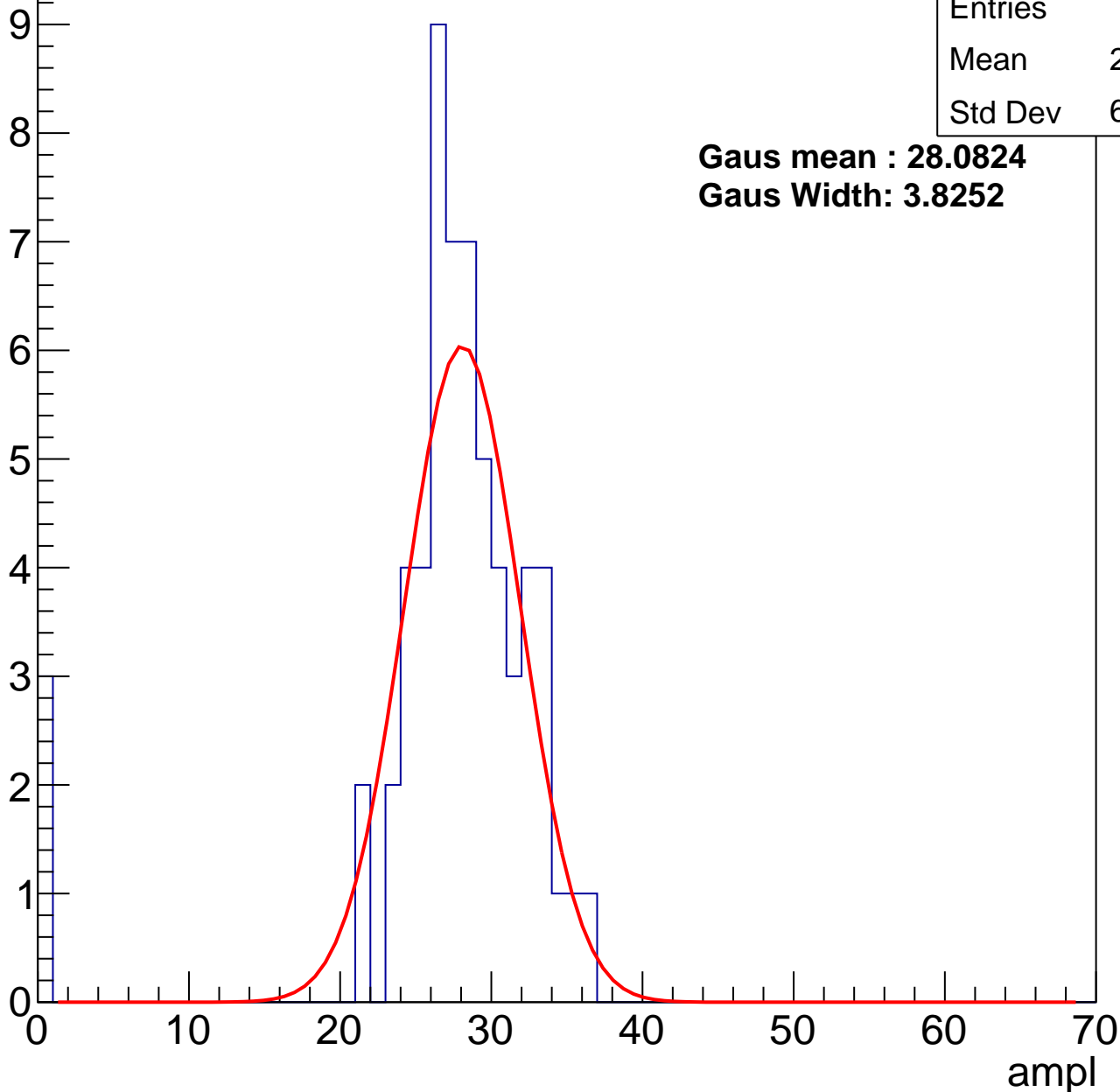
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	26.66
Std Dev	6.888

Gaus mean : 28.0824

Gaus Width: 3.8252



B1L102S, U18-ch70, adc1

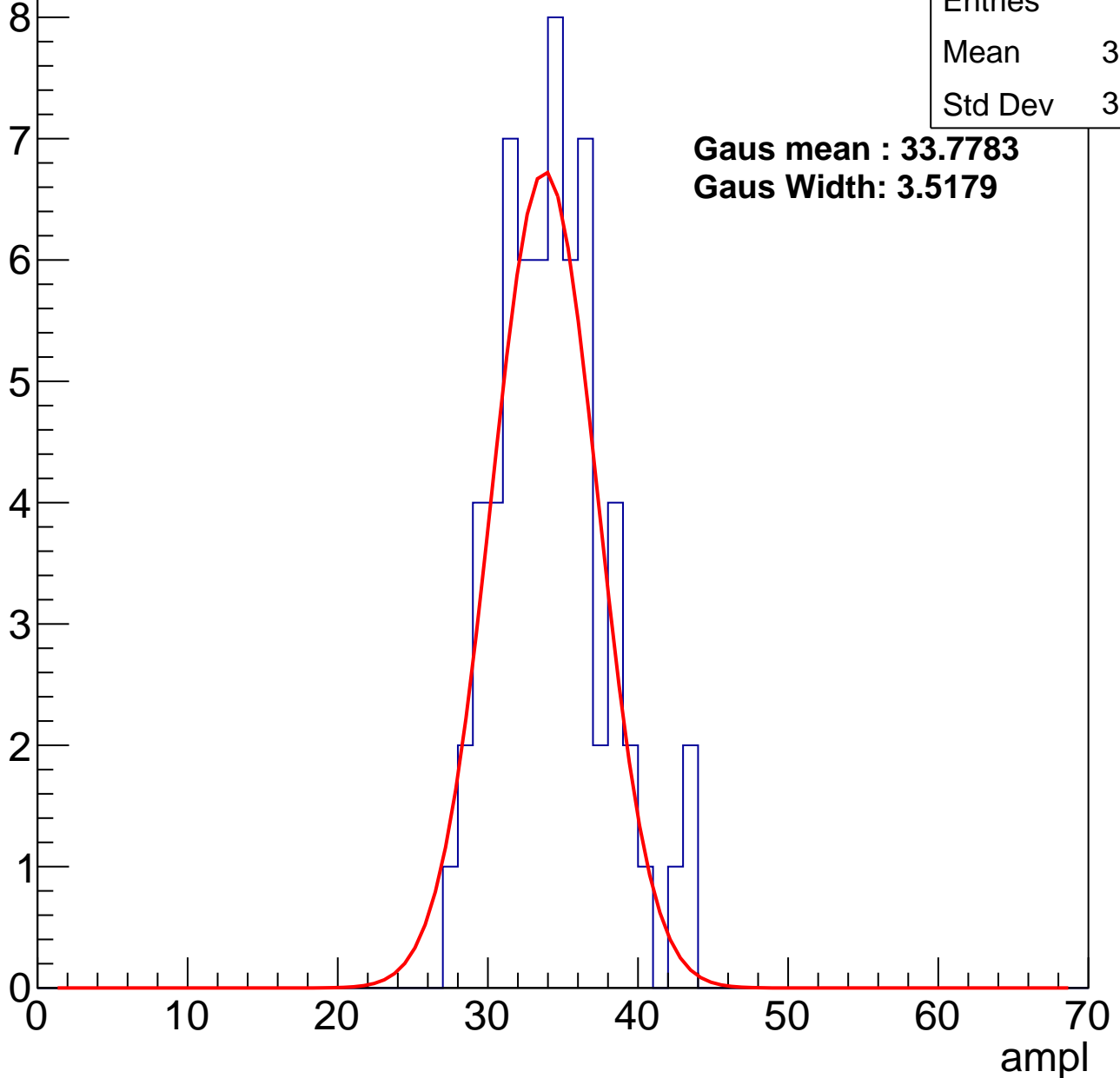
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	33.84
Std Dev	3.573

Gaus mean : 33.7783

Gaus Width: 3.5179



B1L102S, U18-ch70, adc2

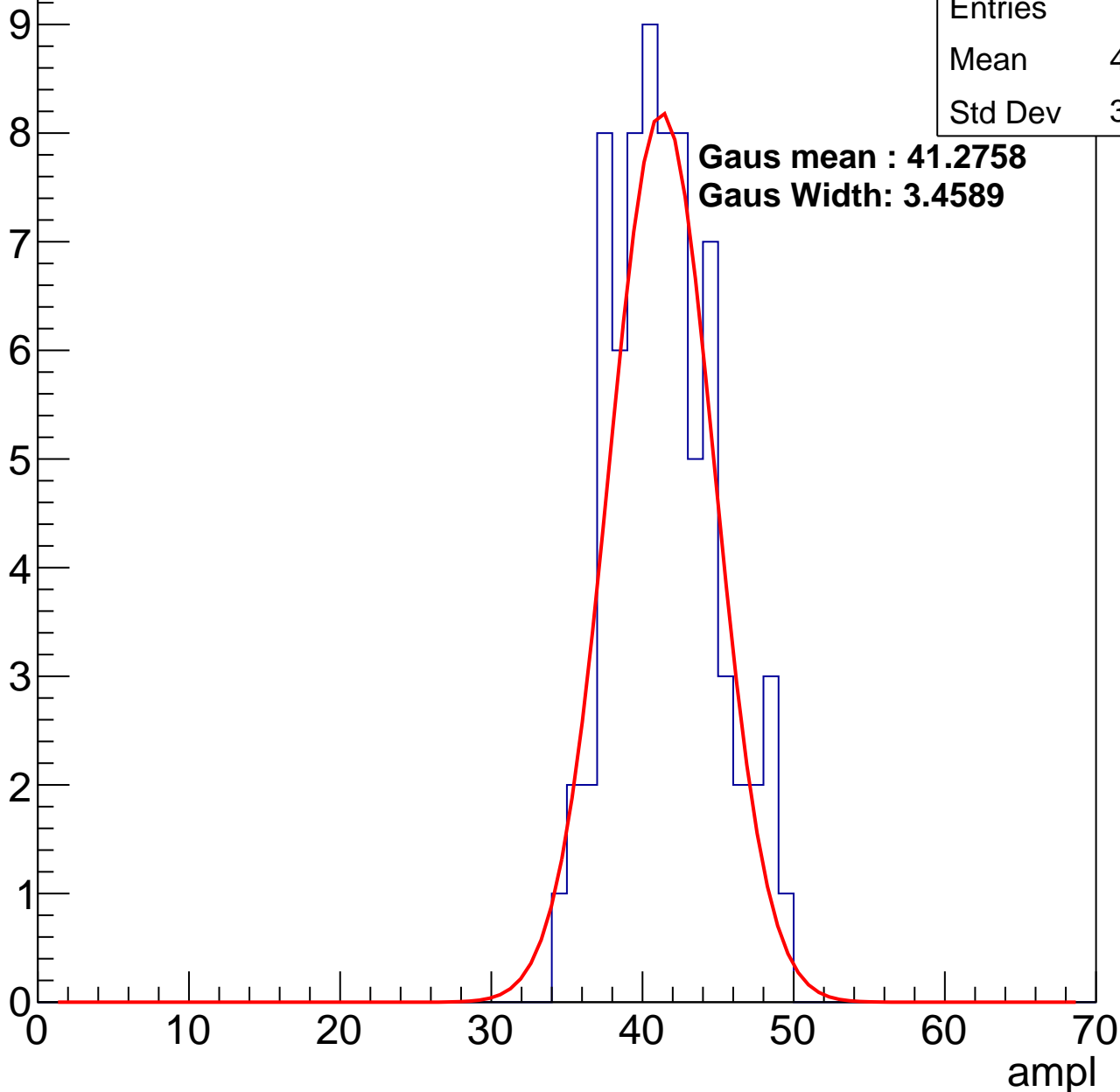
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	40.97
Std Dev	3.398

Gaus mean : 41.2758

Gaus Width: 3.4589

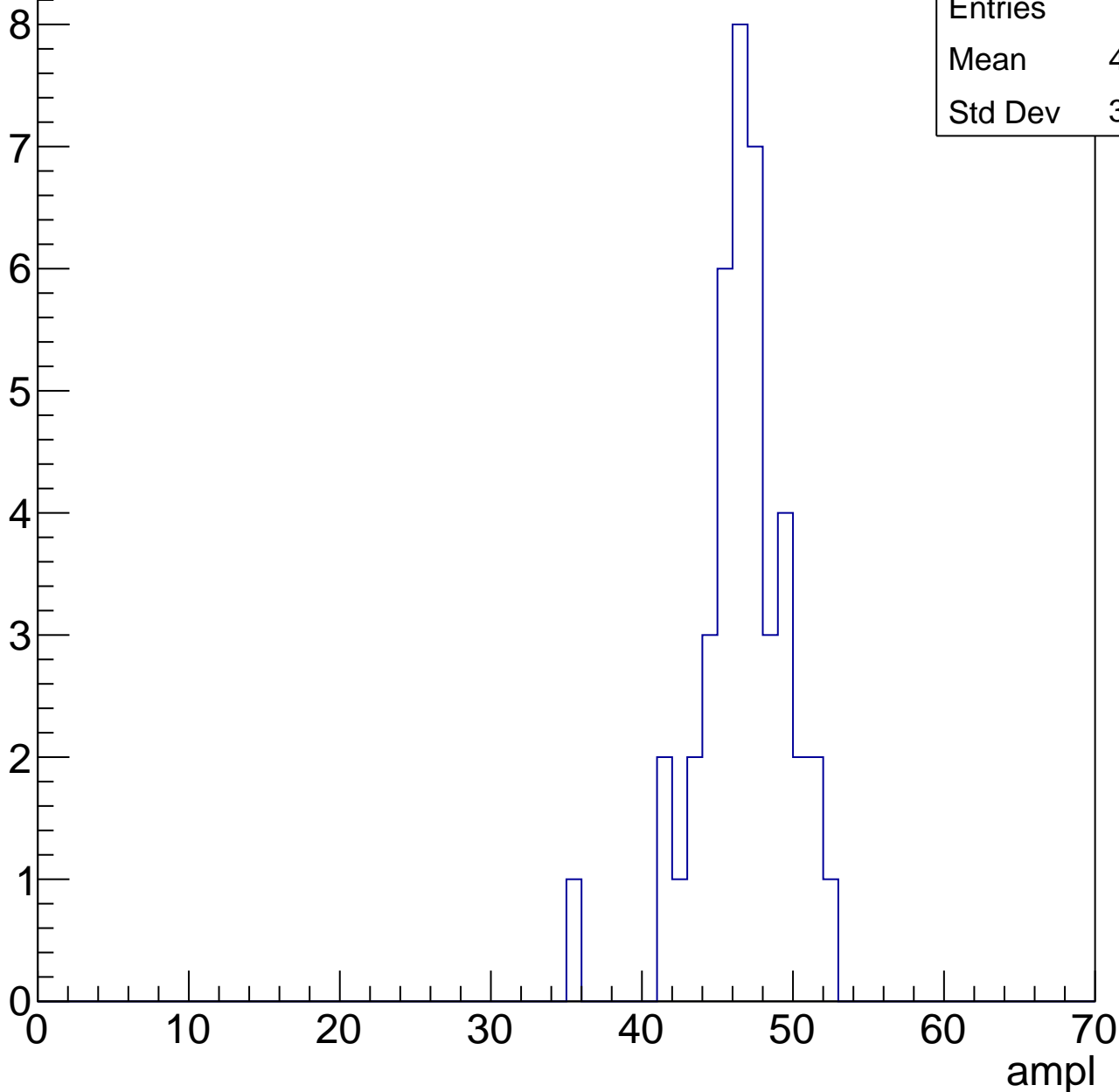


B1L102S, U18-ch70, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	46.14
Std Dev	3.067

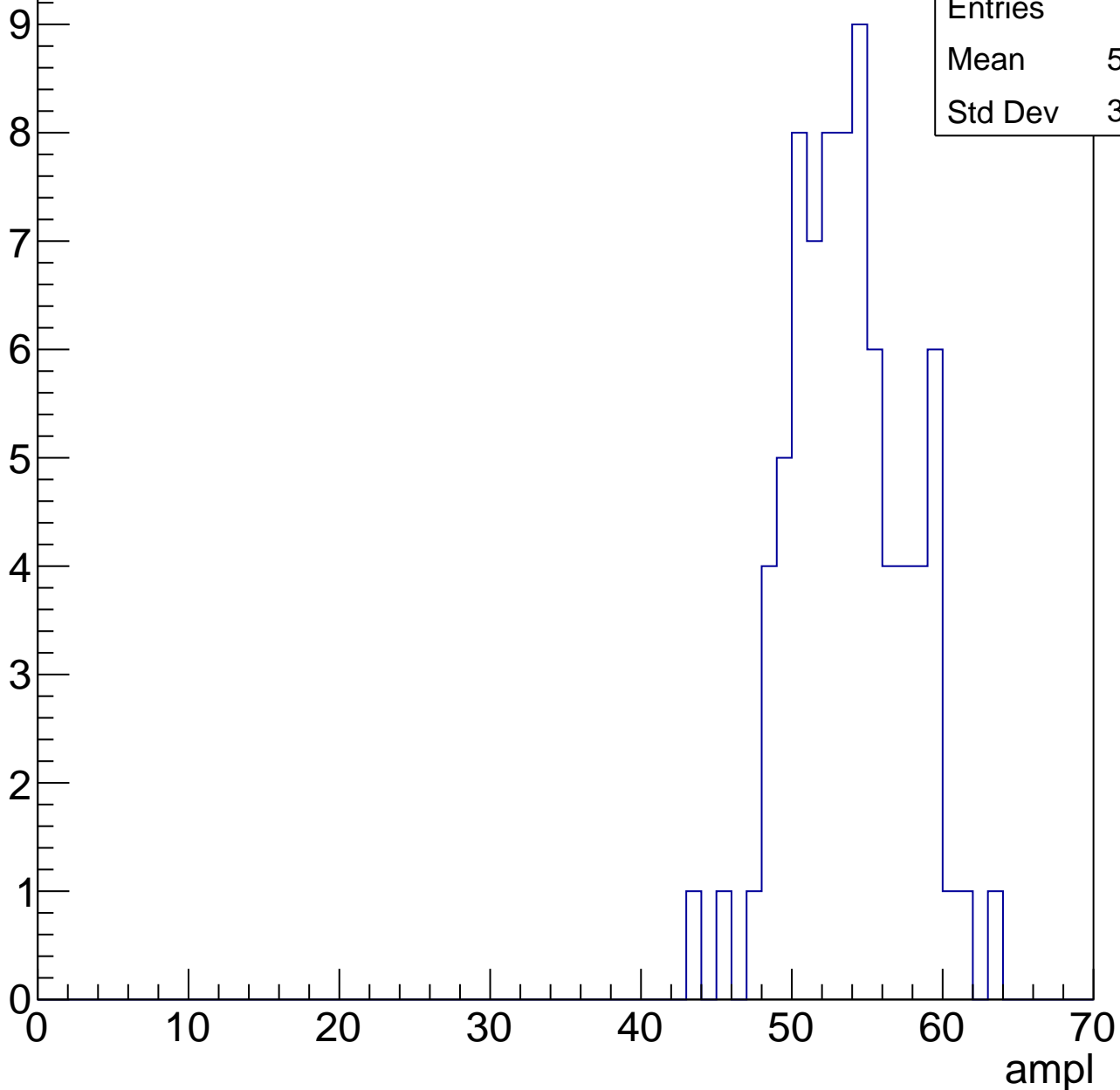


B1L102S, U18-ch70, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	53.25
Std Dev	3.807

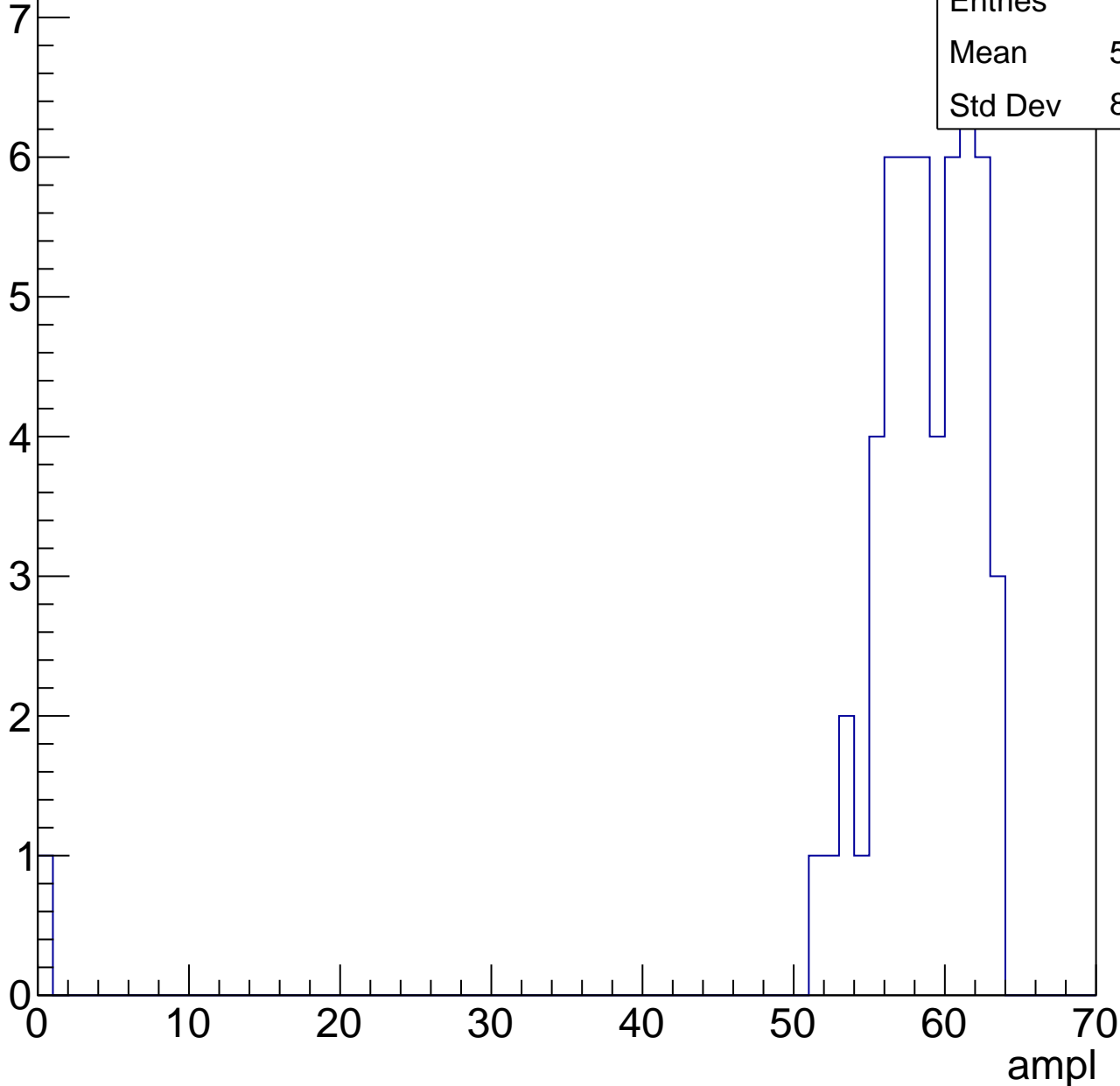


B1L102S, U18-ch70, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.28
Std Dev	8.405



B1L102S, U18-ch70, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

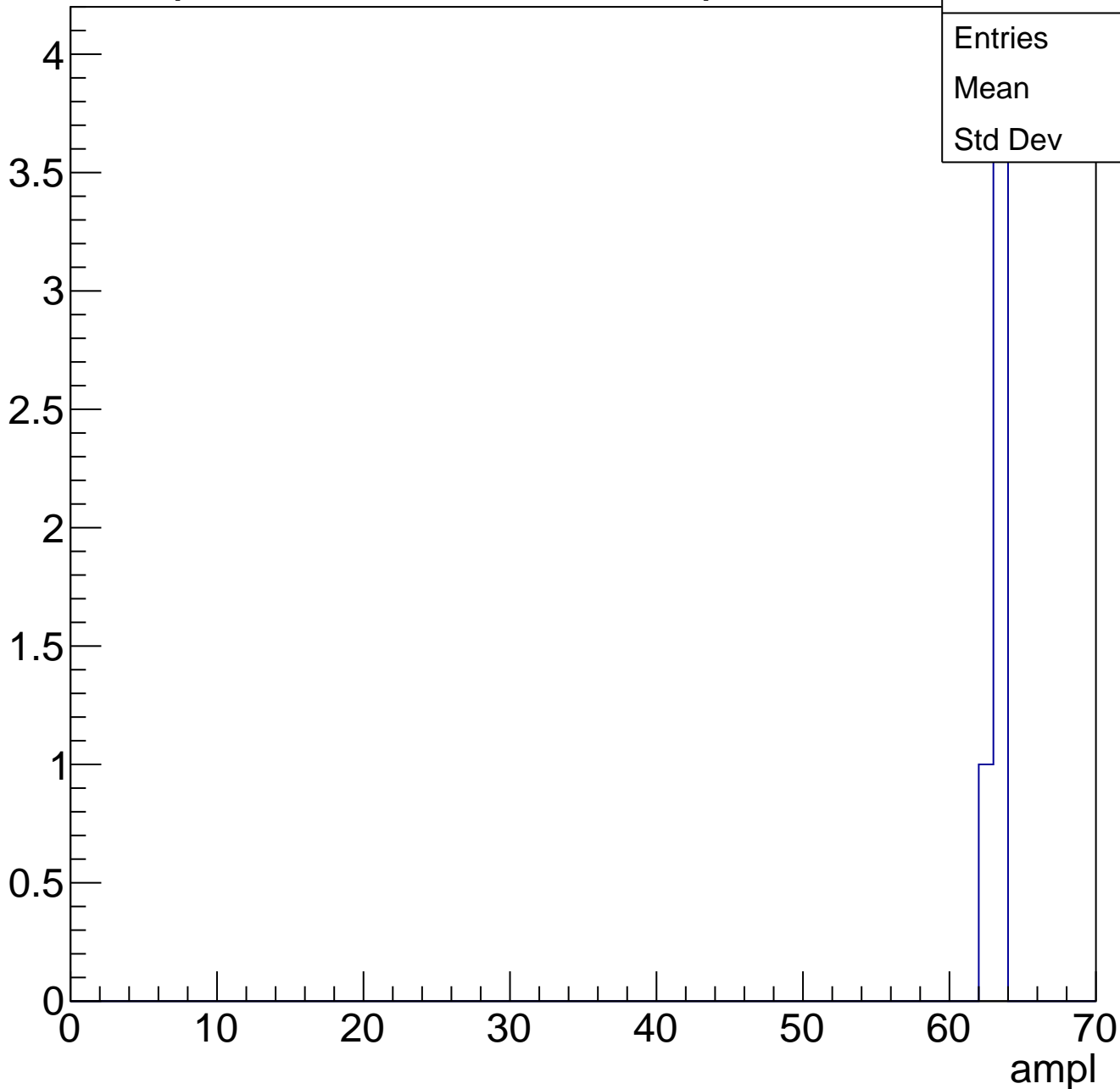
ampl

Entries	10
Mean	61.4
Std Dev	1.114

B1L102S, U18-ch70, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch71, adc0

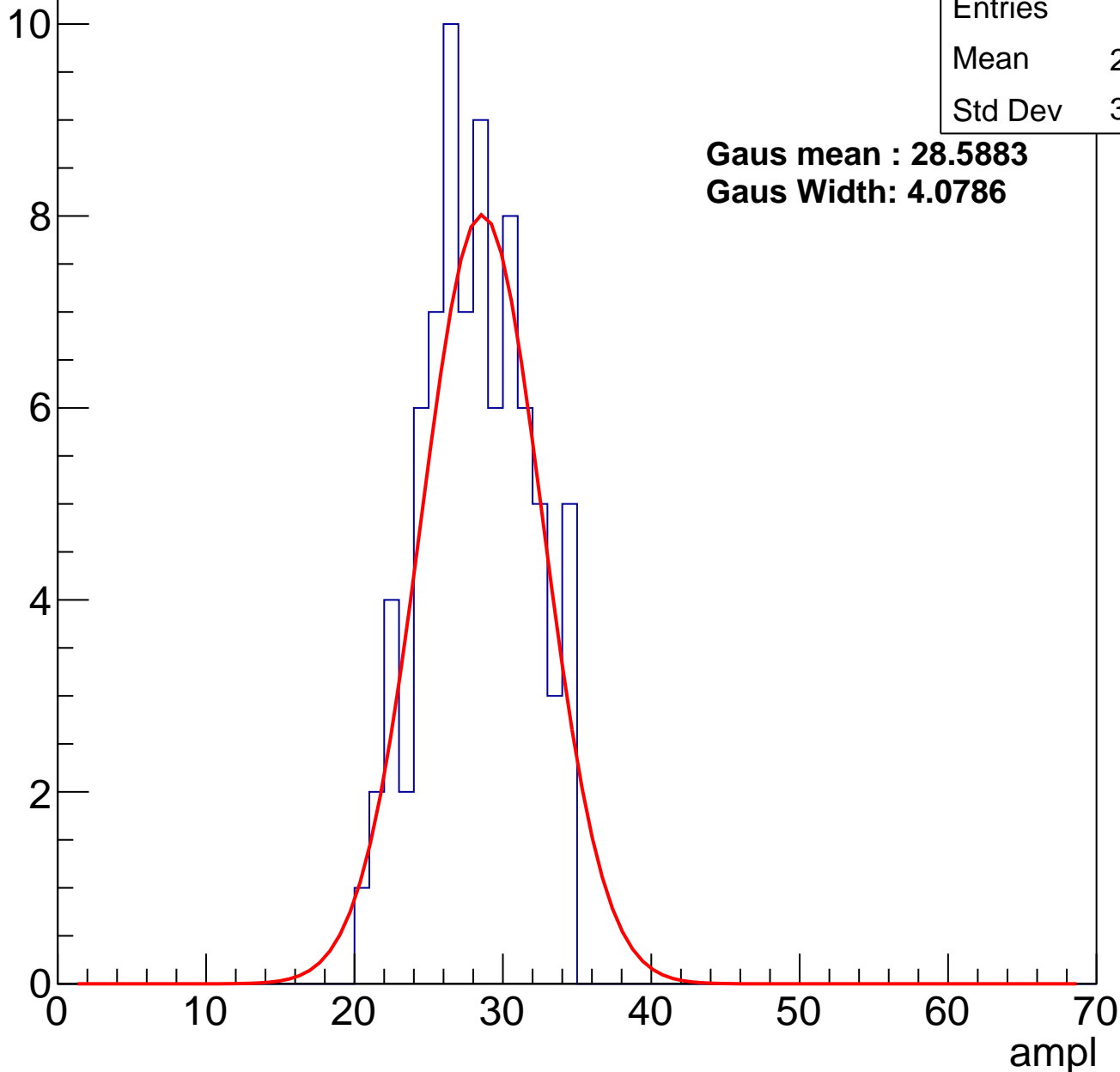
calib_packv5_042523_0143.root, FC#11, port A2

Entries	81
Mean	27.72
Std Dev	3.483

Gaus mean : 28.5883

Gaus Width: 4.0786

Entry



B1L102S, U18-ch71, adc1

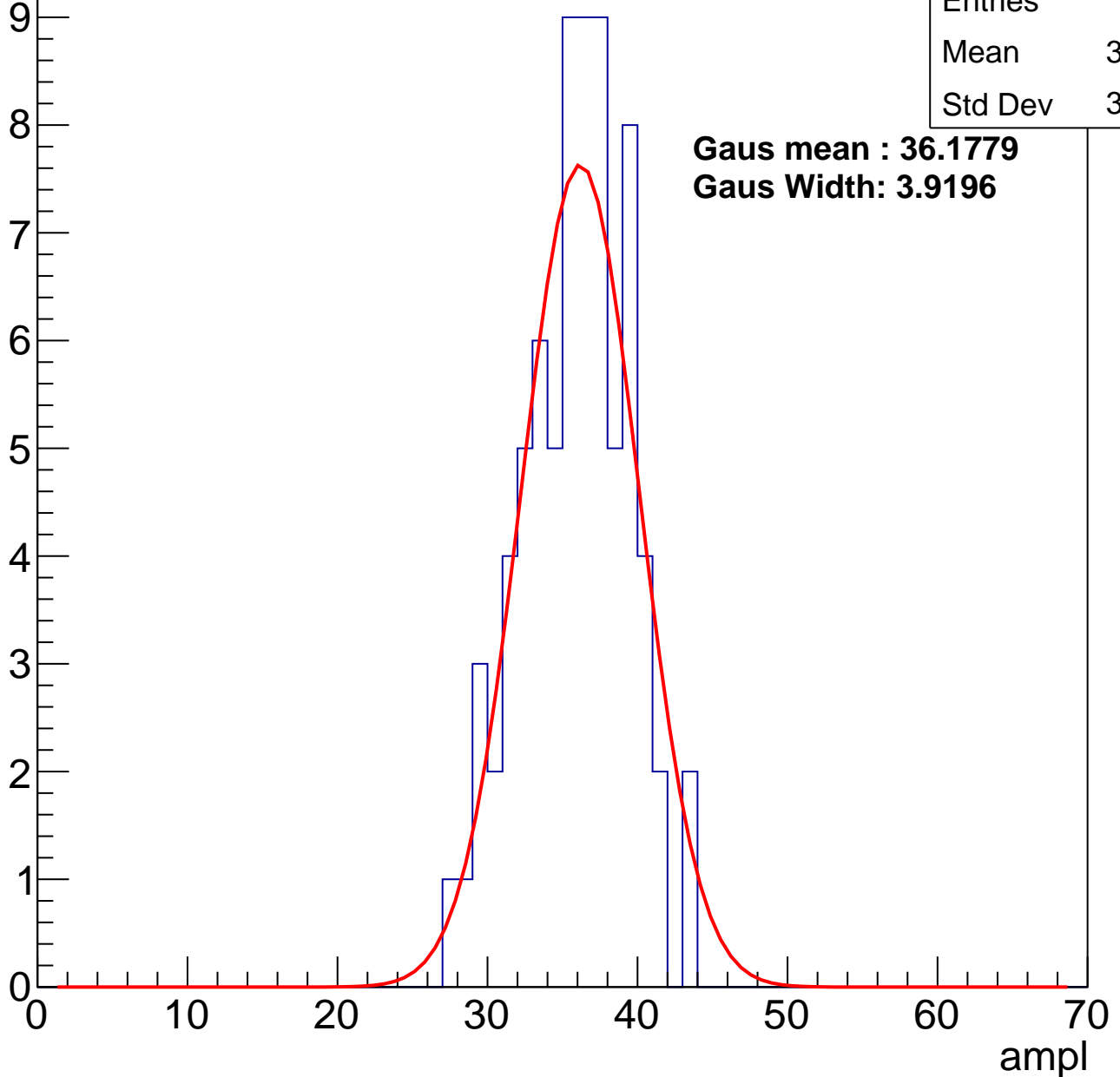
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.41
Std Dev	3.491

Gaus mean : 36.1779

Gaus Width: 3.9196



B1L102S, U18-ch71, adc2

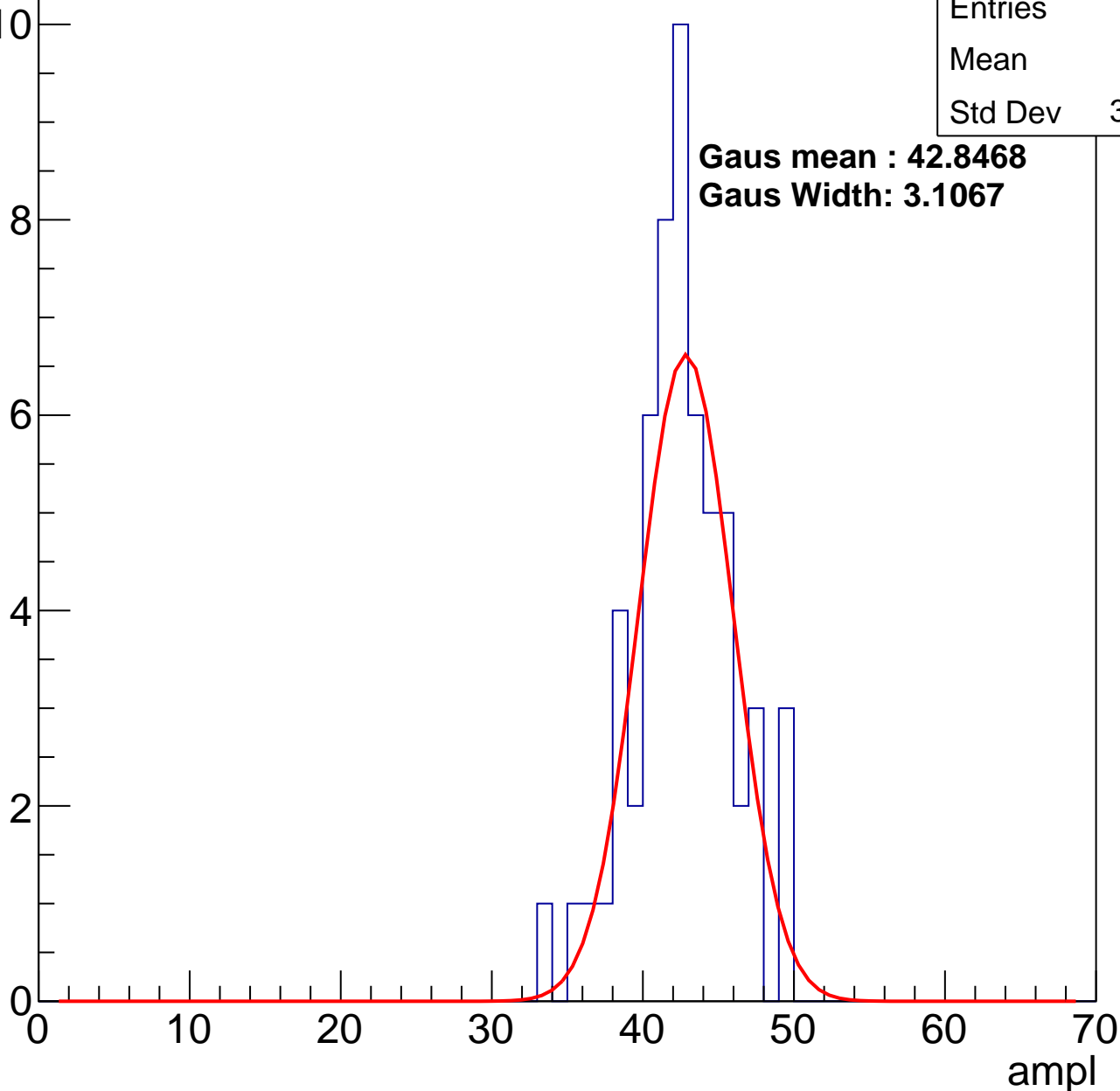
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	42.1
Std Dev	3.294

Gaus mean : 42.8468

Gaus Width: 3.1067

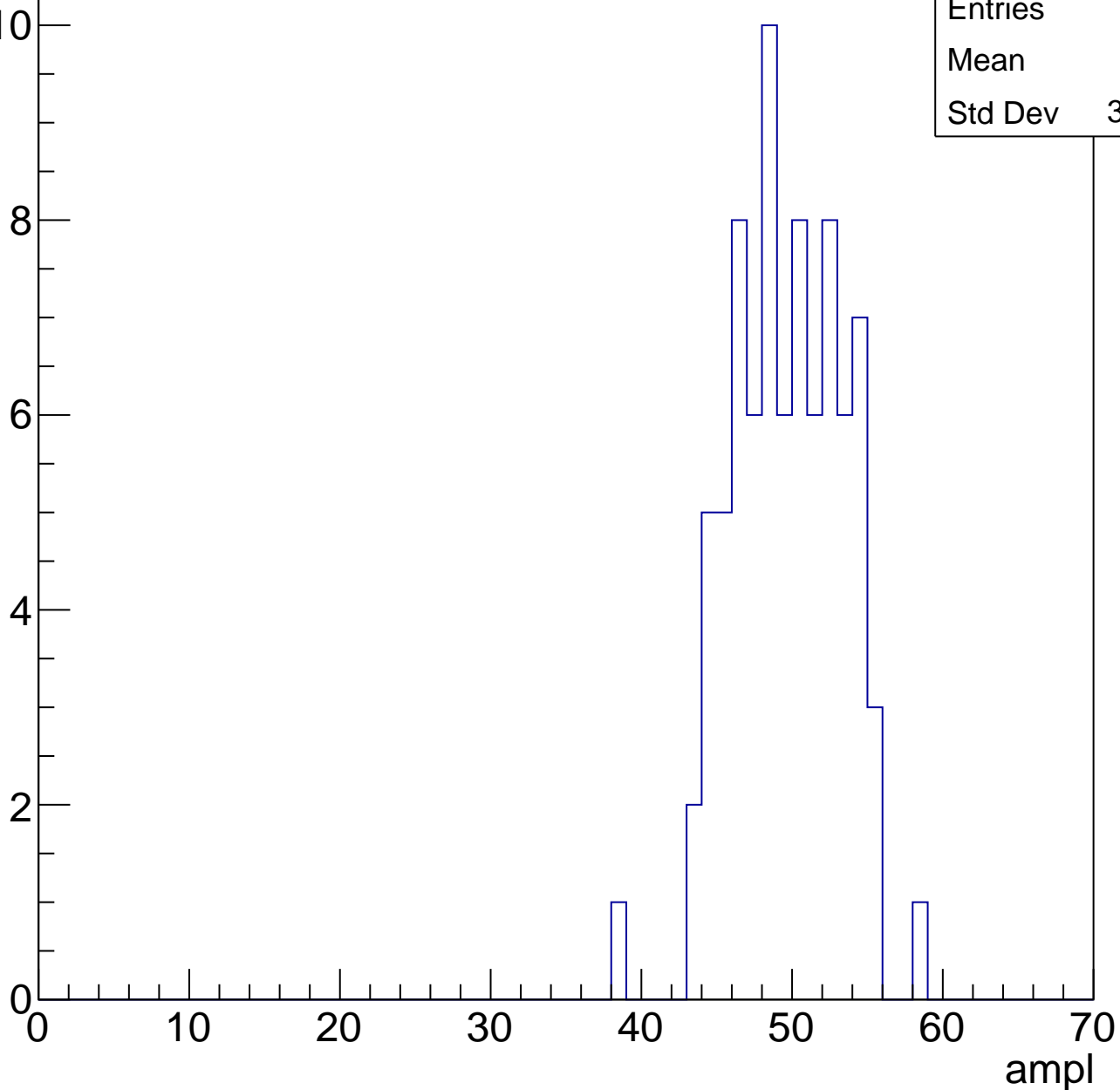


B1L102S, U18-ch71, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	49.2
Std Dev	3.604

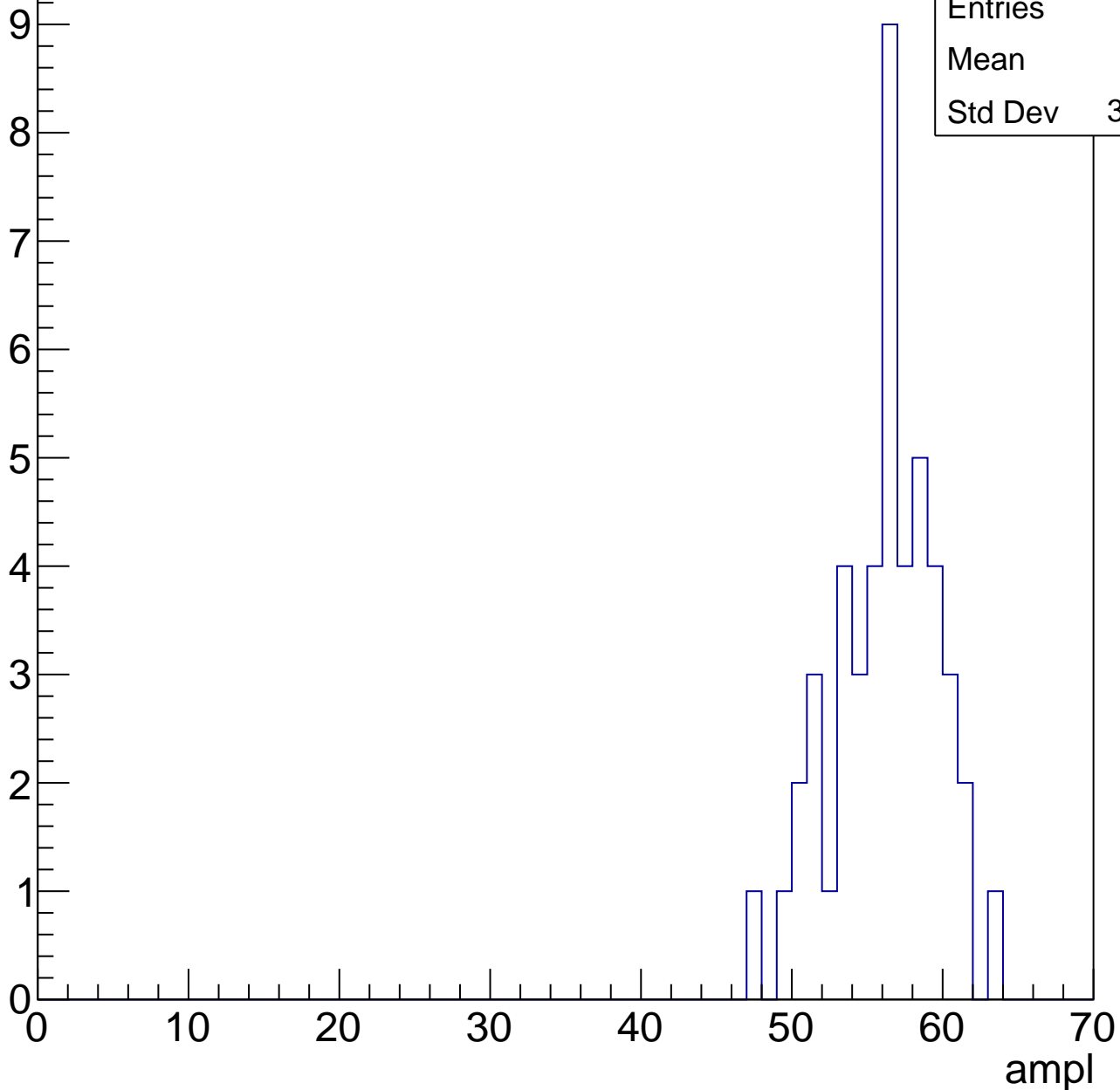


B1L102S, U18-ch71, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	55.7
Std Dev	3.414

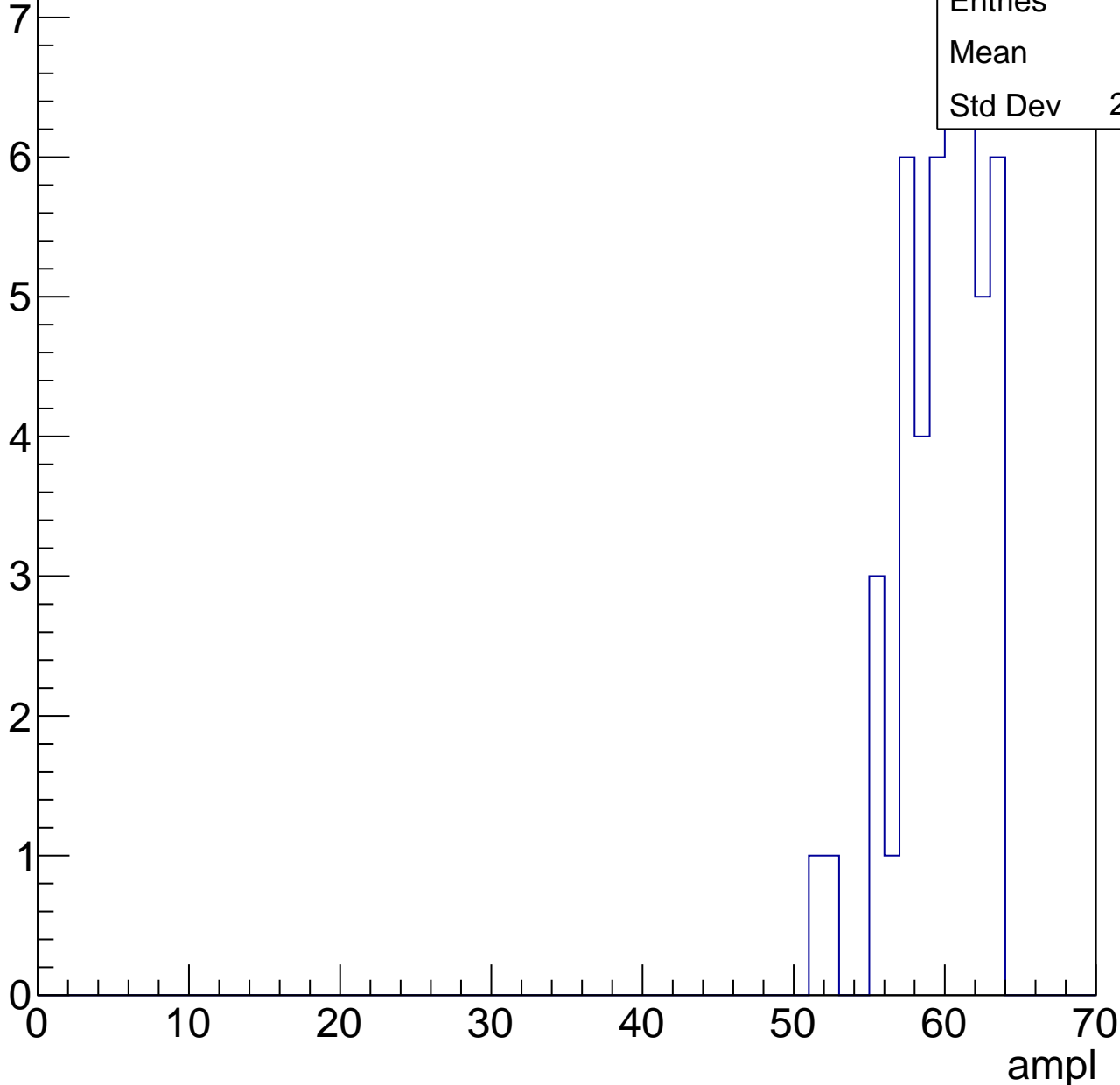


B1L102S, U18-ch71, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	59.3
Std Dev	2.805

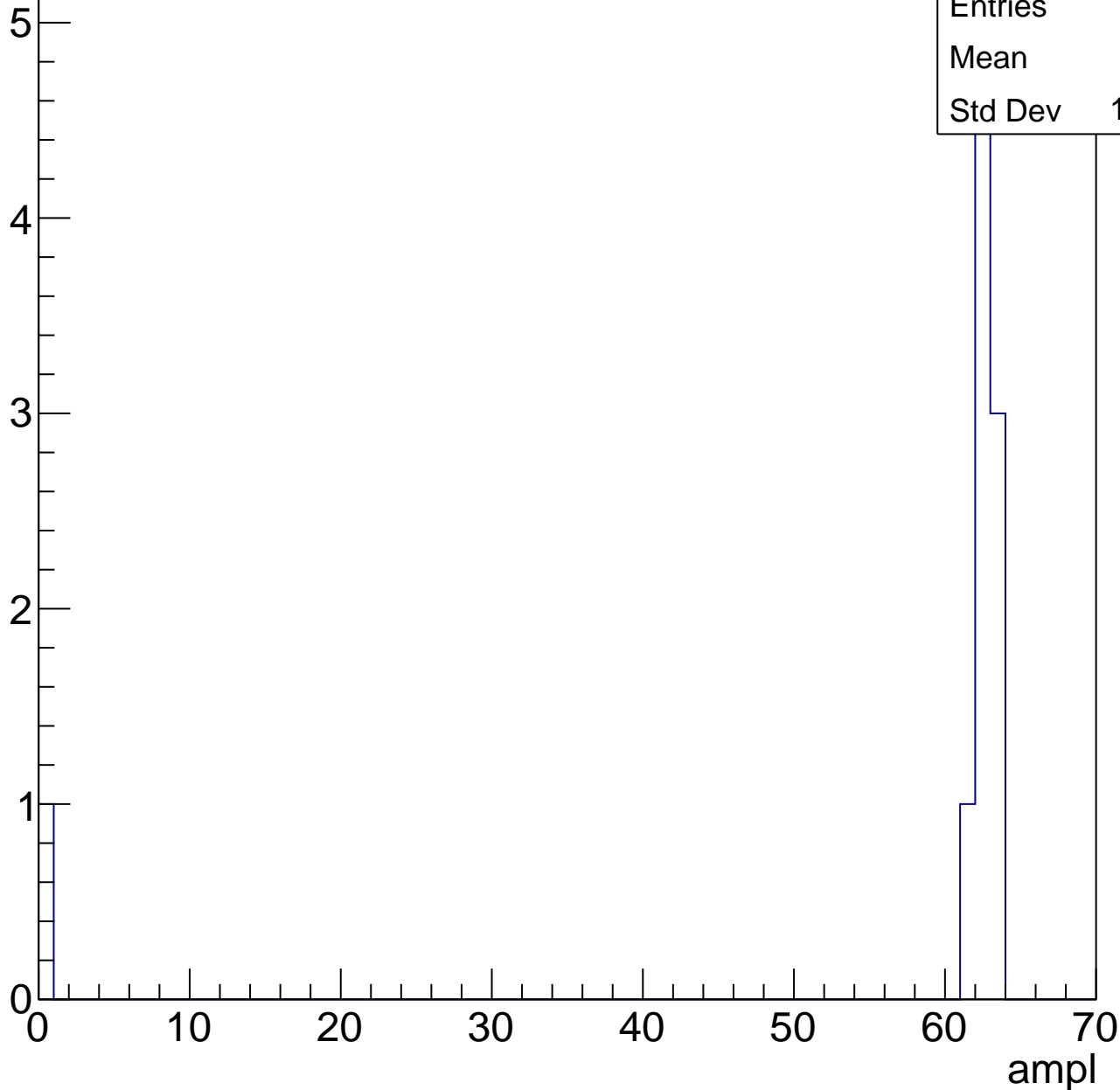


B1L102S, U18-ch71, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

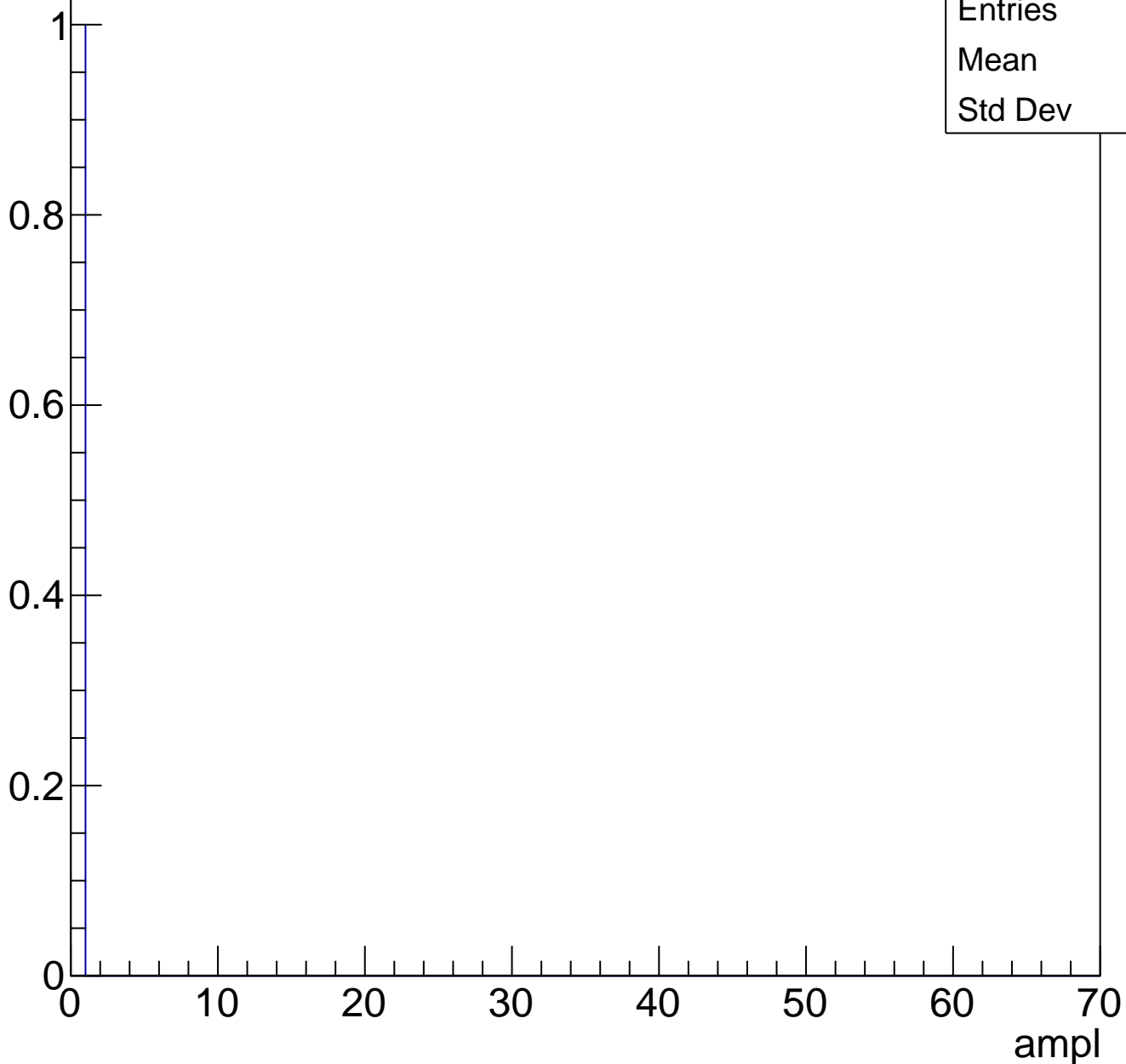
Entries	10
Mean	56
Std Dev	18.68



B1L102S, U18-ch71, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch72, adc0

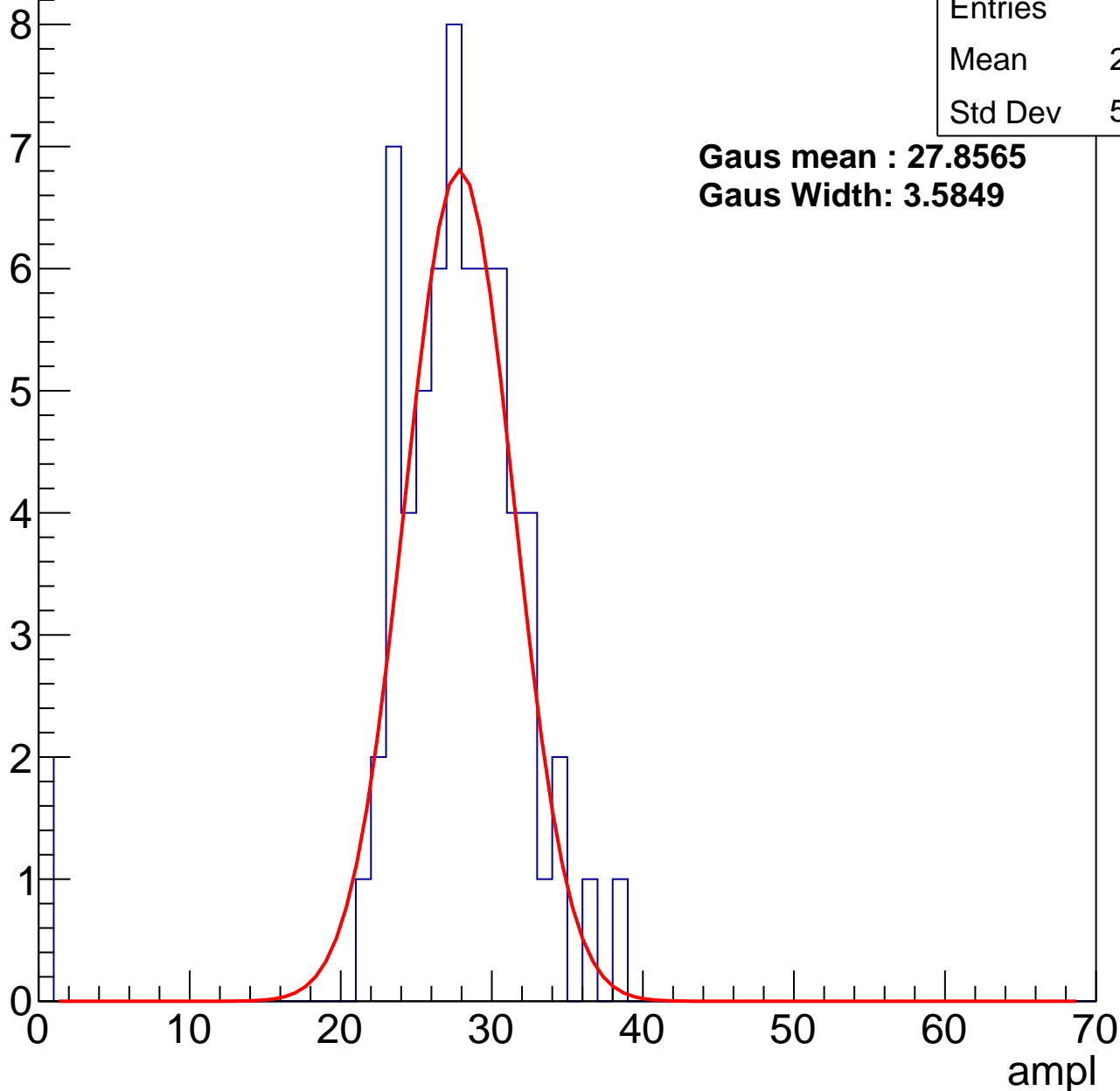
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	26.79
Std Dev	5.902

Gaus mean : 27.8565

Gaus Width: 3.5849



B1L102S, U18-ch72, adc1

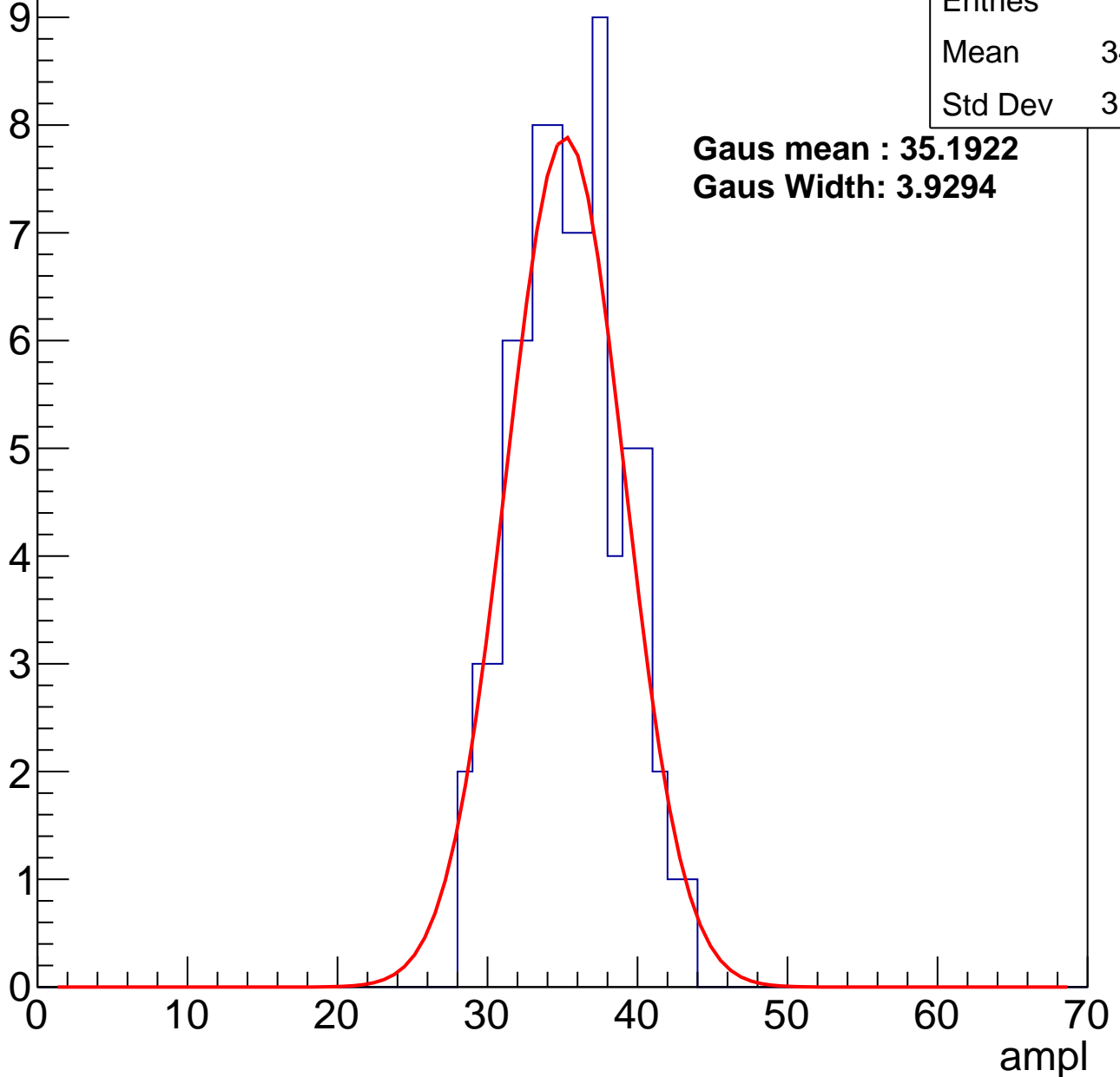
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	34.95
Std Dev	3.494

Gaus mean : 35.1922

Gaus Width: 3.9294



B1L102S, U18-ch72, adc2

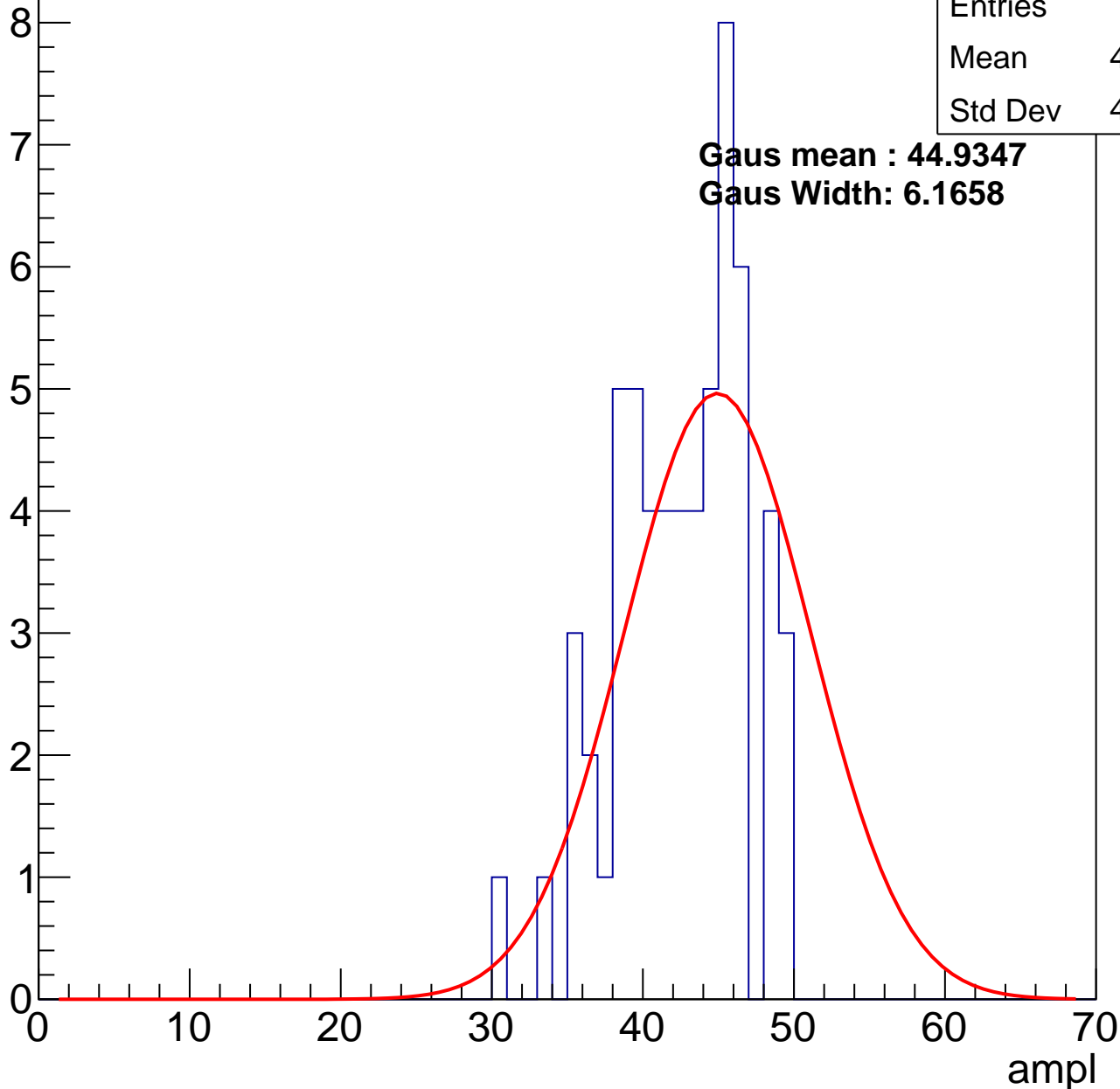
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	42.02
Std Dev	4.276

Gaus mean : 44.9347

Gaus Width: 6.1658



B1L102S, U18-ch72, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	66
Mean	47.35
Std Dev	3.879

Entry

10

8

6

4

2

0

0

10

20

30

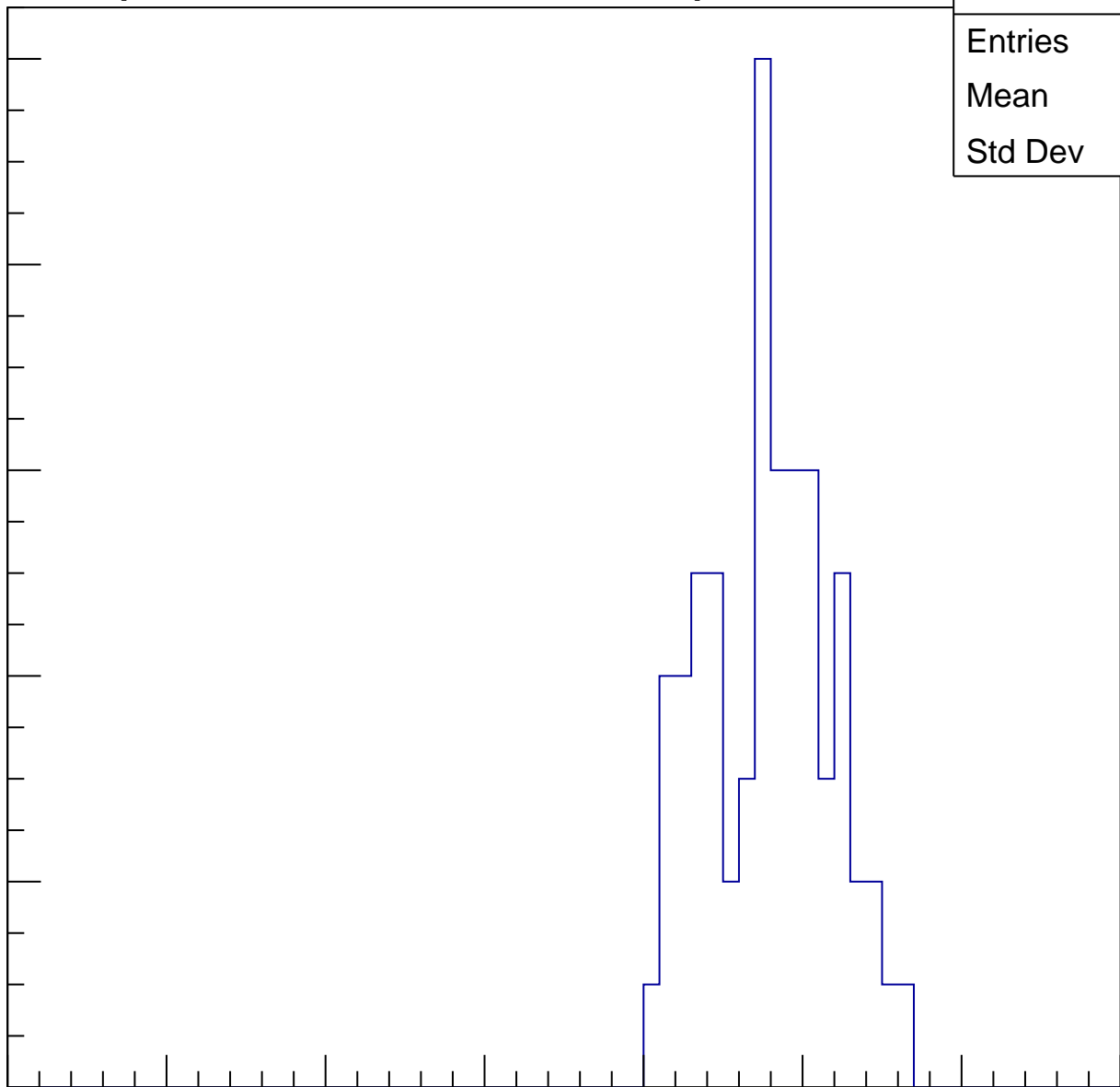
40

50

60

70

ampl

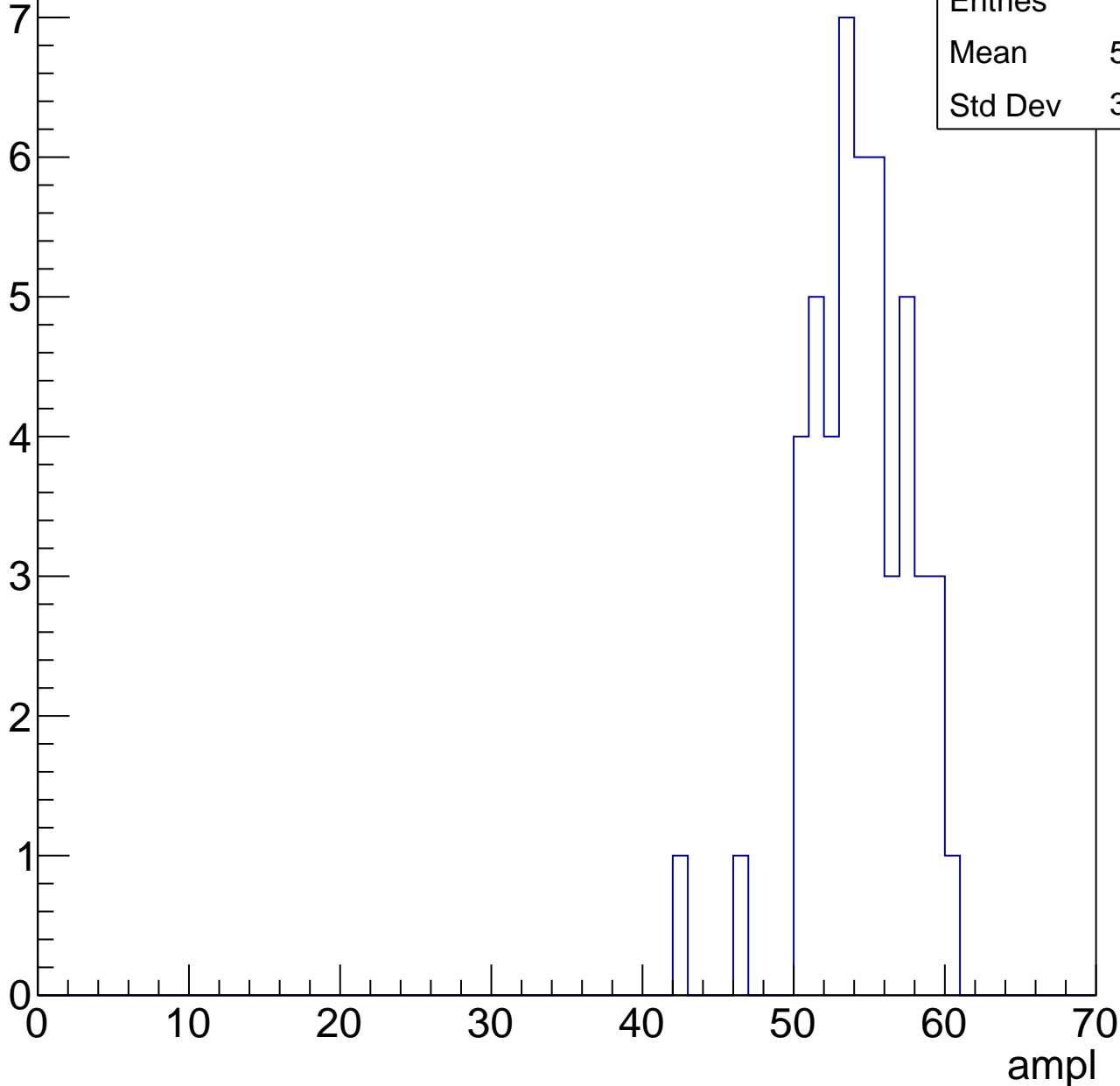


B1L102S, U18-ch72, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	53.88
Std Dev	3.384

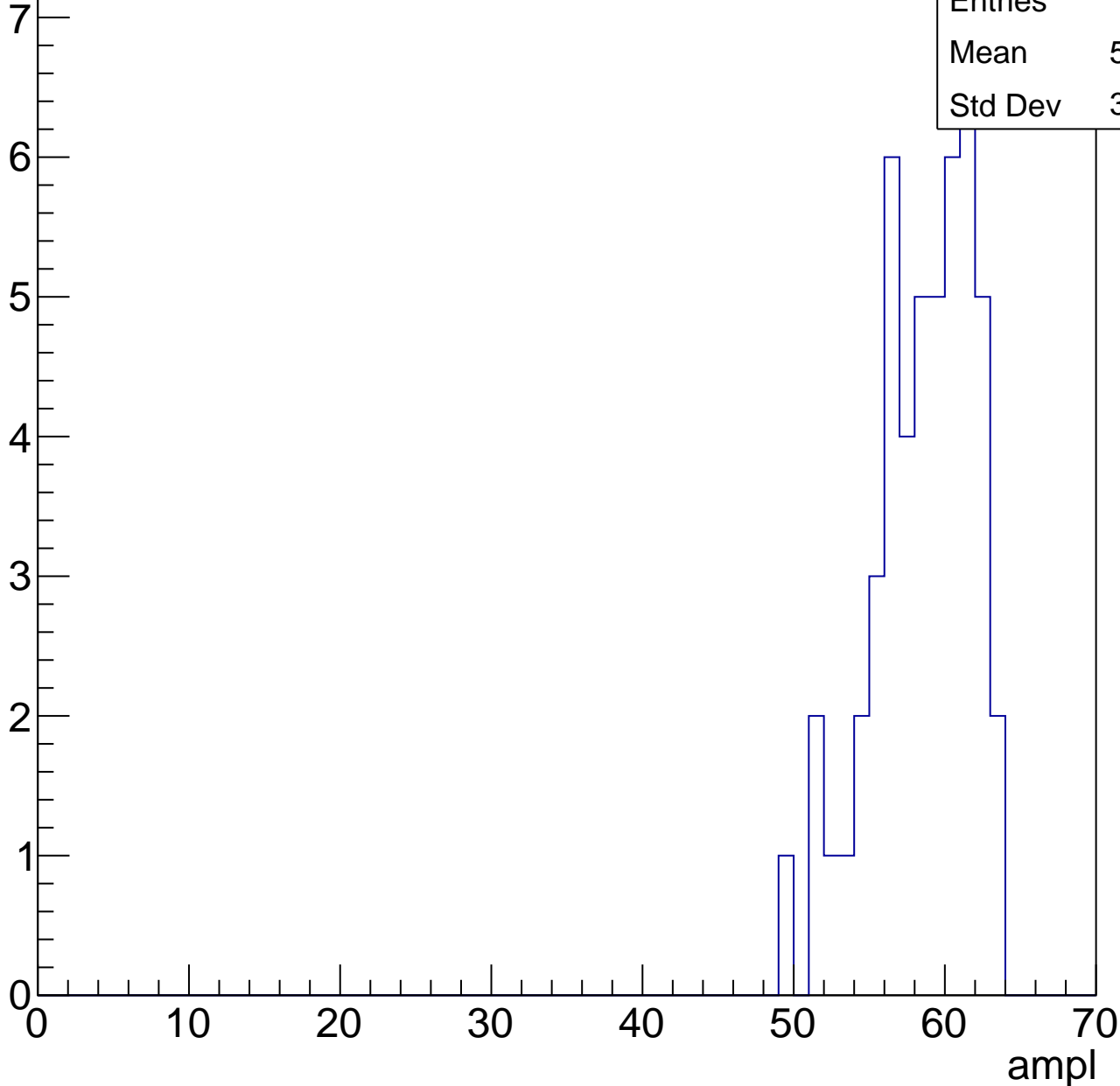


B1L102S, U18-ch72, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

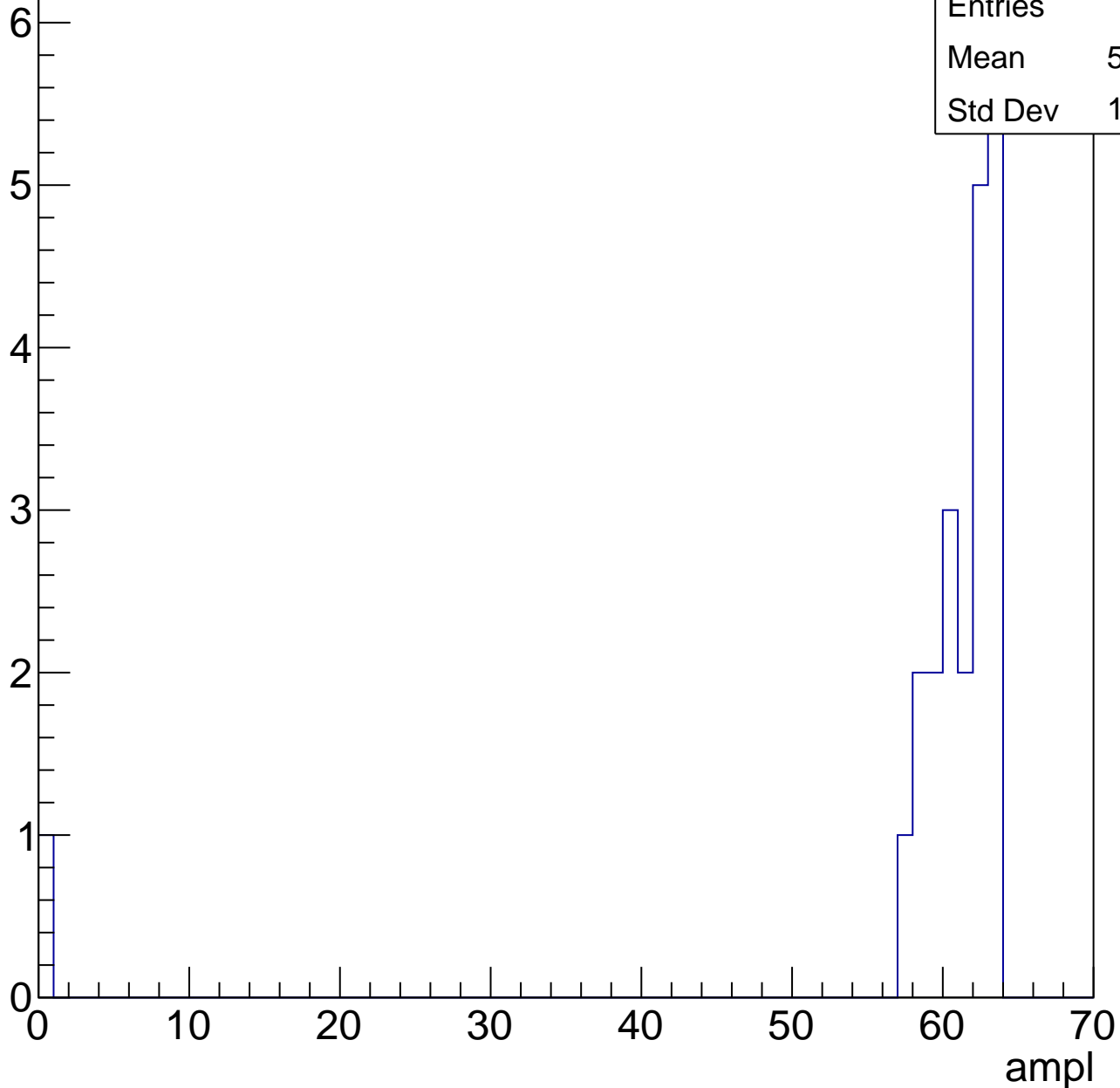
Entries	50
Mean	58.02
Std Dev	3.314



B1L102S, U18-ch72, adc6

calib_packv5_042523_0143.root, FC#11, port A2

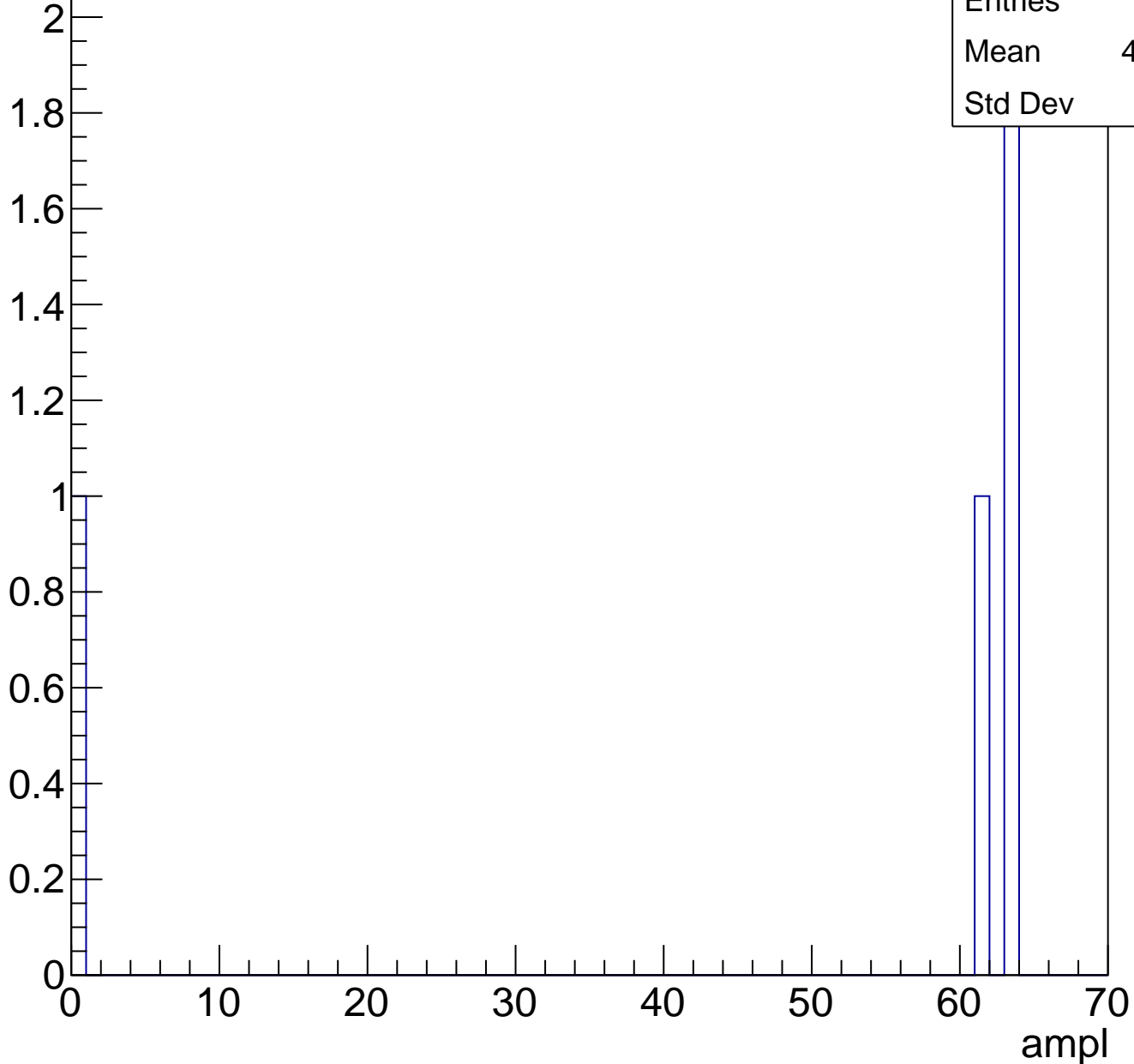
Entry



B1L102S, U18-ch72, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch73, adc0

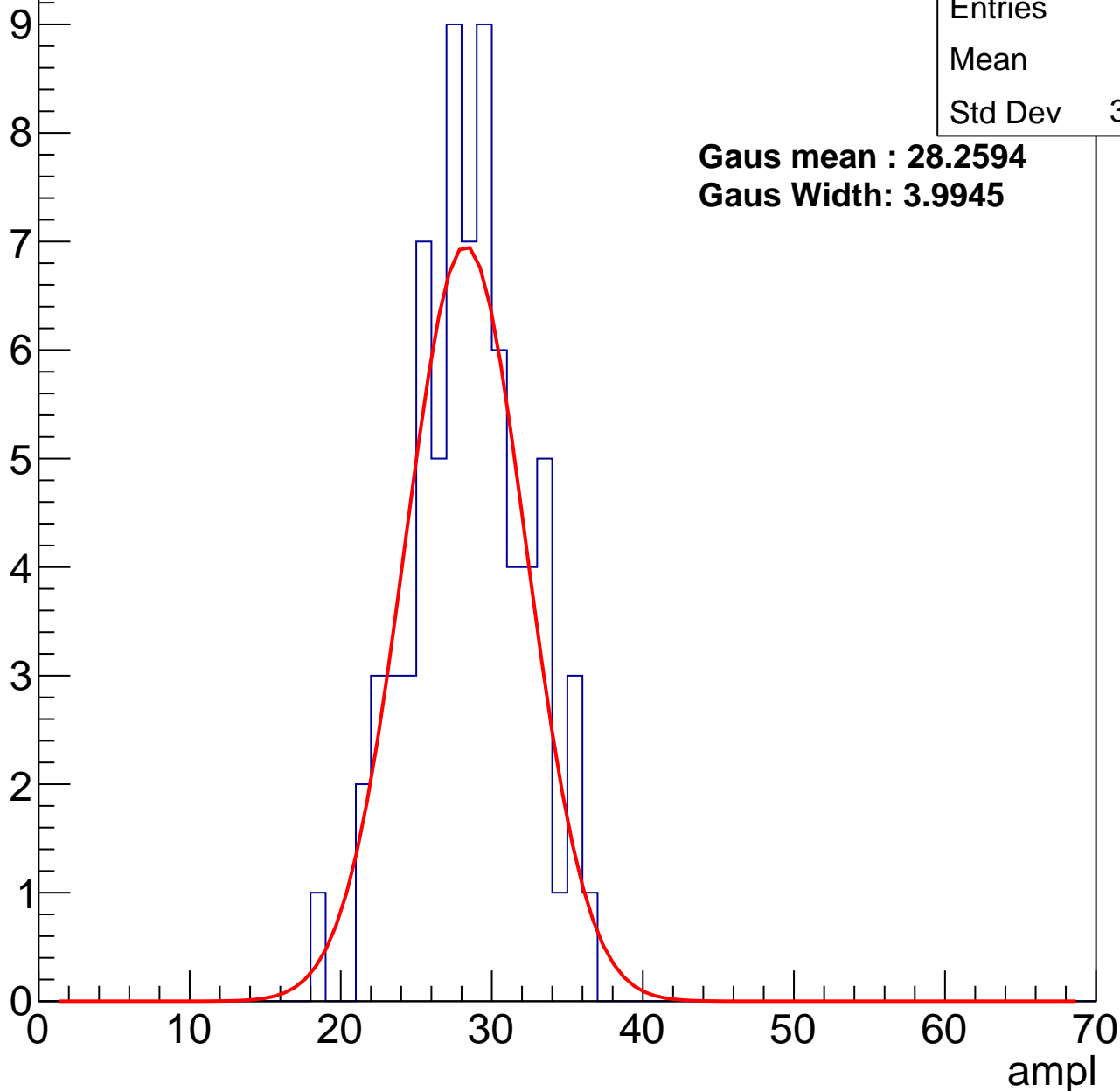
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28
Std Dev	3.749

Gaus mean : 28.2594

Gaus Width: 3.9945



B1L102S, U18-ch73, adc1

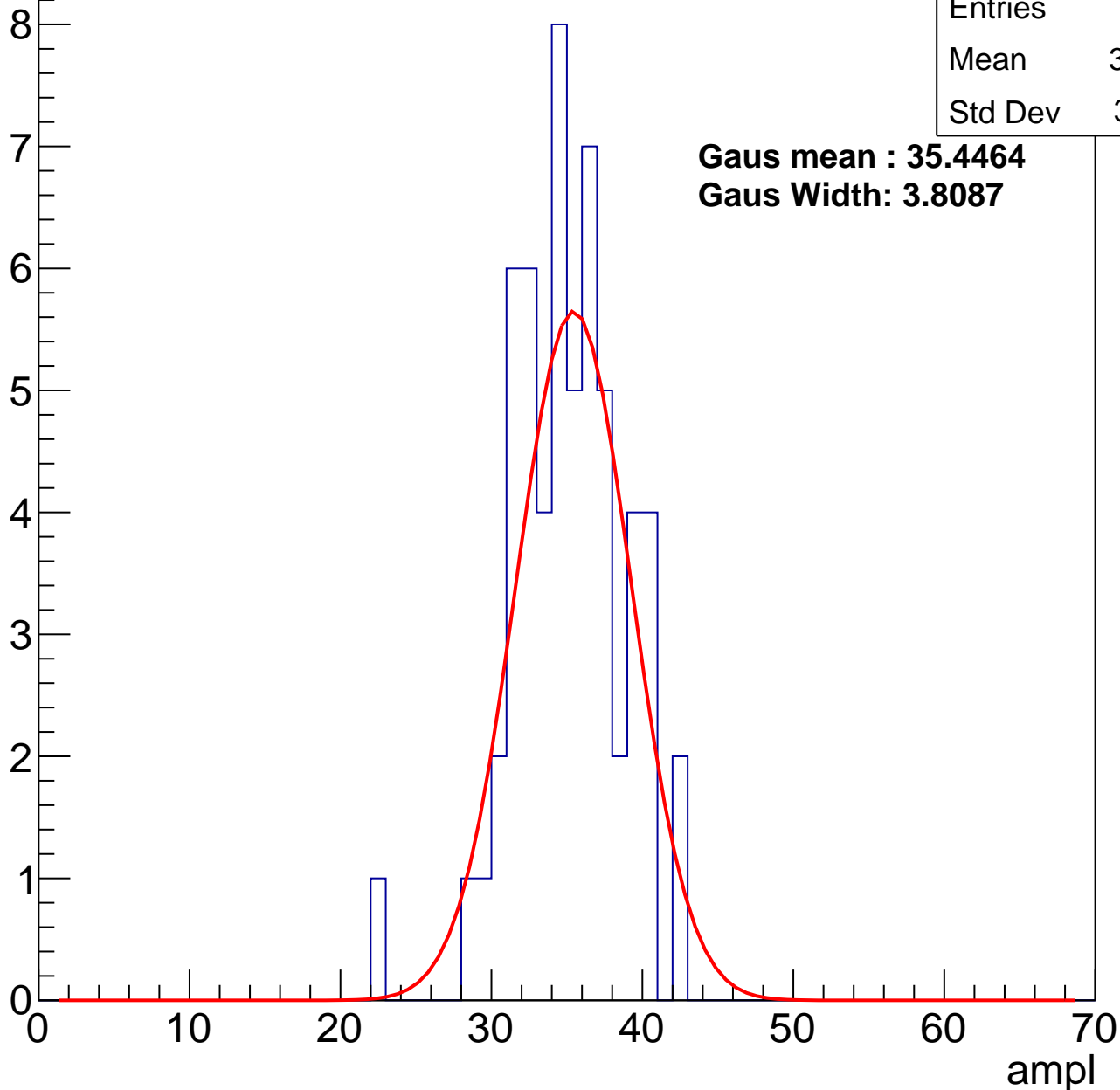
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	34.64
Std Dev	3.661

Gaus mean : 35.4464

Gaus Width: 3.8087



B1L102S, U18-ch73, adc2

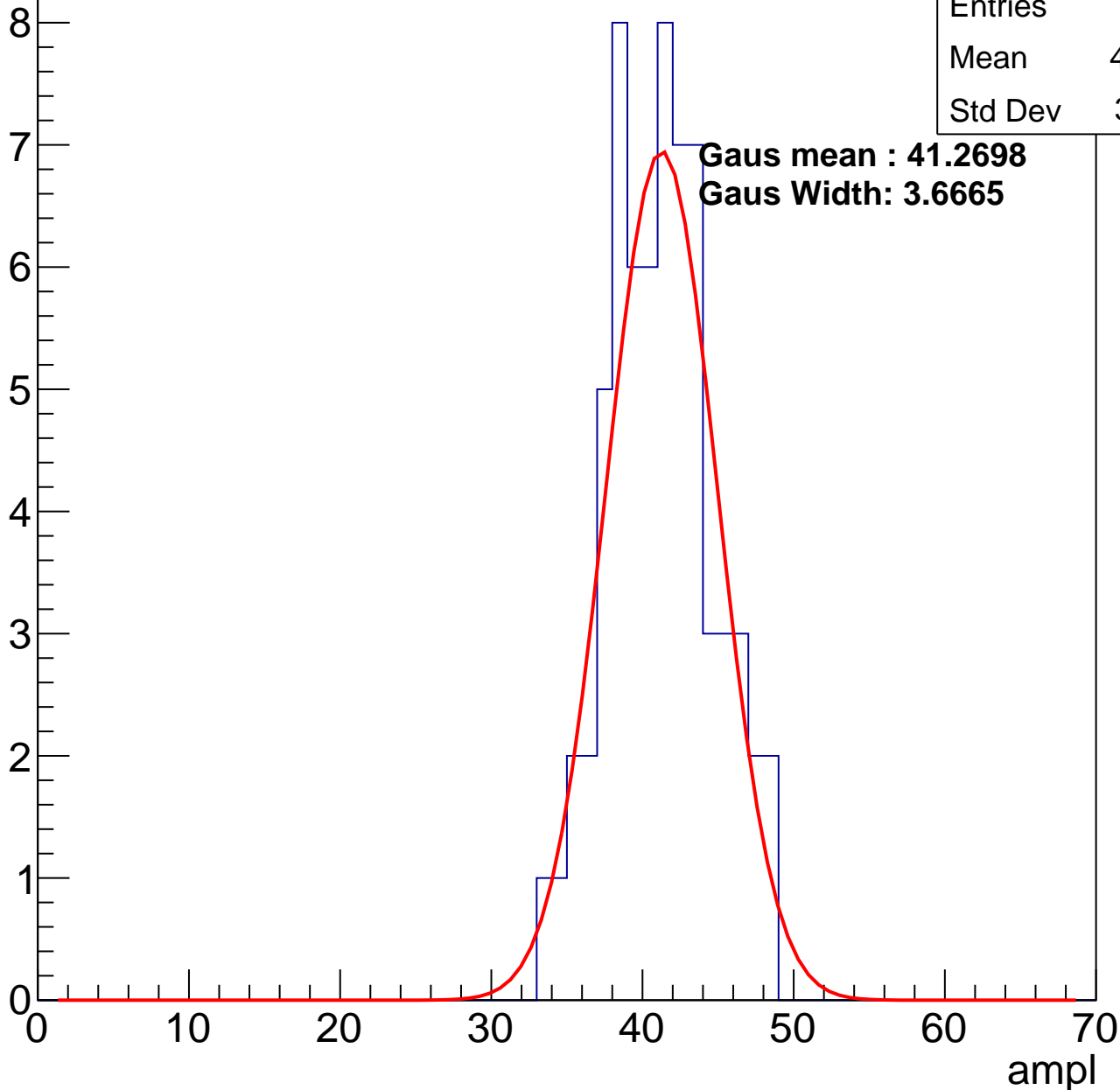
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	40.76
Std Dev	3.411

Gaus mean : 41.2698

Gaus Width: 3.6665

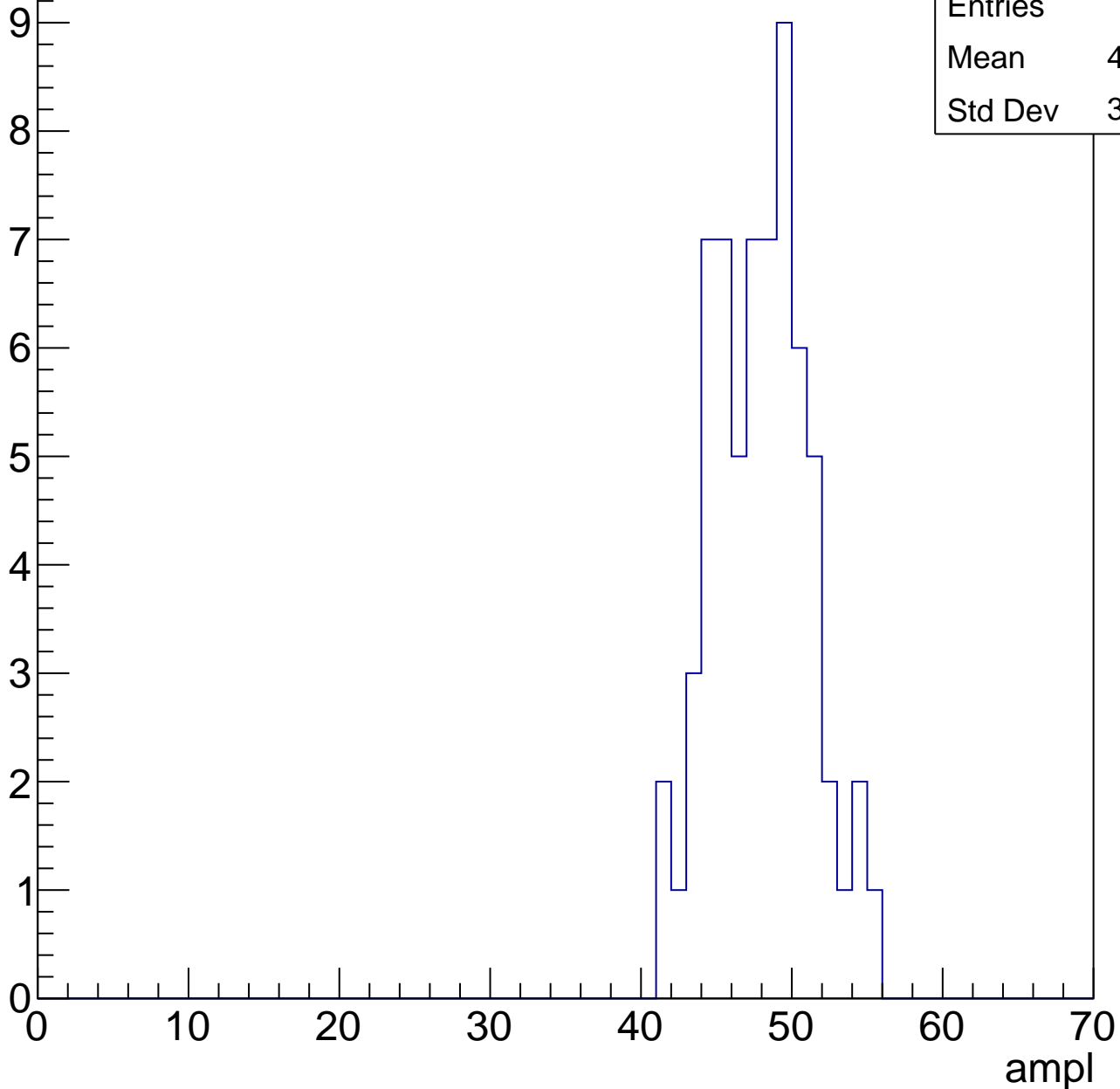


B1L102S, U18-ch73, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	47.49
Std Dev	3.163

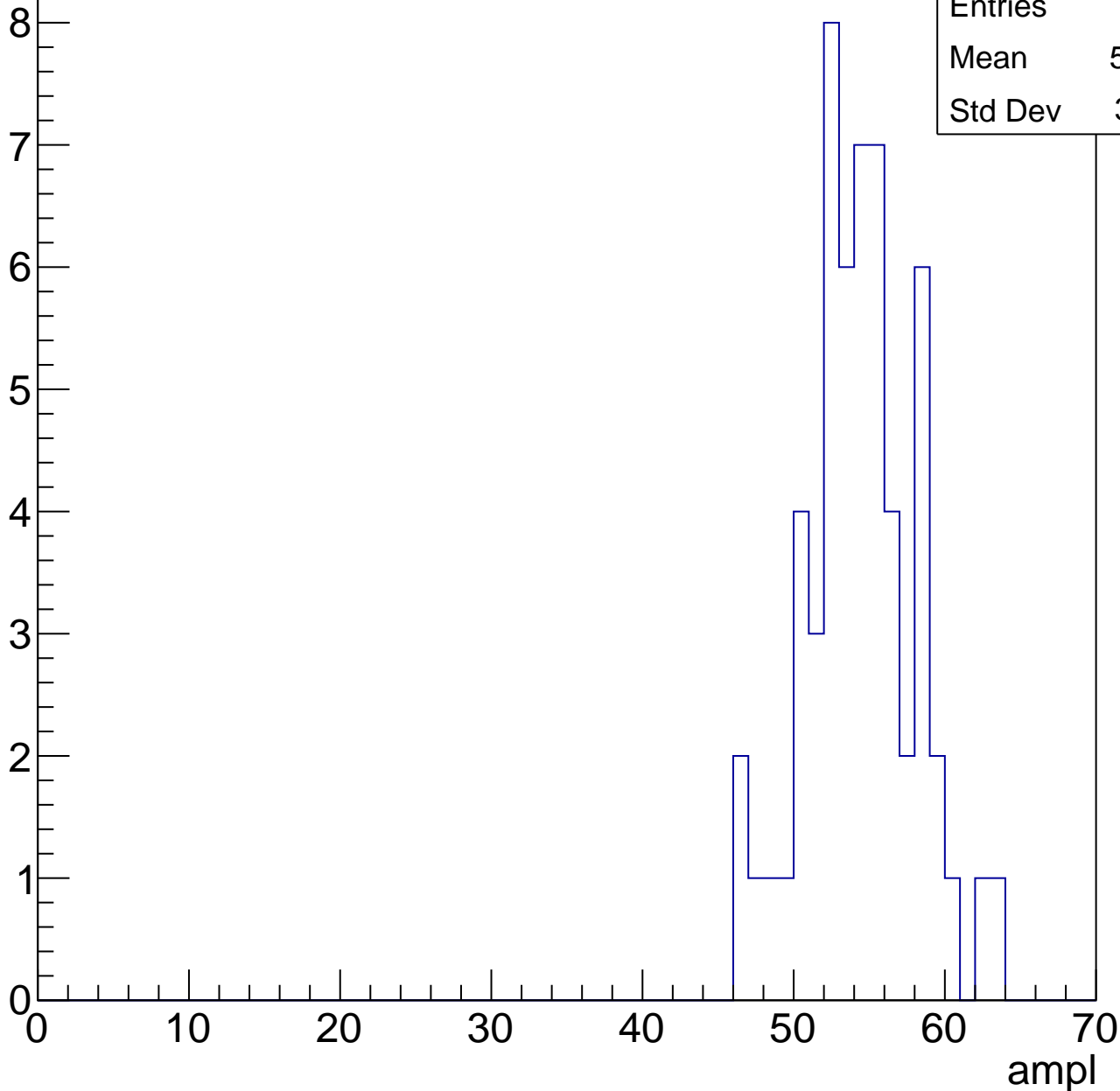


B1L102S, U18-ch73, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.95
Std Dev	3.591

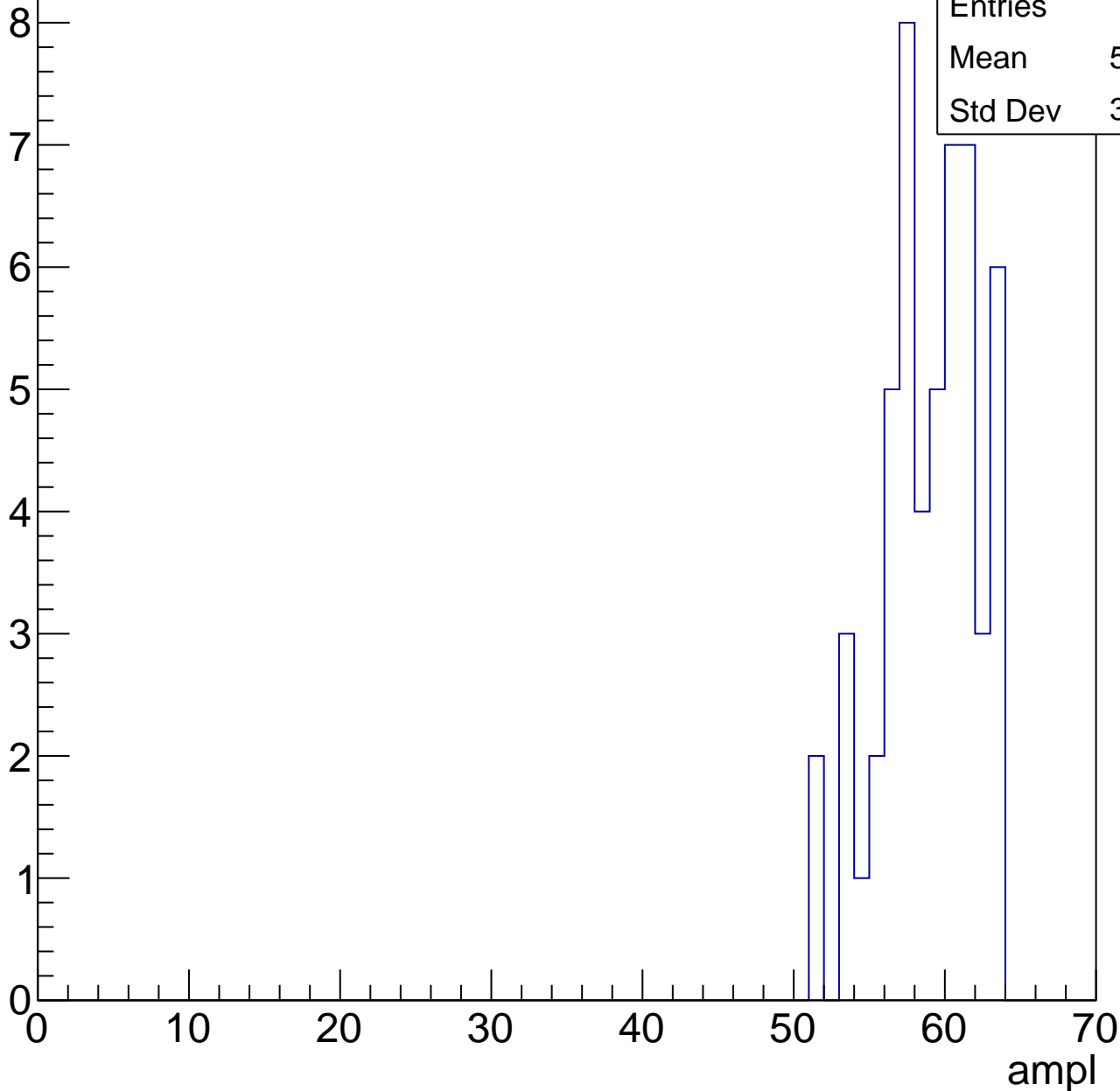


B1L102S, U18-ch73, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.47
Std Dev	3.124

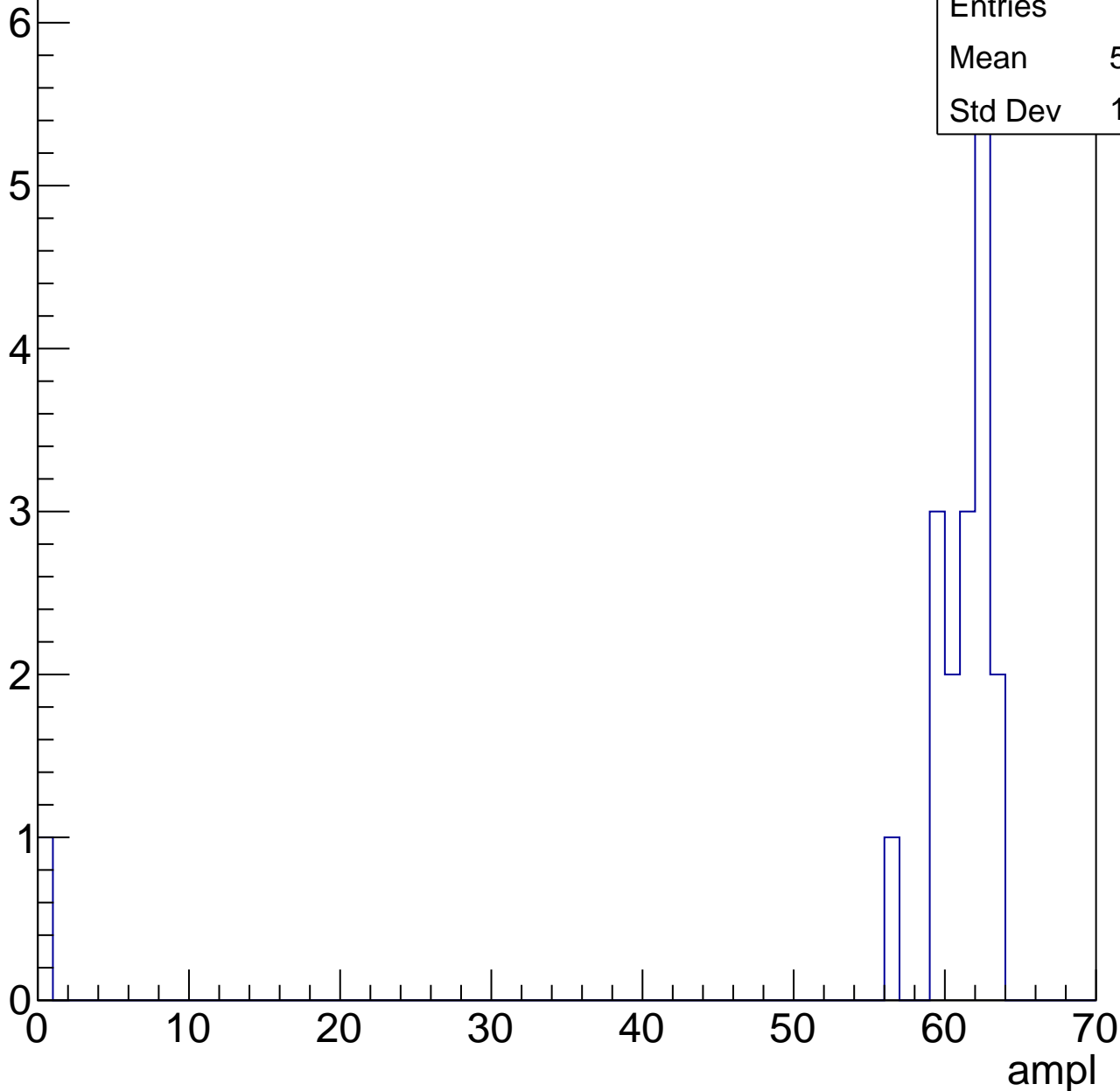


B1L102S, U18-ch73, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	57.44
Std Dev	14.04



B1L102S, U18-ch73, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch74, adc0

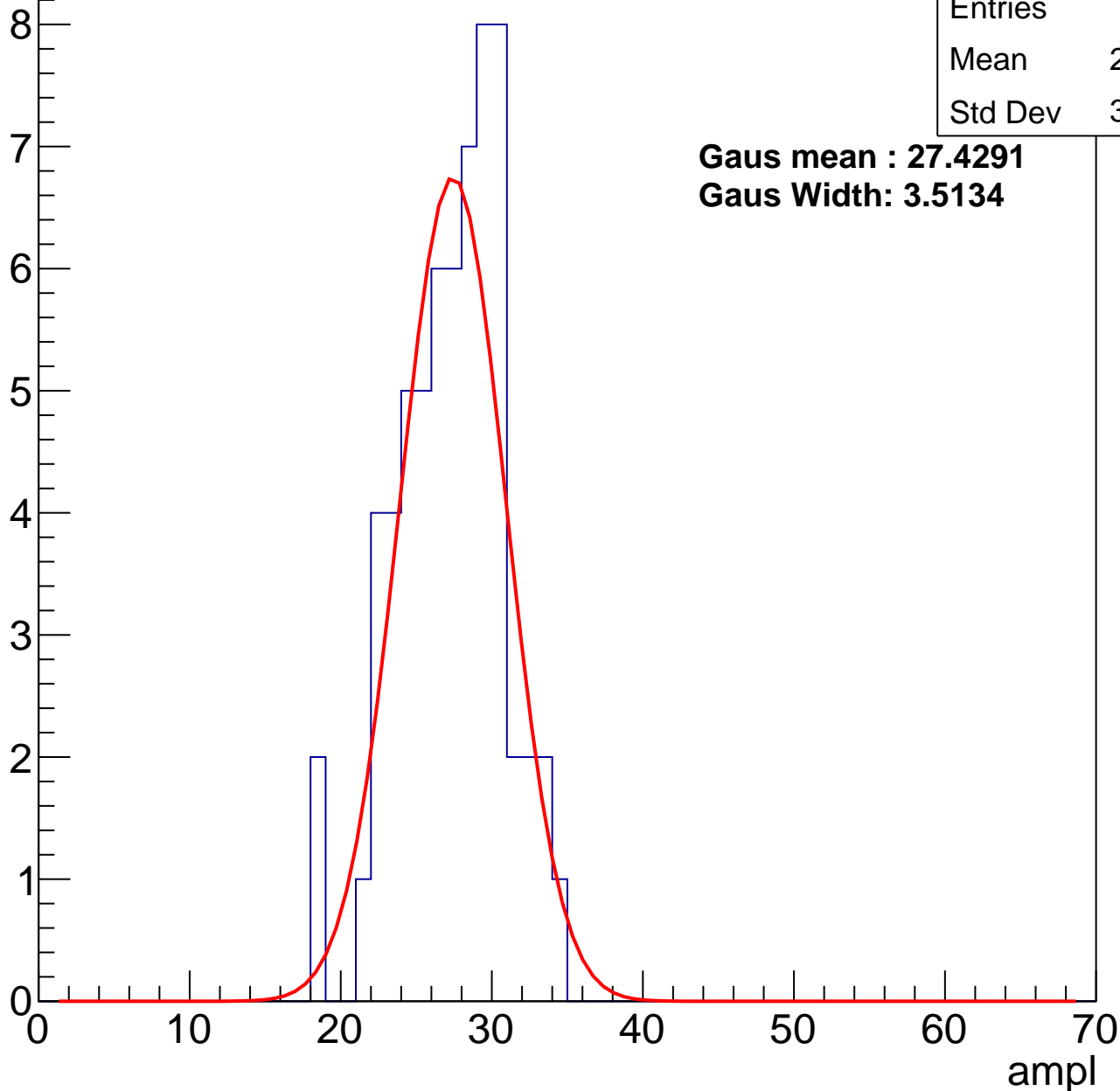
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	26.89
Std Dev	3.442

Gaus mean : 27.4291

Gaus Width: 3.5134



B1L102S, U18-ch74, adc1

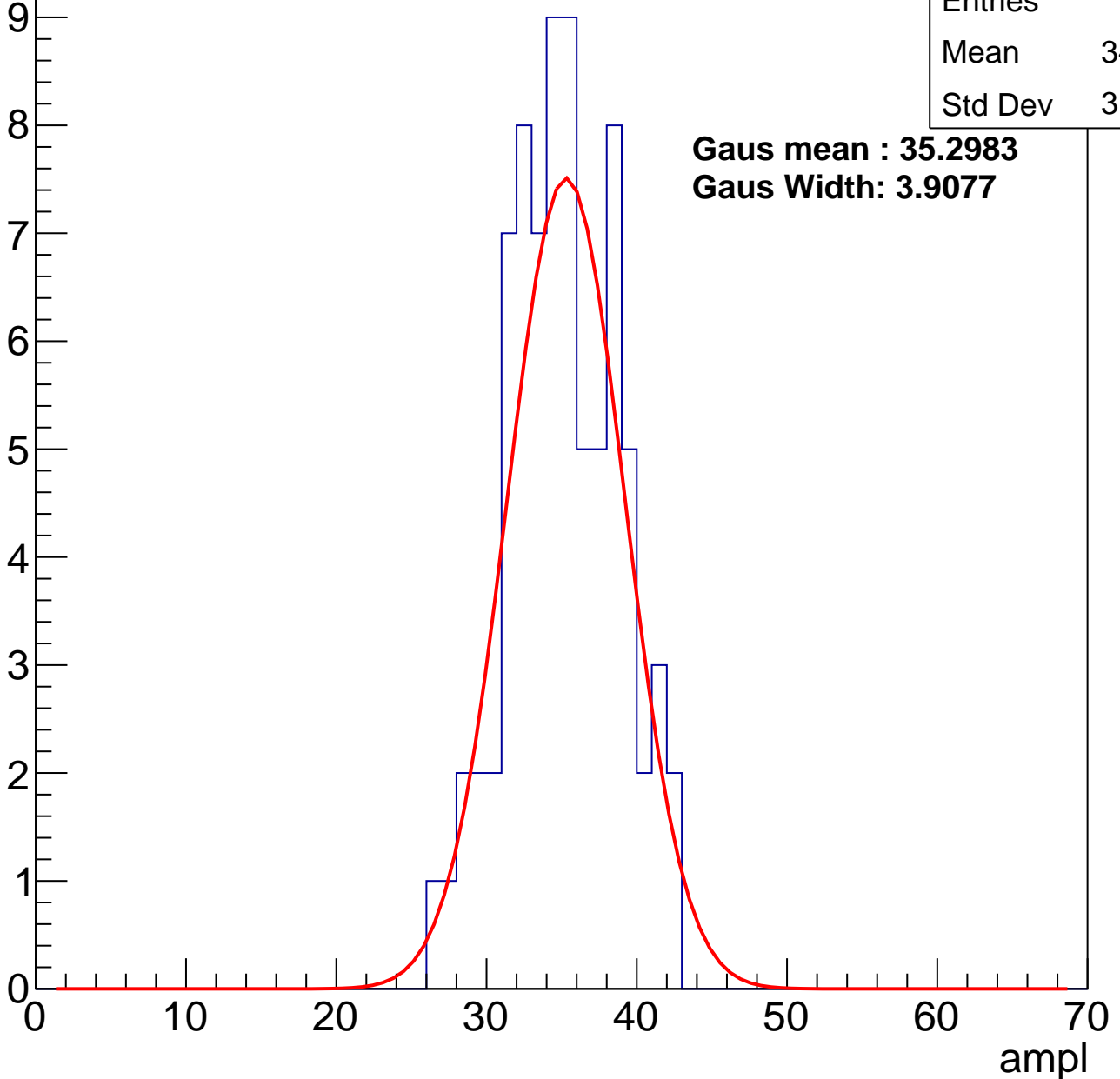
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	34.65
Std Dev	3.598

Gaus mean : 35.2983

Gaus Width: 3.9077



B1L102S, U18-ch74, adc2

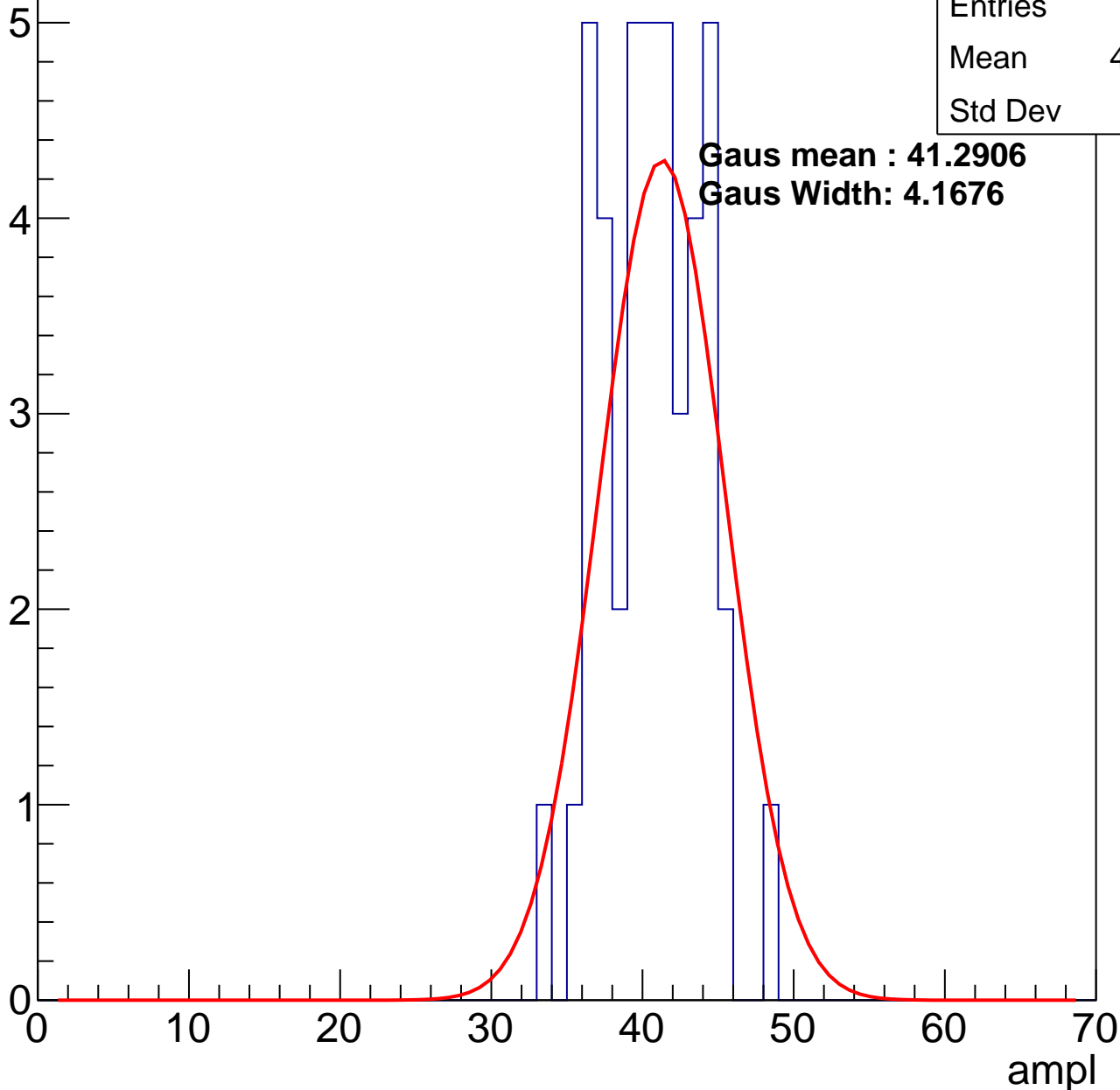
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	40.19
Std Dev	3.23

Gaus mean : 41.2906

Gaus Width: 4.1676



B1L102S, U18-ch74, adc3

calib_packv5_042523_0143.root, FC#11, port A2

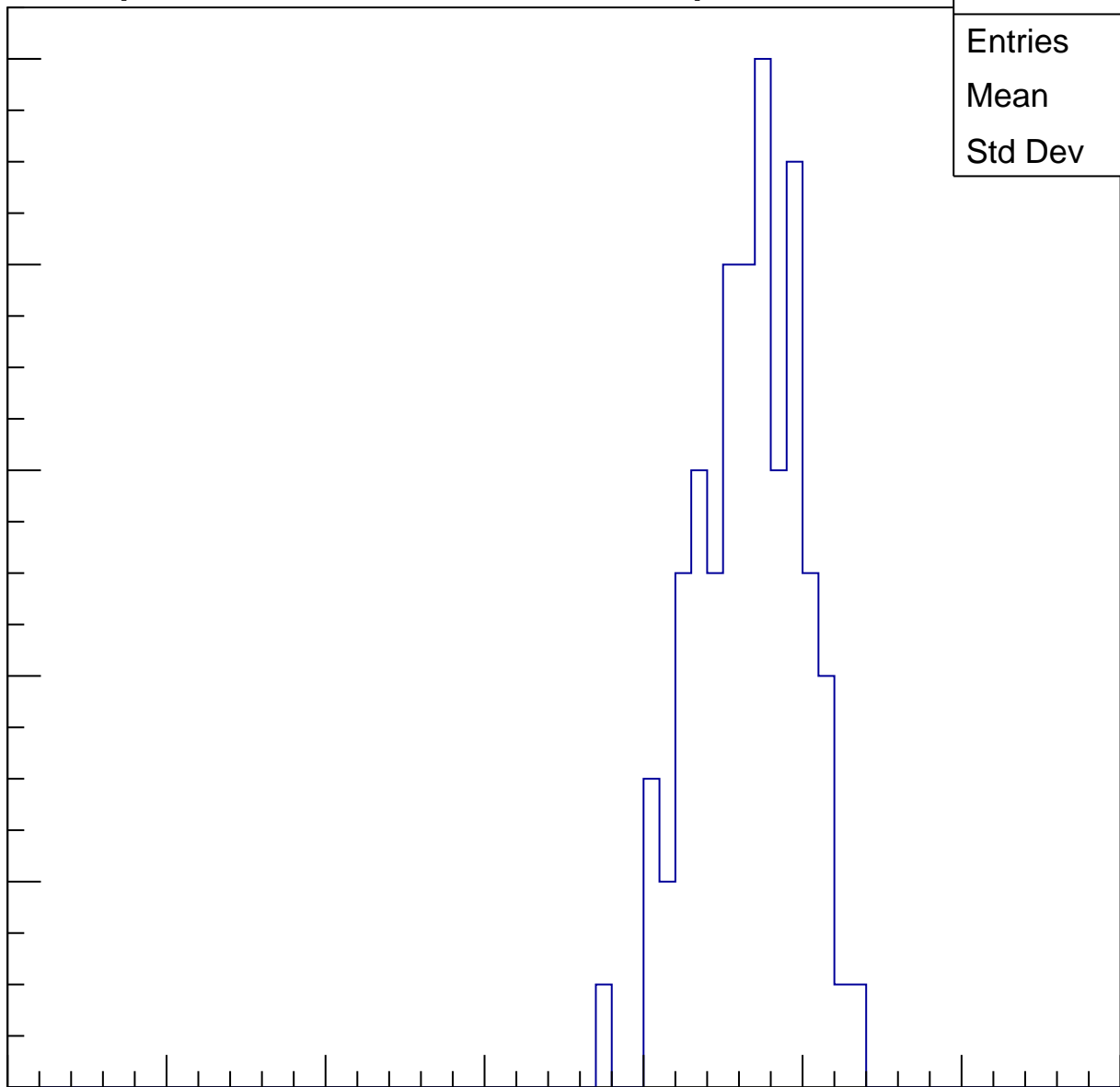
Entries	74
Mean	46.12
Std Dev	3.246

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

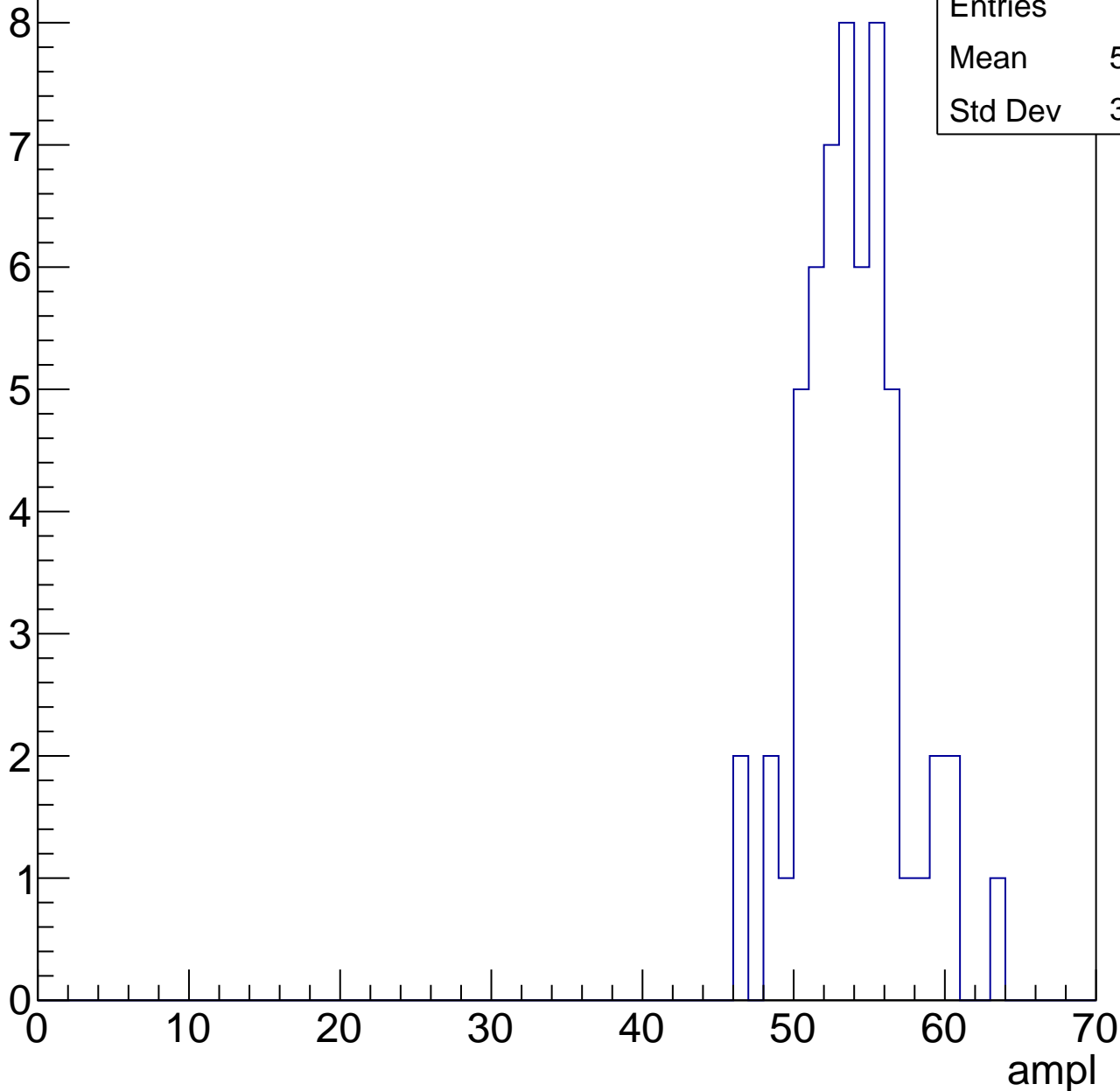


B1L102S, U18-ch74, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	53.35
Std Dev	3.317

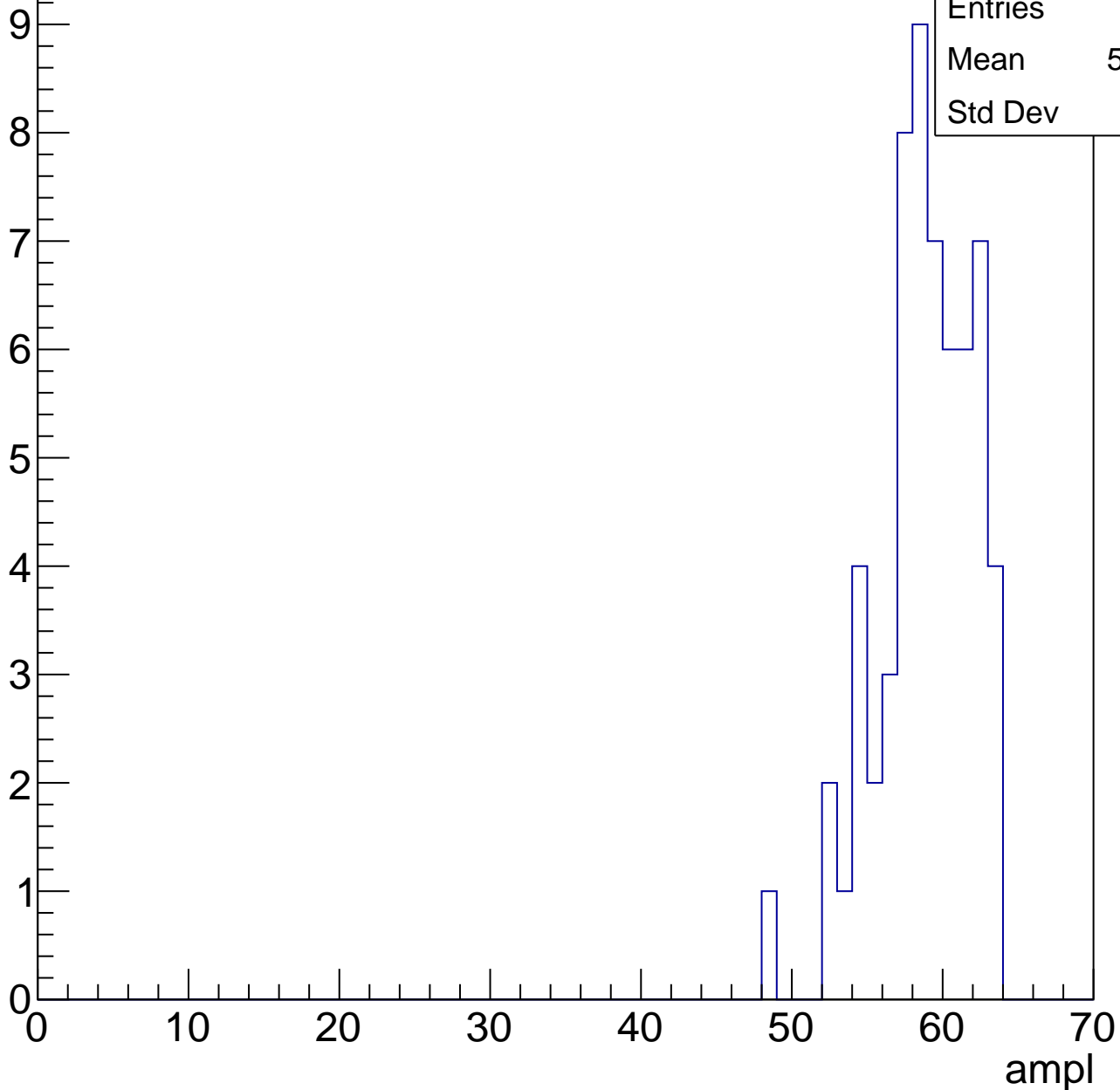


B1L102S, U18-ch74, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

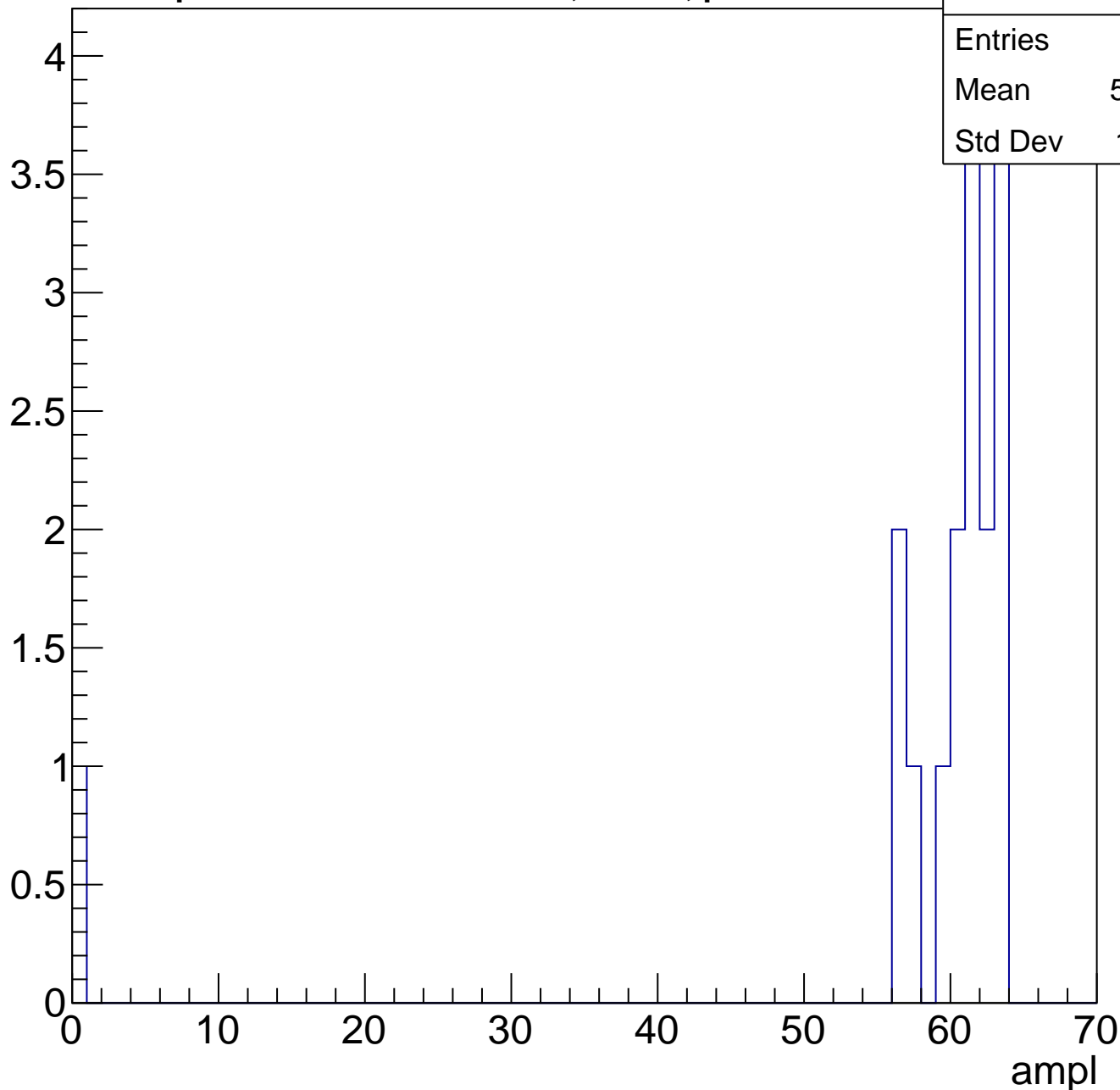
Entries	60
Mean	58.37
Std Dev	3.13



B1L102S, U18-ch74, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch74, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch75, adc0

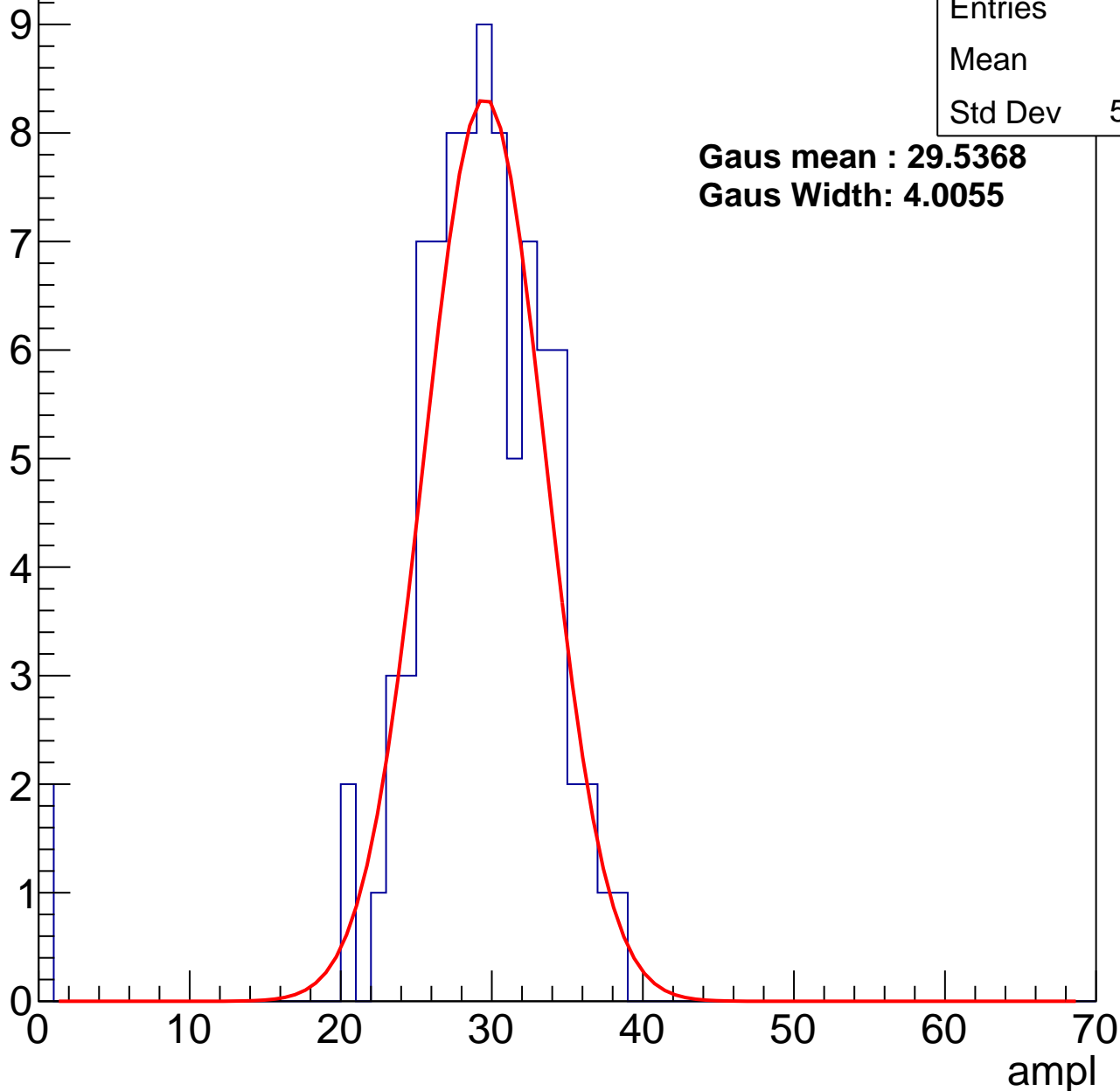
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	88
Mean	28.4
Std Dev	5.752

Gaus mean : 29.5368

Gaus Width: 4.0055



B1L102S, U18-ch75, adc1

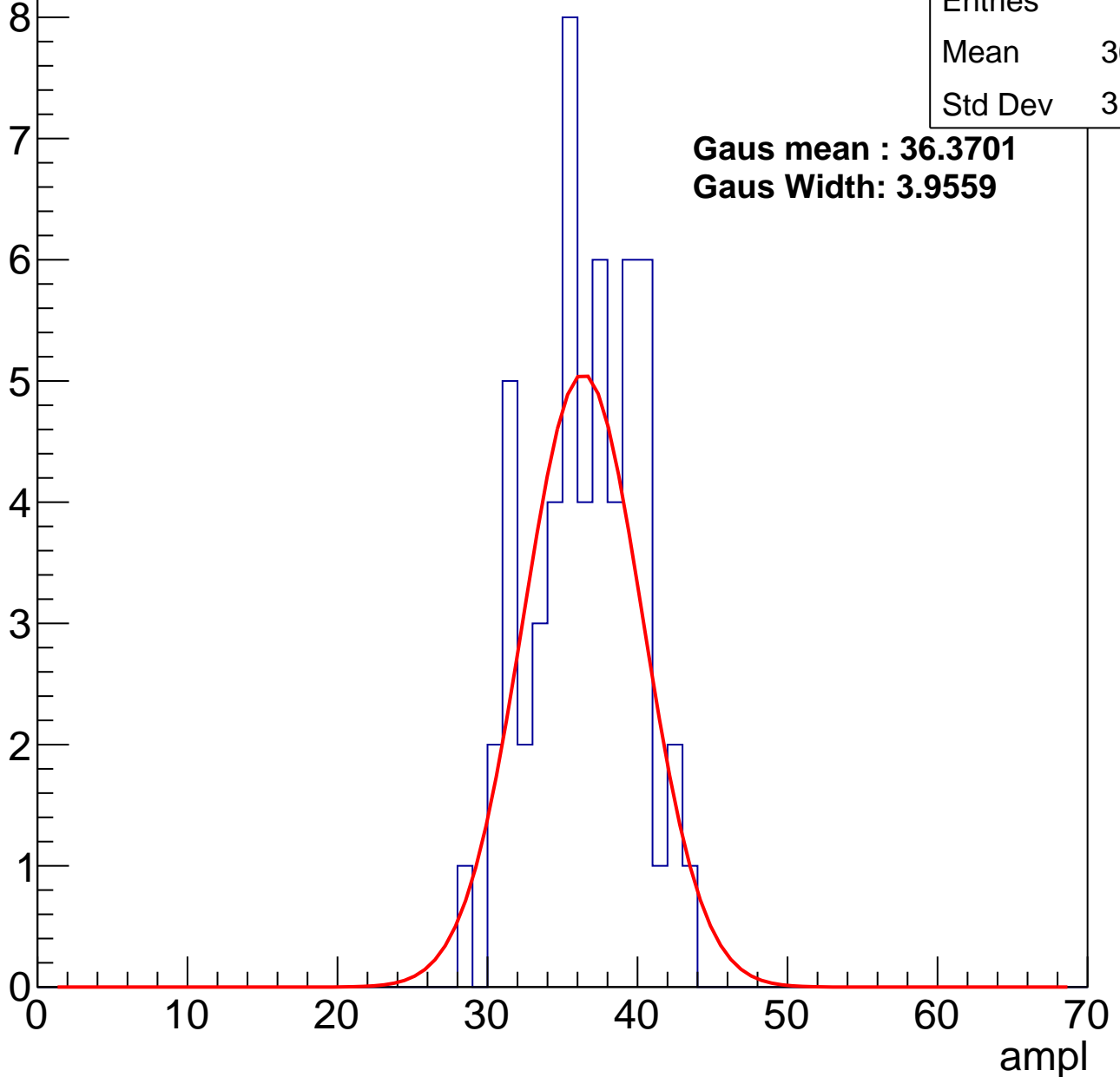
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	36.04
Std Dev	3.469

Gaus mean : 36.3701

Gaus Width: 3.9559



B1L102S, U18-ch75, adc2

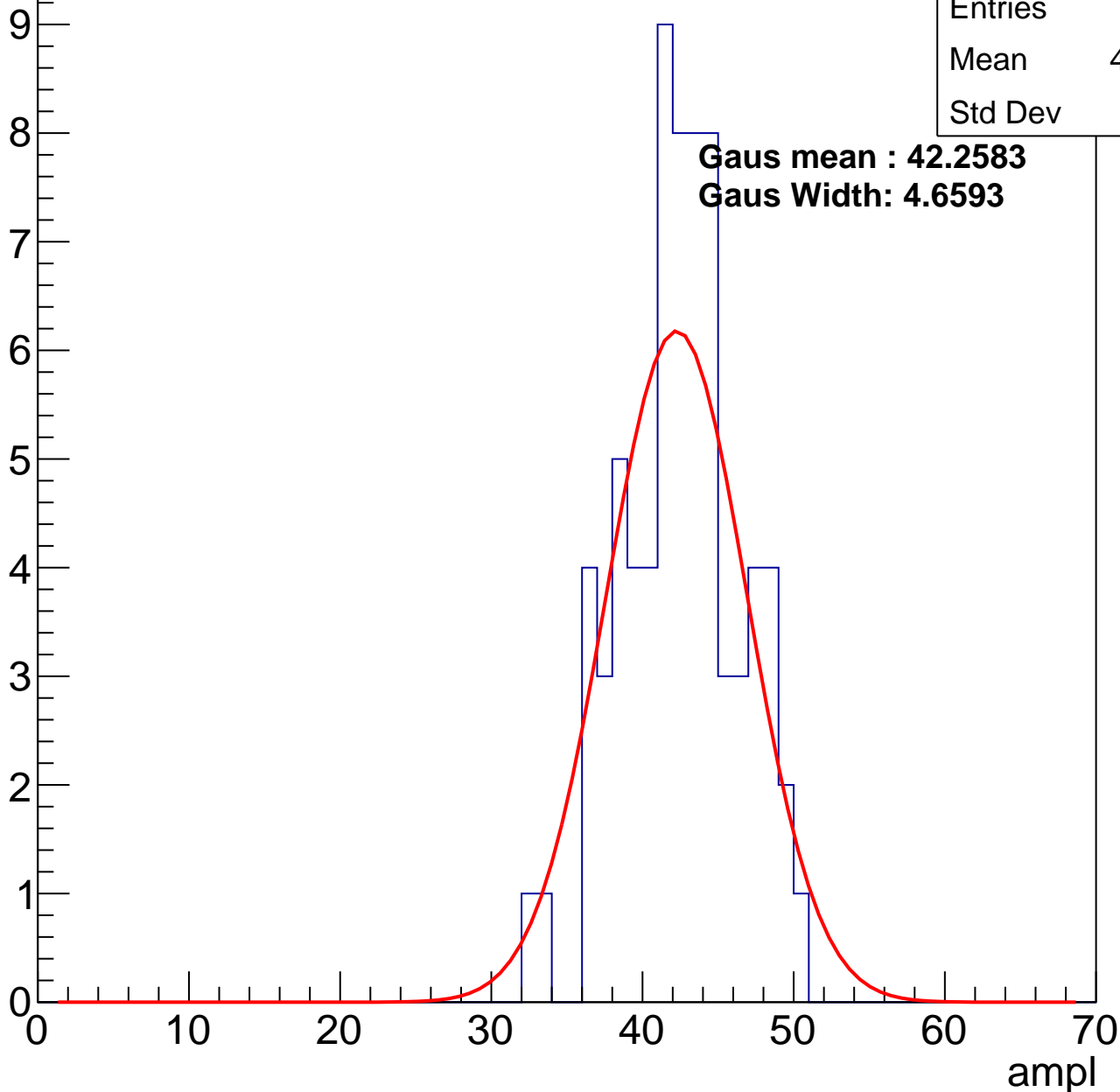
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	42.06
Std Dev	3.84

Gaus mean : 42.2583

Gaus Width: 4.6593

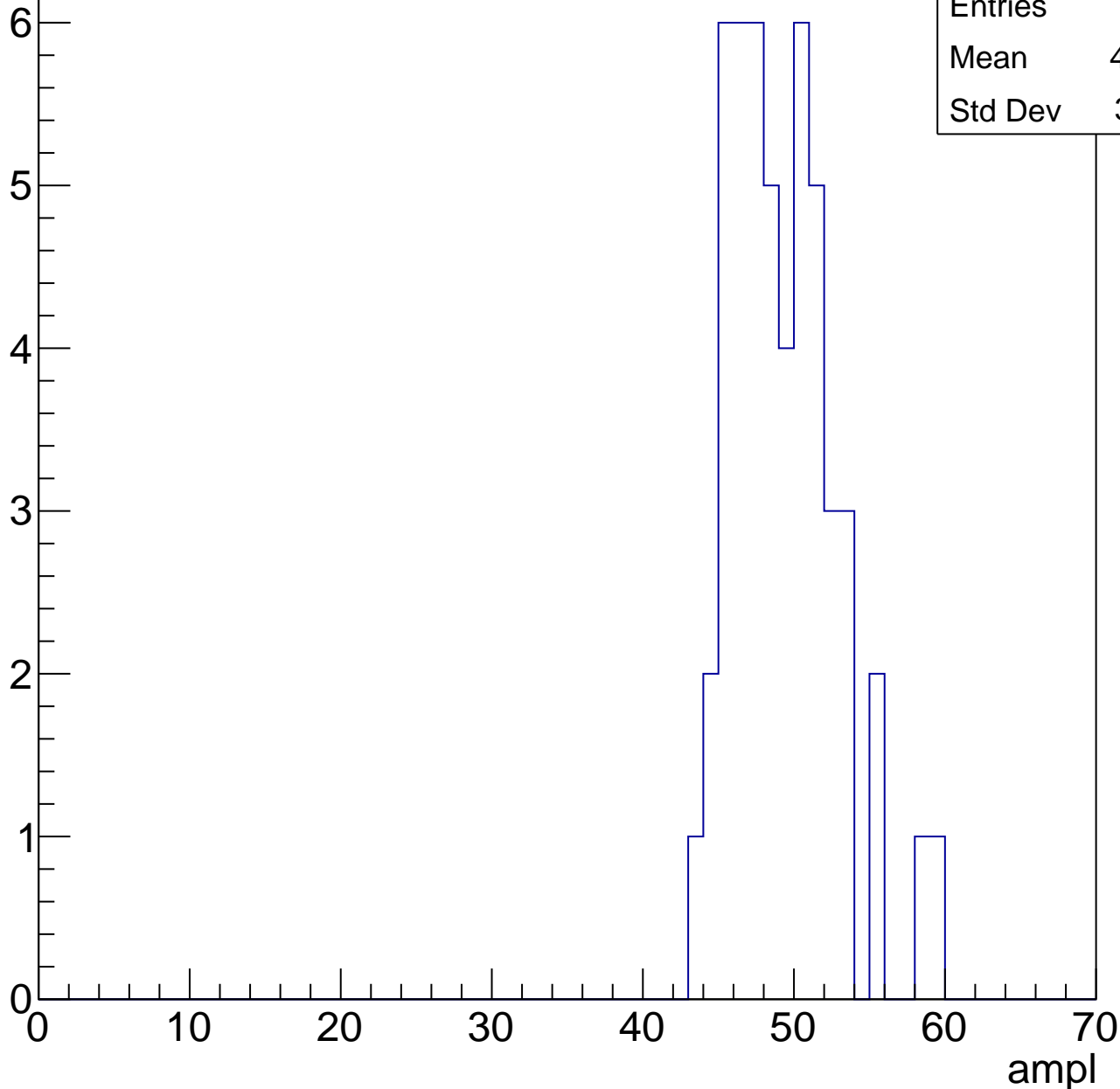


B1L102S, U18-ch75, adc3

calib_packv5_042523_0143.root, FC#11, port A2

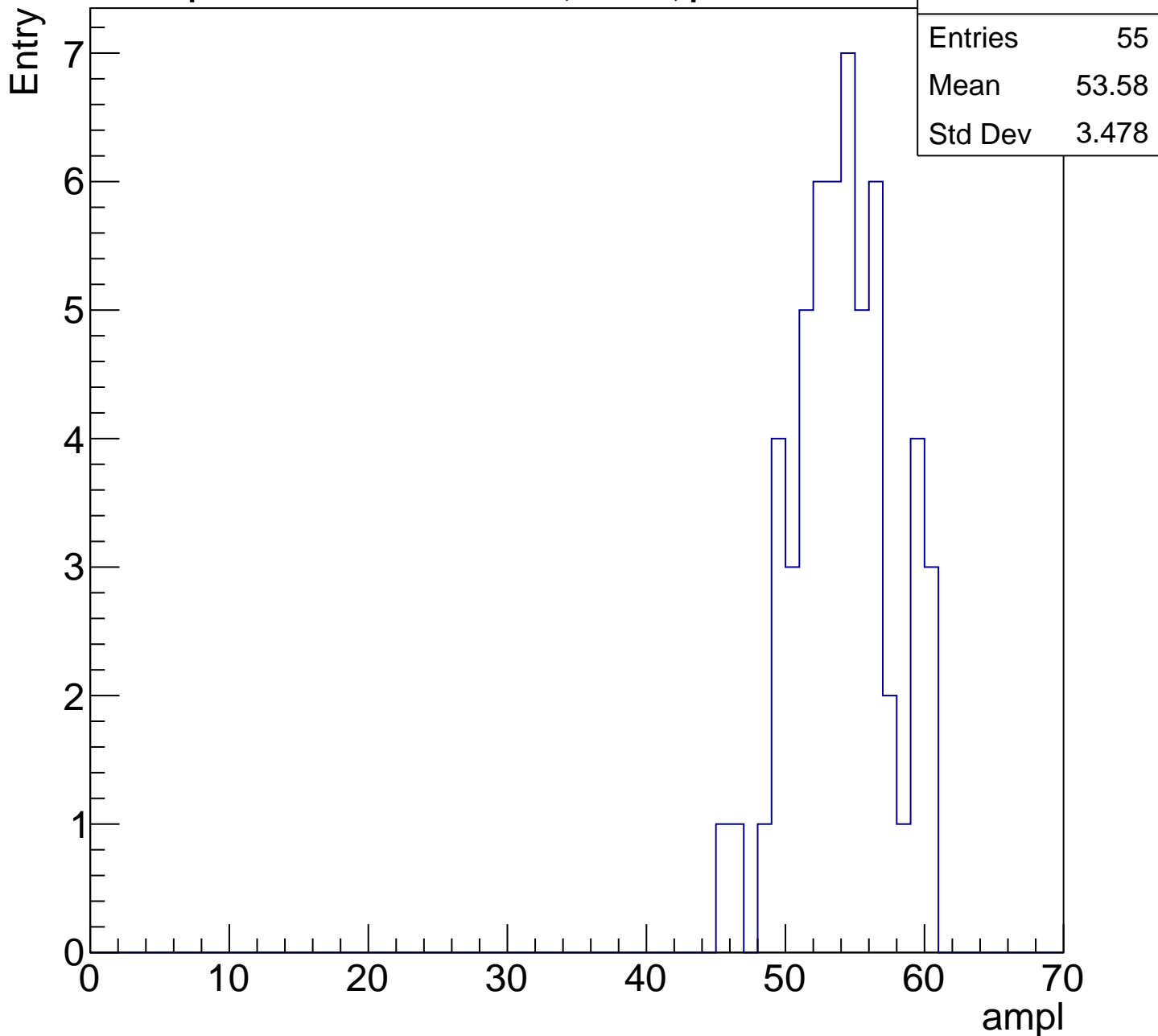
Entry

Entries	51
Mean	48.86
Std Dev	3.481



B1L102S, U18-ch75, adc4

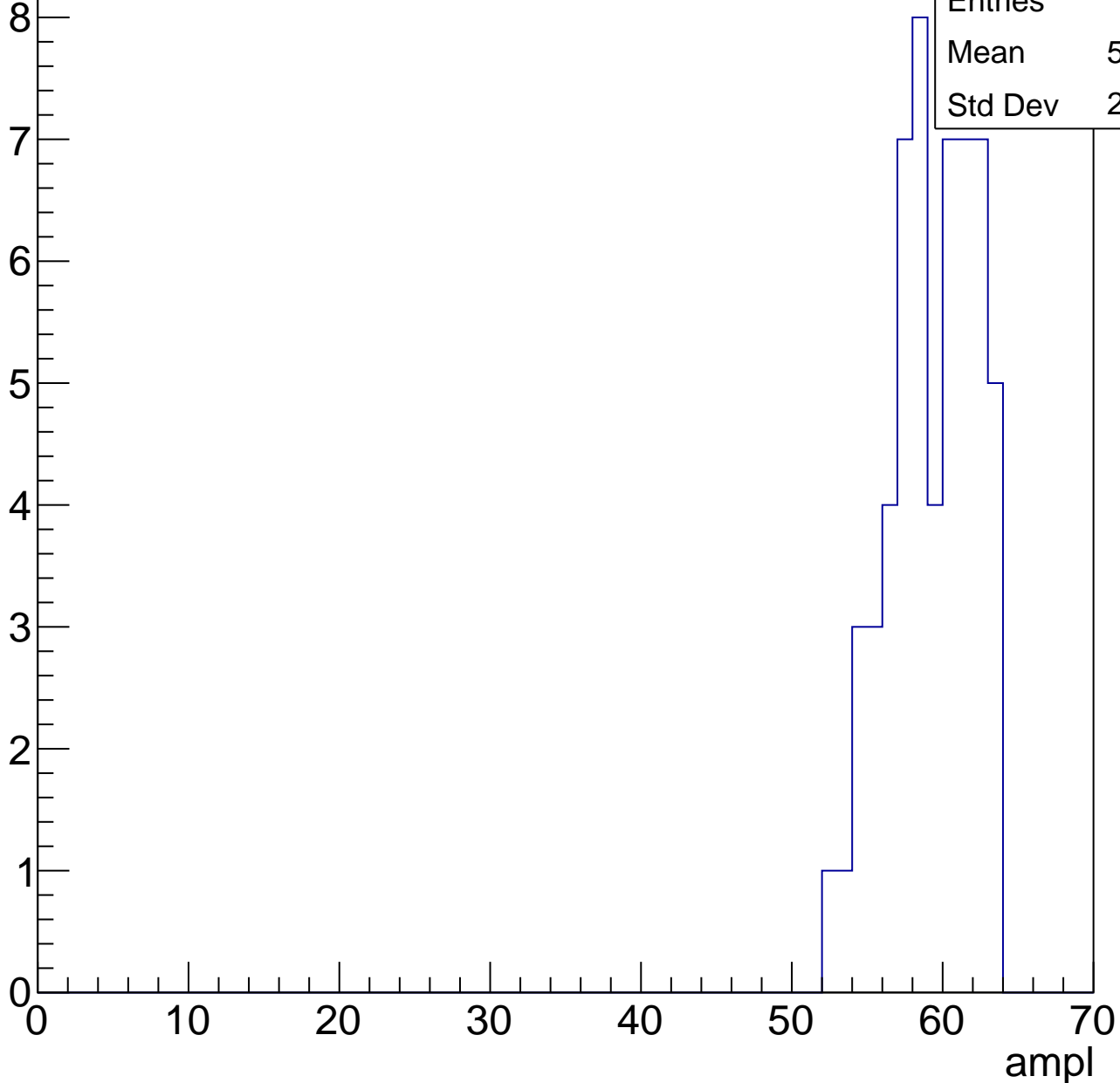
calib_packv5_042523_0143.root, FC#11, port A2



B1L102S, U18-ch75, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



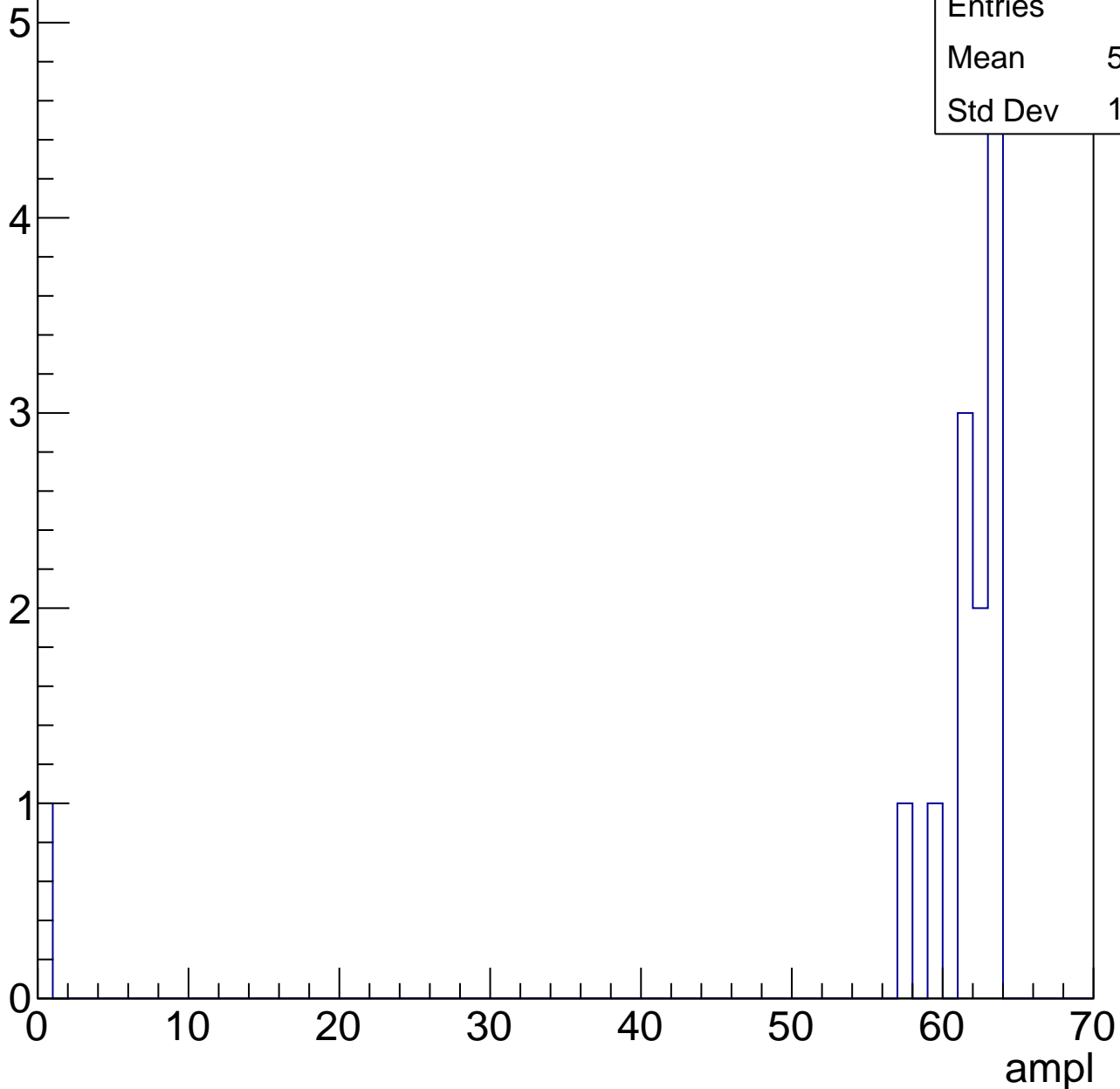
Entries	57
Mean	58.79
Std Dev	2.827

B1L102S, U18-ch75, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.77
Std Dev	16.48



B1L102S, U18-ch75, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L102S, U18-ch76, adc0

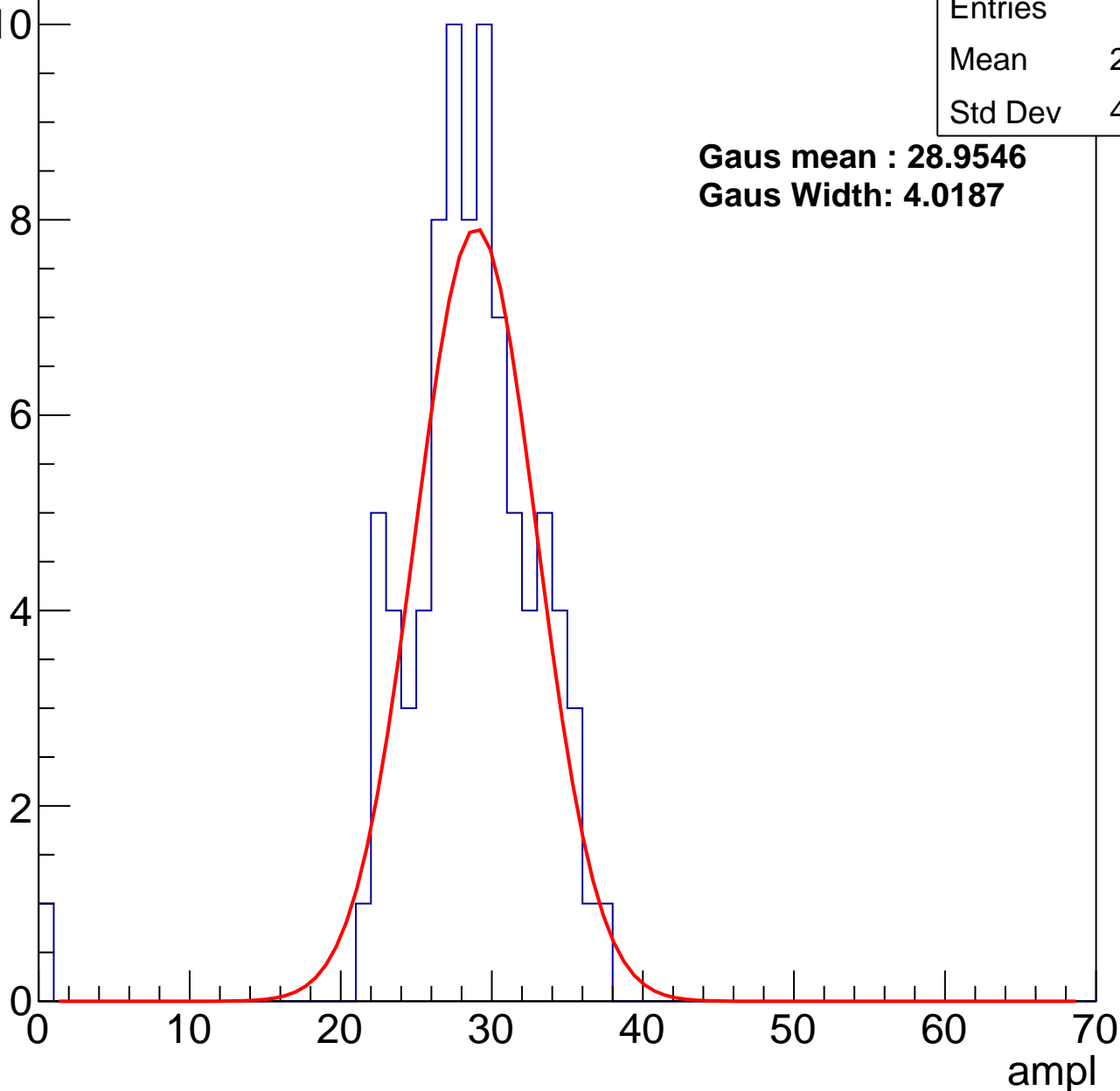
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	28.08
Std Dev	4.814

Gaus mean : 28.9546

Gaus Width: 4.0187



B1L102S, U18-ch76, adc1

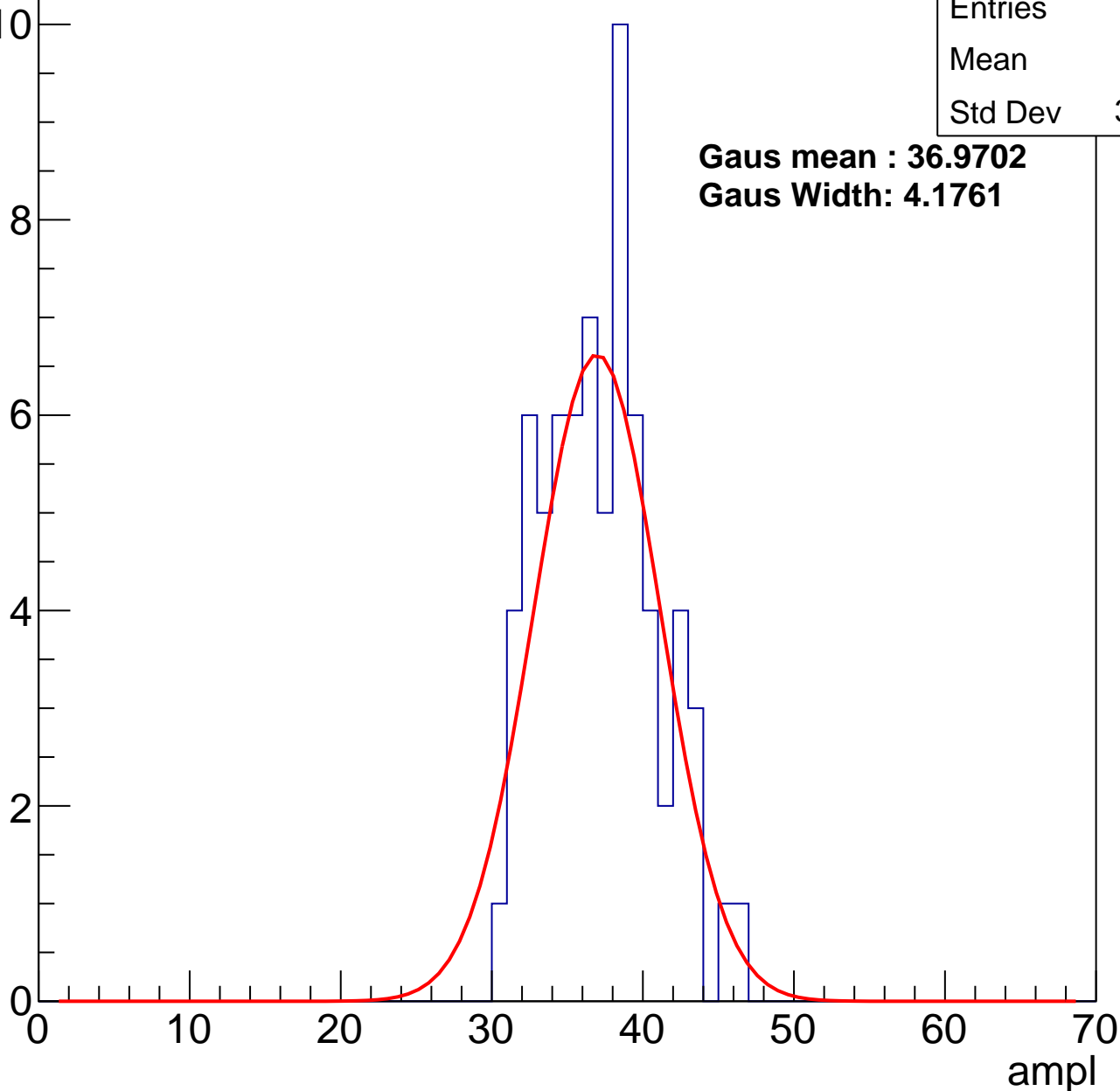
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.7
Std Dev	3.671

Gaus mean : 36.9702

Gaus Width: 4.1761



B1L102S, U18-ch76, adc2

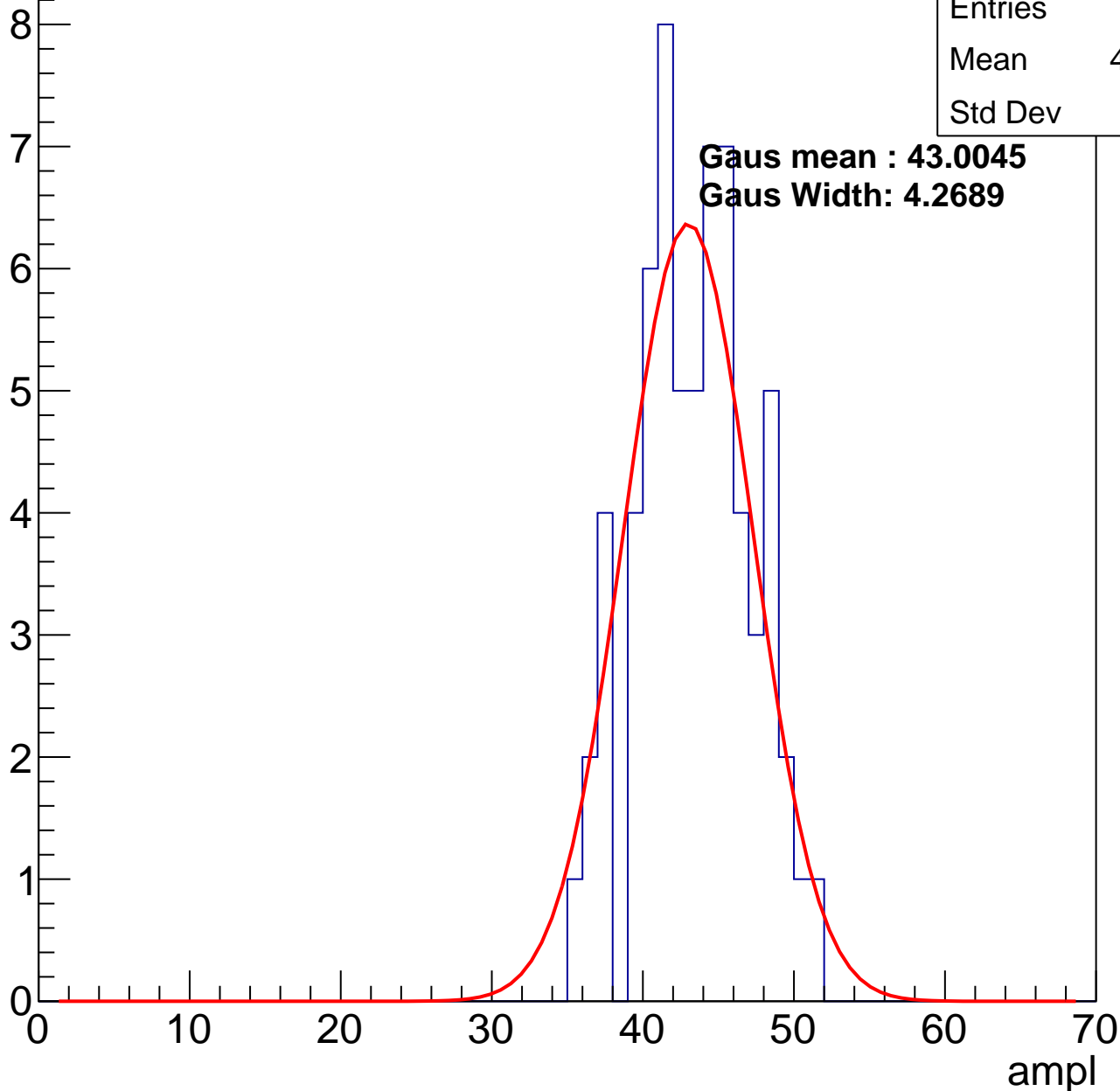
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	42.94
Std Dev	3.7

Gaus mean : 43.0045

Gaus Width: 4.2689

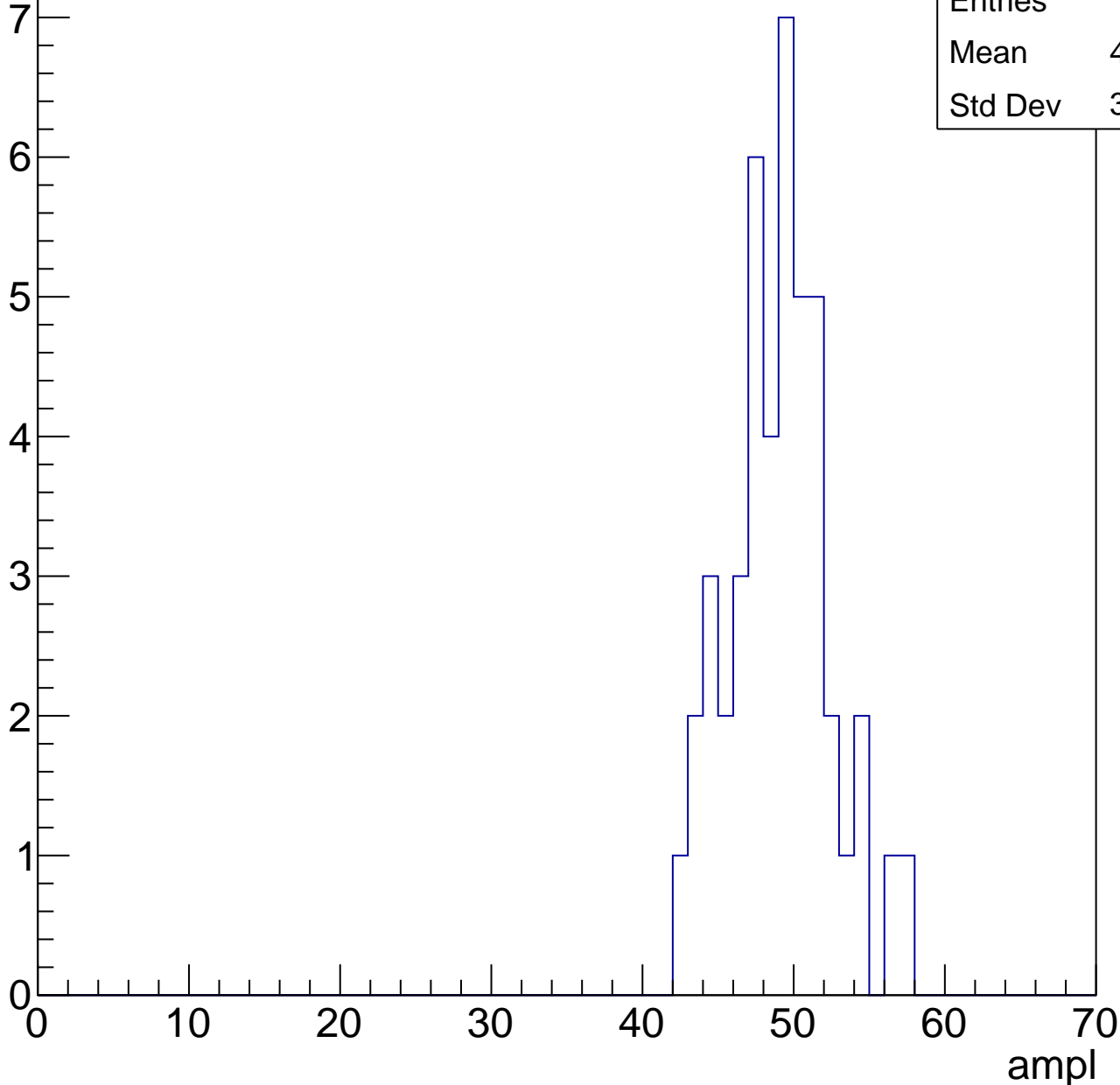


B1L102S, U18-ch76, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	48.62
Std Dev	3.329

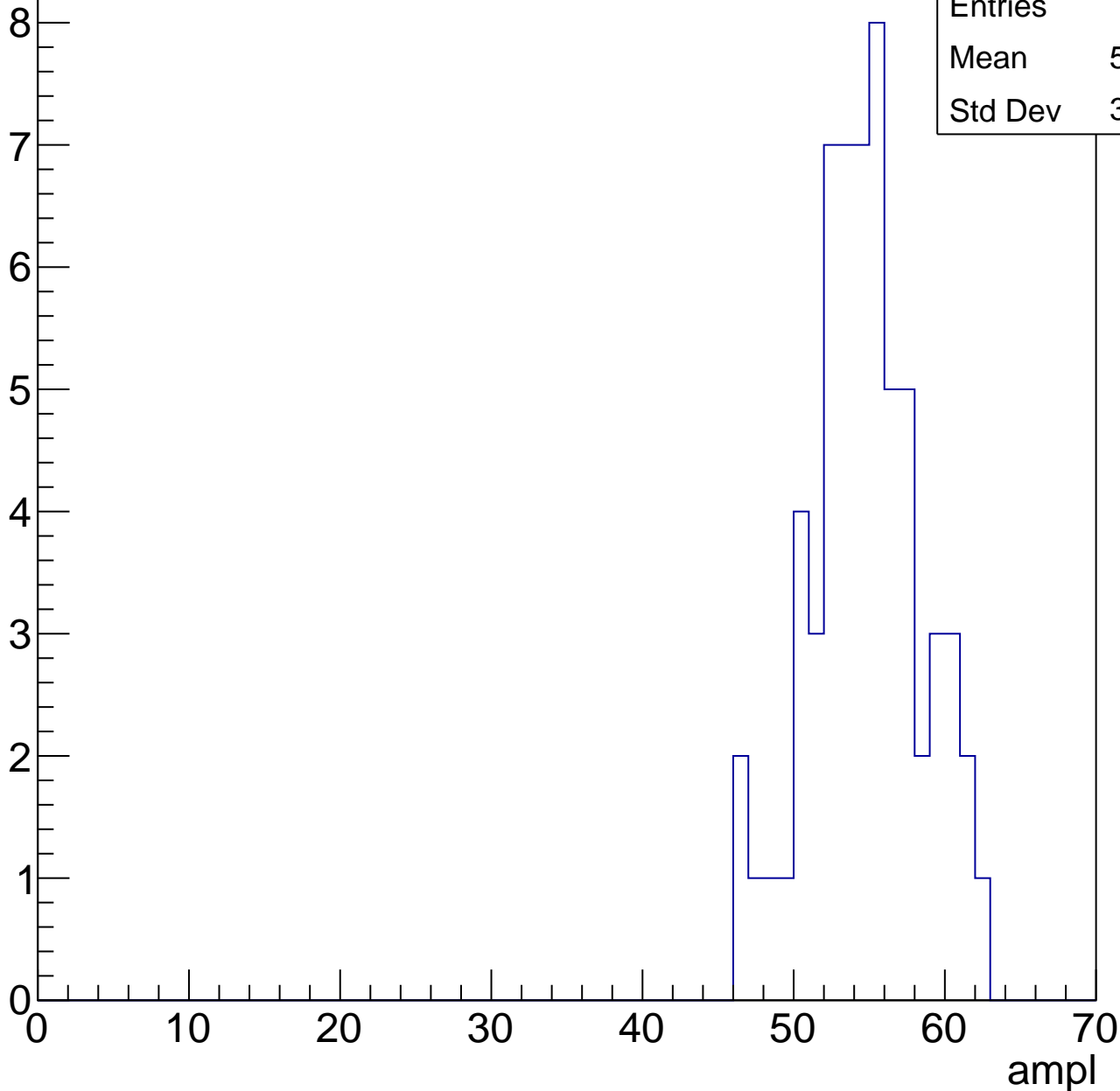


B1L102S, U18-ch76, adc4

calib_packv5_042523_0143.root, FC#11, port A2

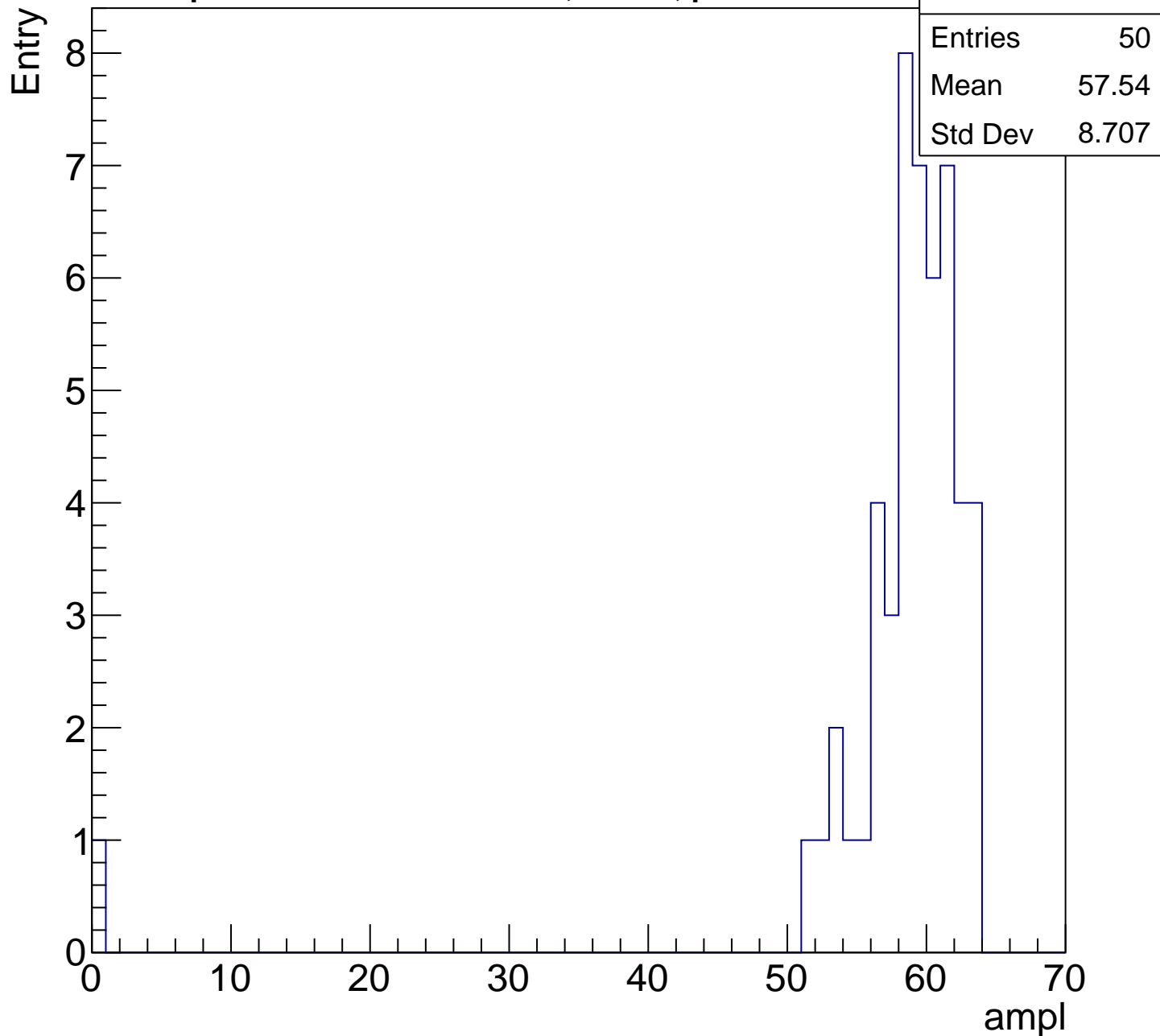
Entry

Entries	62
Mean	54.26
Std Dev	3.614



B1L102S, U18-ch76, adc5

calib_packv5_042523_0143.root, FC#11, port A2

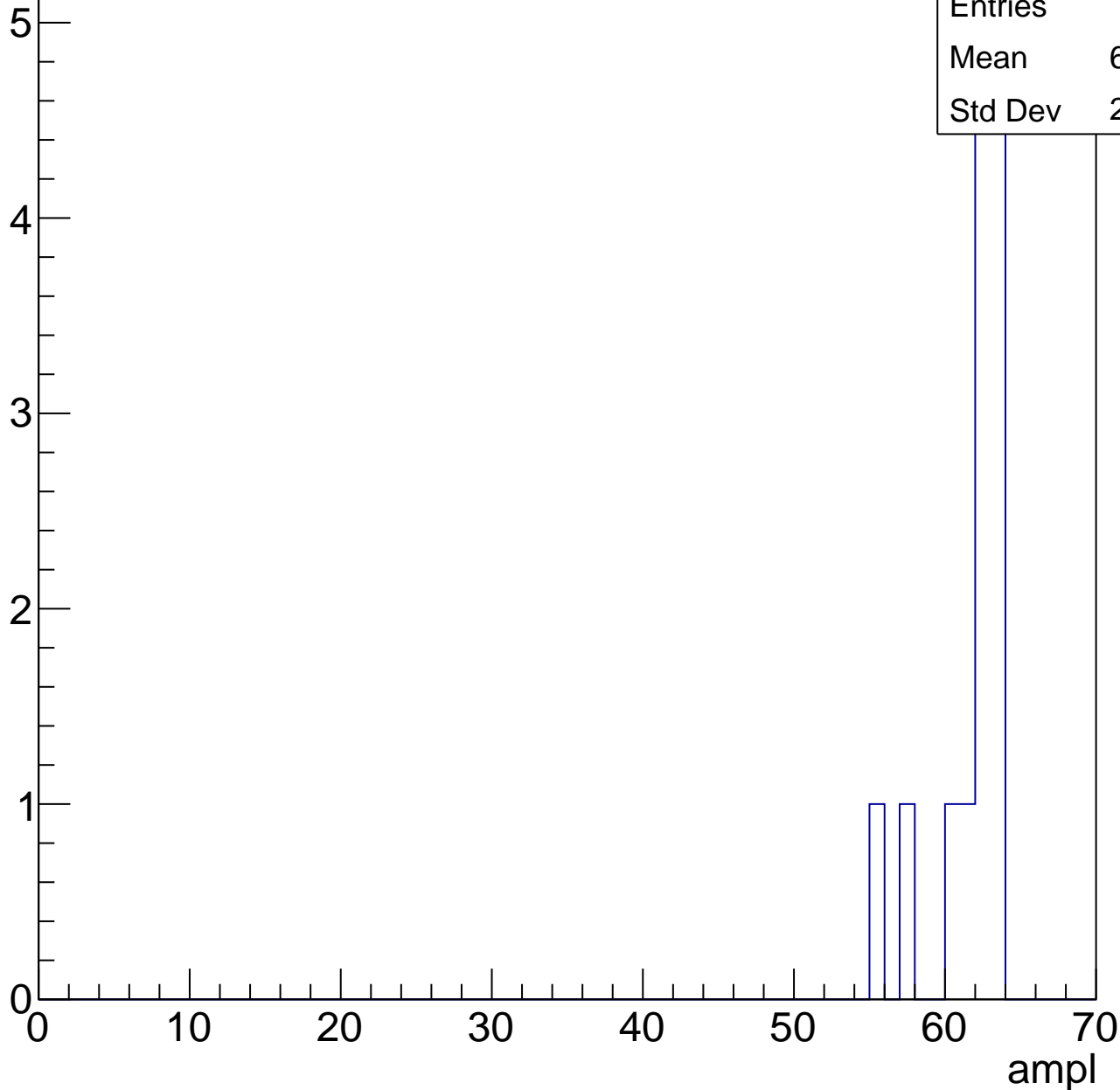


B1L102S, U18-ch76, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.29
Std Dev	2.343



B1L102S, U18-ch76, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch77, adc0

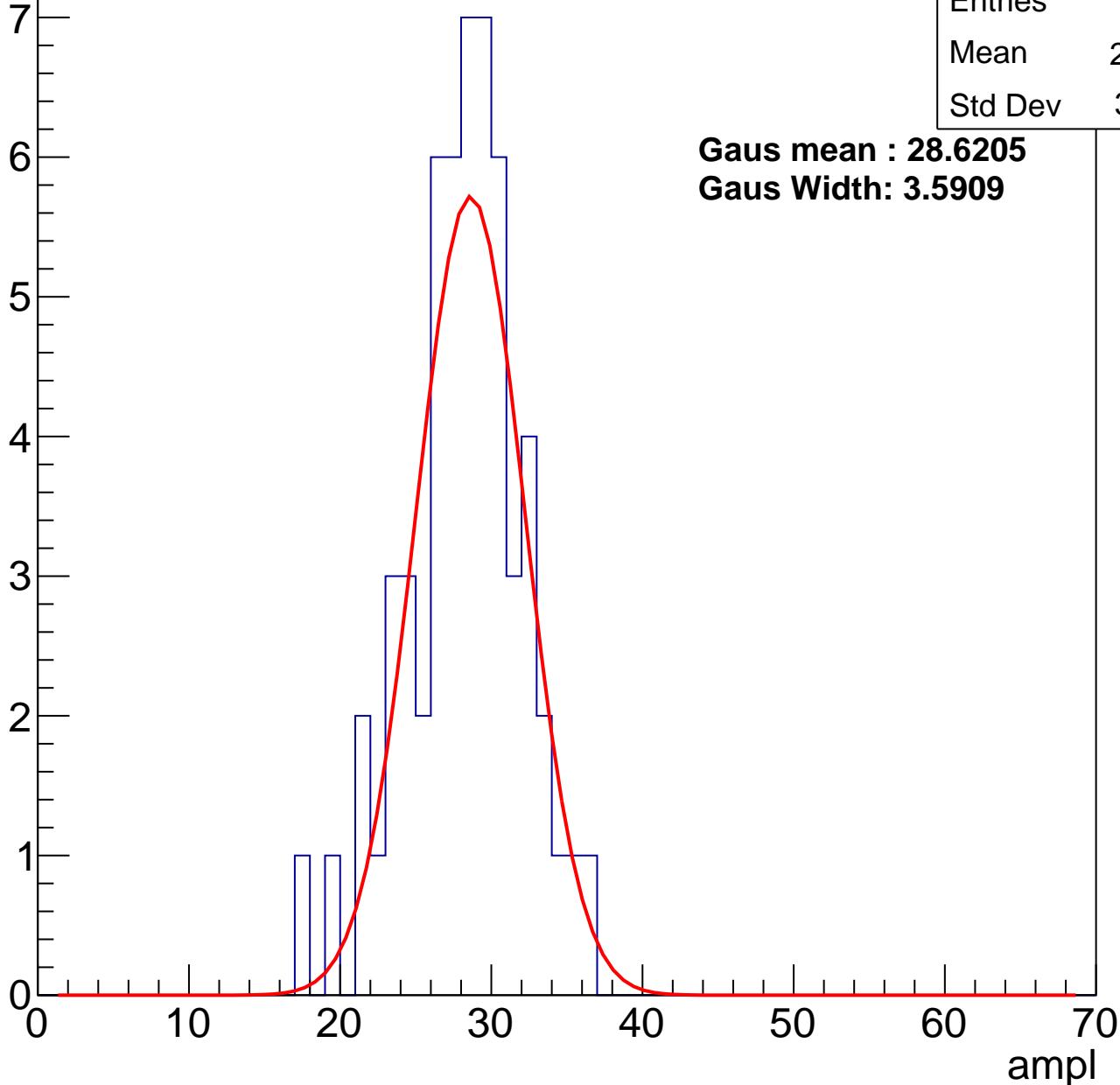
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.72
Std Dev	3.801

Gaus mean : 28.6205

Gaus Width: 3.5909



B1L102S, U18-ch77, adc1

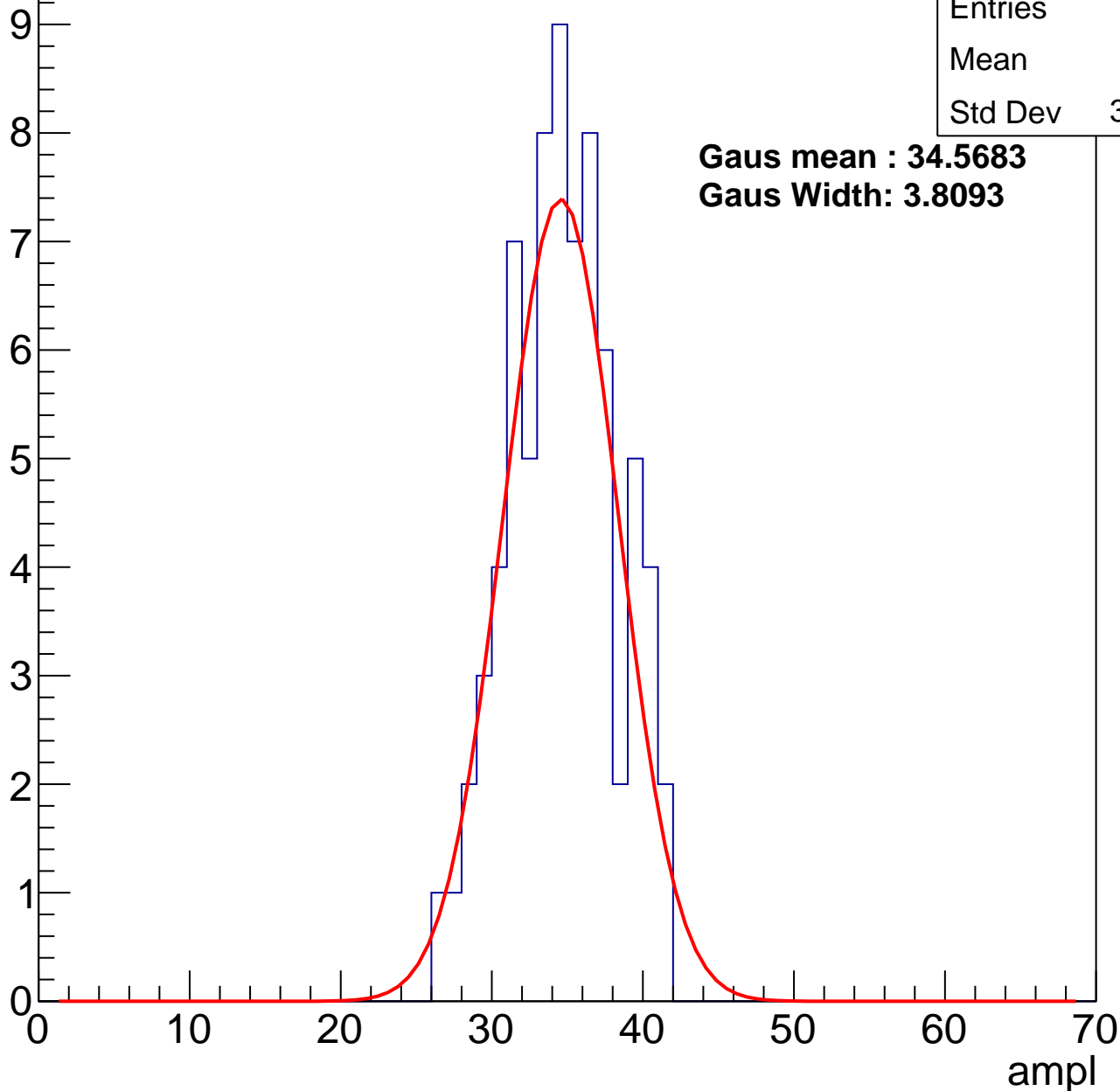
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.2
Std Dev	3.495

Gaus mean : 34.5683

Gaus Width: 3.8093



B1L102S, U18-ch77, adc2

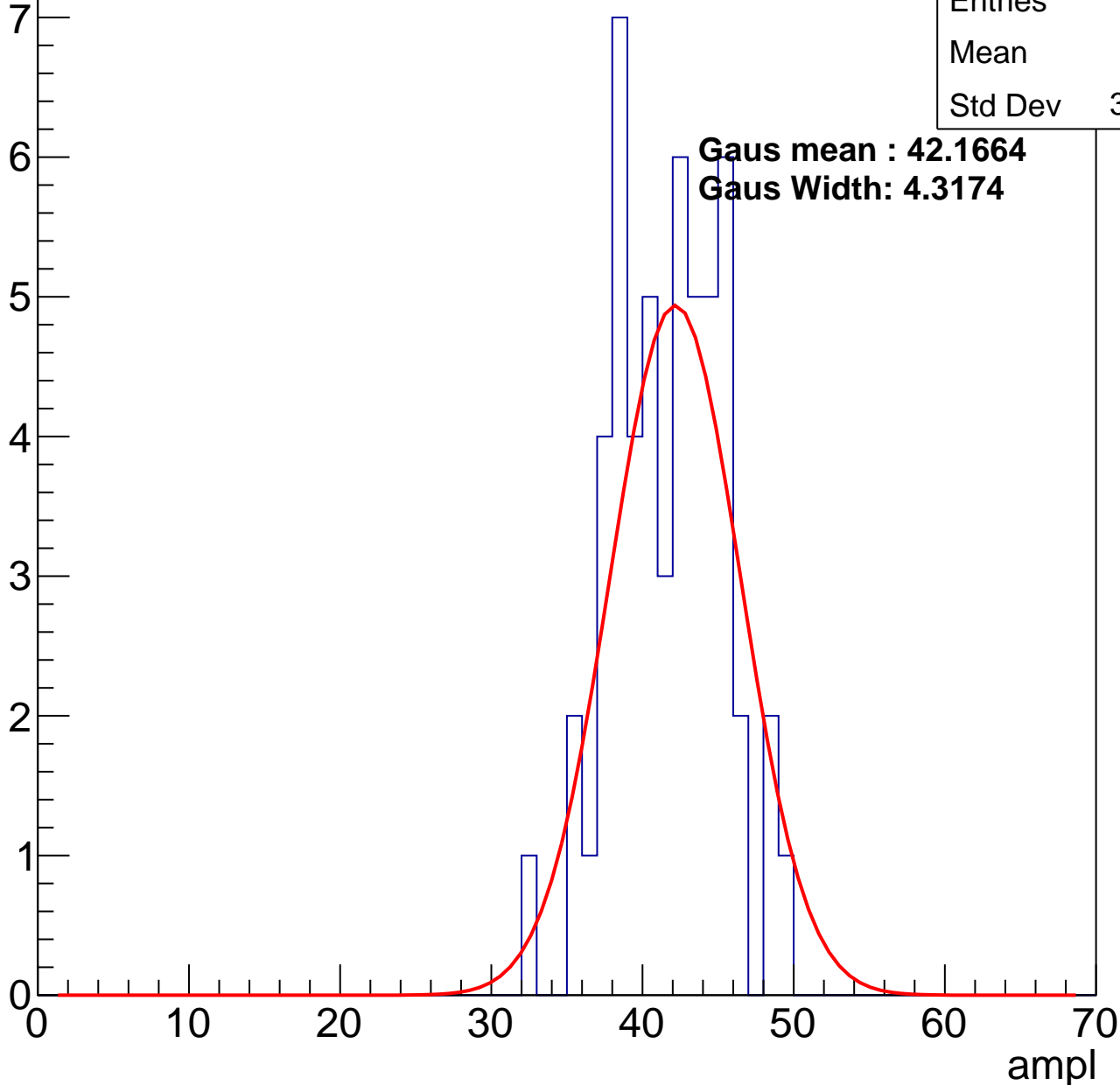
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	41.2
Std Dev	3.608

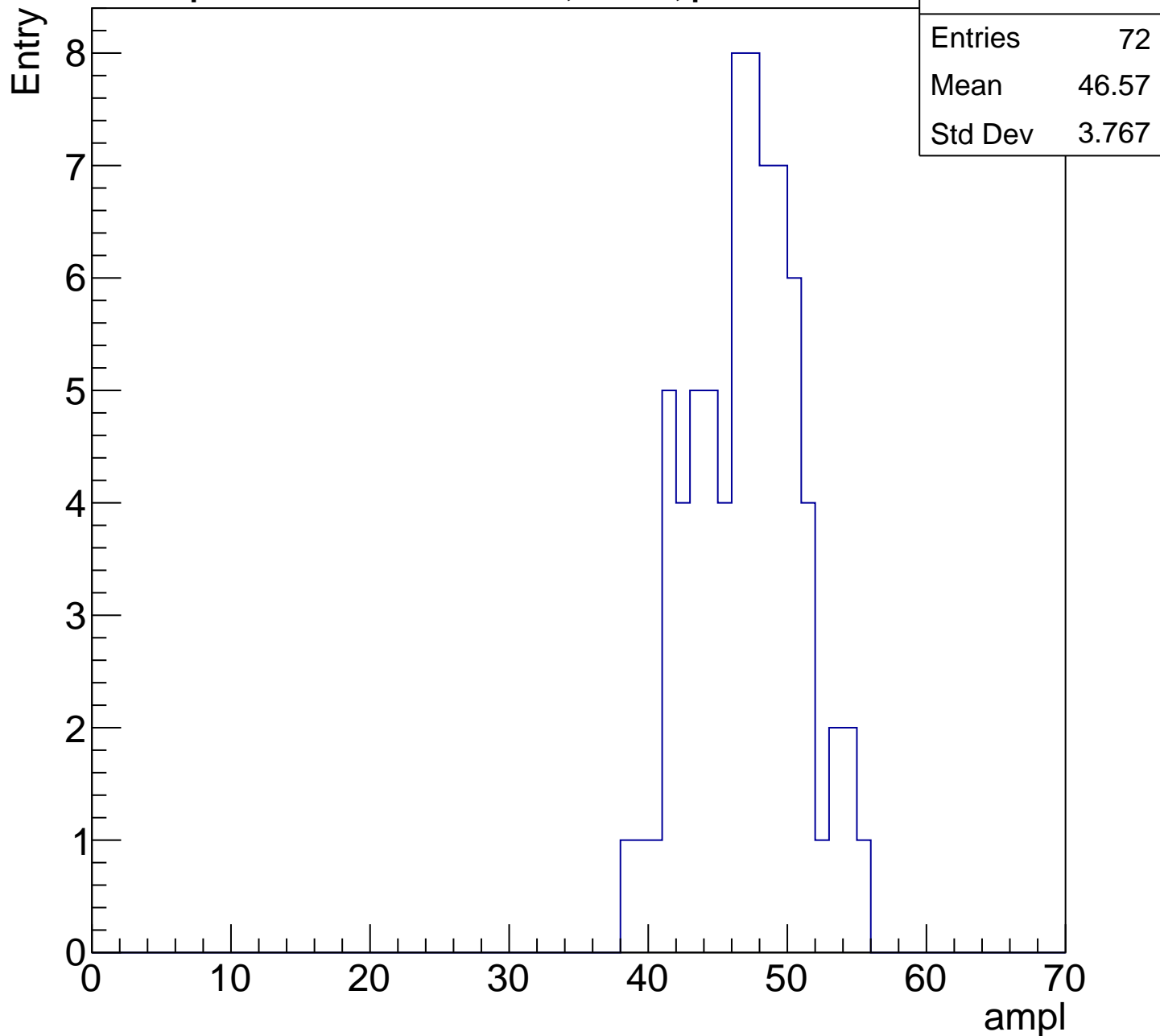
Gaus mean : 42.1664

Gaus Width: 4.3174



B1L102S, U18-ch77, adc3

calib_packv5_042523_0143.root, FC#11, port A2

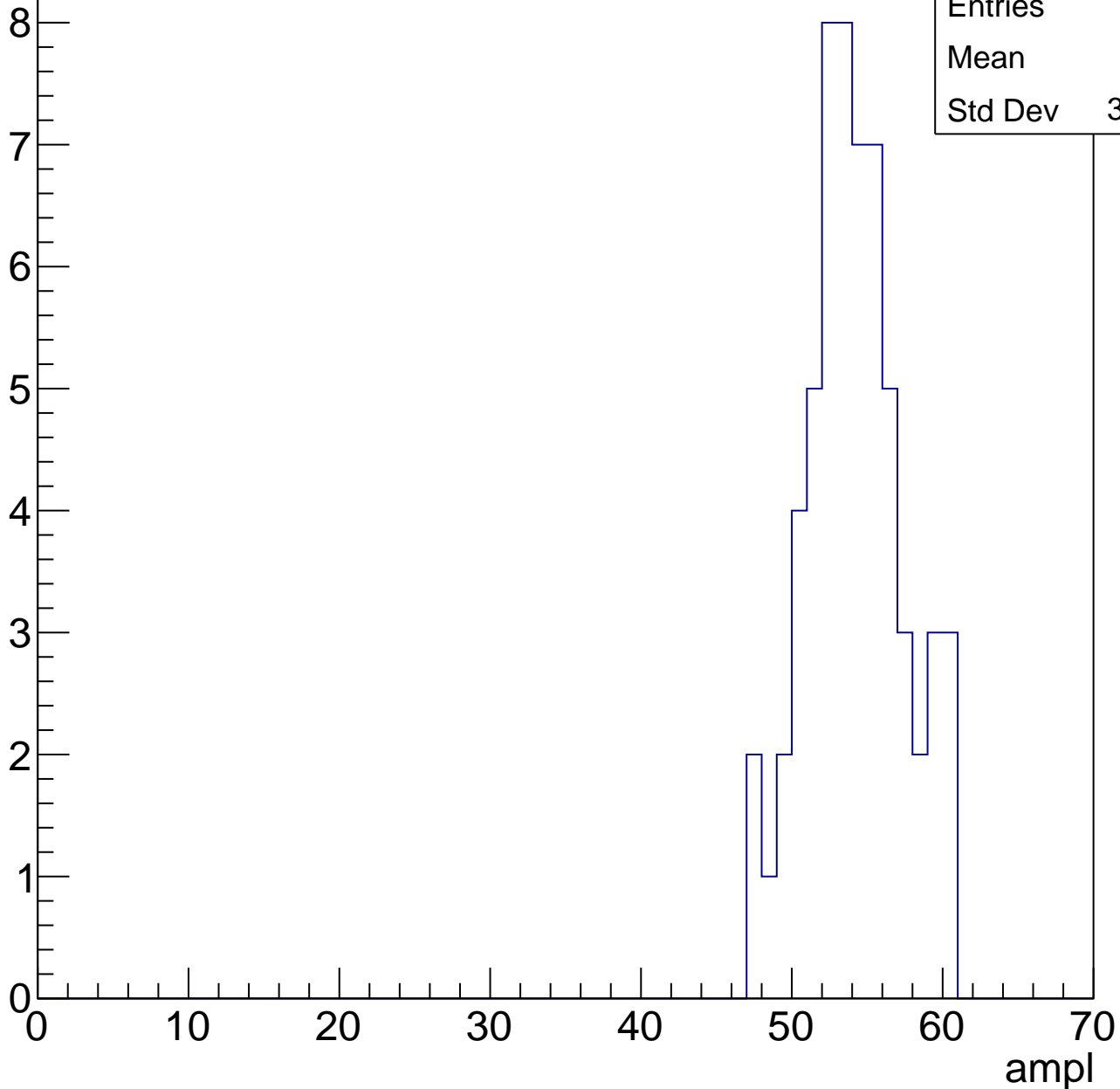


B1L102S, U18-ch77, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	53.7
Std Dev	3.153

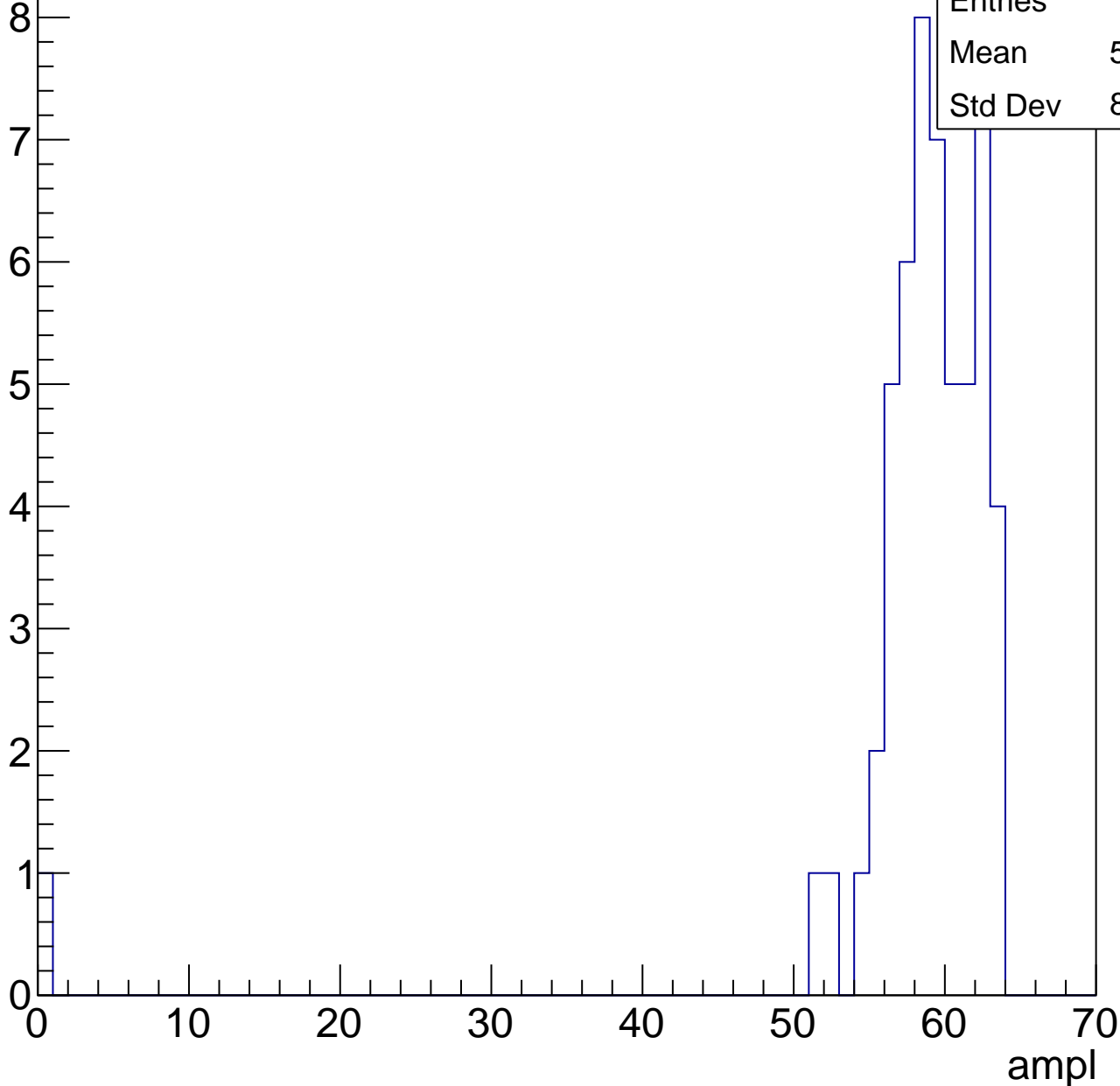


B1L102S, U18-ch77, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	57.76
Std Dev	8.395

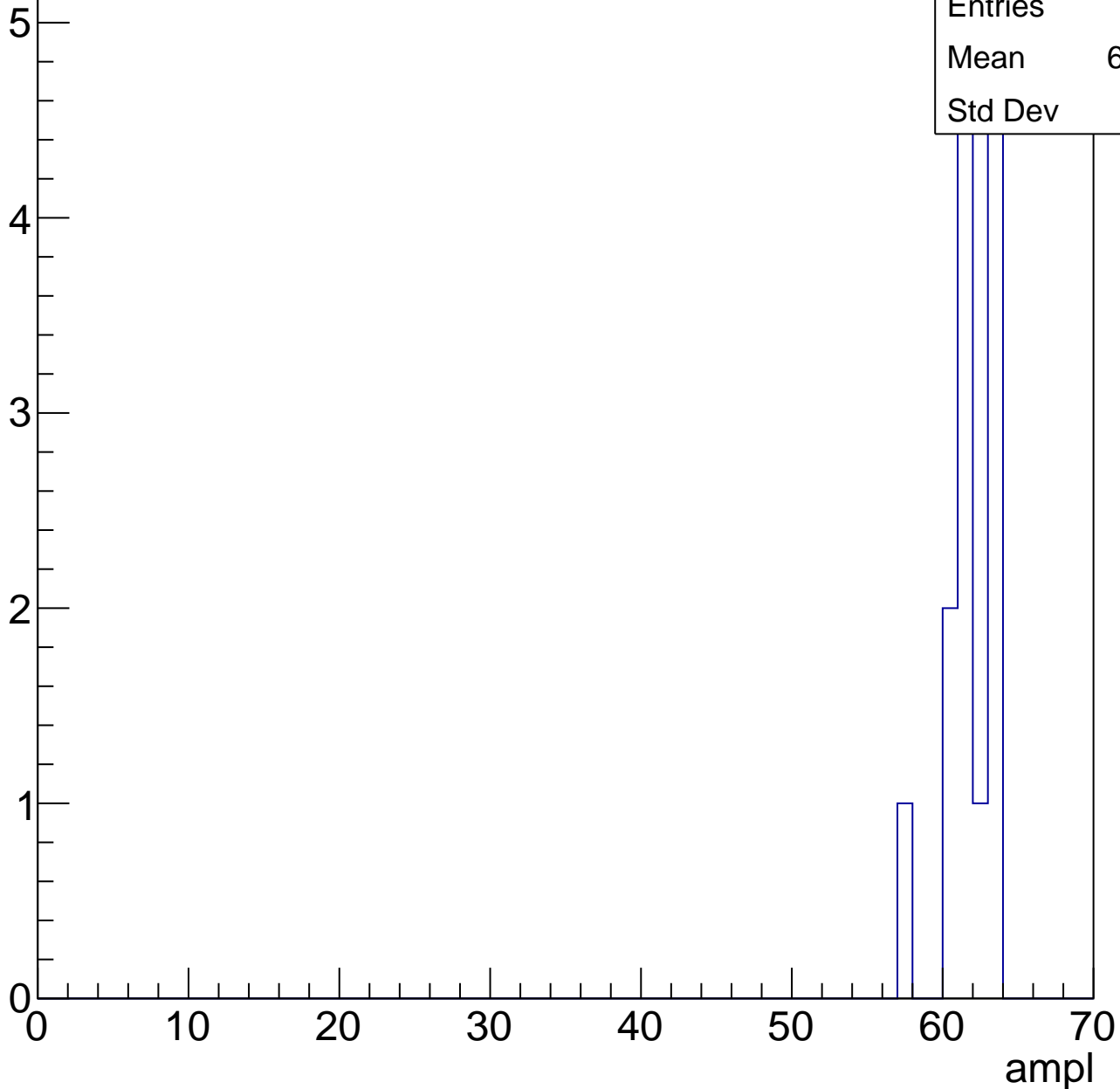


B1L102S, U18-ch77, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.36
Std Dev	1.63



B1L102S, U18-ch77, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L102S, U18-ch78, adc0

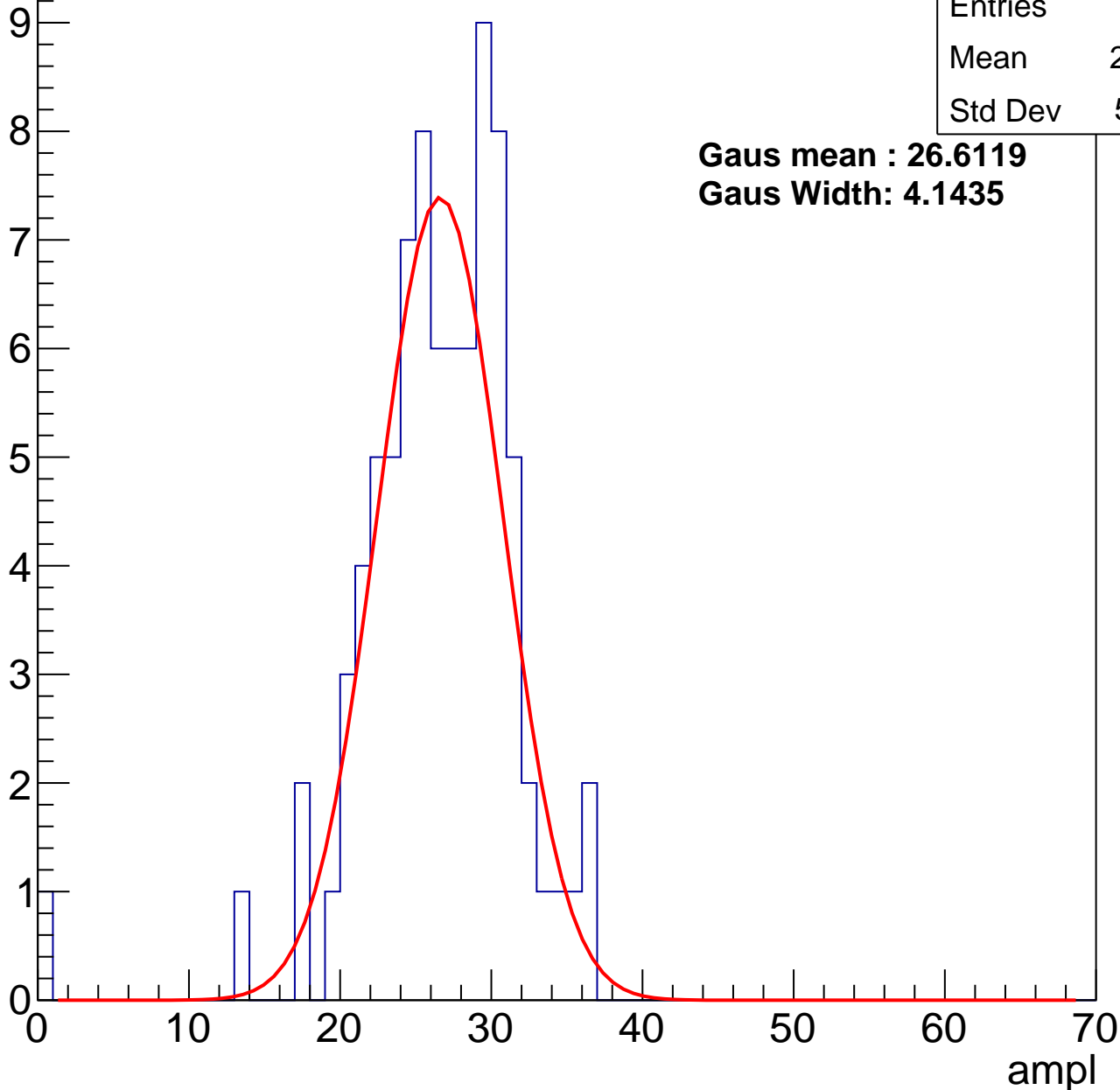
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	84
Mean	25.99
Std Dev	5.181

Gaus mean : 26.6119

Gaus Width: 4.1435



B1L102S, U18-ch78, adc1

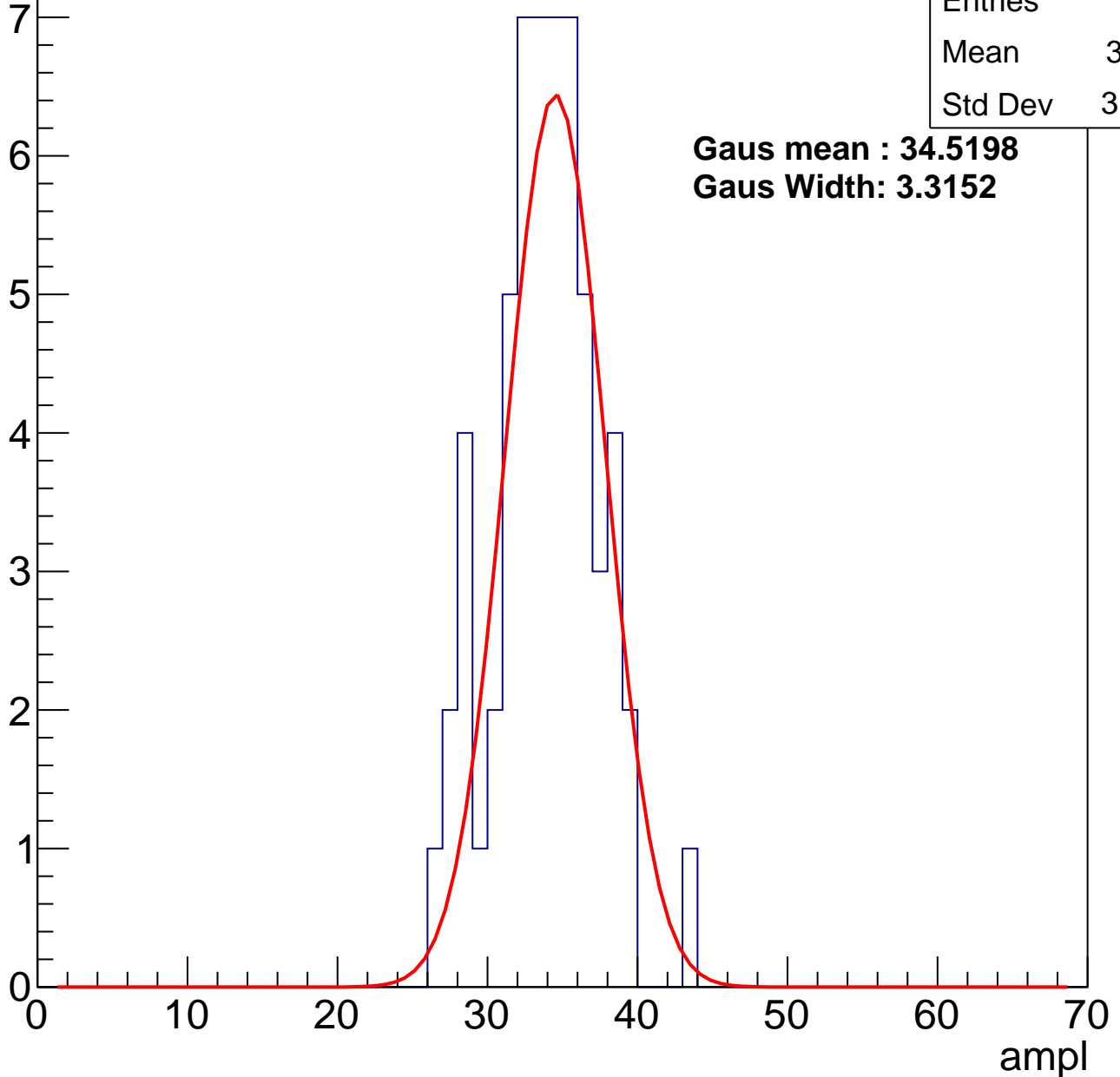
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	33.41
Std Dev	3.404

Gaus mean : 34.5198

Gaus Width: 3.3152



B1L102S, U18-ch78, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	74
Mean	40.36
Std Dev	3.718

Gaus mean : 40.8376

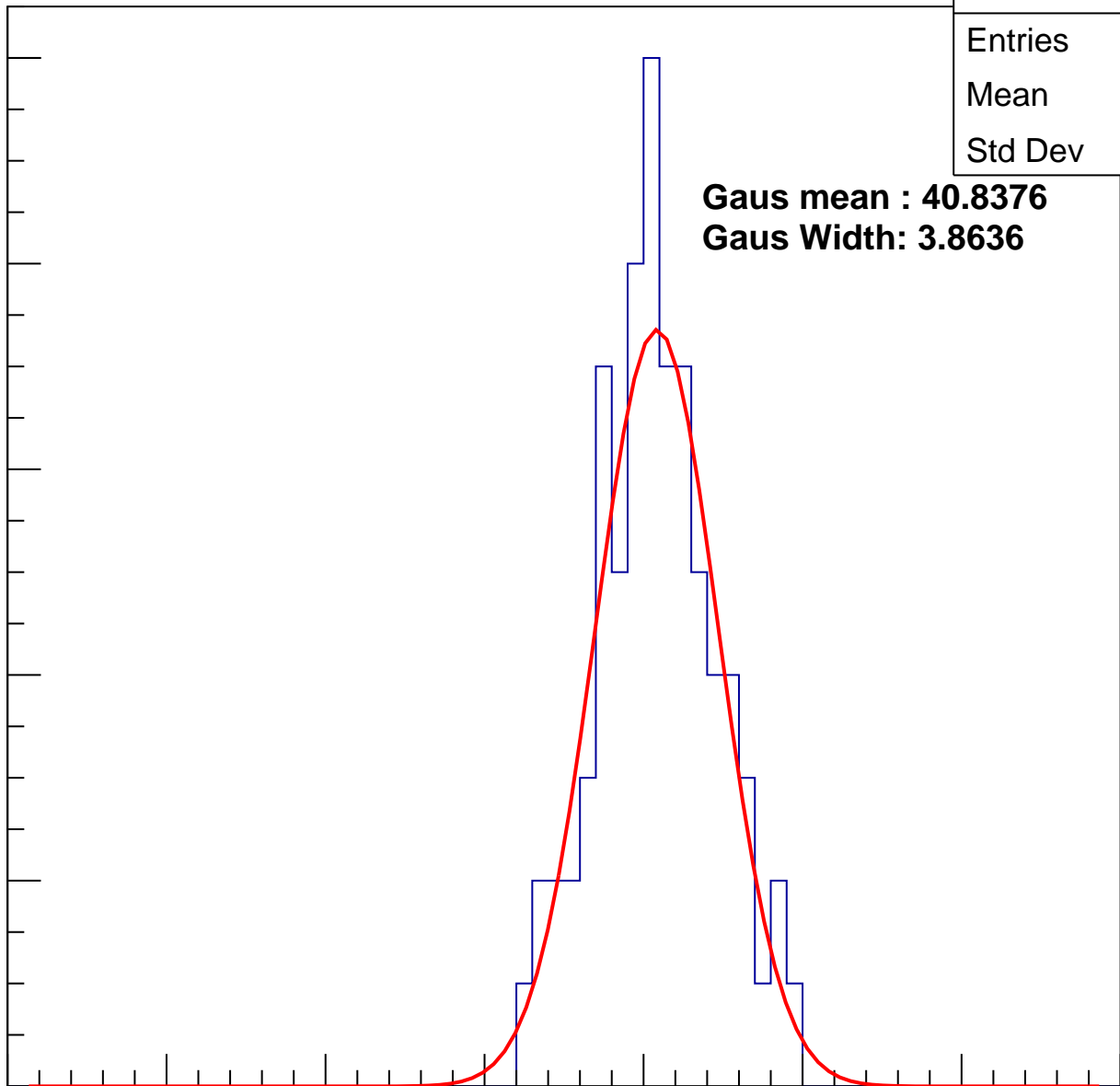
Gaus Width: 3.8636

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

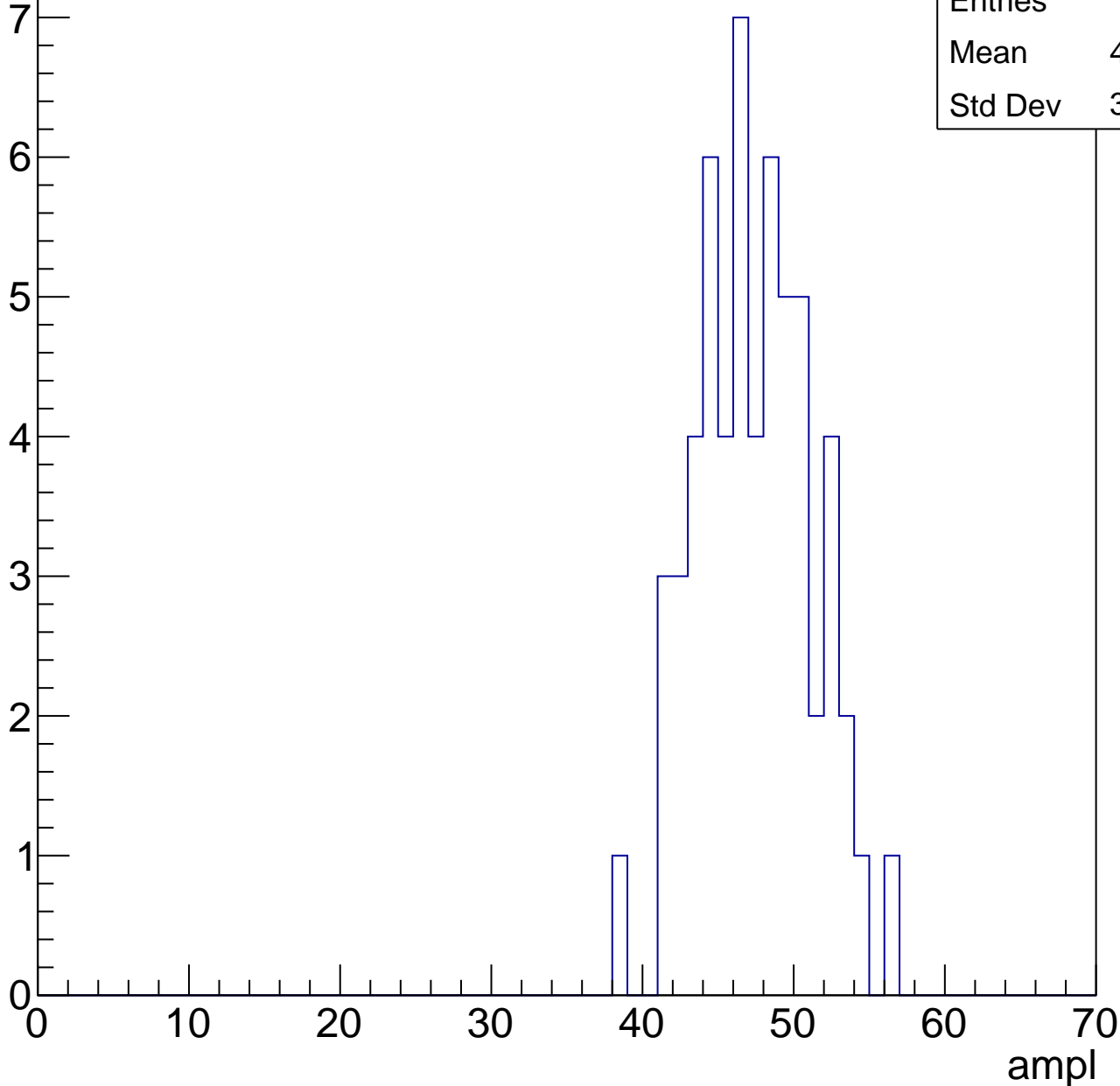


B1L102S, U18-ch78, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	46.93
Std Dev	3.736

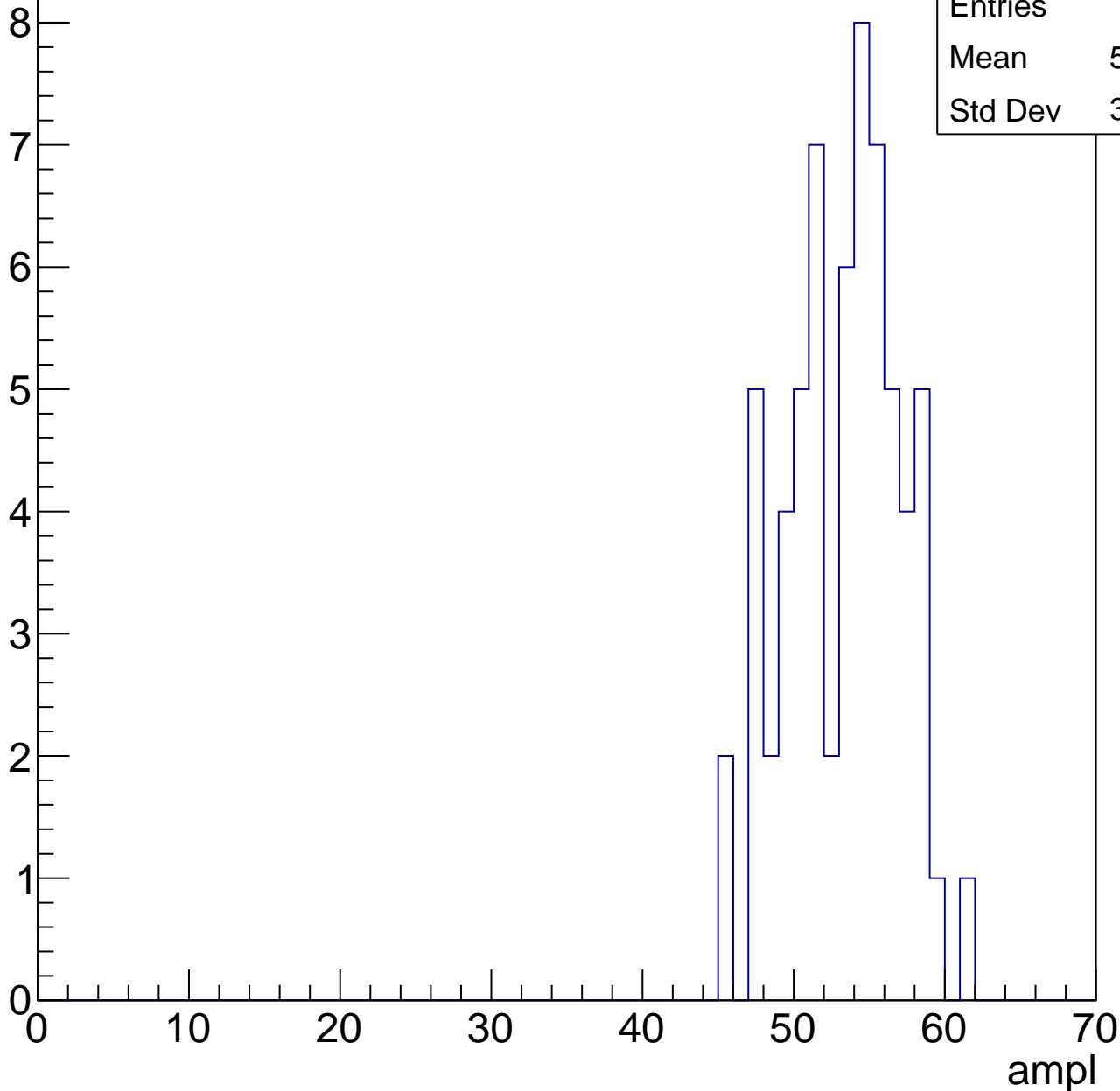


B1L102S, U18-ch78, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	52.83
Std Dev	3.677

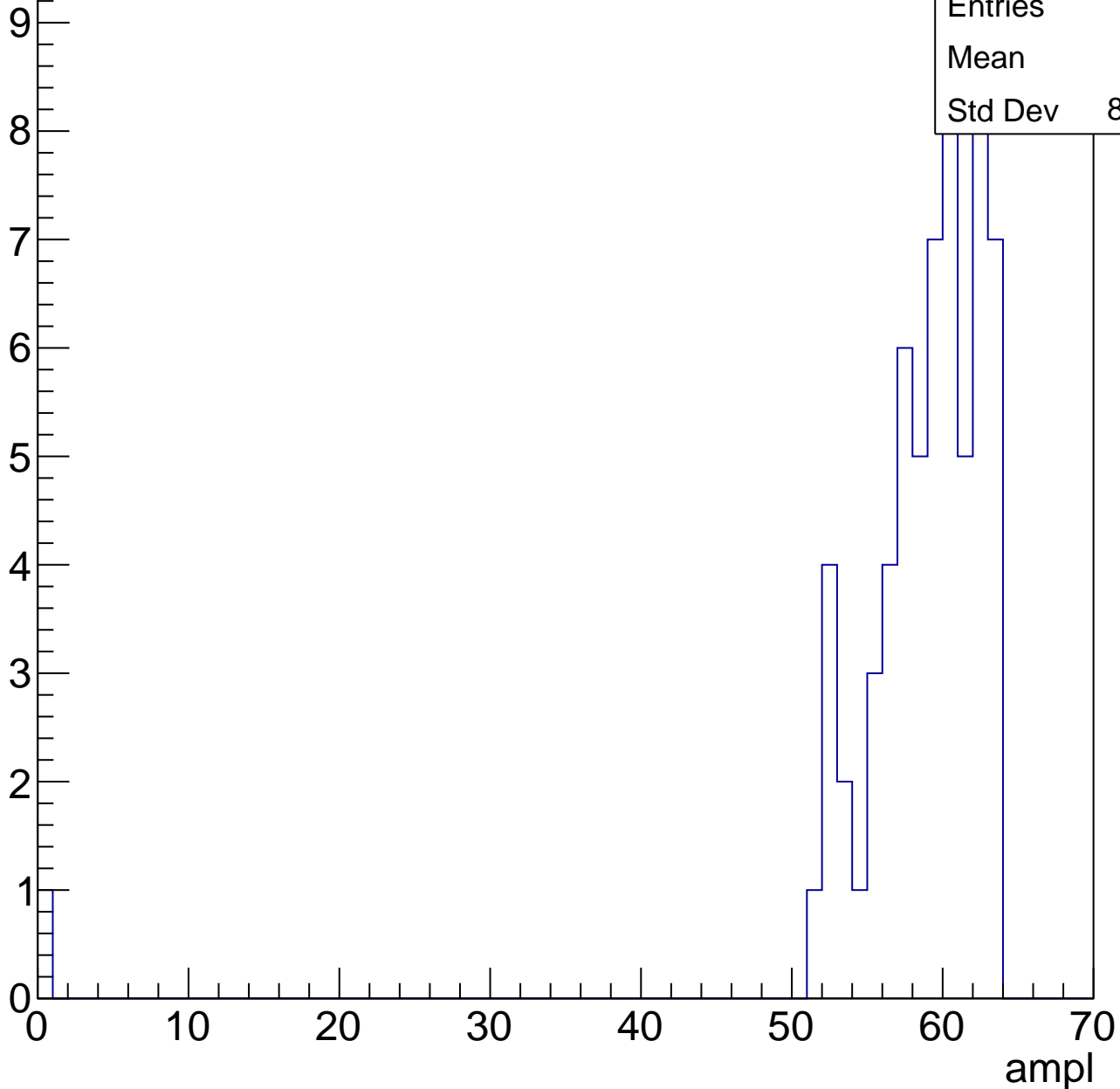


B1L102S, U18-ch78, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

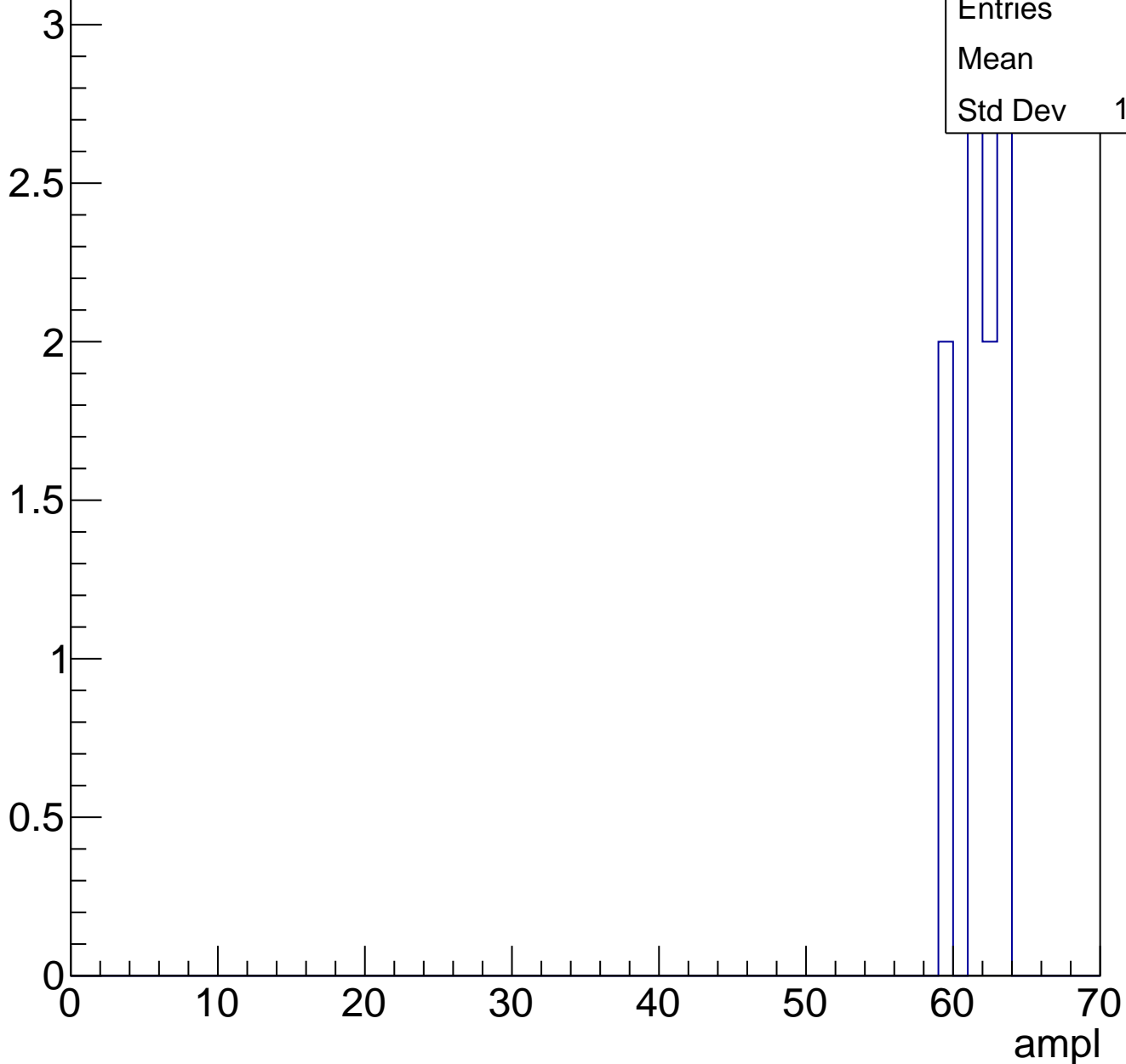
Entries	63
Mean	57.7
Std Dev	8.025



B1L102S, U18-ch78, adc6

calib_packv5_042523_0143.root, FC#11, port A2

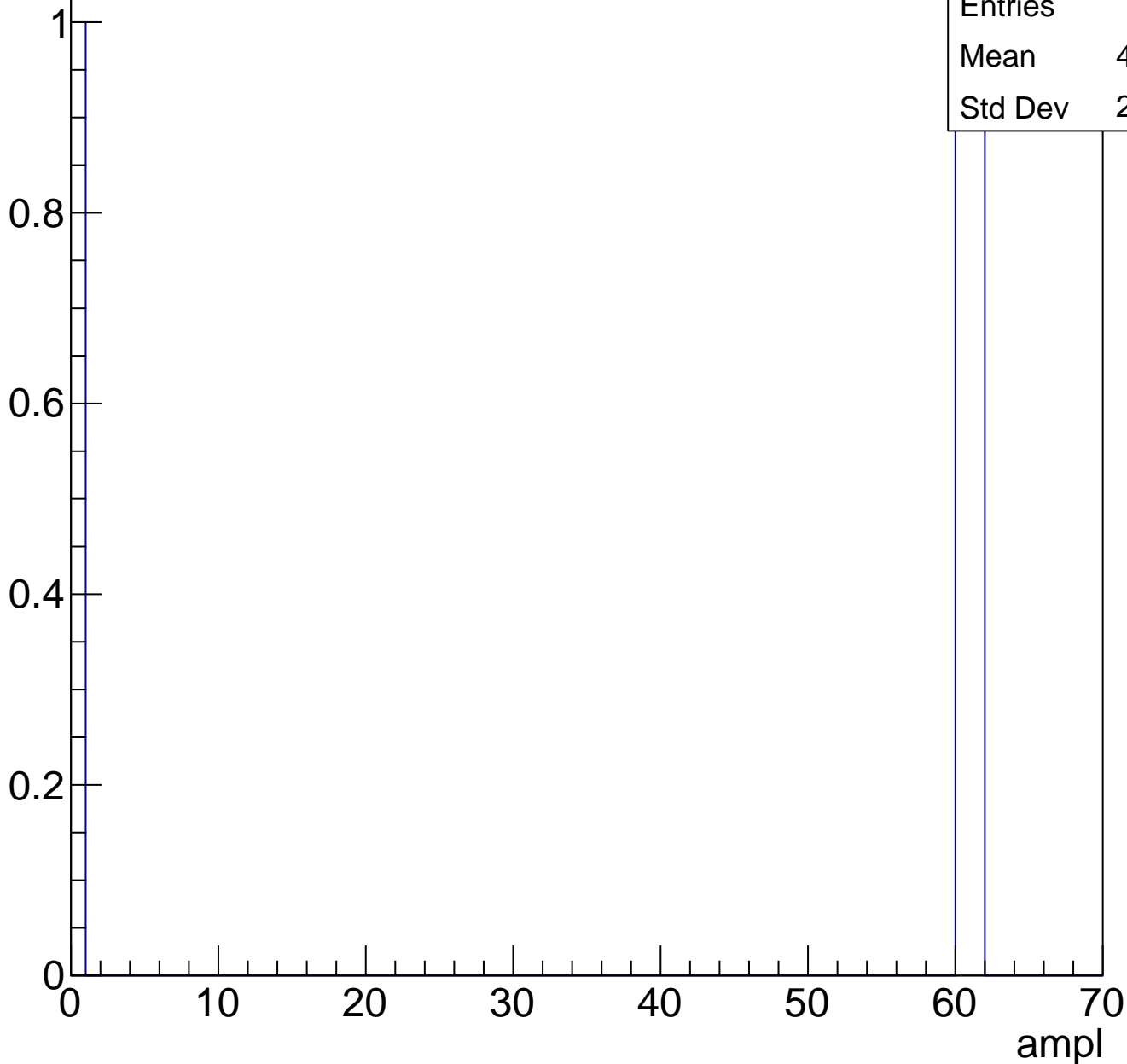
Entry



B1L102S, U18-ch78, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch79, adc0

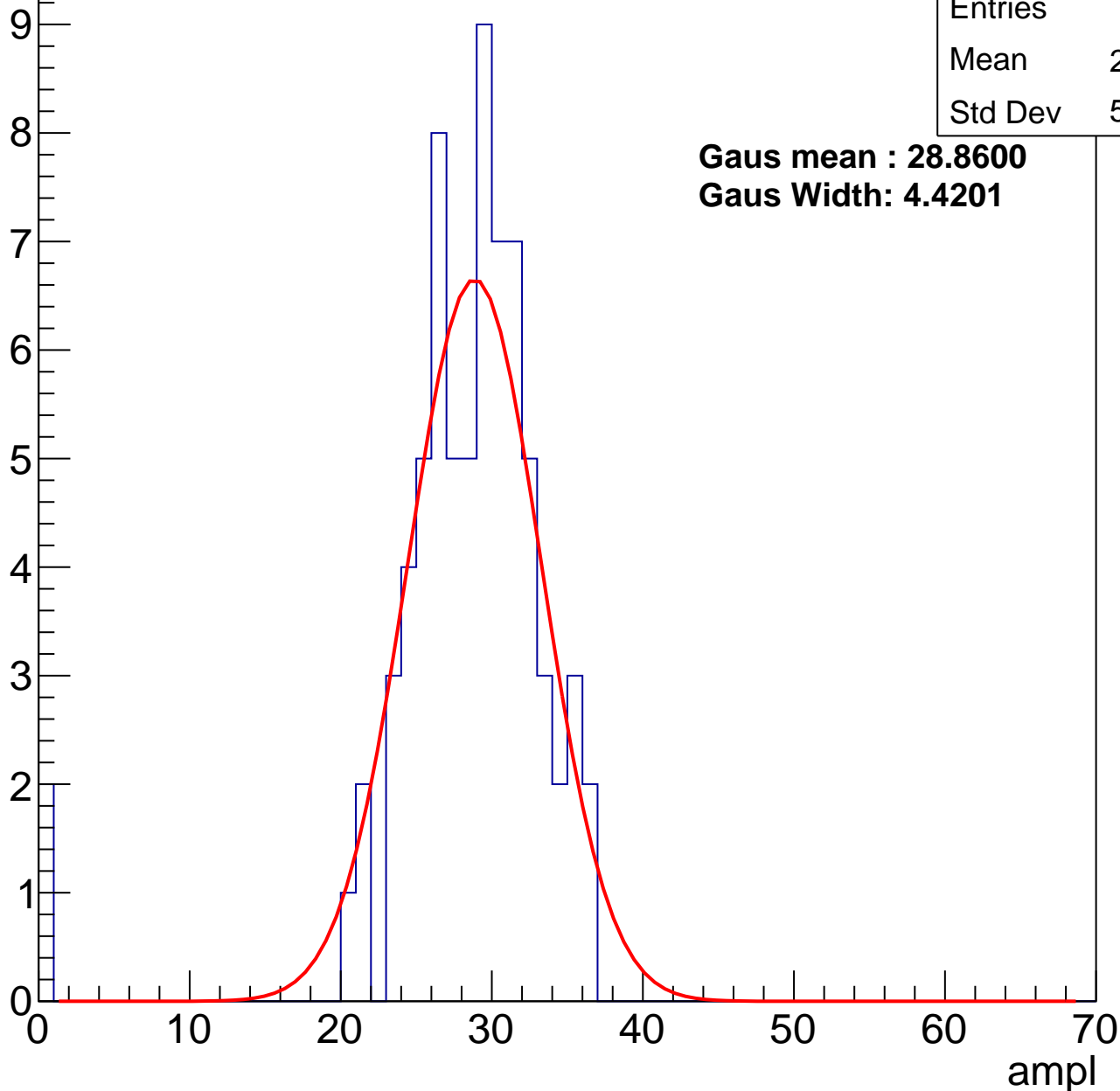
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	27.77
Std Dev	5.909

Gaus mean : 28.8600

Gaus Width: 4.4201



B1L102S, U18-ch79, adc1

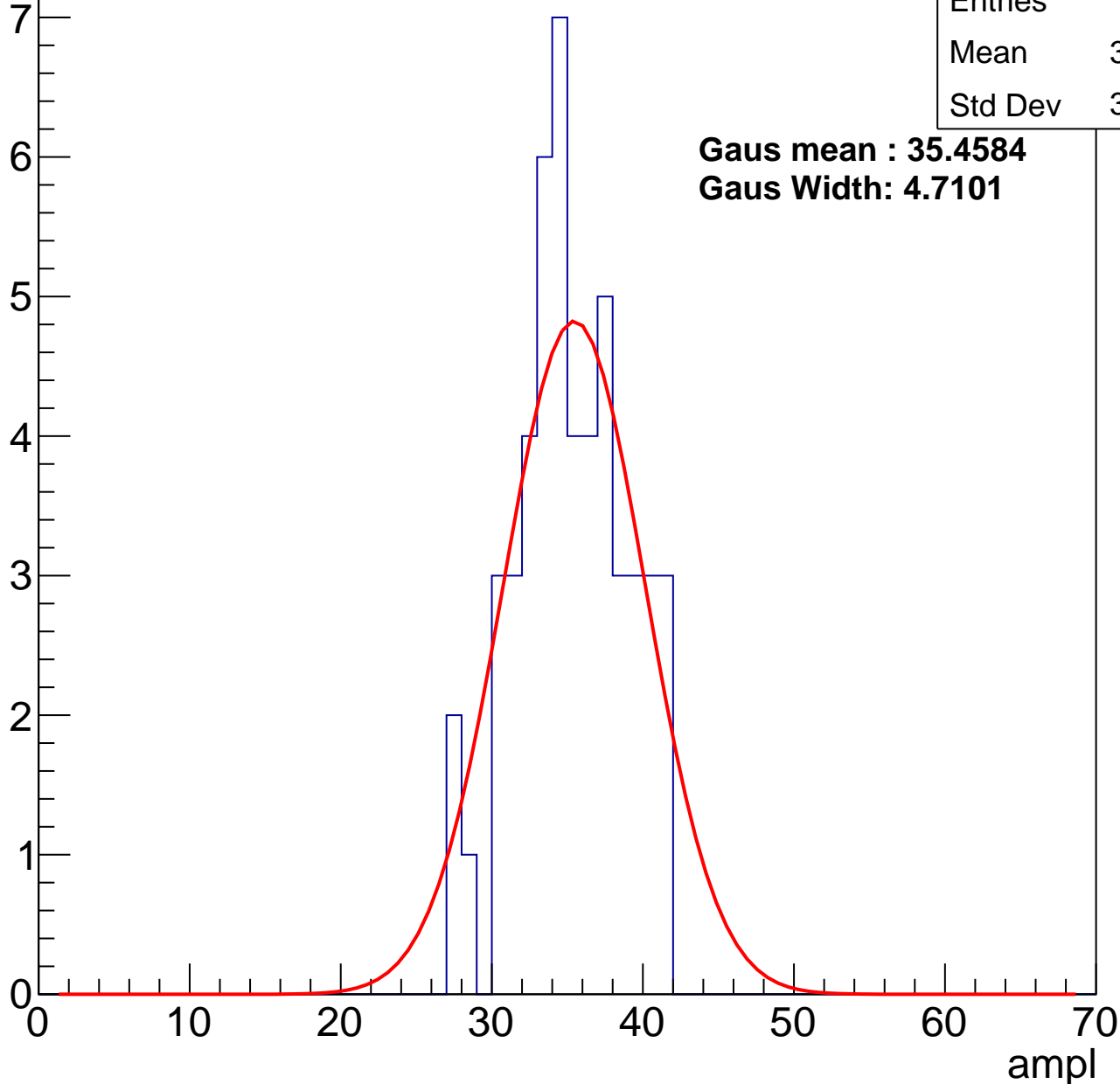
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	34.75
Std Dev	3.558

Gaus mean : 35.4584

Gaus Width: 4.7101



B1L102S, U18-ch79, adc2

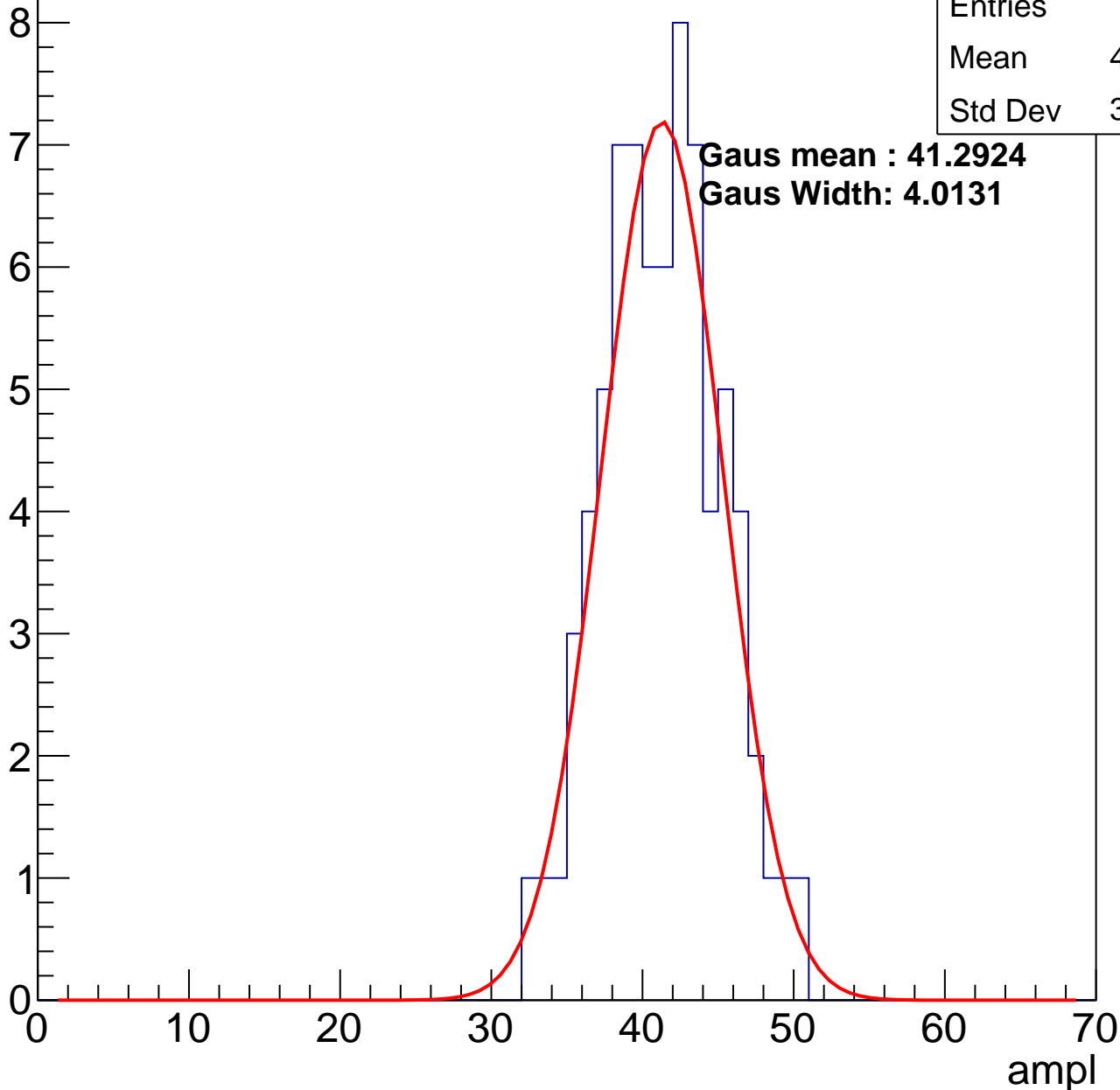
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	40.82
Std Dev	3.846

Gaus mean : 41.2924

Gaus Width: 4.0131

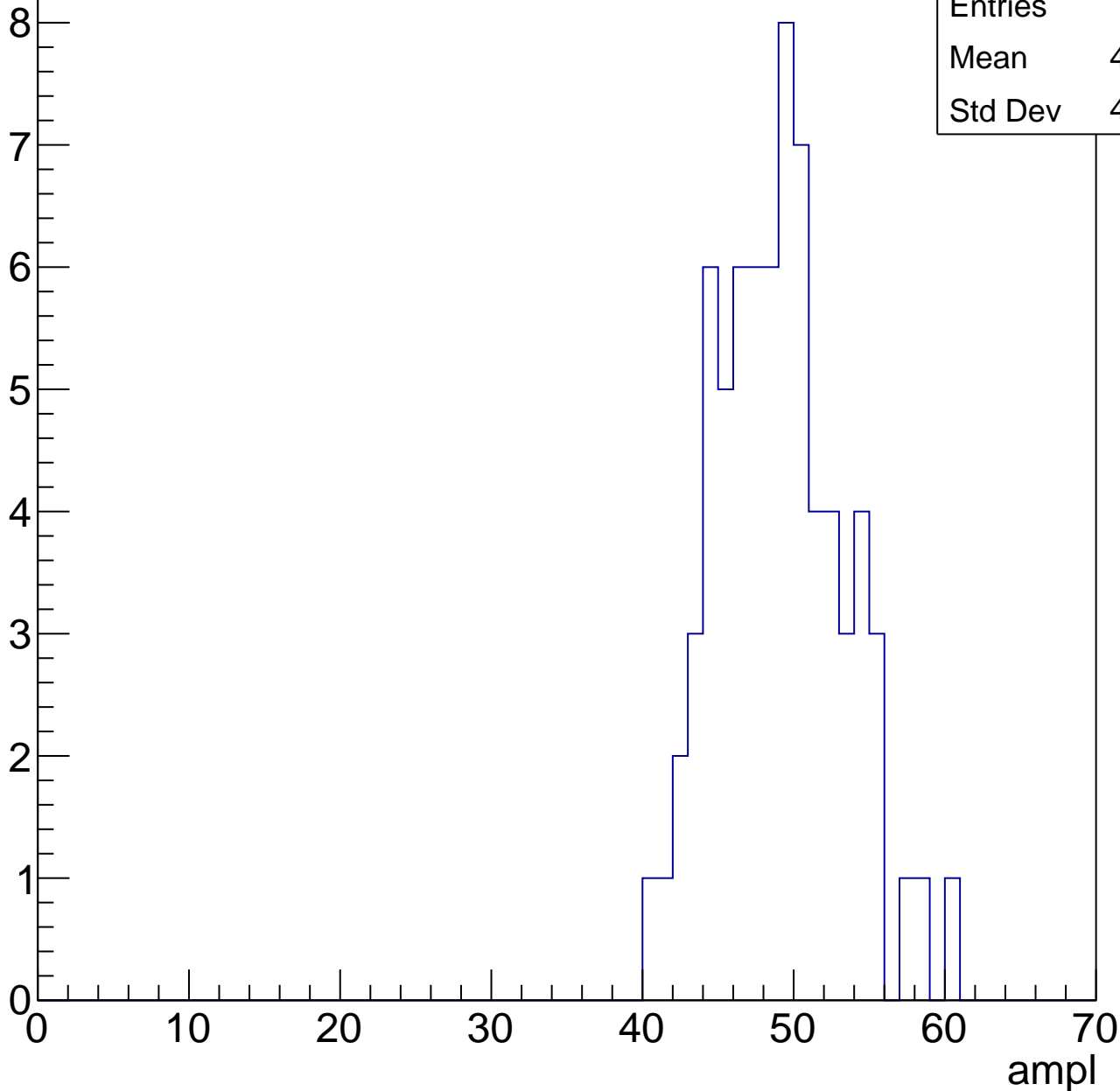


B1L102S, U18-ch79, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.58
Std Dev	4.139

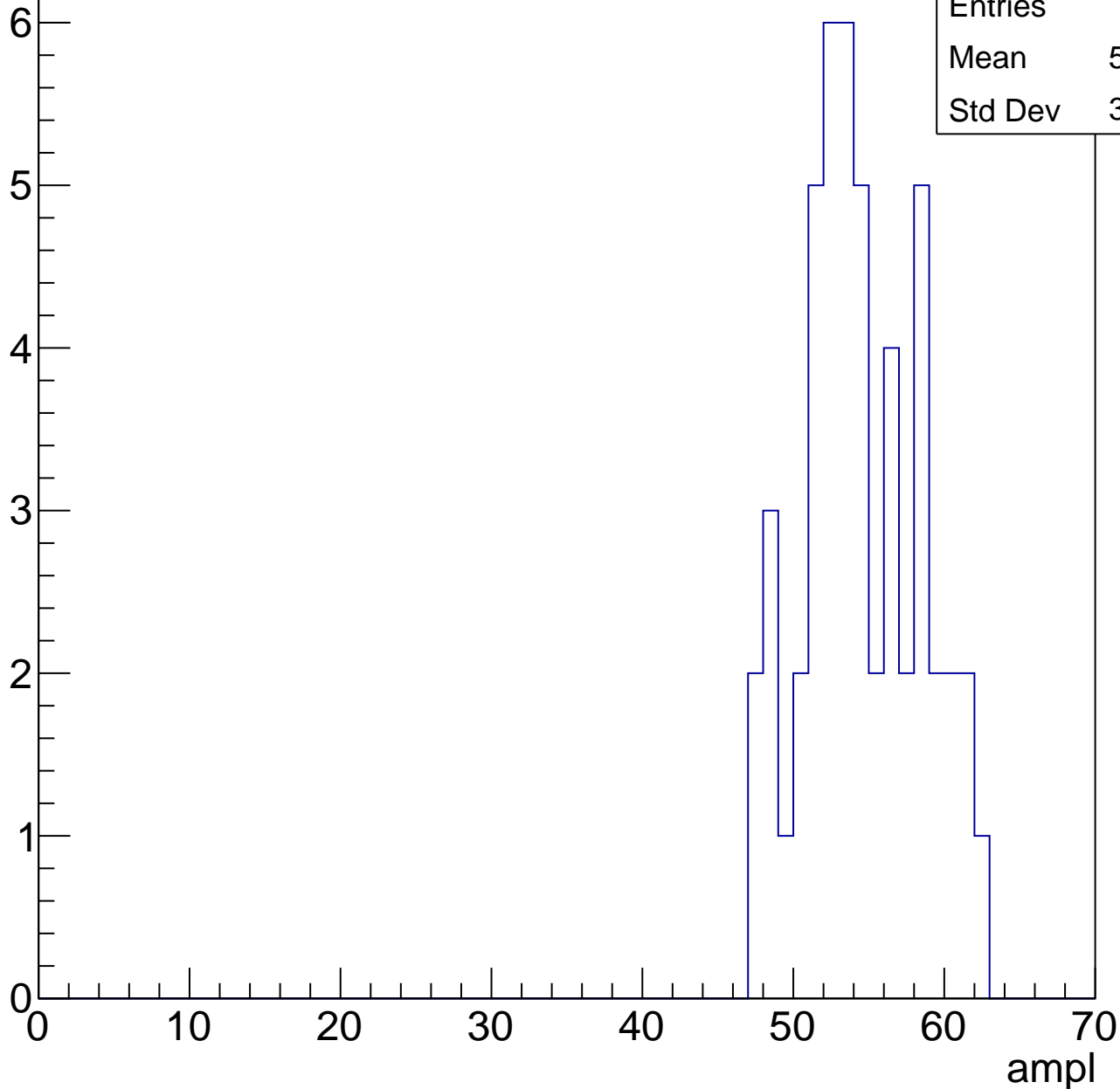


B1L102S, U18-ch79, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	54.04
Std Dev	3.842

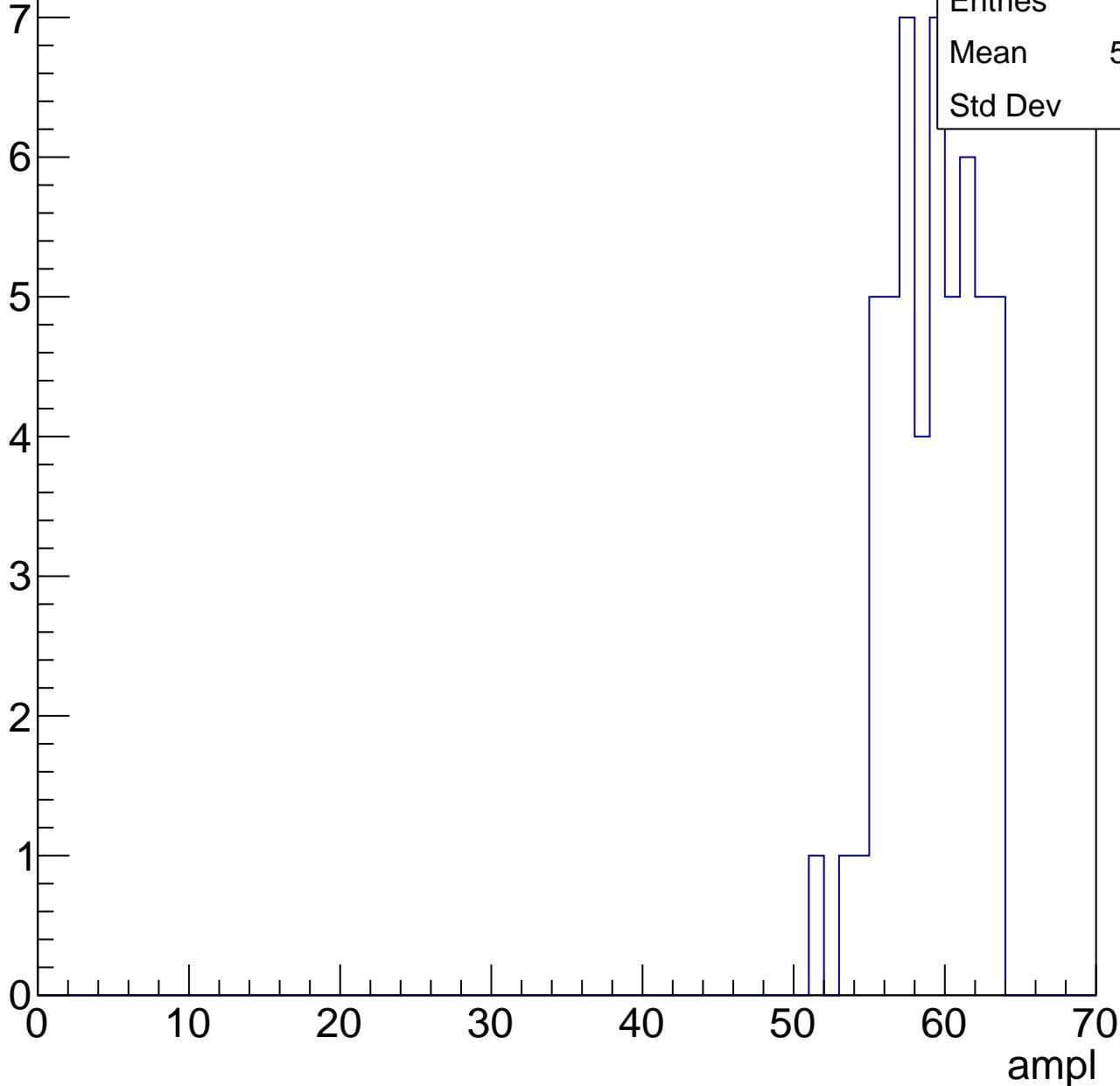


B1L102S, U18-ch79, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.62
Std Dev	2.87

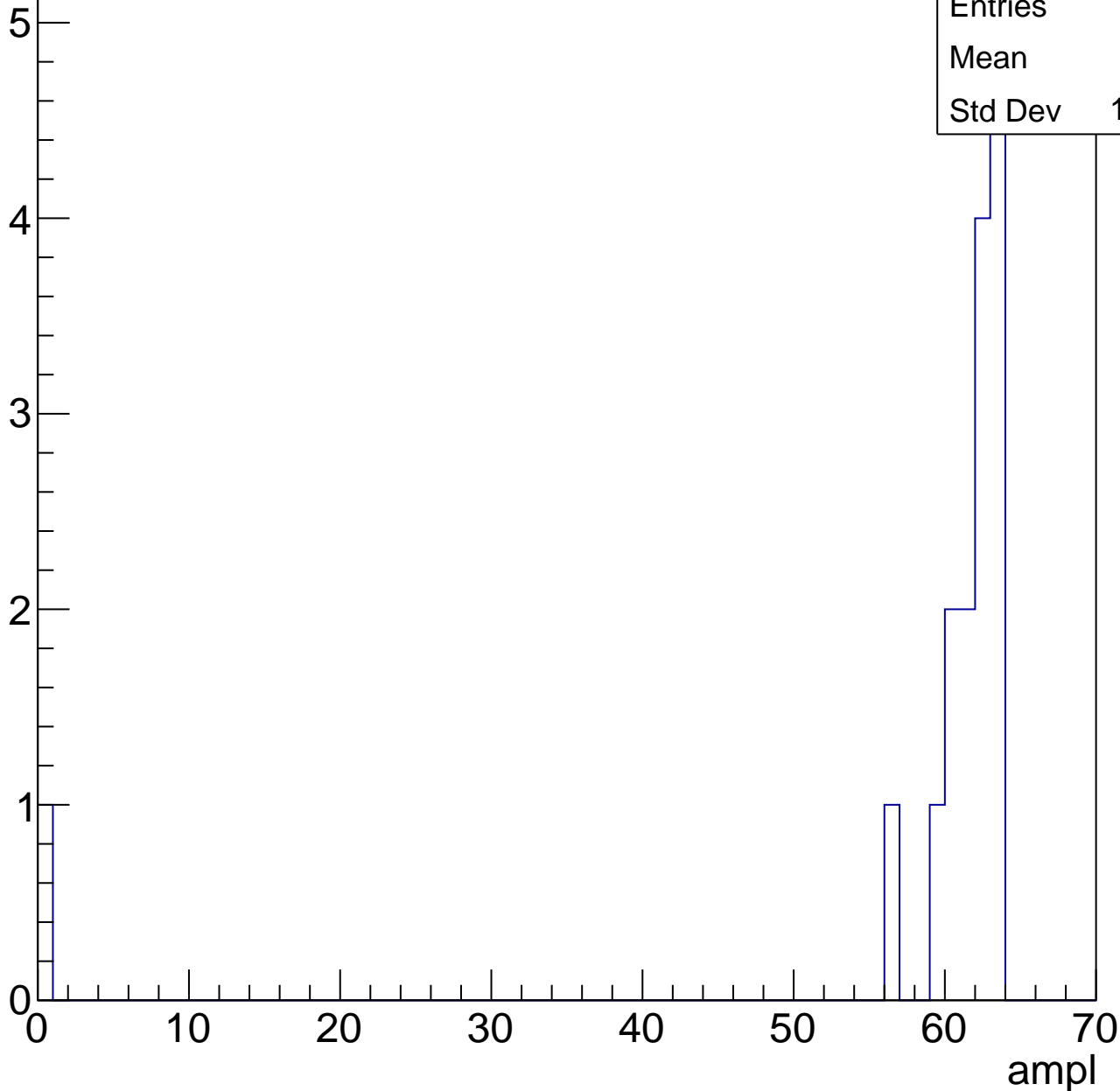


B1L102S, U18-ch79, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	16
Mean	57.5
Std Dev	14.96



B1L102S, U18-ch79, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch80, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	91
Mean	26.31
Std Dev	7.348

Gaus mean : 28.2825

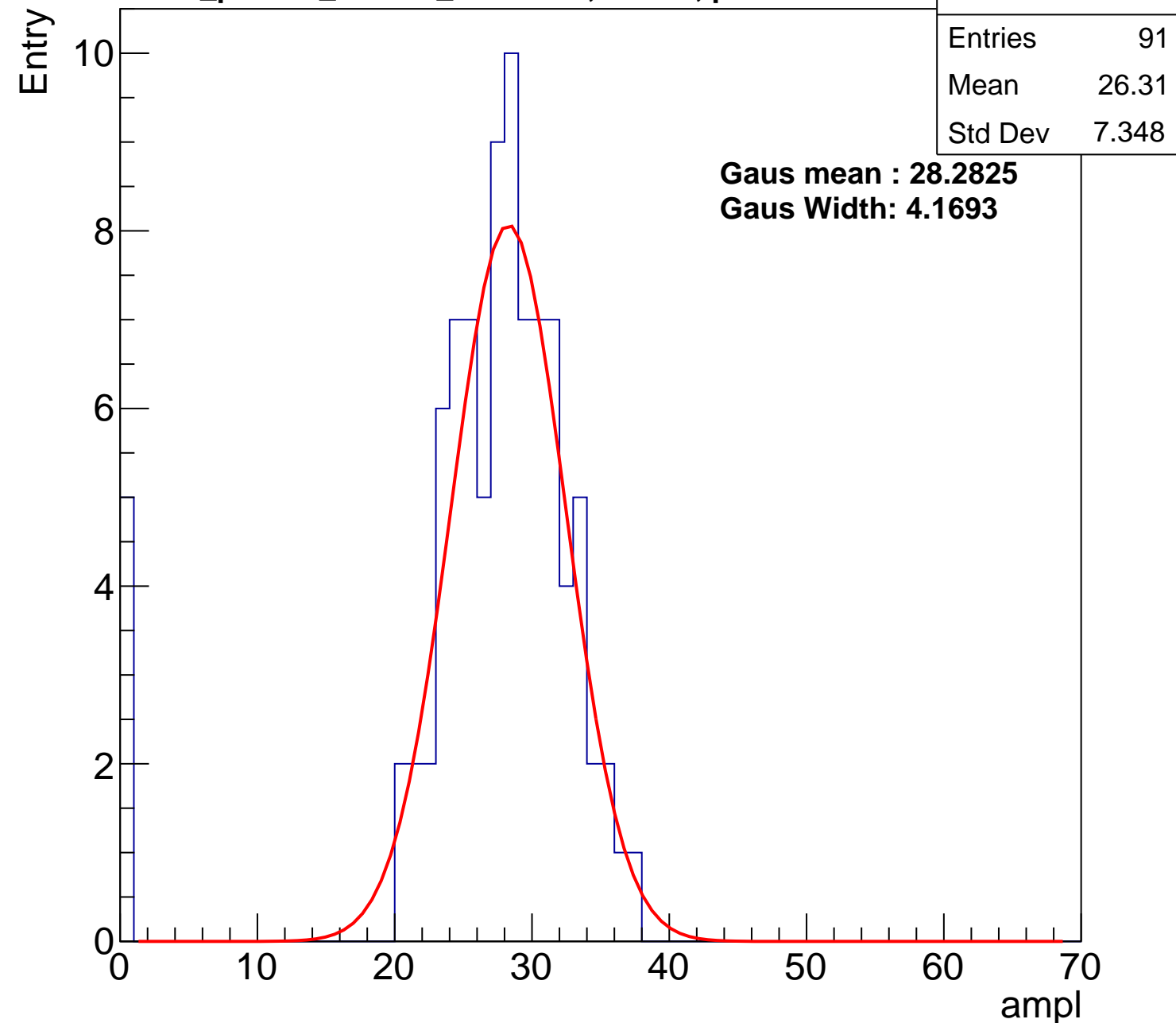
Gaus Width: 4.1693

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch80, adc1

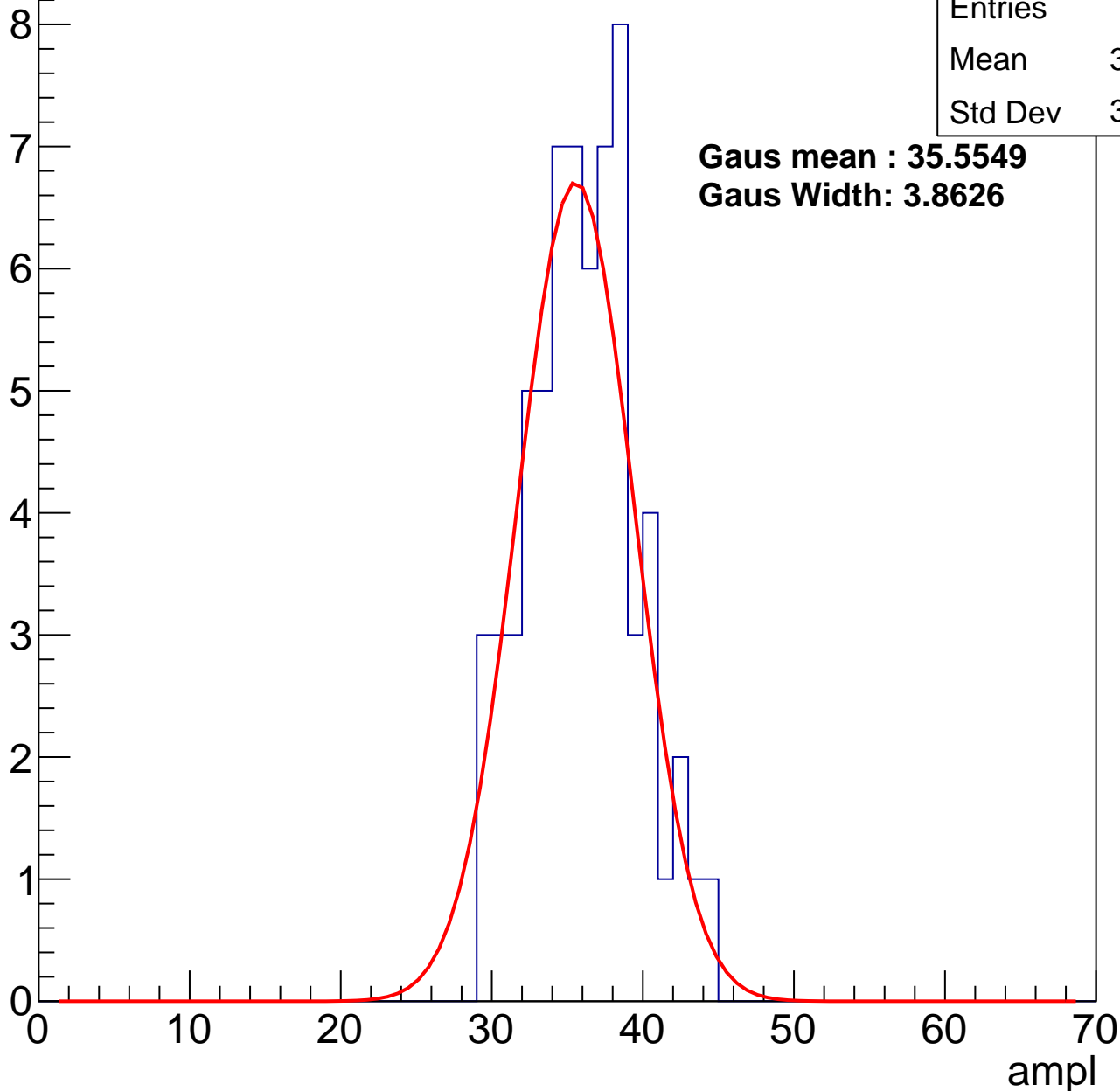
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	35.55
Std Dev	3.517

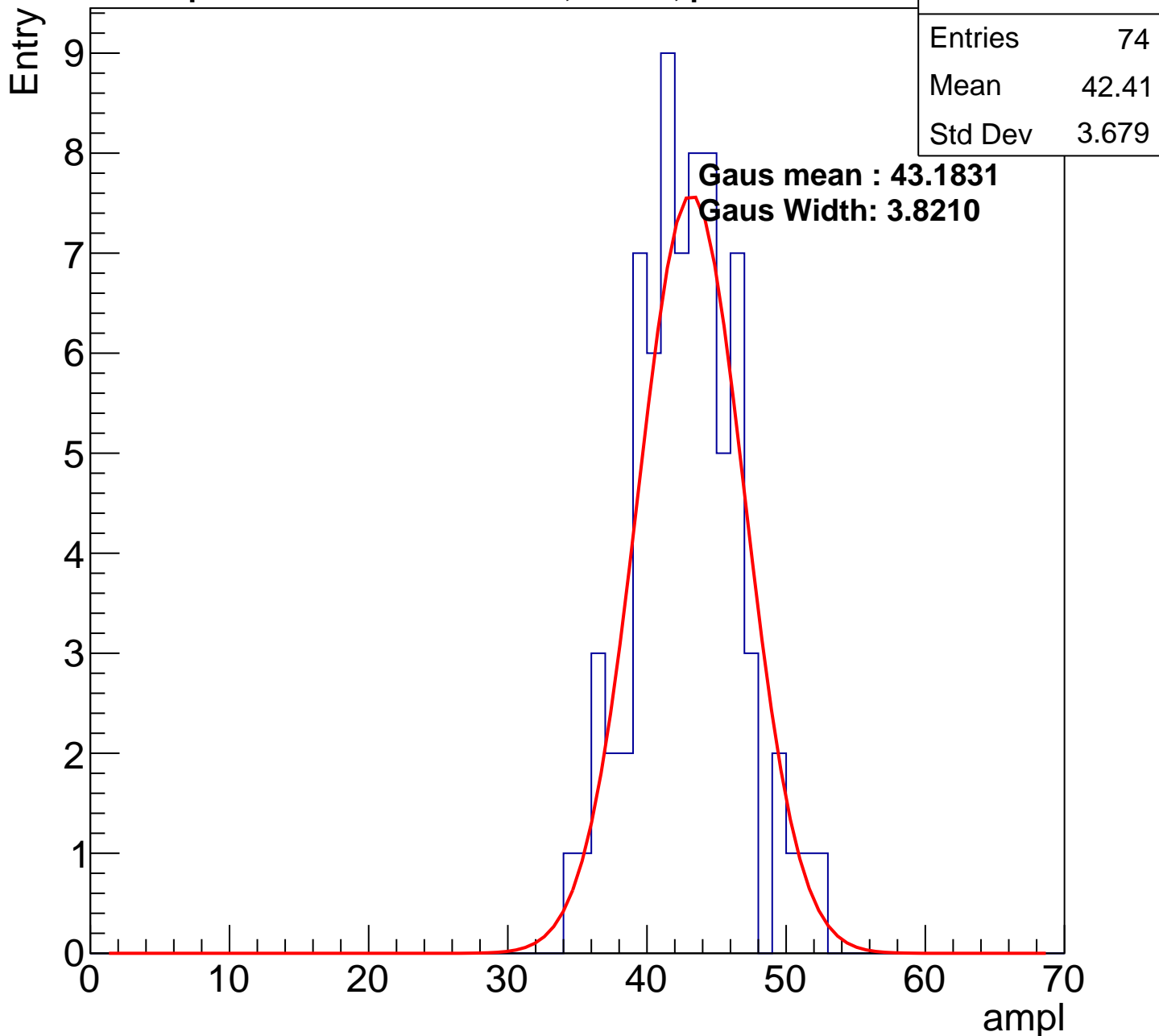
Gaus mean : 35.5549

Gaus Width: 3.8626



B1L102S, U18-ch80, adc2

calib_packv5_042523_0143.root, FC#11, port A2

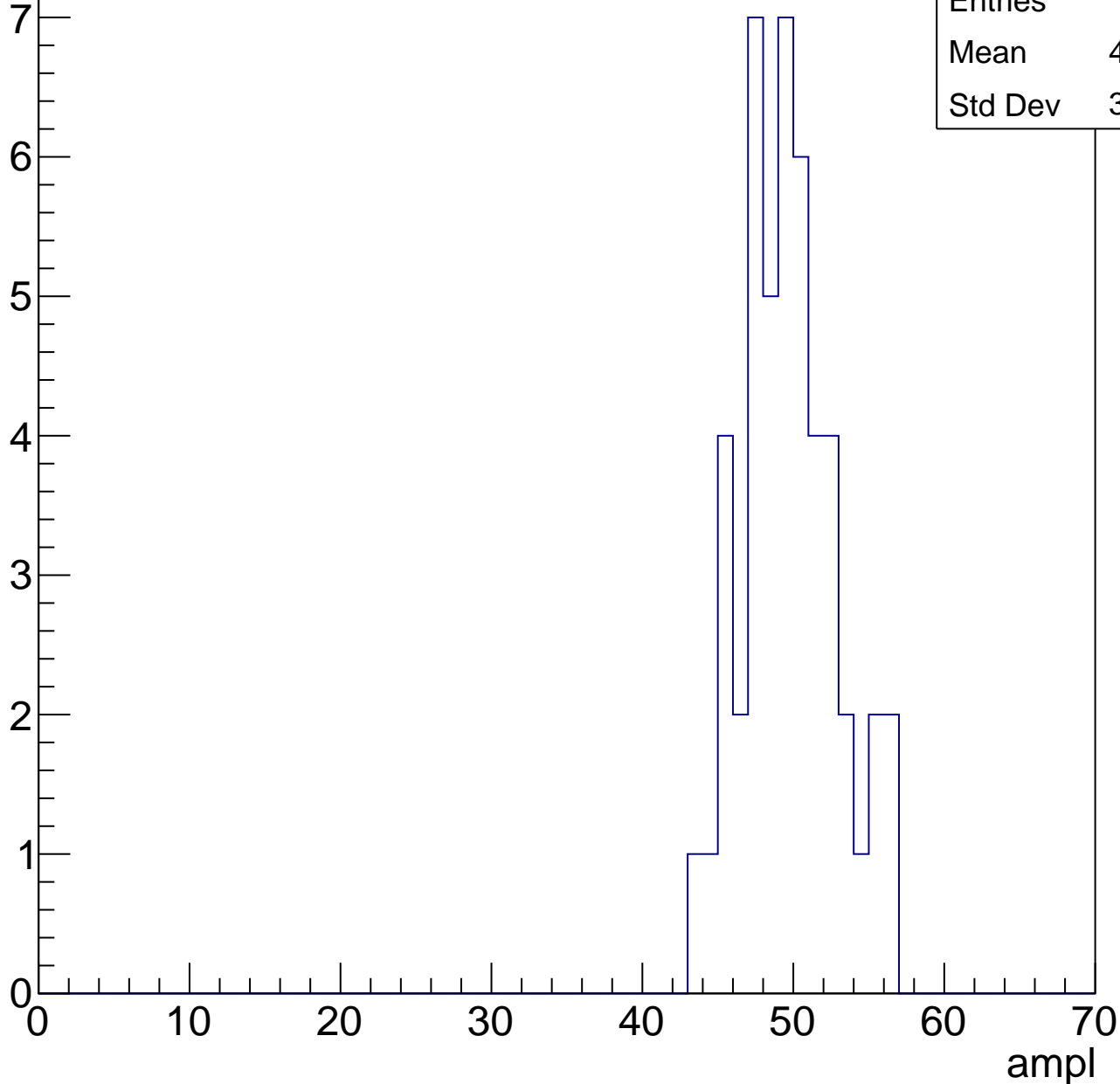


B1L102S, U18-ch80, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	49.27
Std Dev	3.087

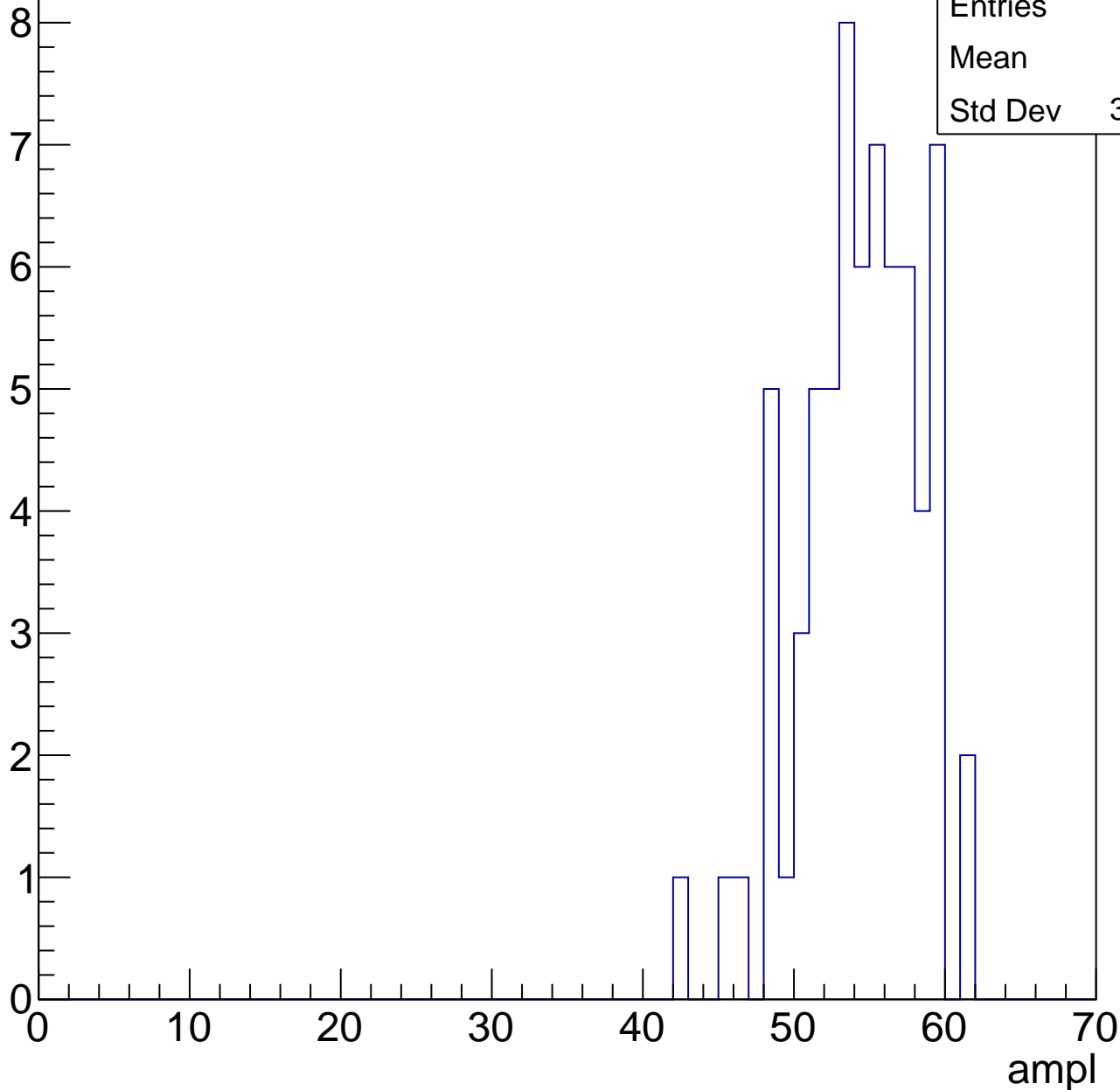


B1L102S, U18-ch80, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	53.9
Std Dev	3.904

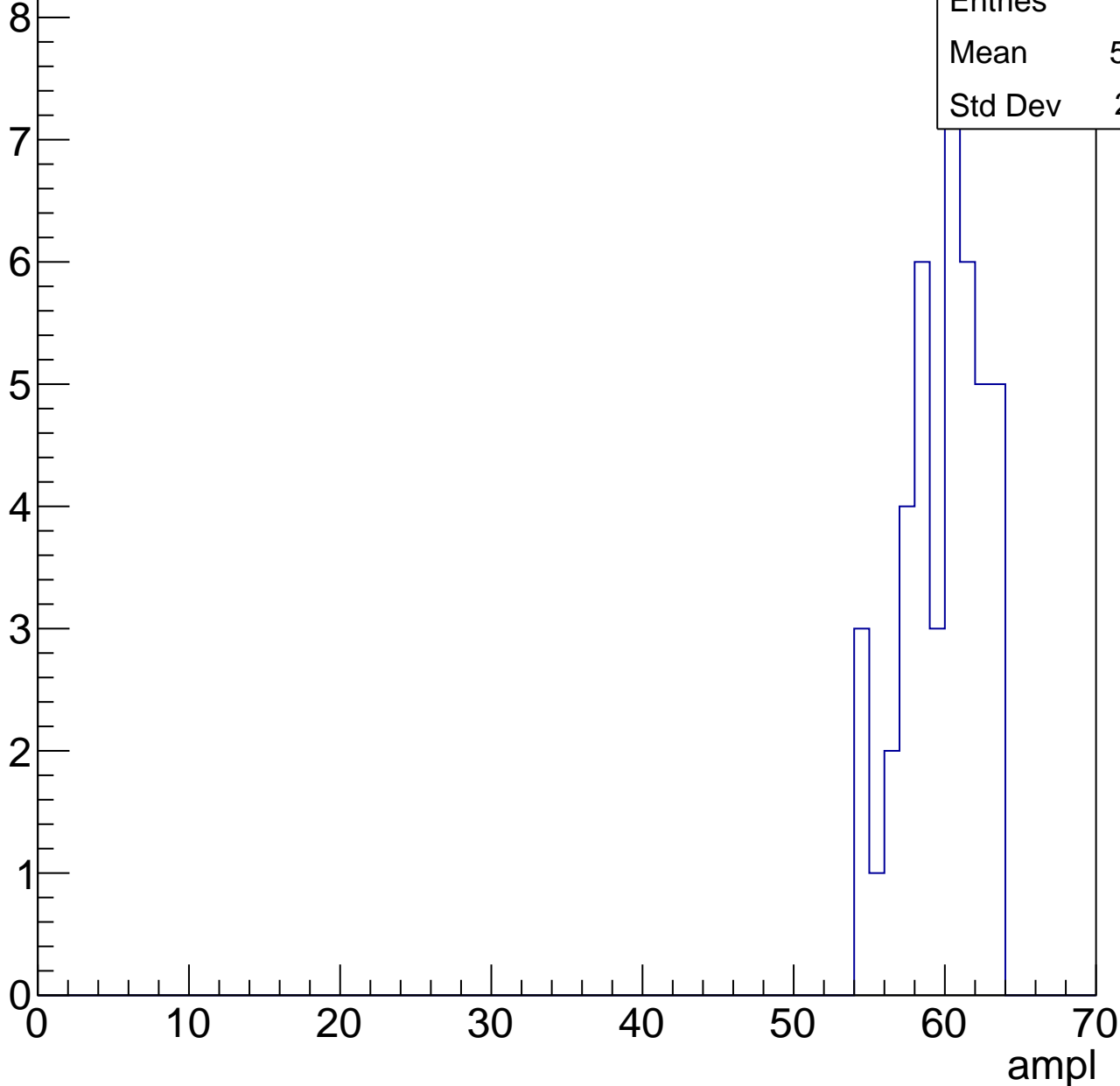


B1L102S, U18-ch80, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	59.37
Std Dev	2.561

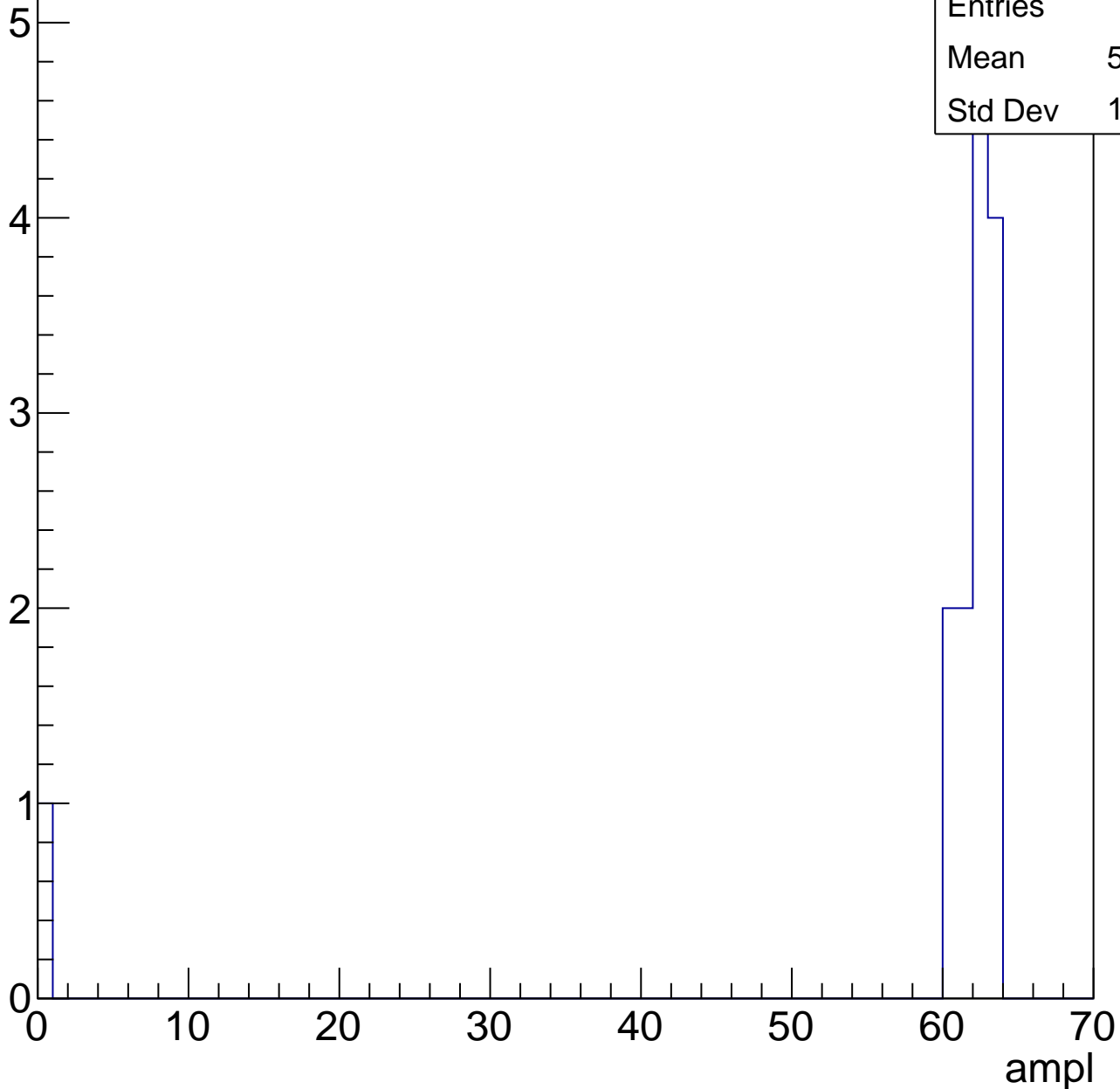


B1L102S, U18-ch80, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	57.43
Std Dev	15.96



B1L102S, U18-ch80, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch81, adc0

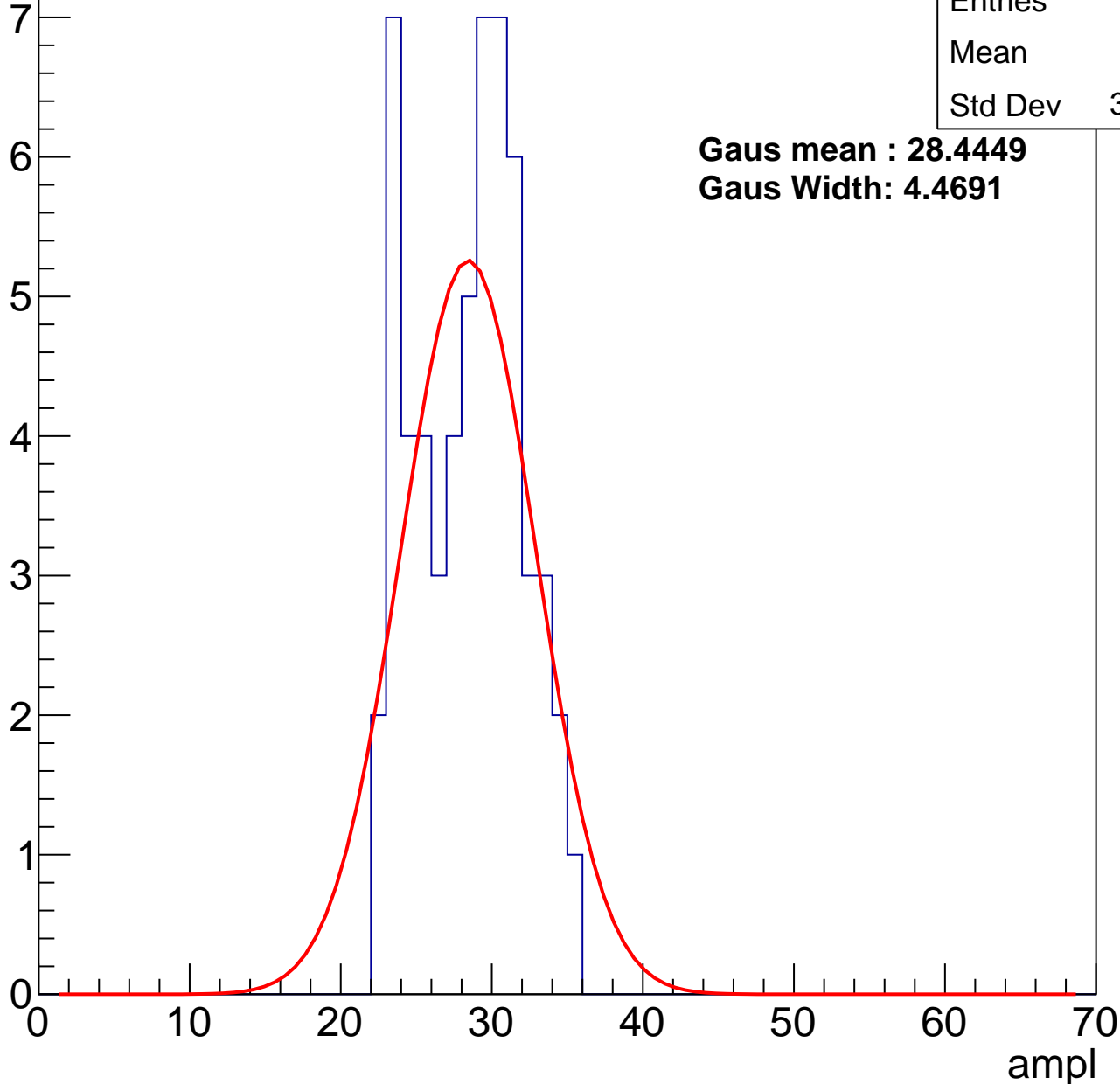
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28
Std Dev	3.464

Gaus mean : 28.4449

Gaus Width: 4.4691



B1L102S, U18-ch81, adc1

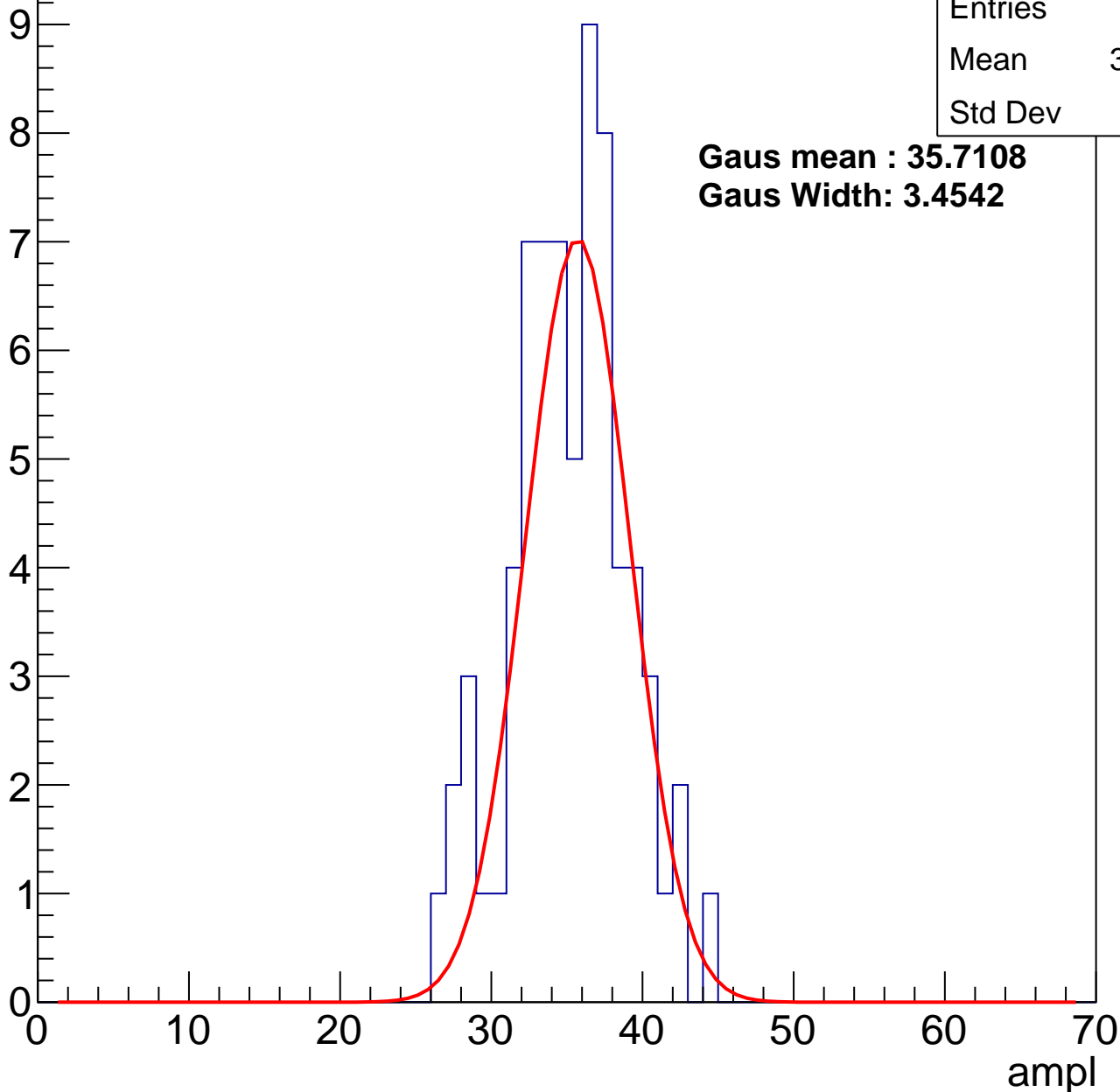
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	34.74
Std Dev	3.79

Gaus mean : 35.7108

Gaus Width: 3.4542



B1L102S, U18-ch81, adc2

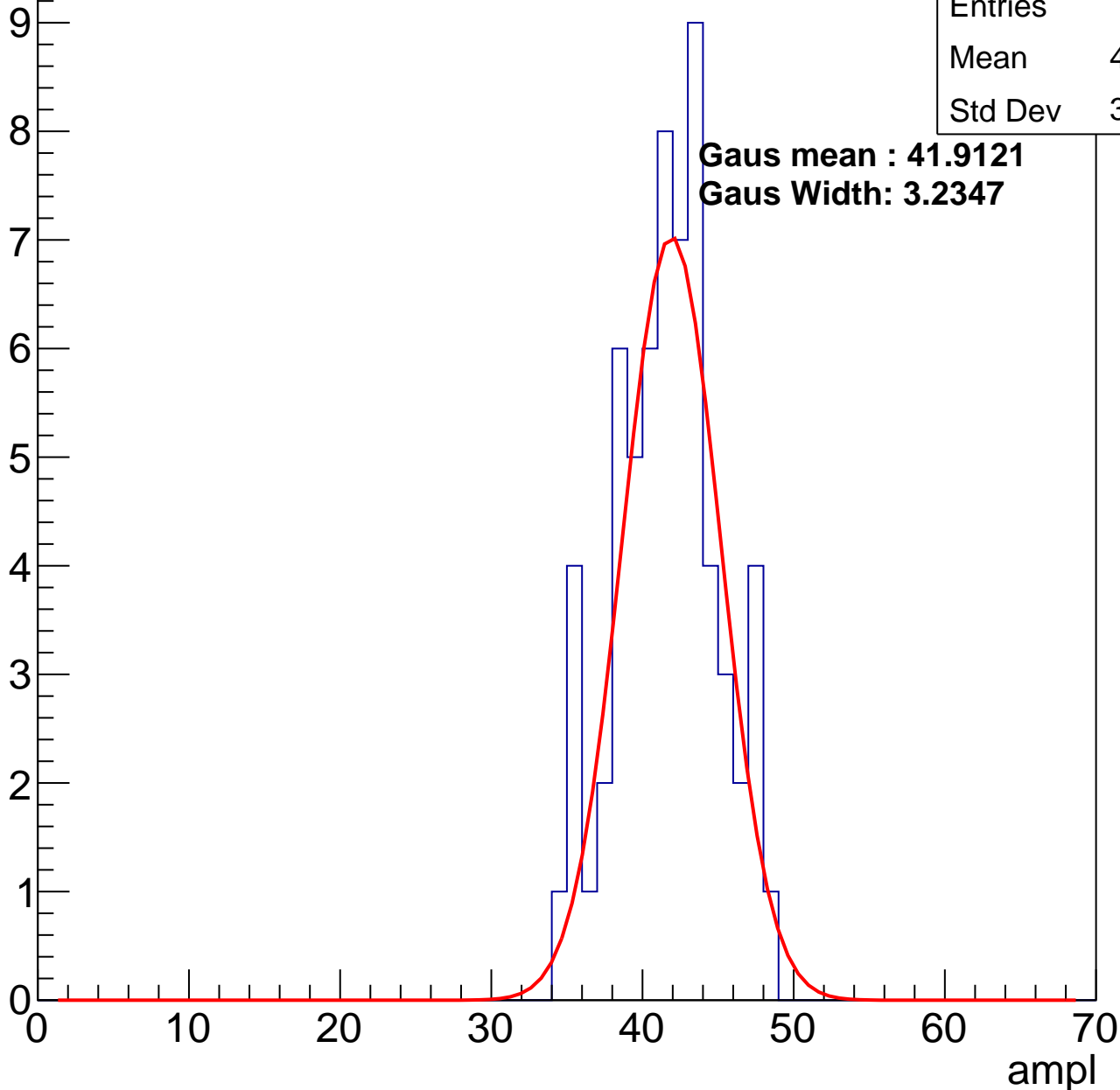
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	41.19
Std Dev	3.328

Gaus mean : 41.9121

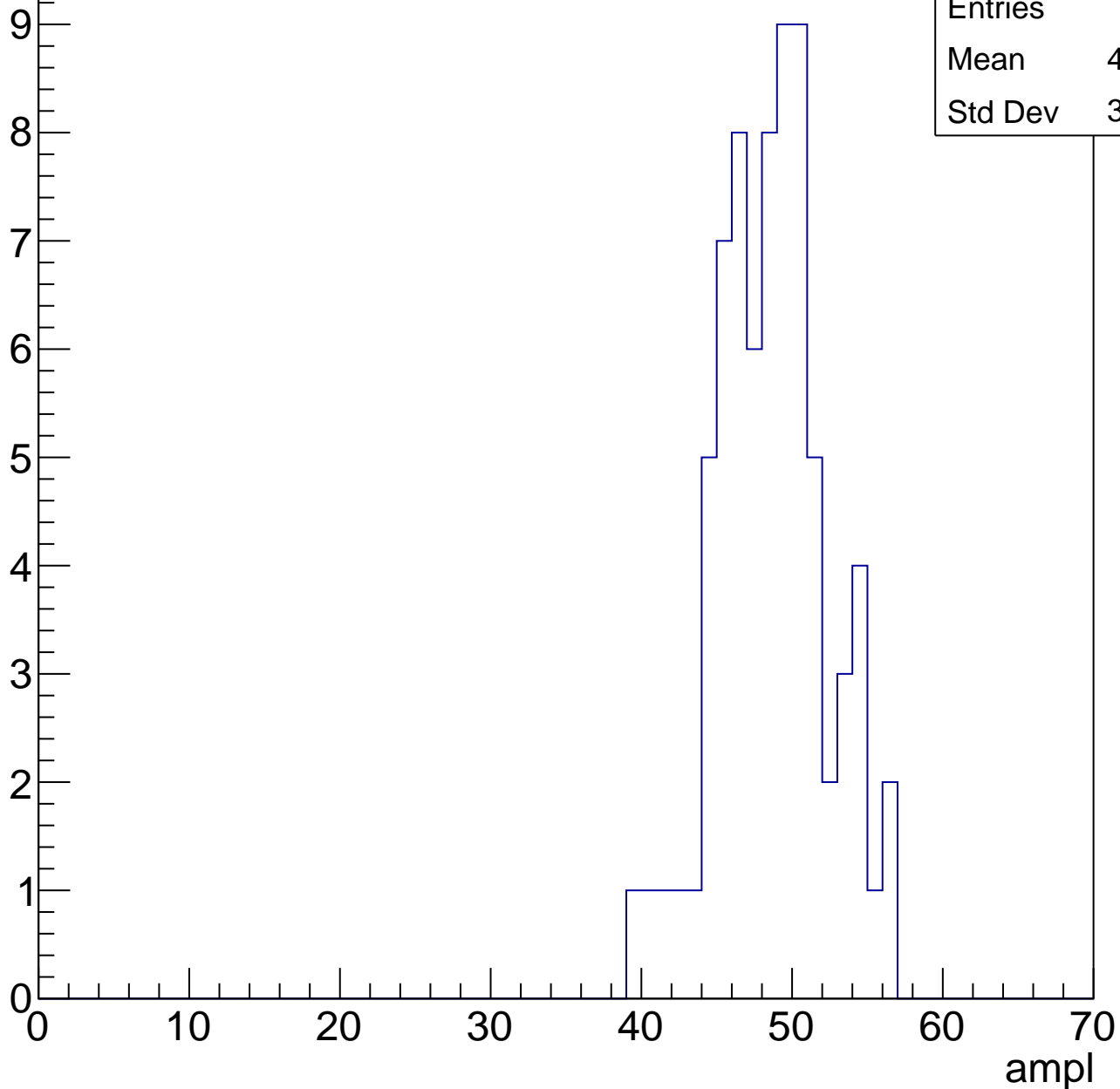
Gaus Width: 3.2347



B1L102S, U18-ch81, adc3

calib_packv5_042523_0143.root, FC#11, port A2

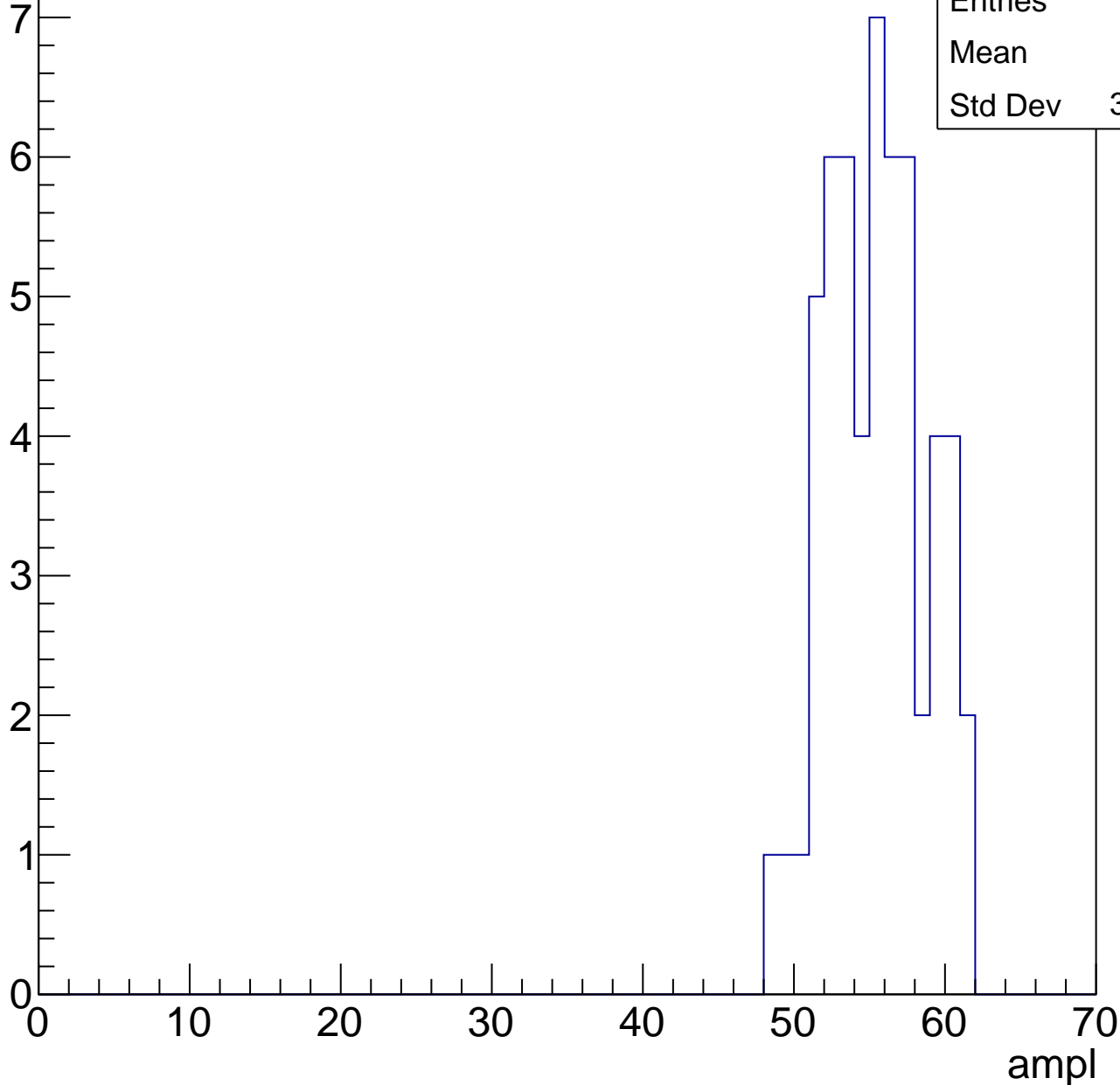
Entry



B1L102S, U18-ch81, adc4

calib_packv5_042523_0143.root, FC#11, port A2

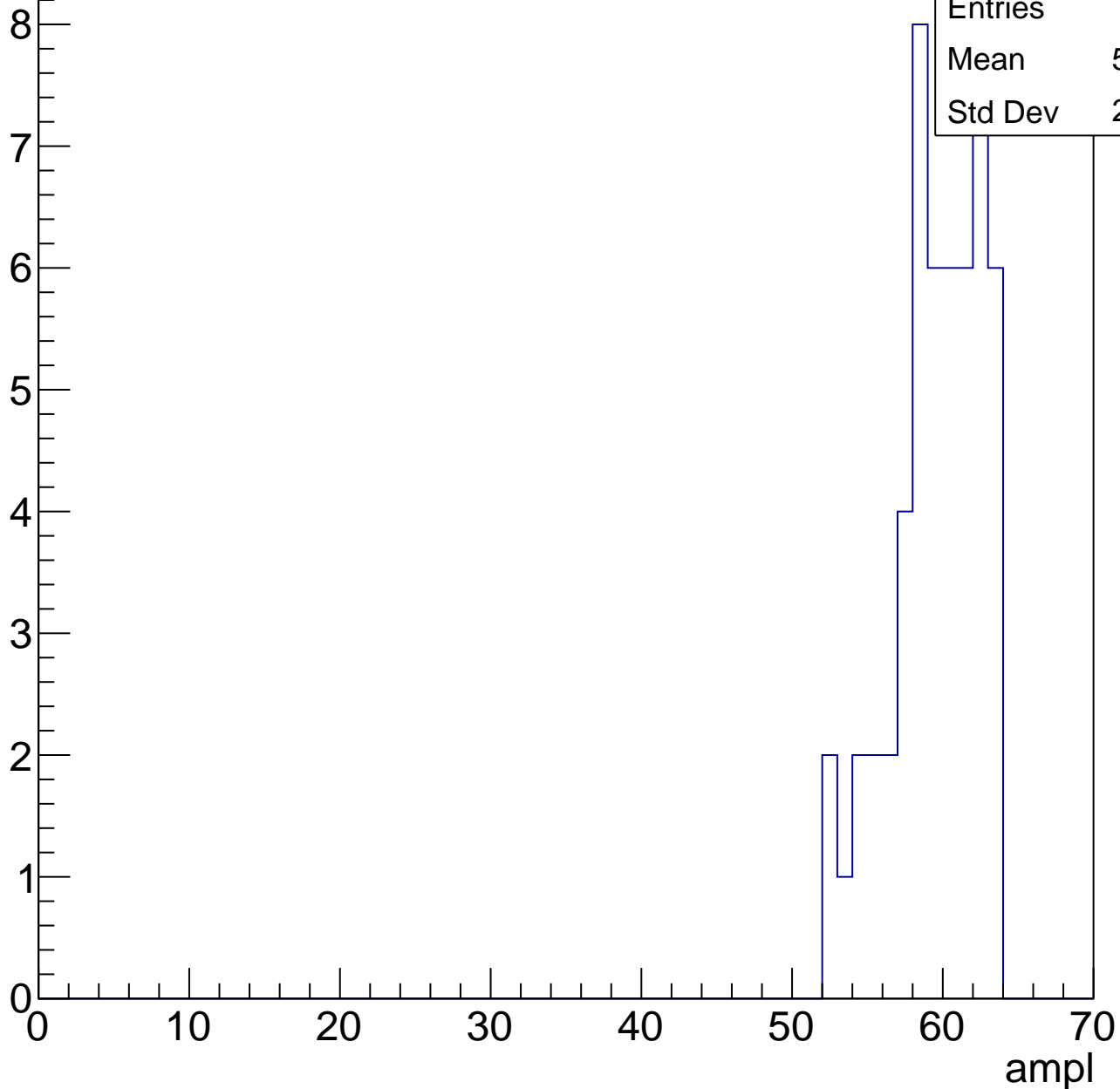
Entry



B1L102S, U18-ch81, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

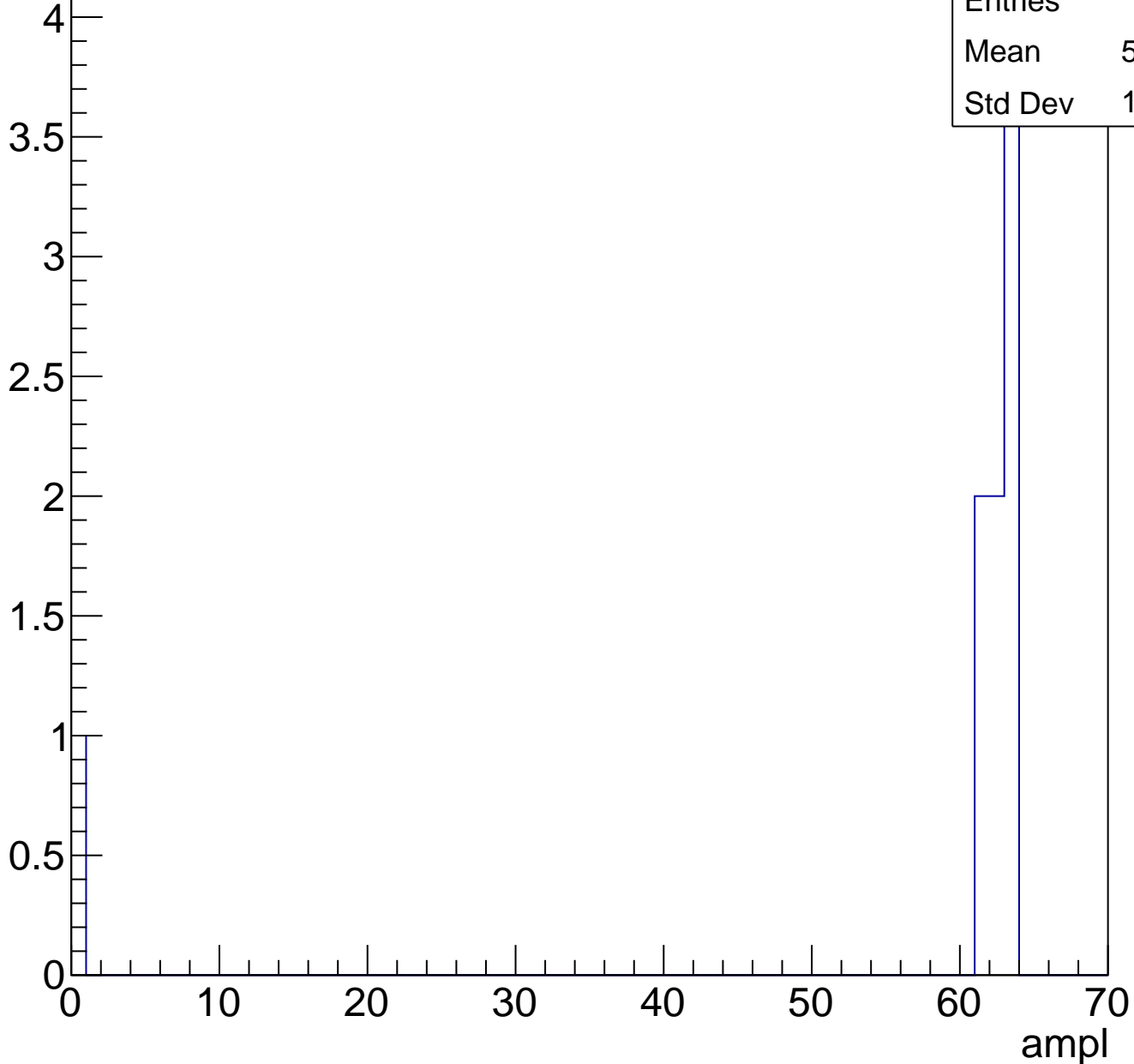


Entries	53
Mean	59.11
Std Dev	2.931

B1L102S, U18-ch81, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	9
Mean	55.33
Std Dev	19.58

B1L102S, U18-ch81, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	0
Std Dev	0

B1L102S, U18-ch82, adc0

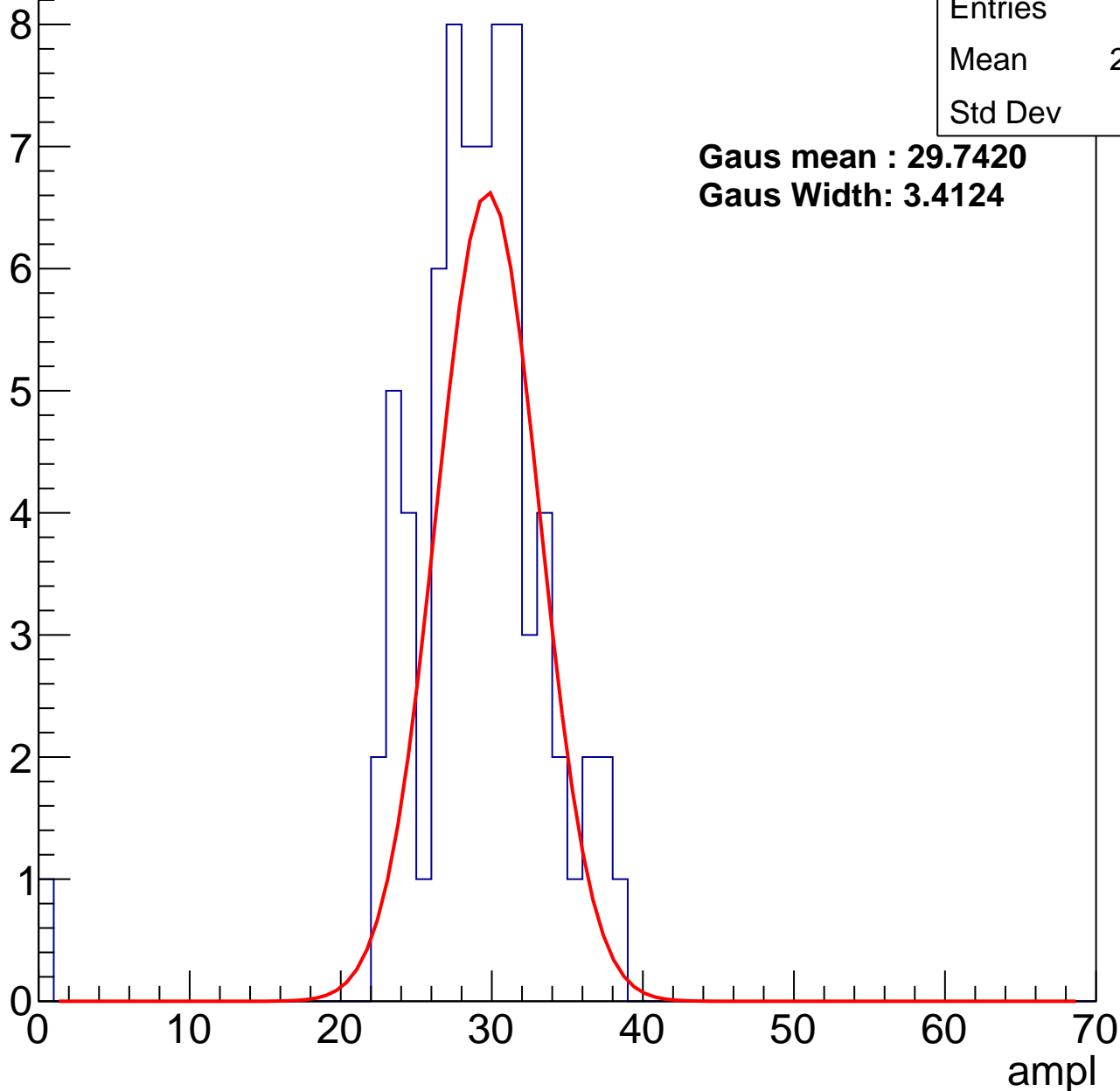
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.53
Std Dev	5.05

Gaus mean : 29.7420

Gaus Width: 3.4124



B1L102S, U18-ch82, adc1

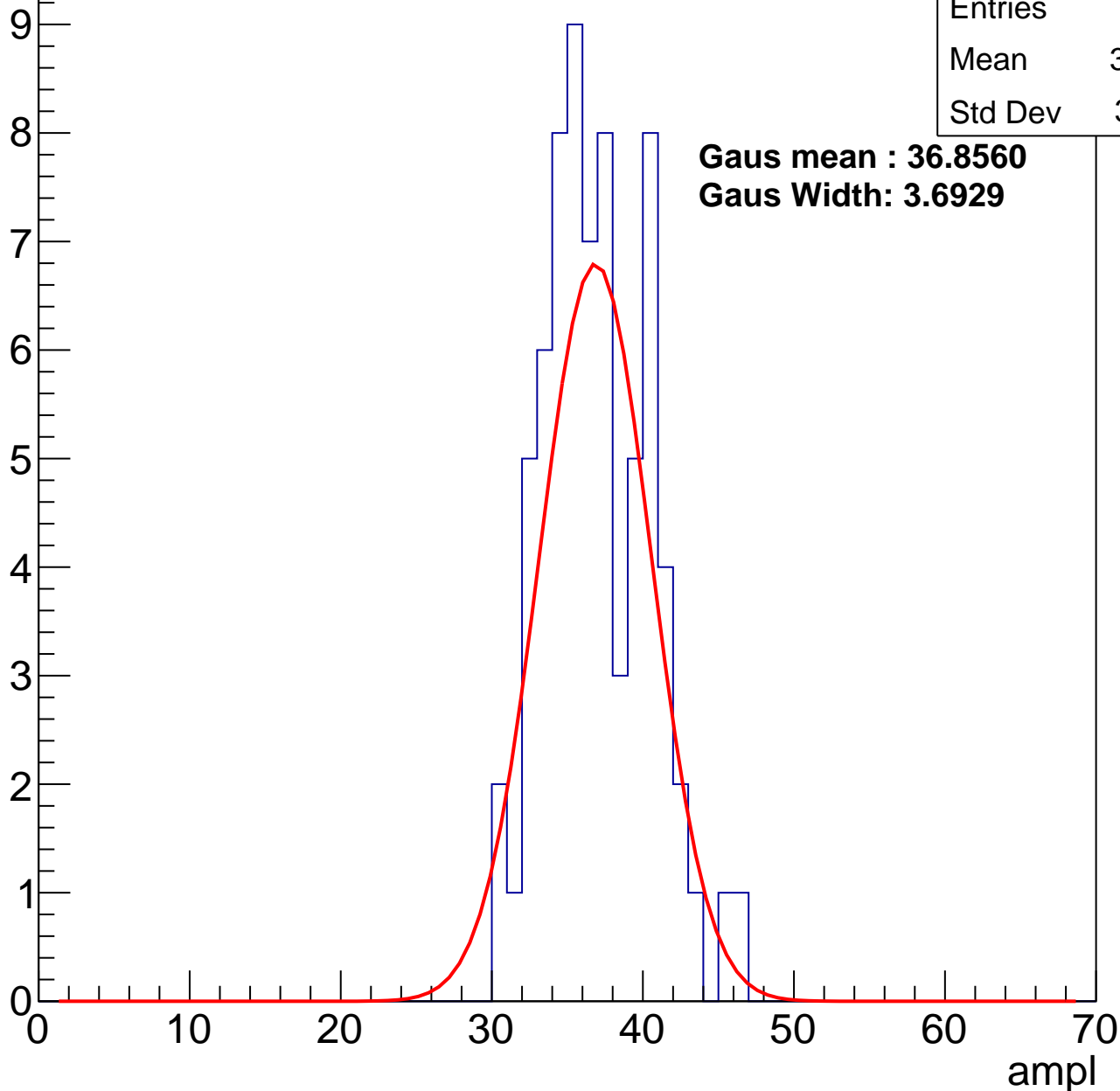
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	36.55
Std Dev	3.451

Gaus mean : 36.8560

Gaus Width: 3.6929



B1L102S, U18-ch82, adc2

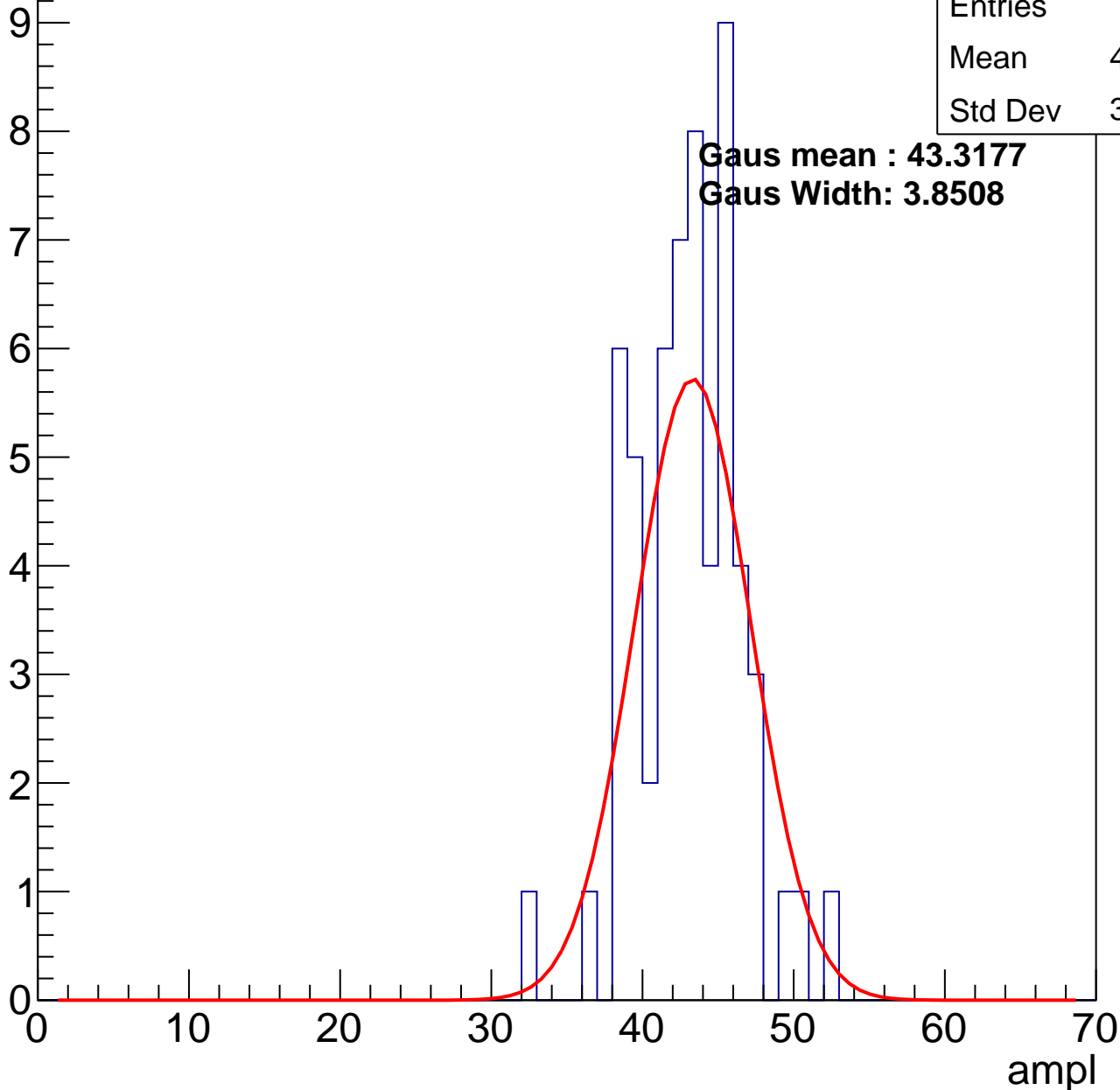
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	42.58
Std Dev	3.514

Gaus mean : 43.3177

Gaus Width: 3.8508

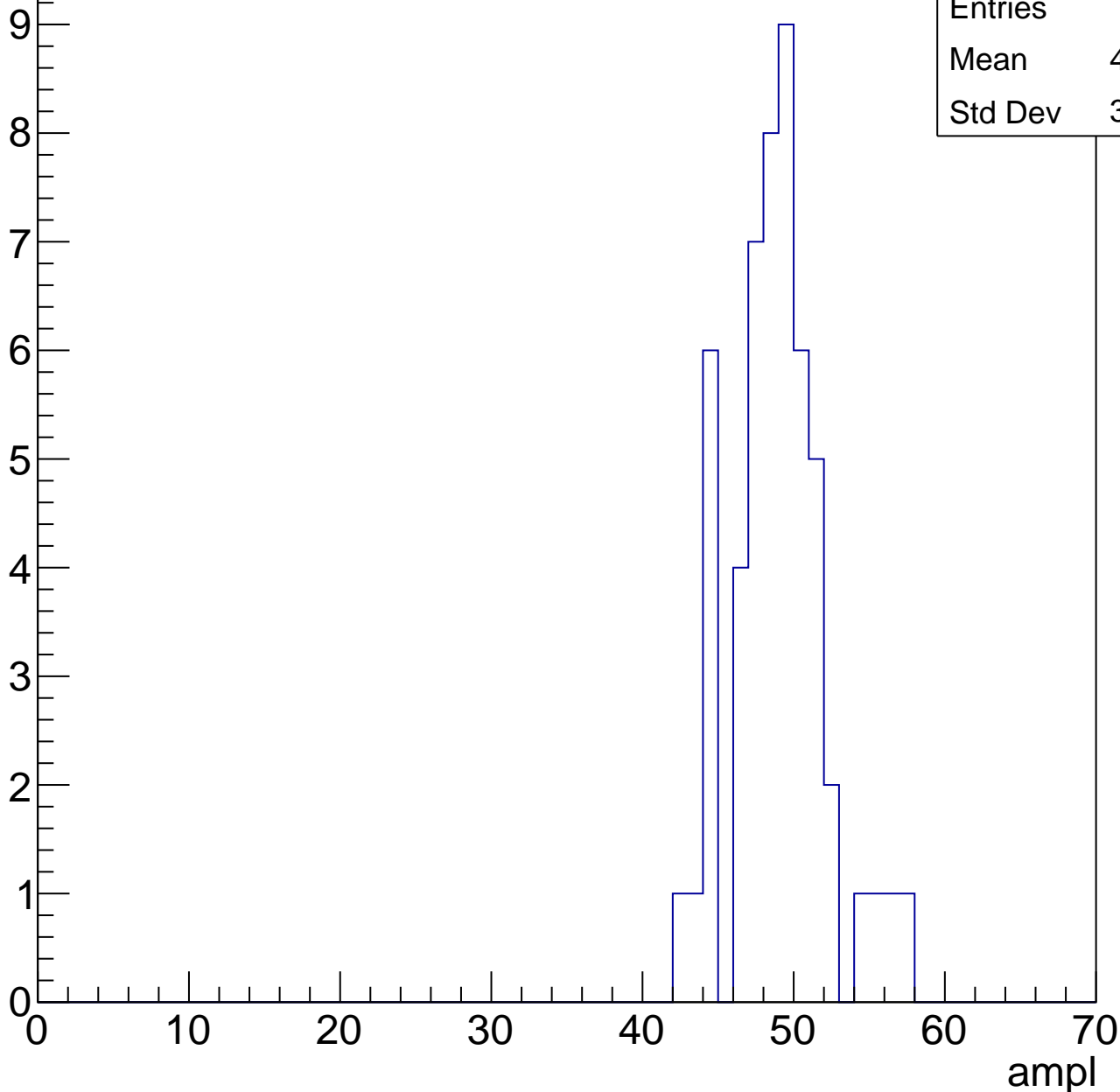


B1L102S, U18-ch82, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	48.45
Std Dev	3.099

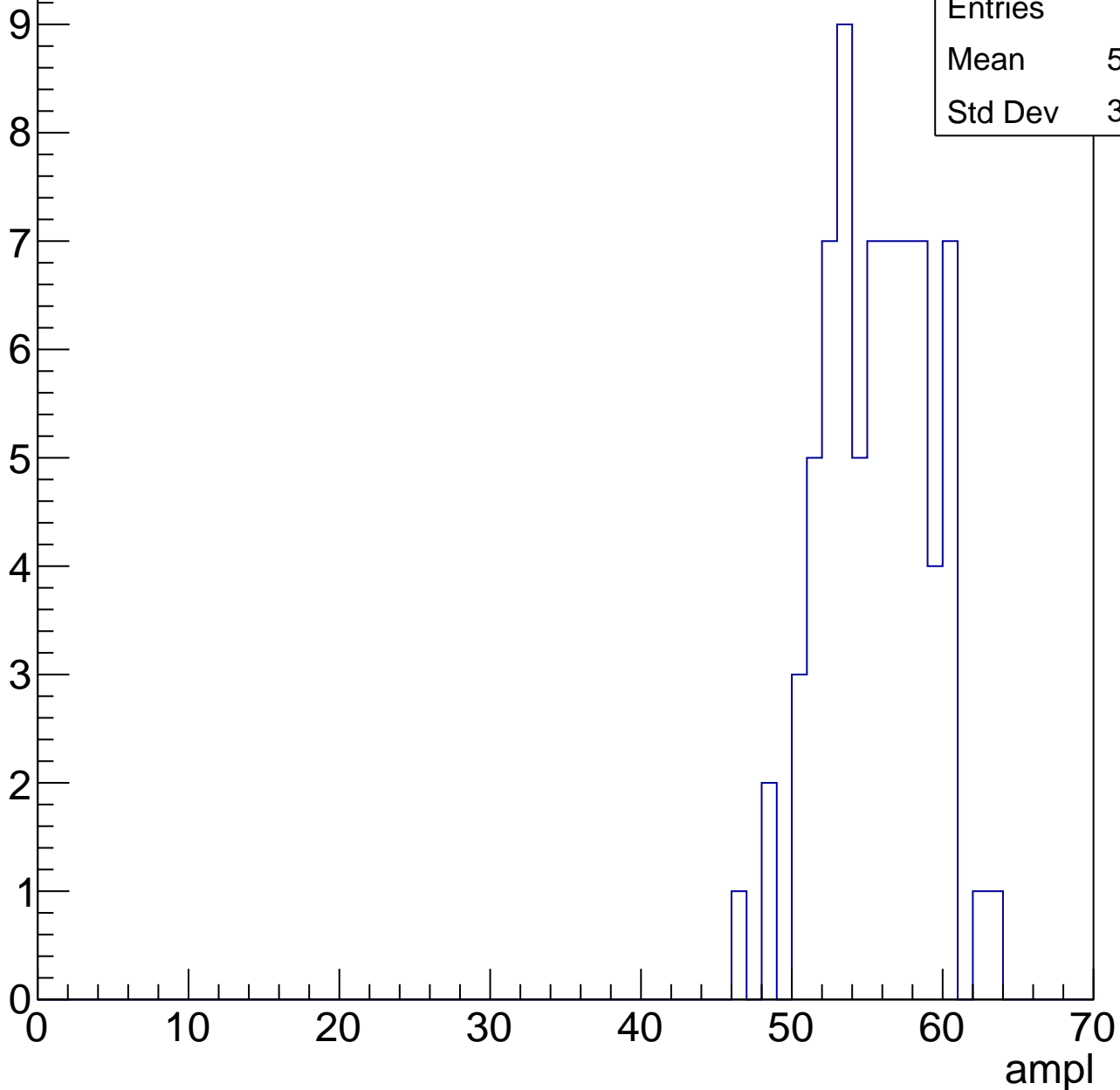


B1L102S, U18-ch82, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	55.08
Std Dev	3.487

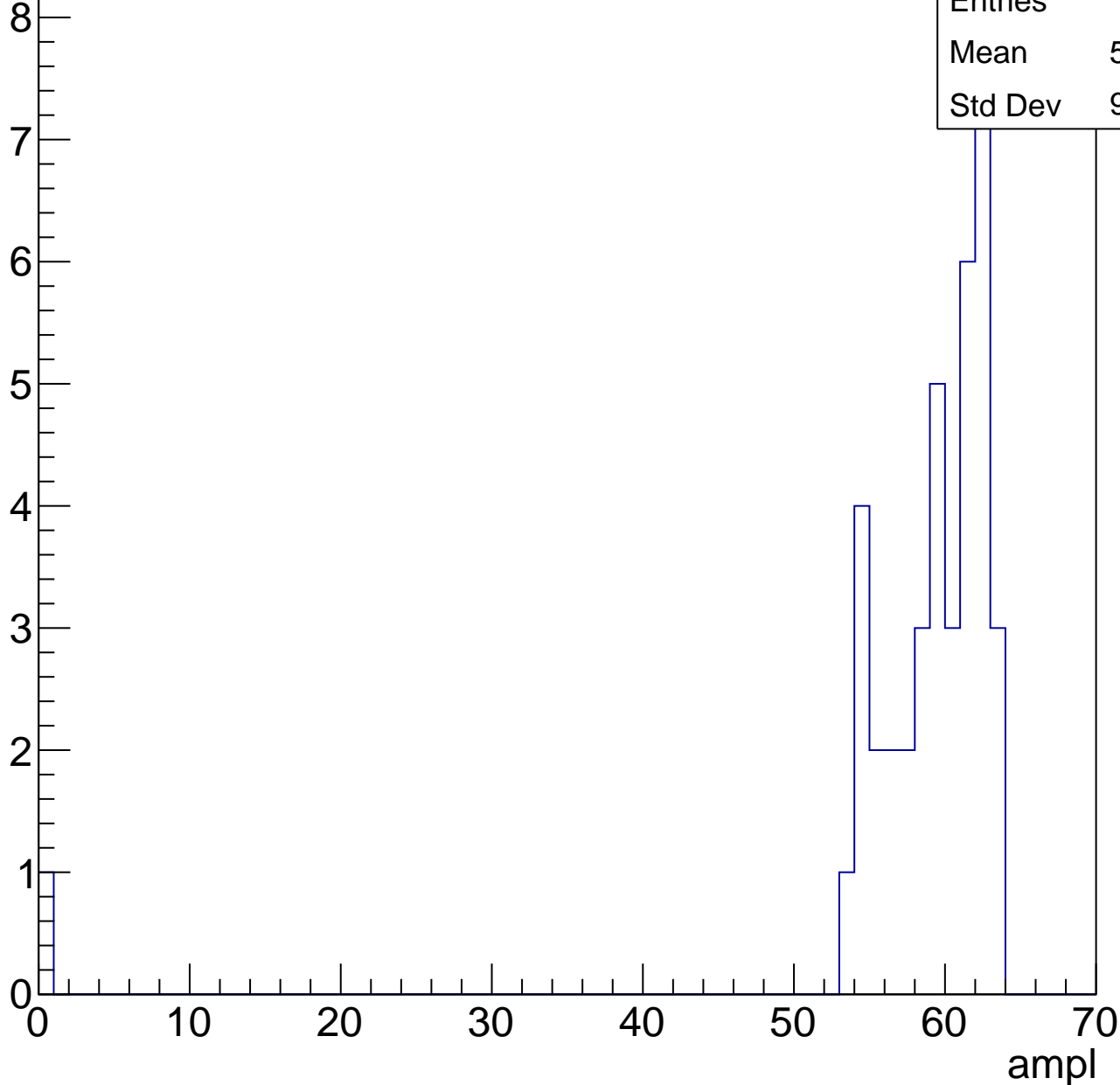


B1L102S, U18-ch82, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	40
Mean	57.62
Std Dev	9.682

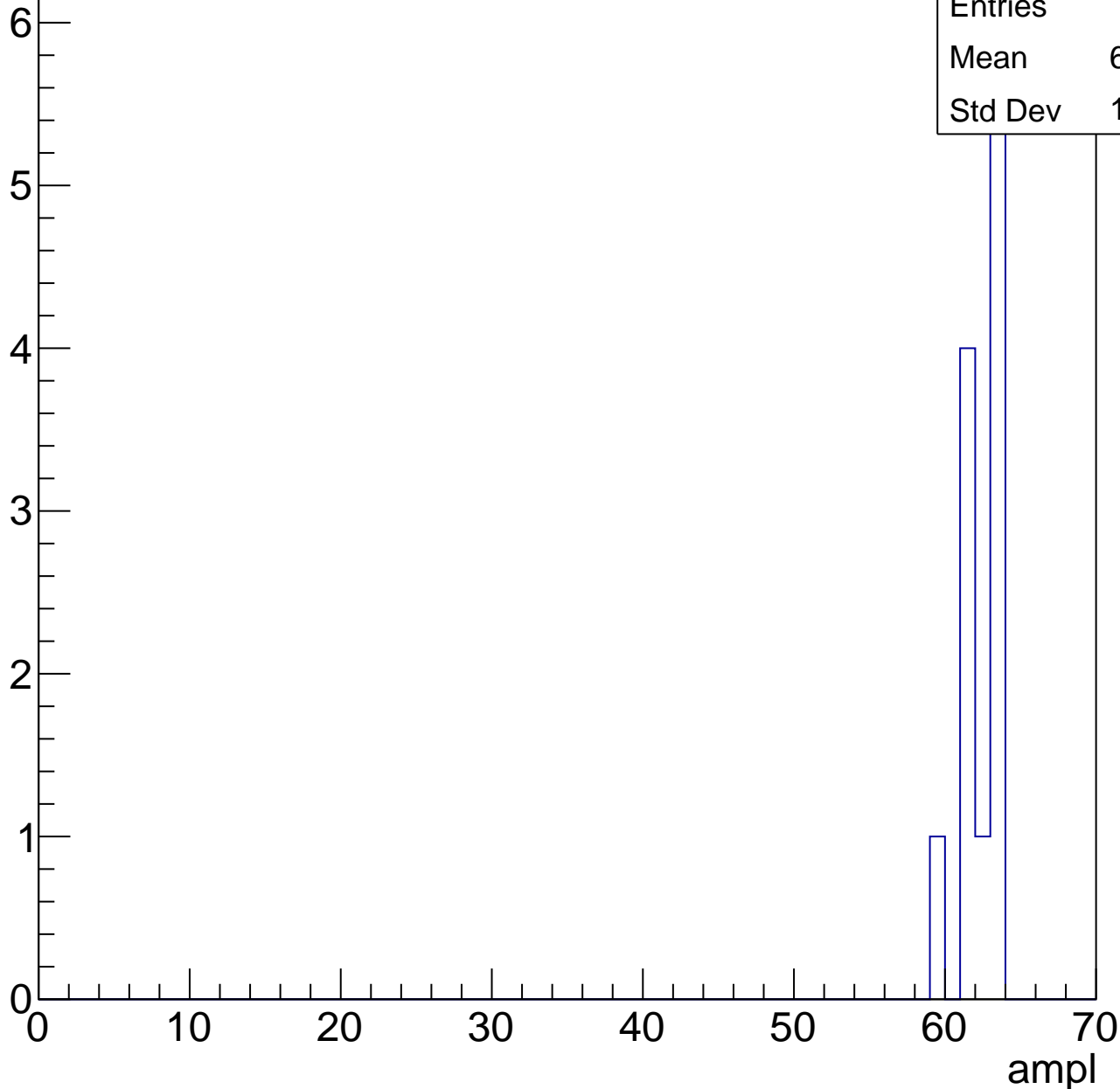


B1L102S, U18-ch82, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	12
Mean	61.92
Std Dev	1.256



B1L102S, U18-ch82, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch83, adc0

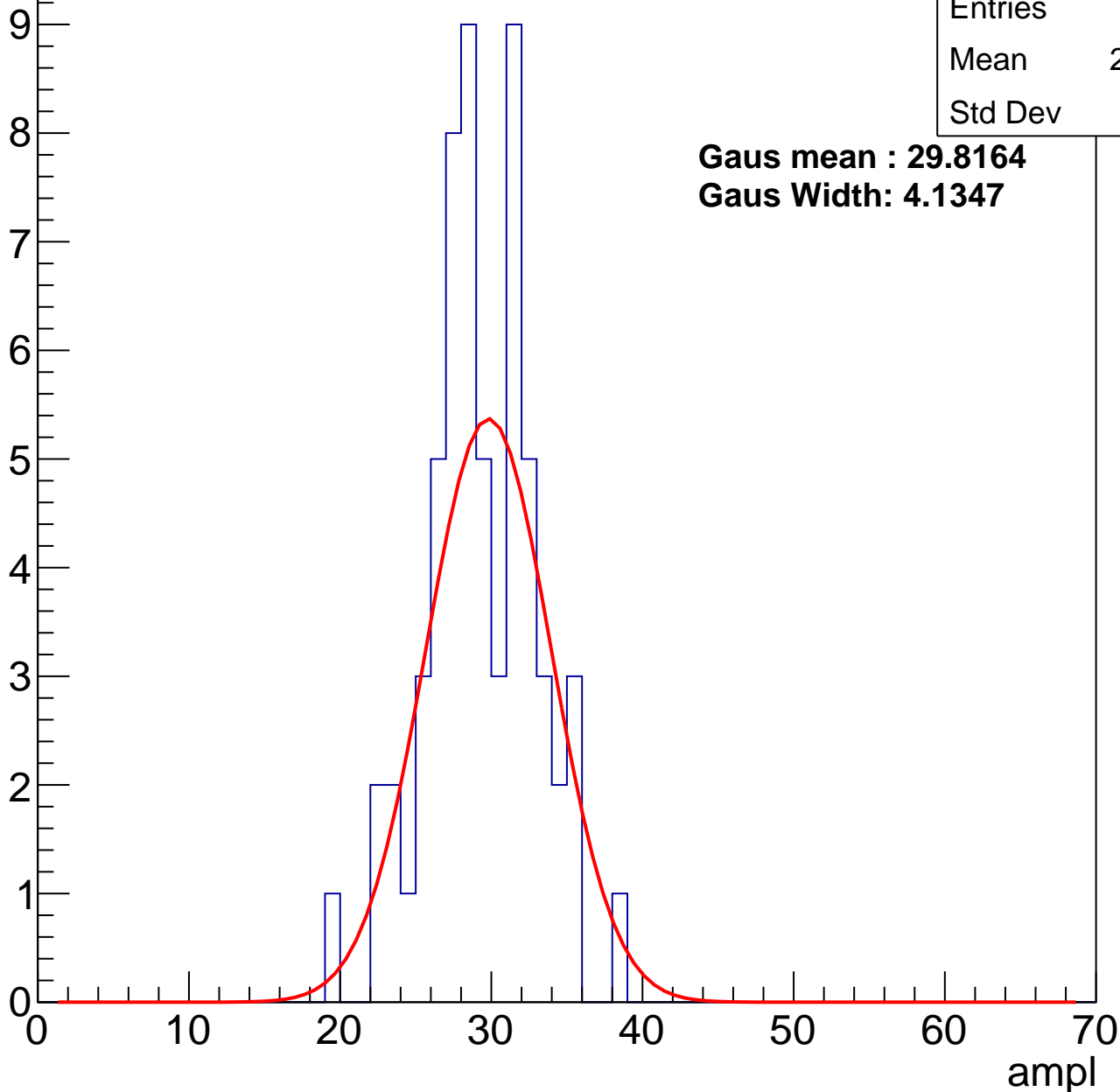
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	28.87
Std Dev	3.59

Gaus mean : 29.8164

Gaus Width: 4.1347



B1L102S, U18-ch83, adc1

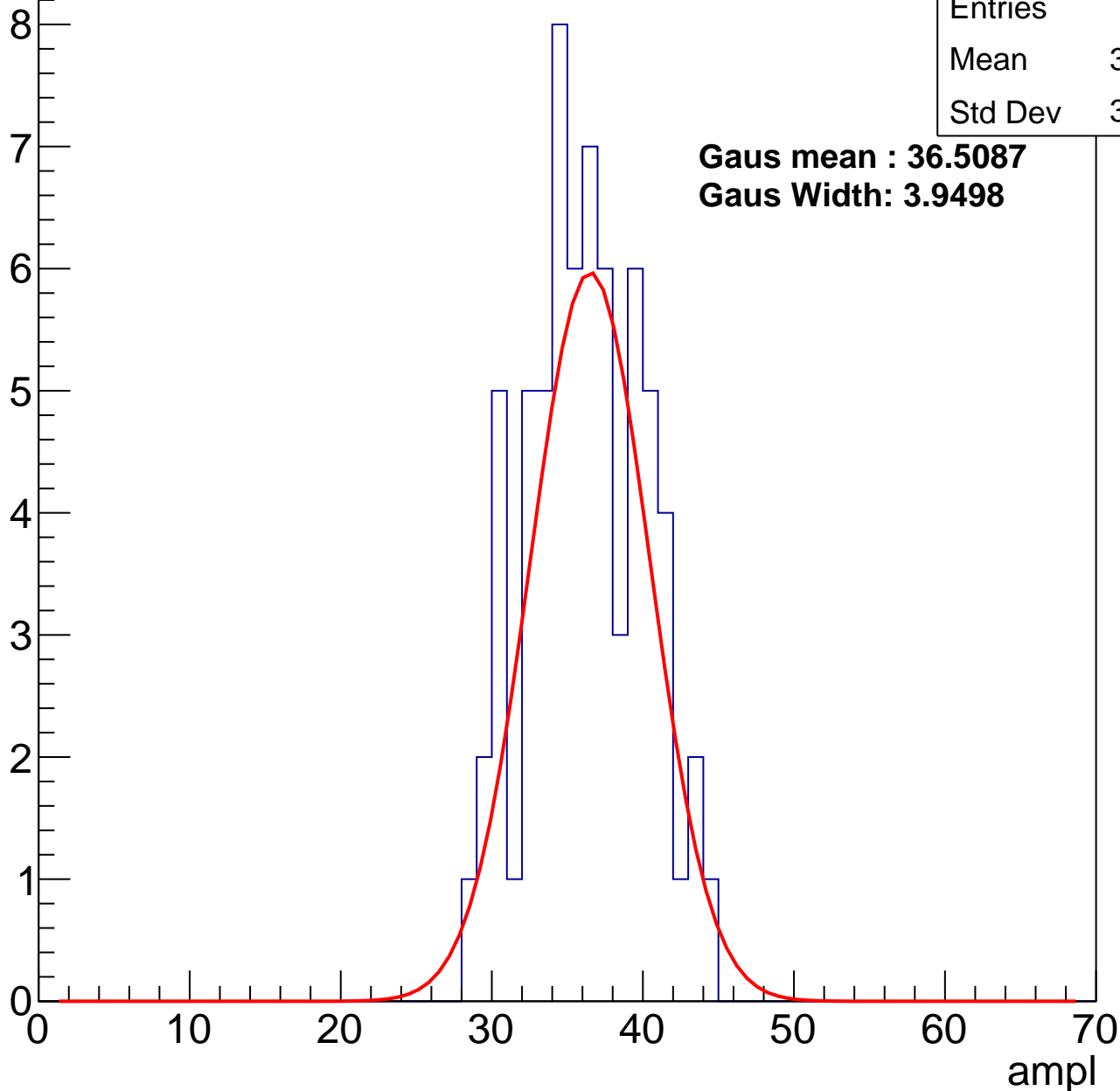
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	35.76
Std Dev	3.789

Gaus mean : 36.5087

Gaus Width: 3.9498



B1L102S, U18-ch83, adc2

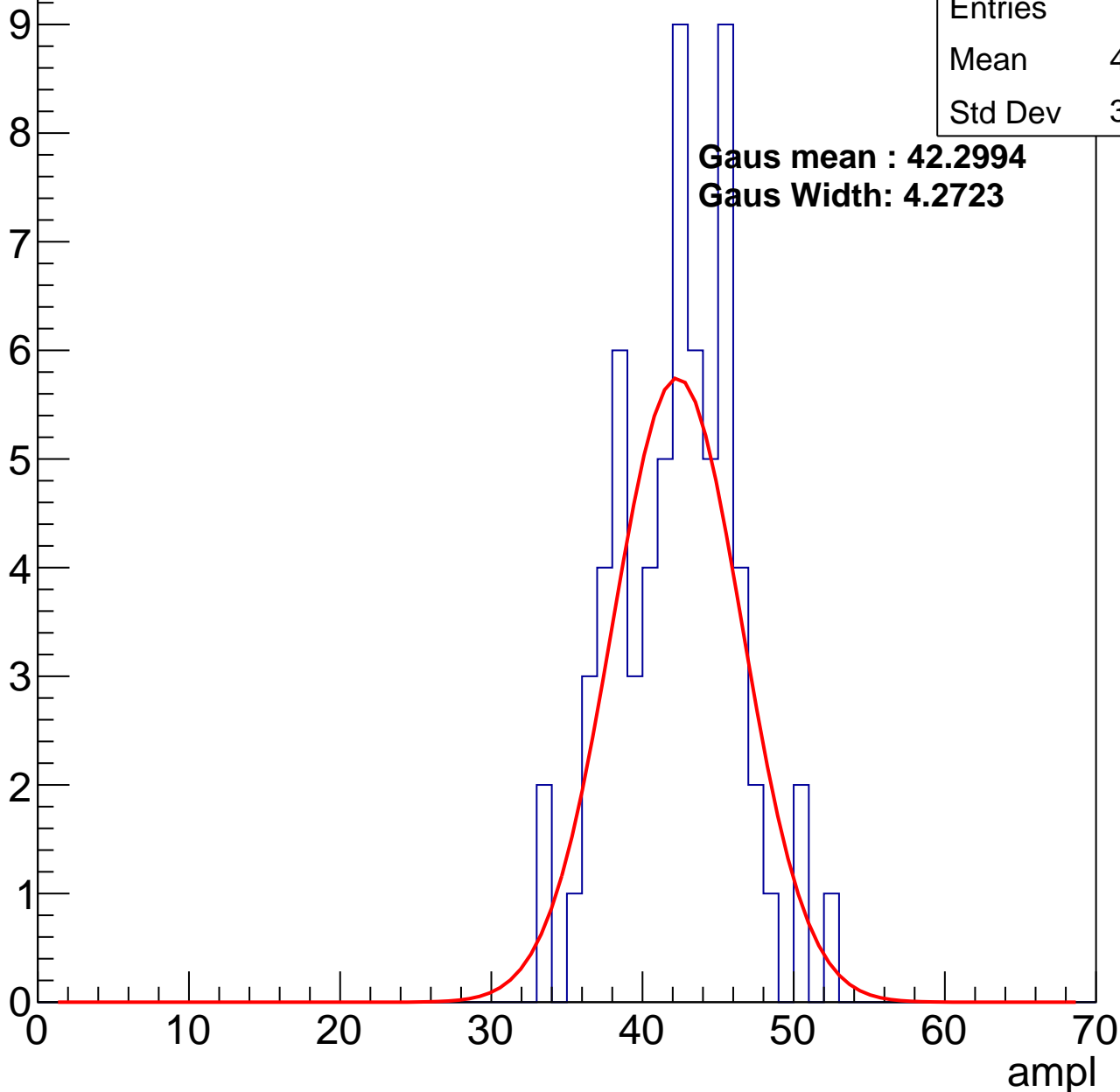
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	41.88
Std Dev	3.953

Gaus mean : 42.2994

Gaus Width: 4.2723

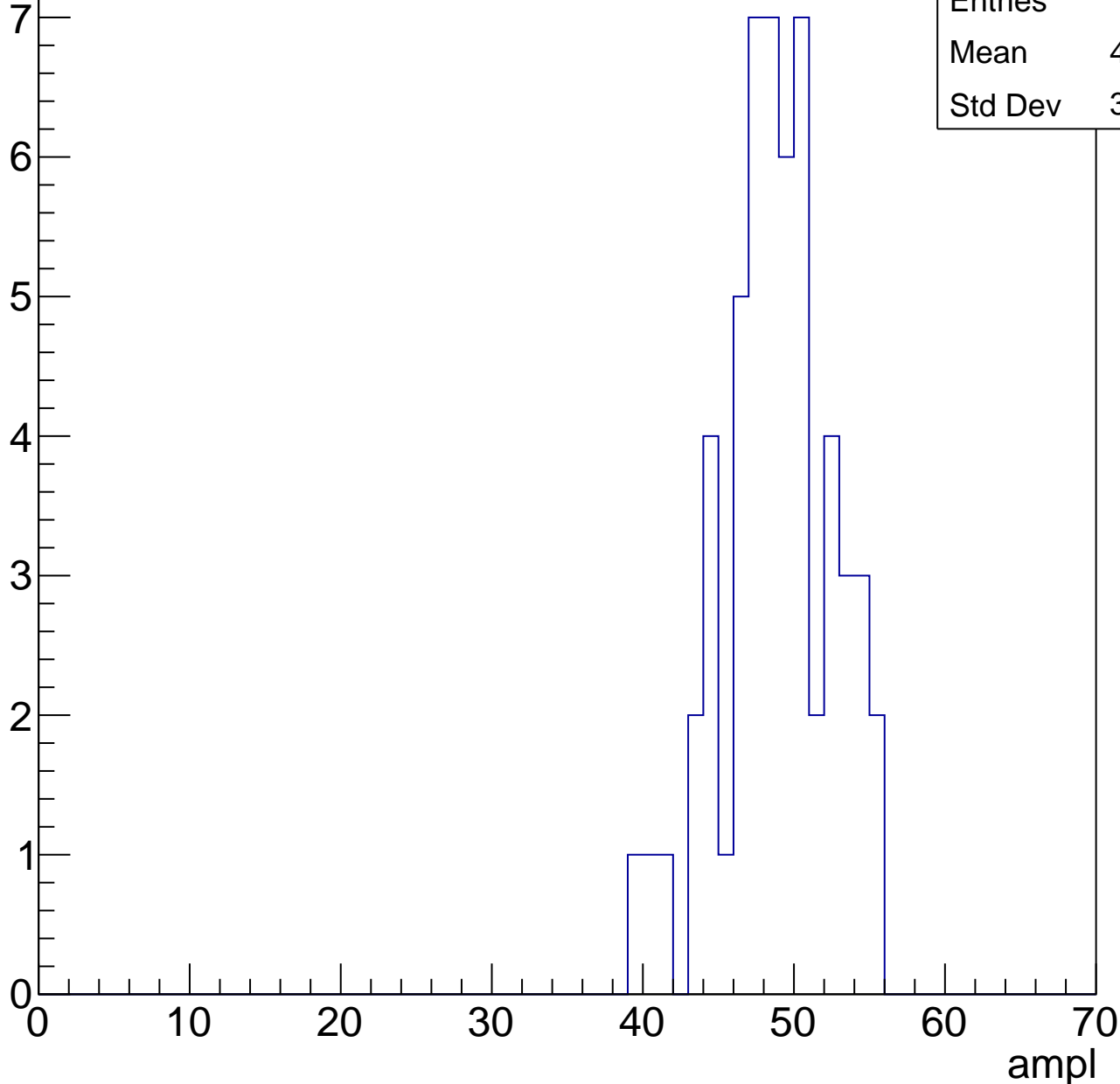


B1L102S, U18-ch83, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.34
Std Dev	3.622

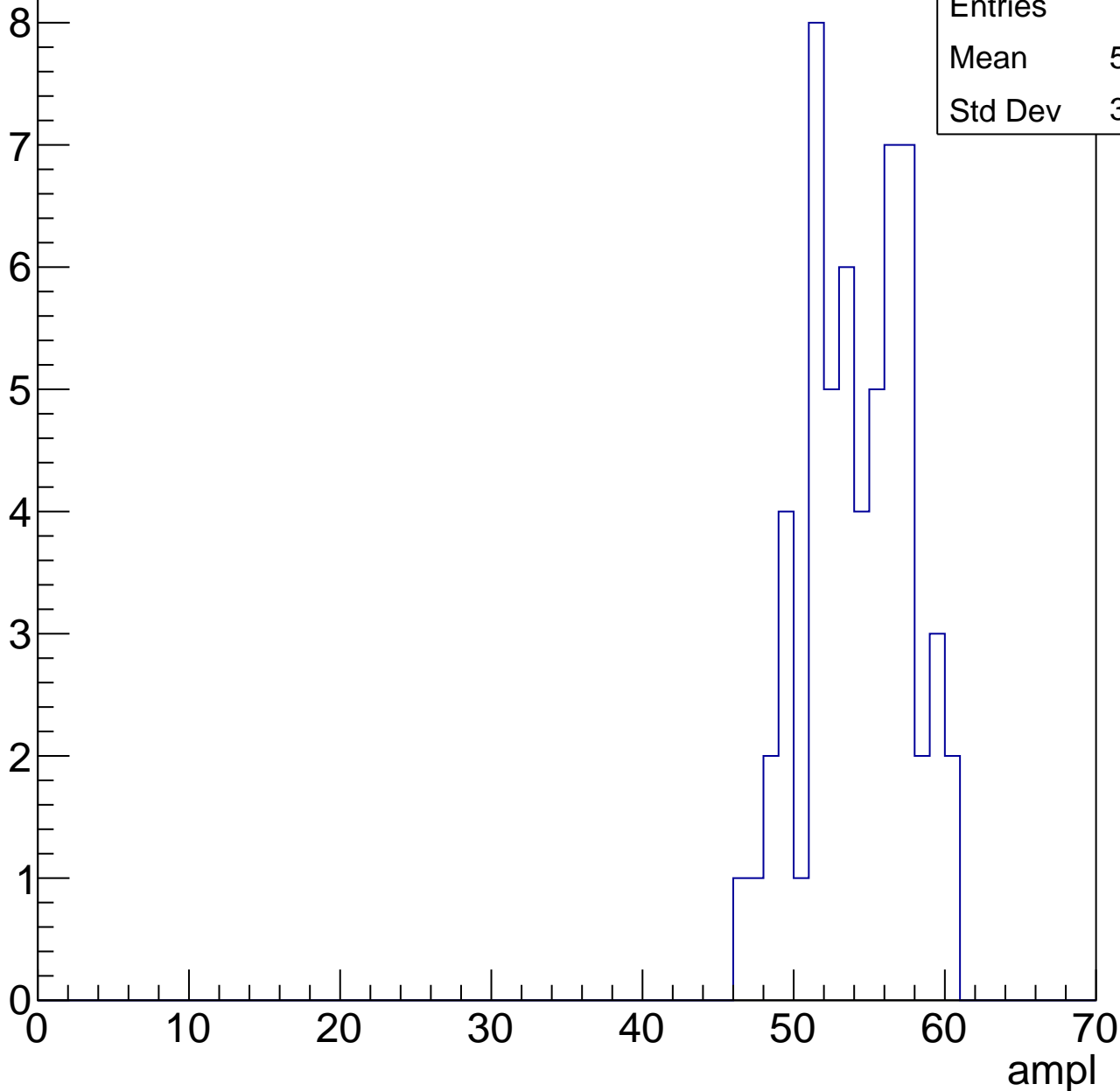


B1L102S, U18-ch83, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

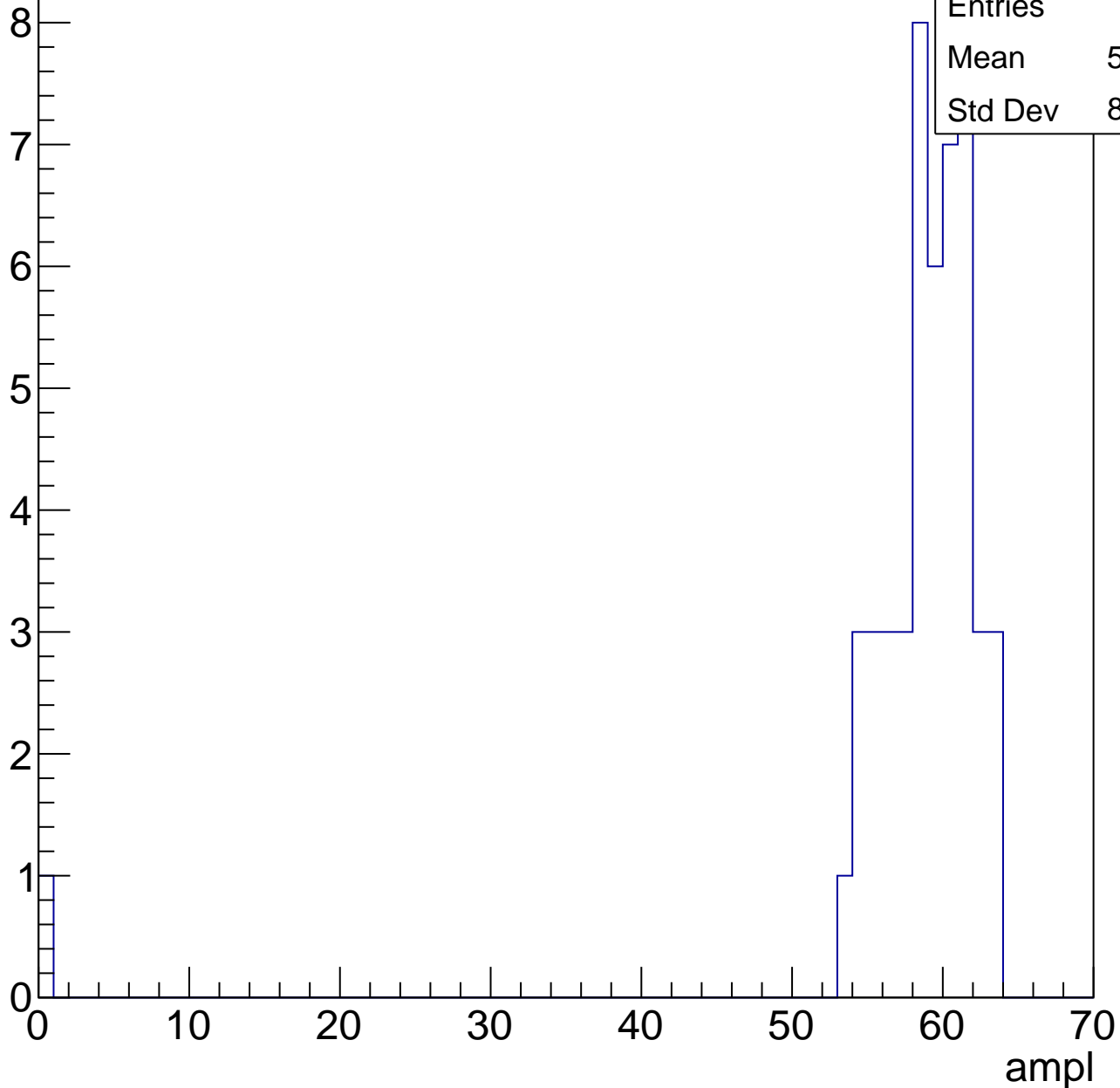
Entries	58
Mean	53.72
Std Dev	3.398



B1L102S, U18-ch83, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

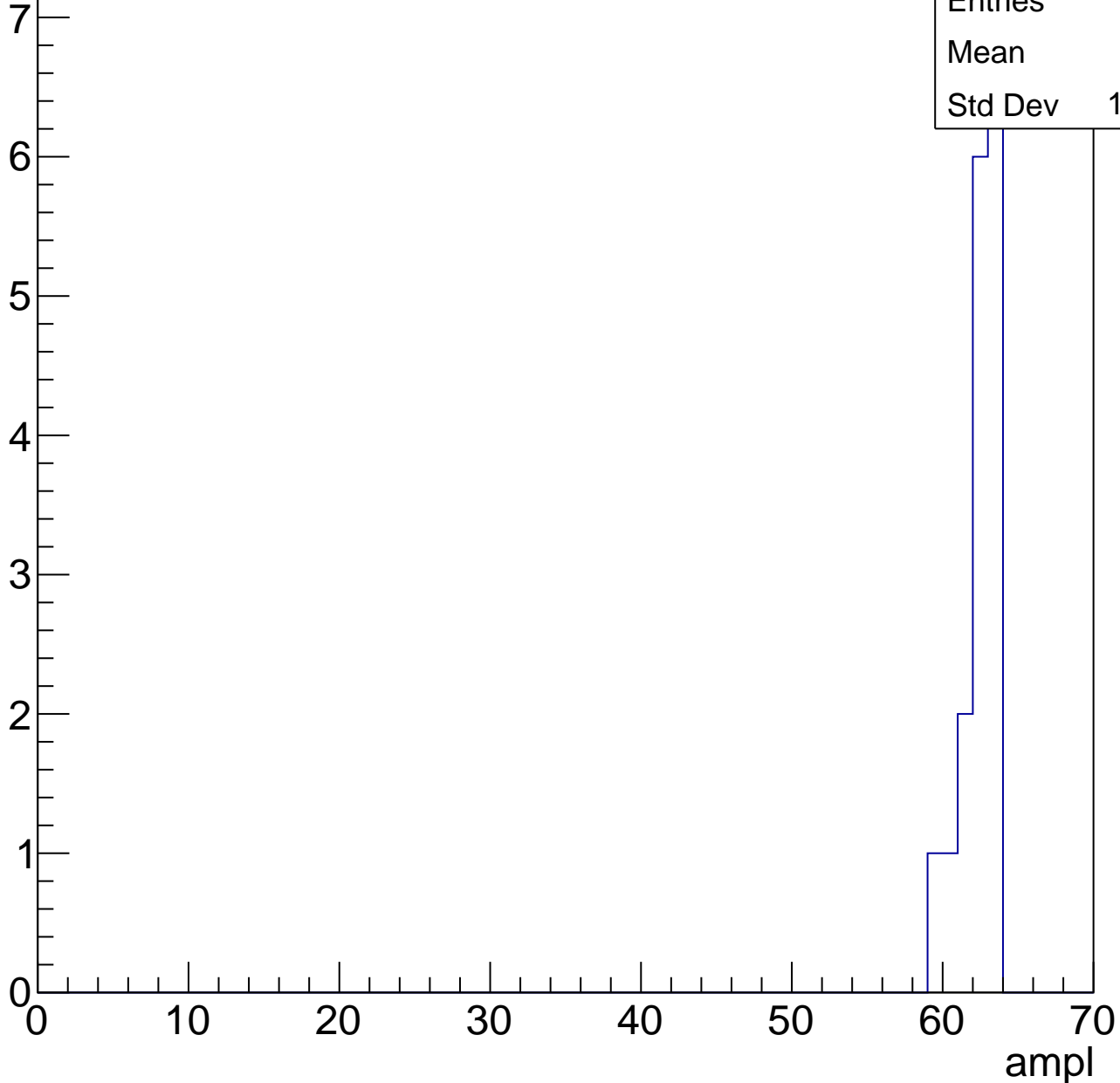


B1L102S, U18-ch83, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

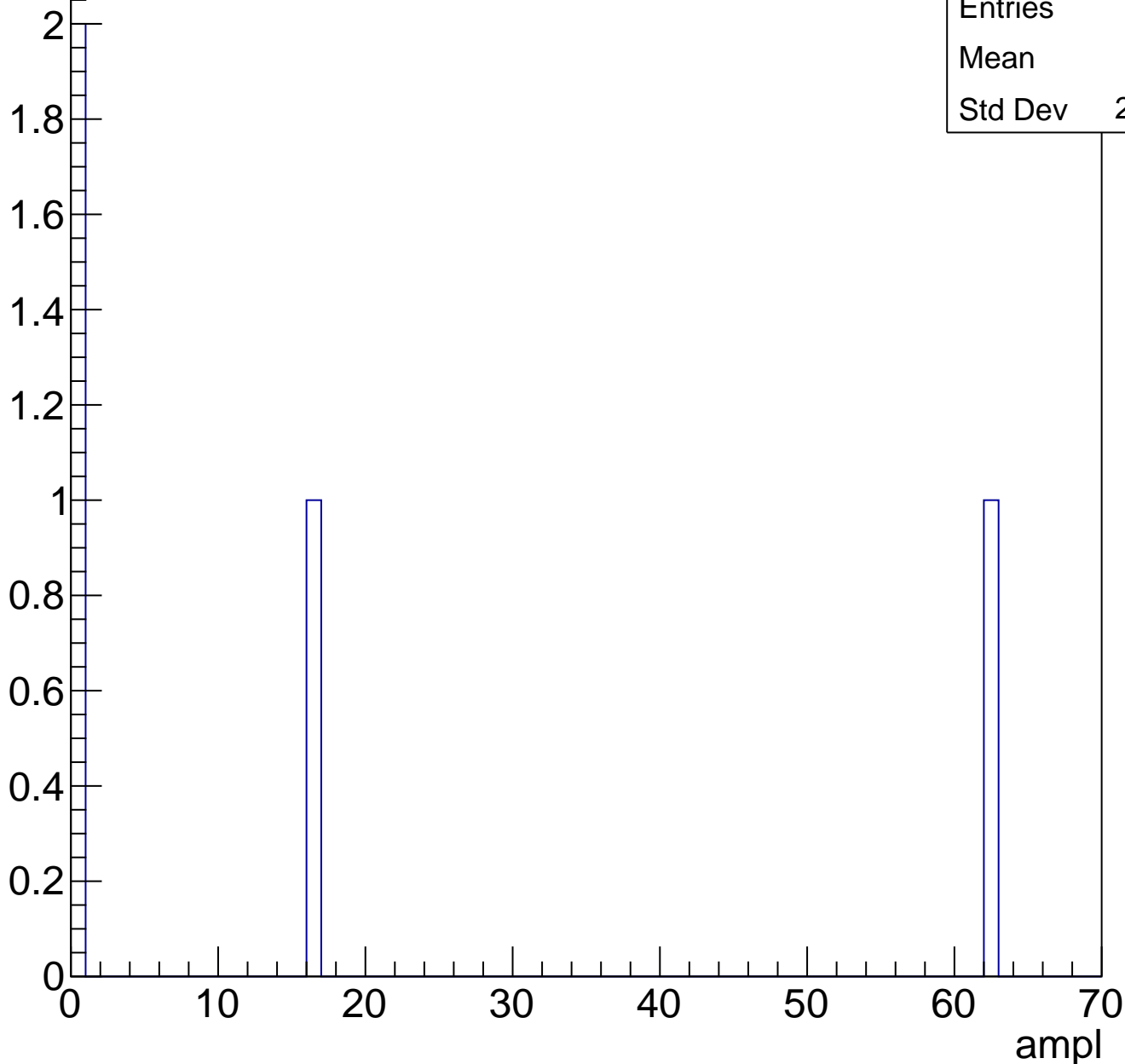
Entries	17
Mean	62
Std Dev	1.138



B1L102S, U18-ch83, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	19.5
Std Dev	25.39

B1L102S, U18-ch84, adc0

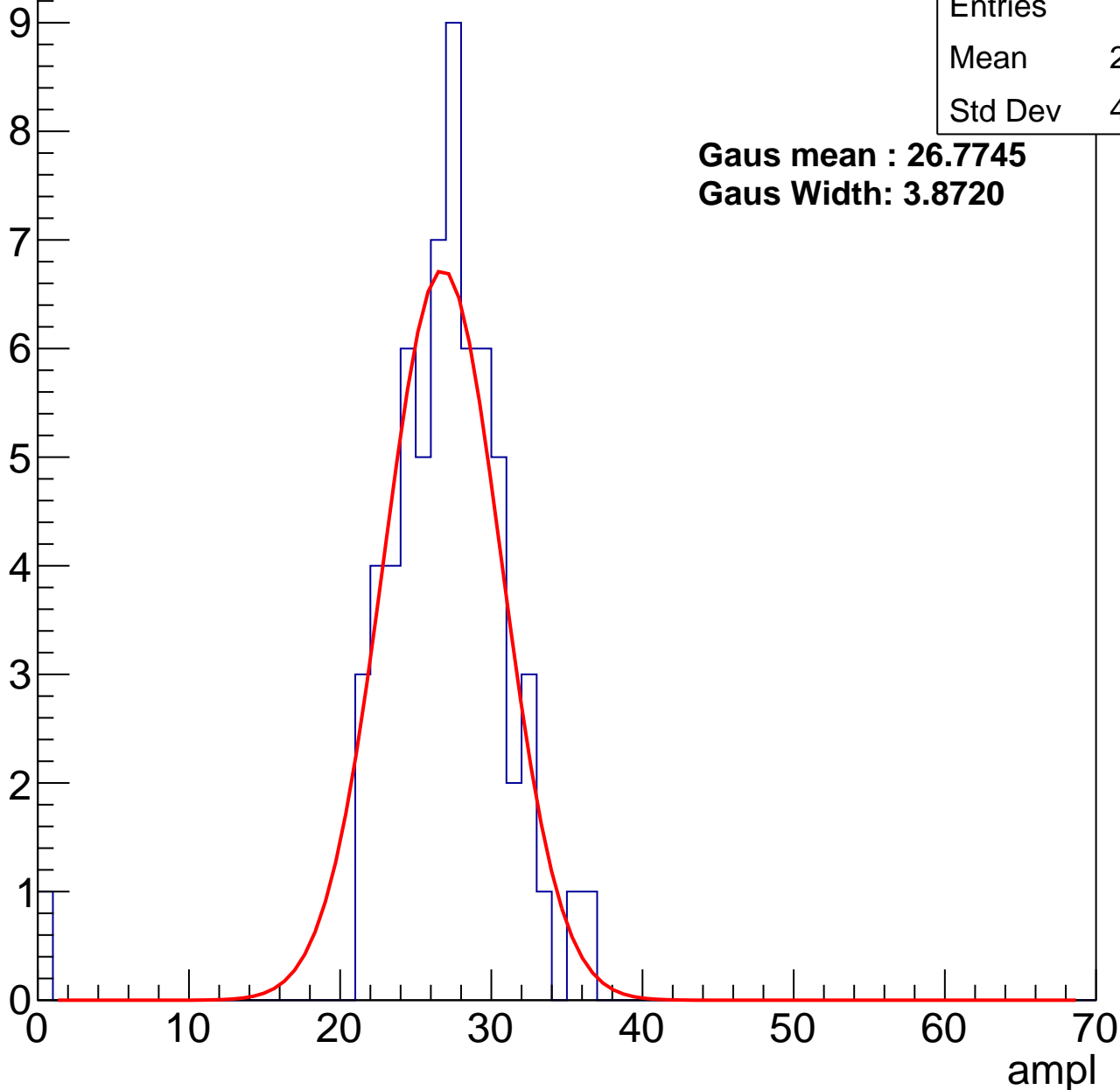
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	26.42
Std Dev	4.716

Gaus mean : 26.7745

Gaus Width: 3.8720



B1L102S, U18-ch84, adc1

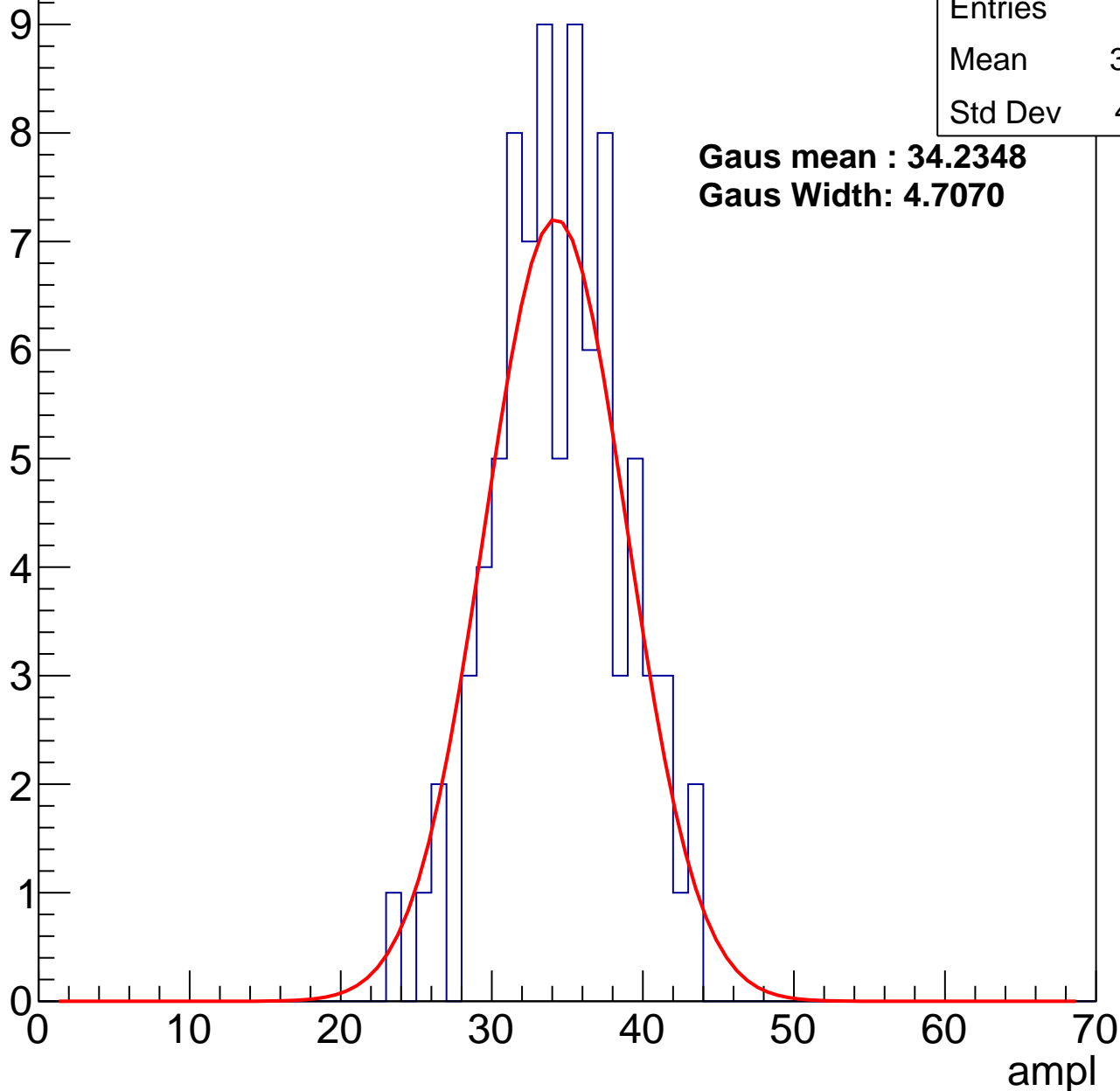
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	34.07
Std Dev	4.181

Gaus mean : 34.2348

Gaus Width: 4.7070



B1L102S, U18-ch84, adc2

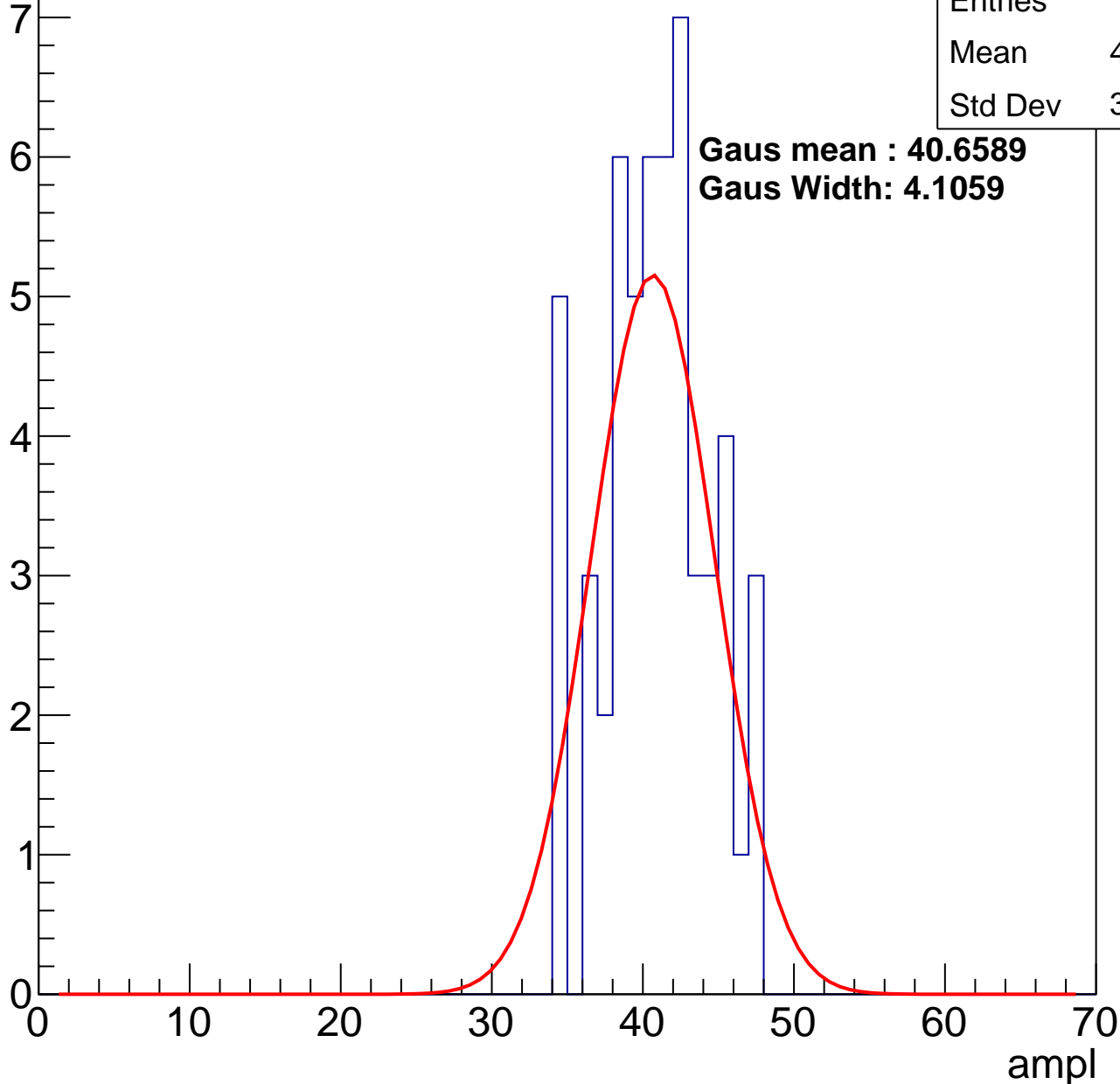
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	40.43
Std Dev	3.489

Gaus mean : 40.6589

Gaus Width: 4.1059

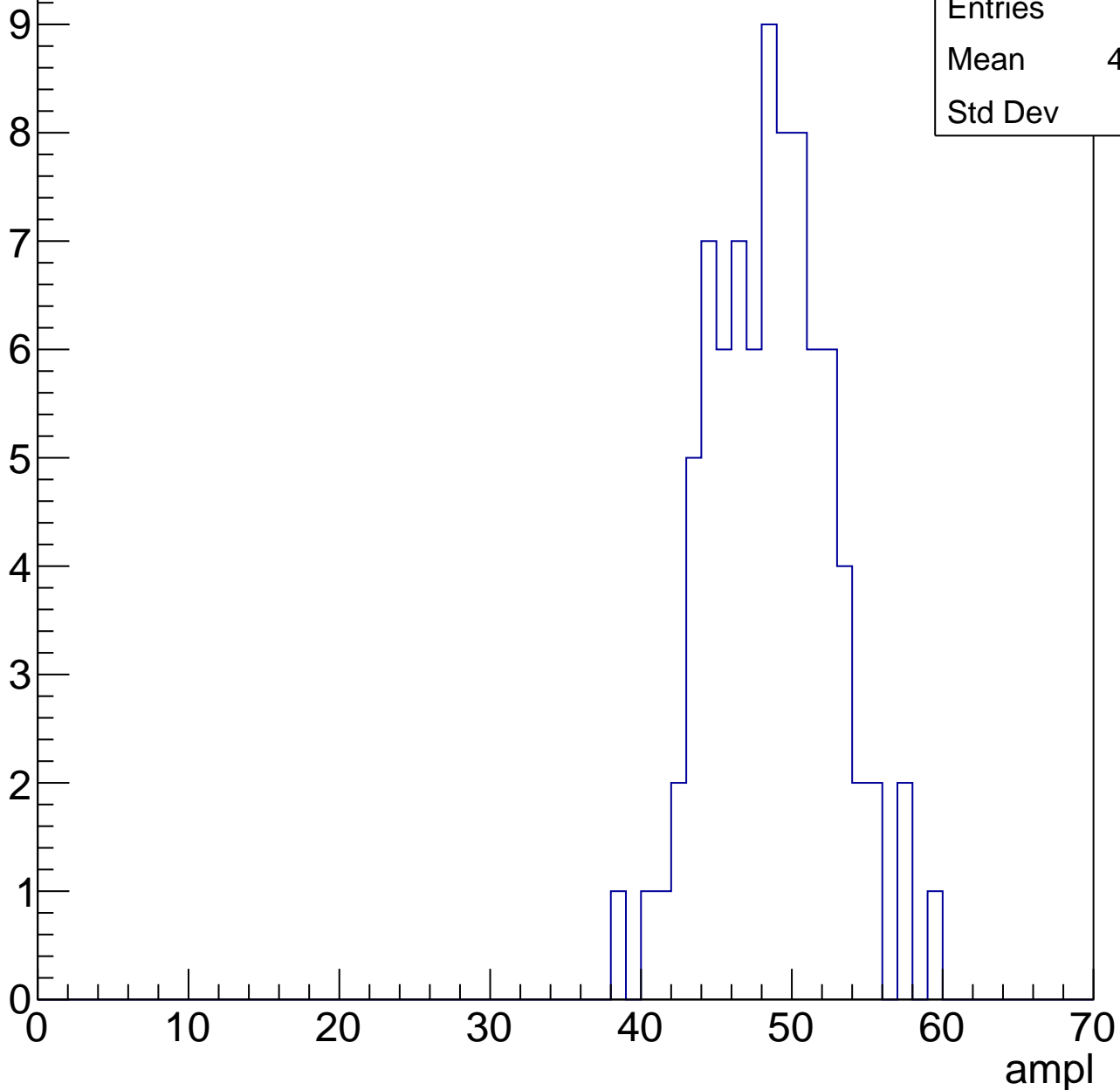


B1L102S, U18-ch84, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

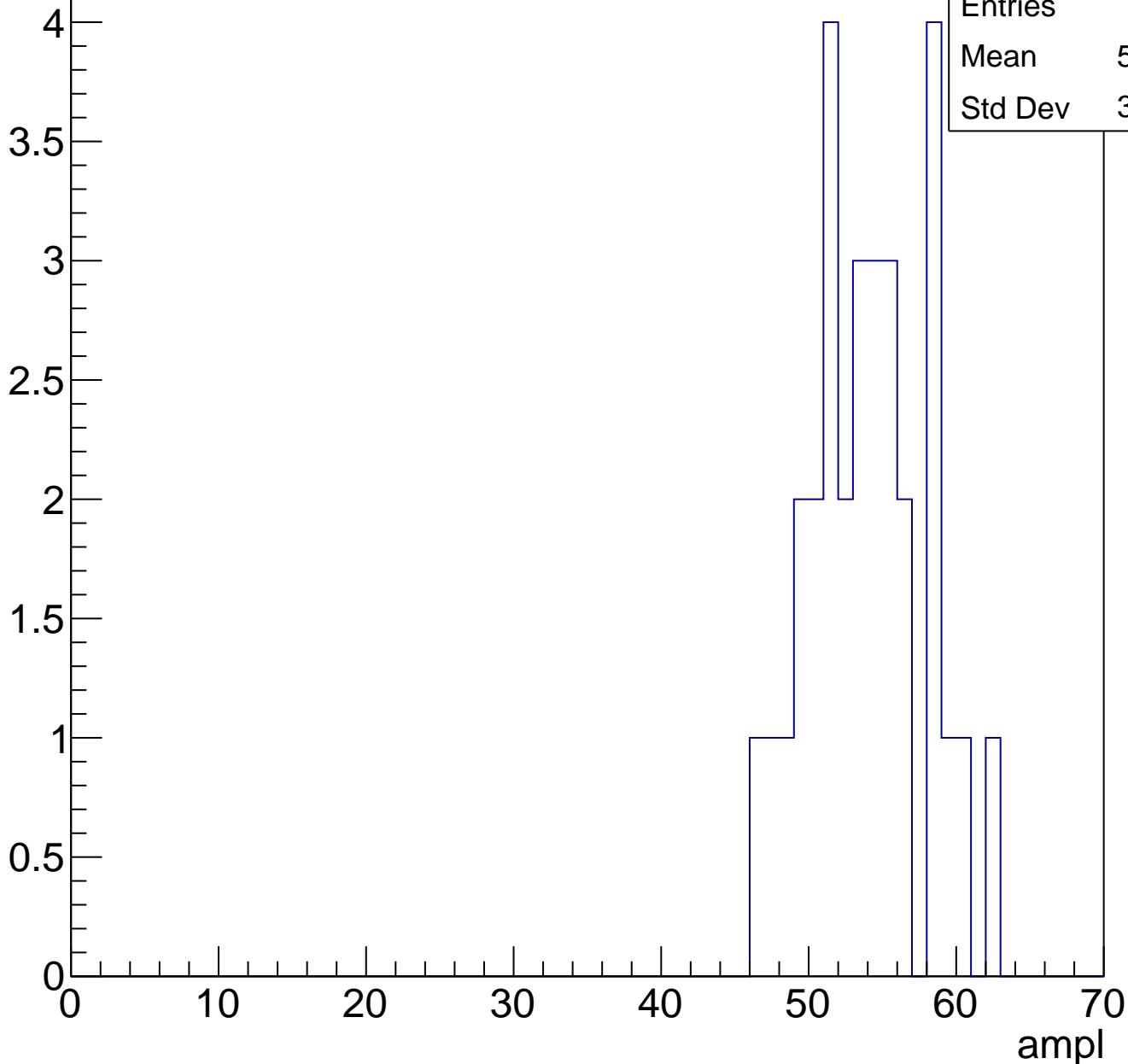
Entries	84
Mean	48.15
Std Dev	4.01



B1L102S, U18-ch84, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

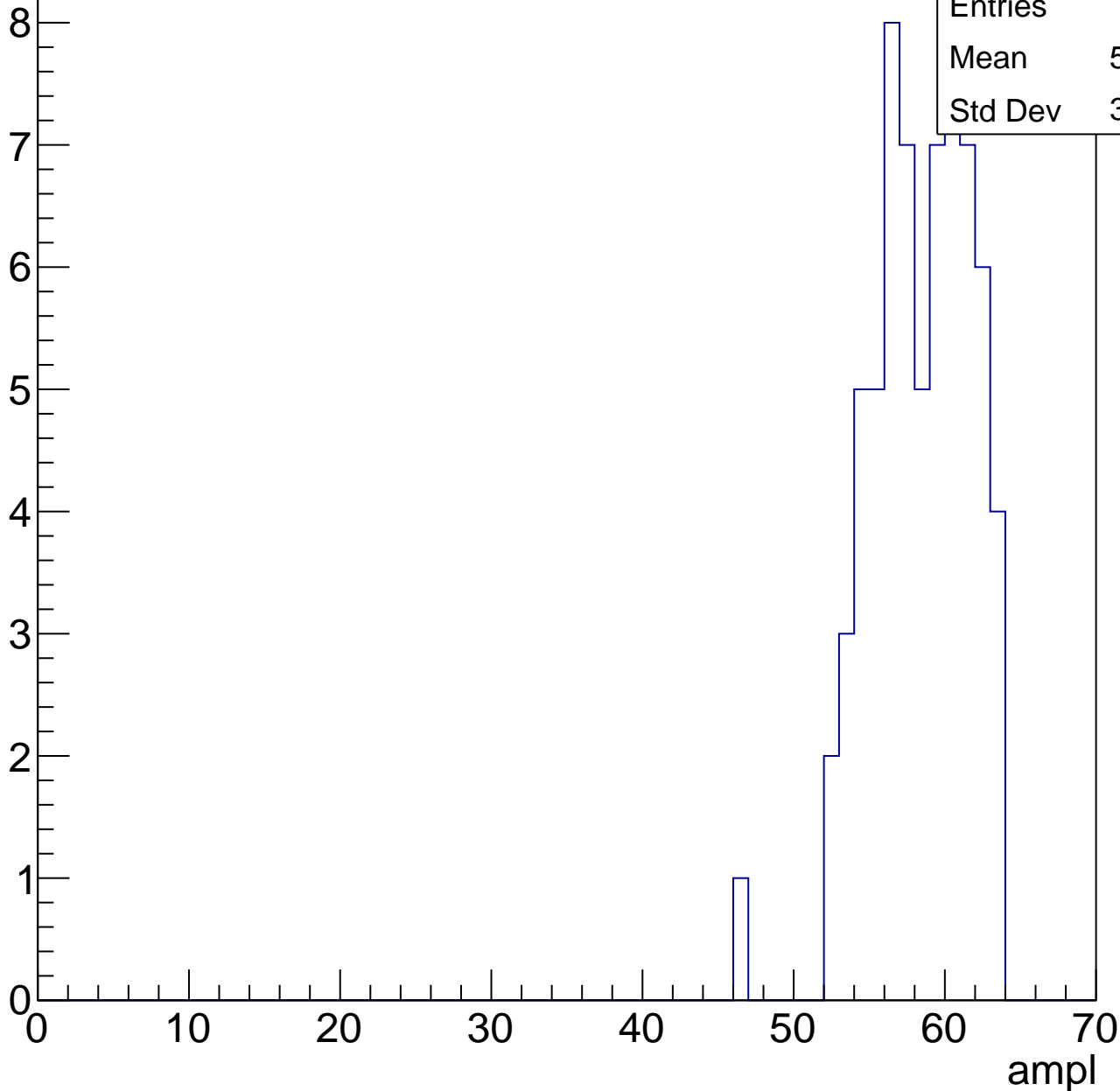


B1L102S, U18-ch84, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	57.87
Std Dev	3.325

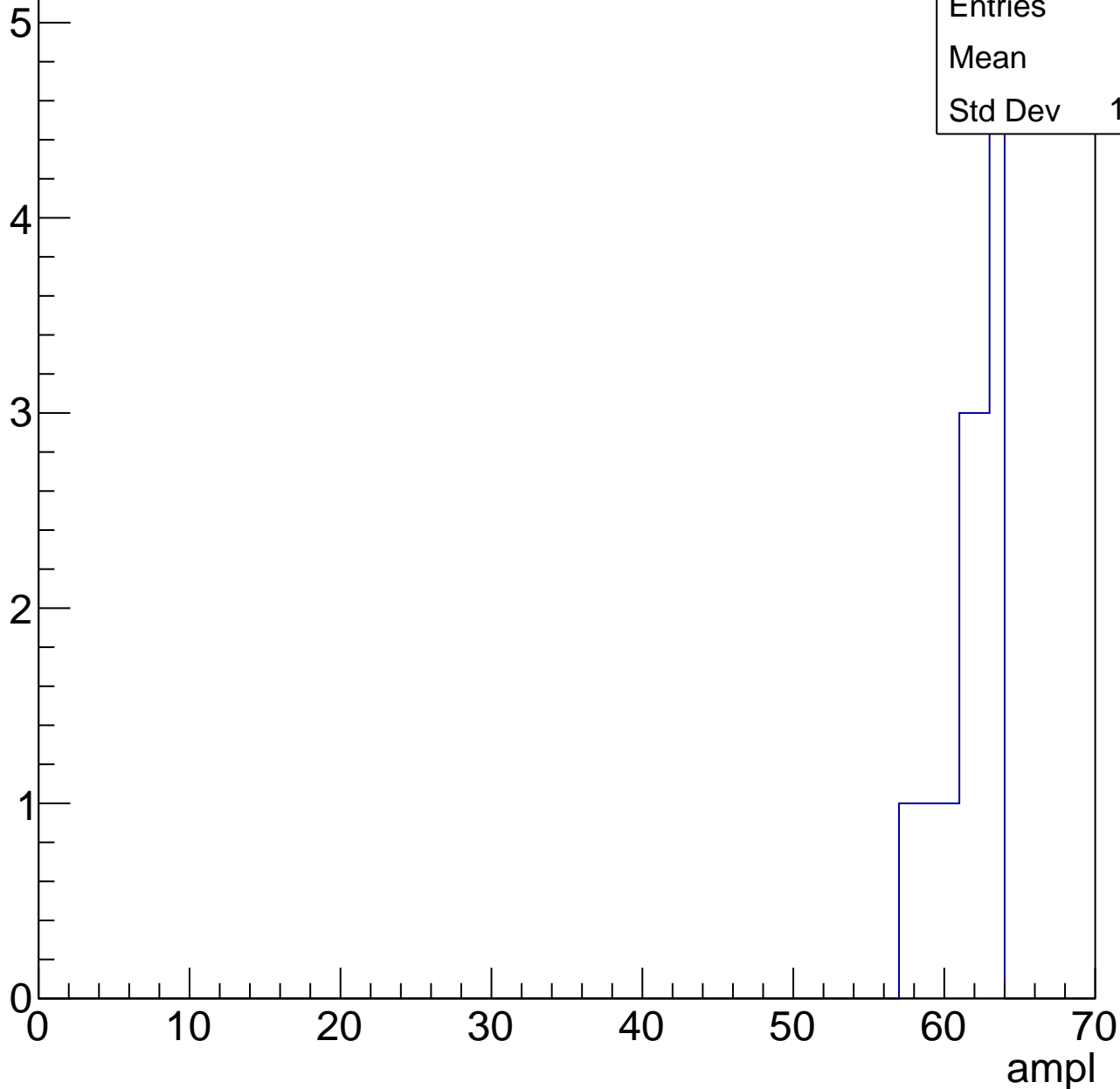


B1L102S, U18-ch84, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	15
Mean	61.2
Std Dev	1.869



B1L102S, U18-ch84, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U18-ch85, adc0

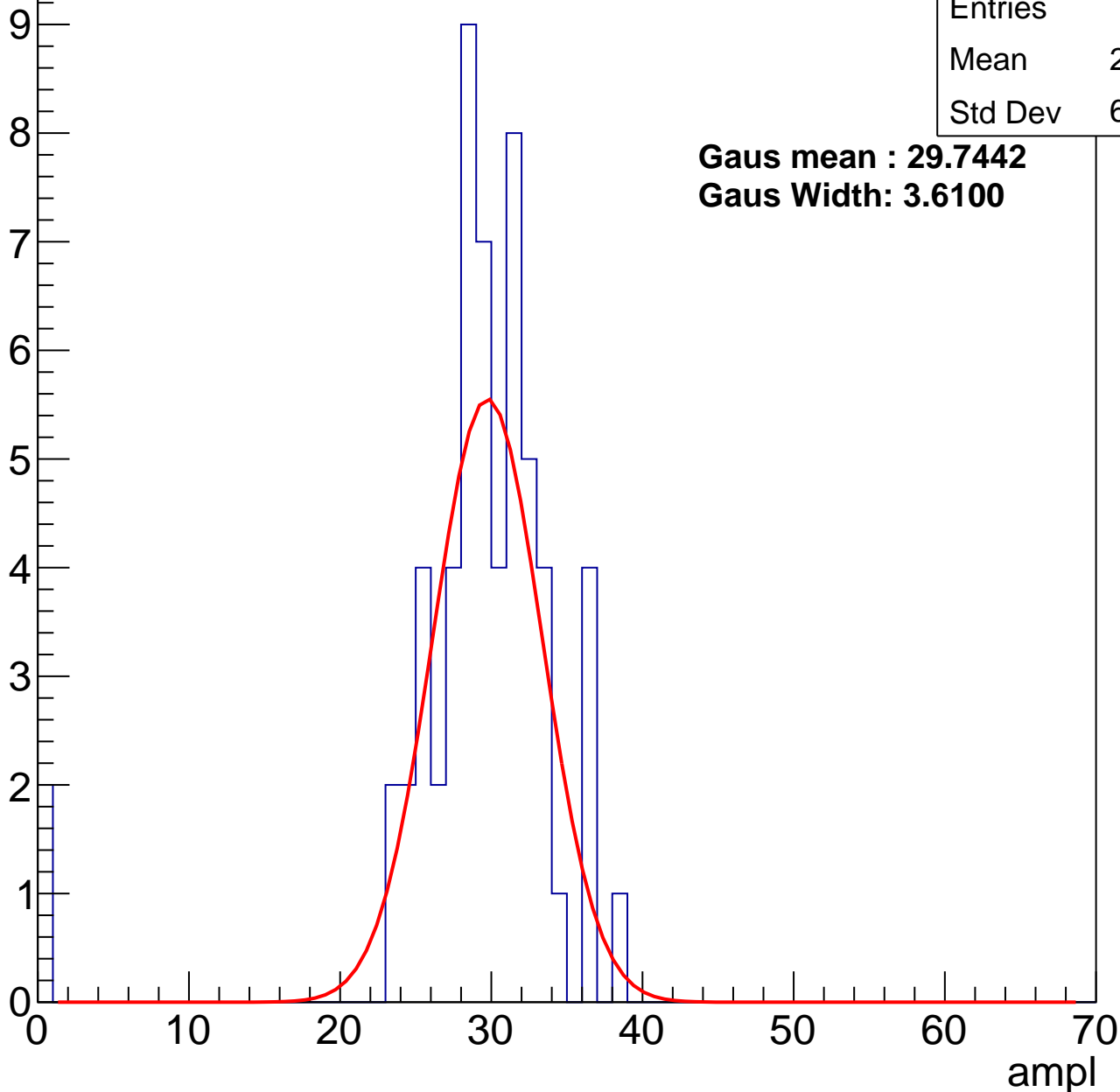
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.56
Std Dev	6.306

Gaus mean : 29.7442

Gaus Width: 3.6100



B1L102S, U18-ch85, adc1

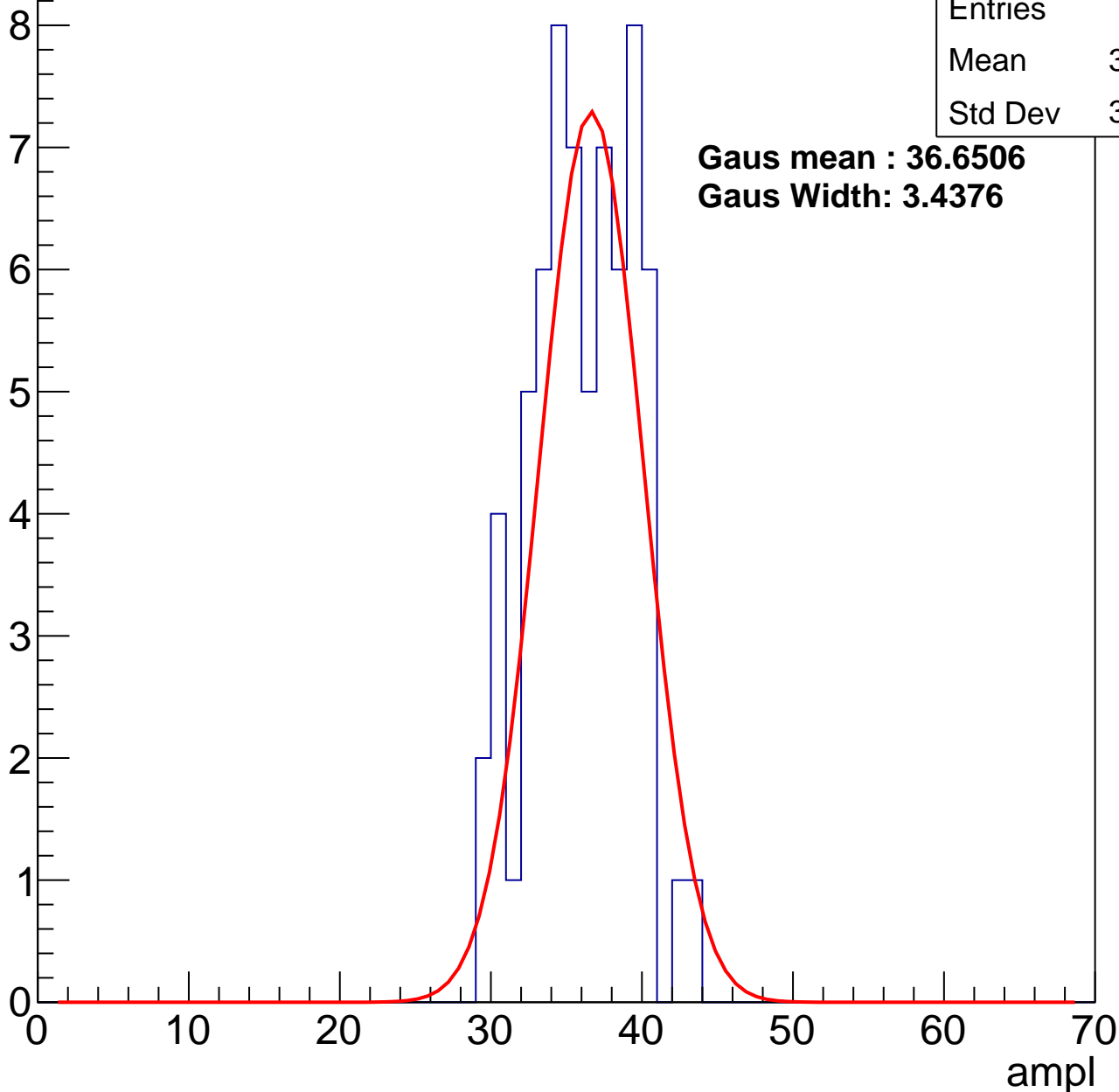
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	35.64
Std Dev	3.268

Gaus mean : 36.6506

Gaus Width: 3.4376



B1L102S, U18-ch85, adc2

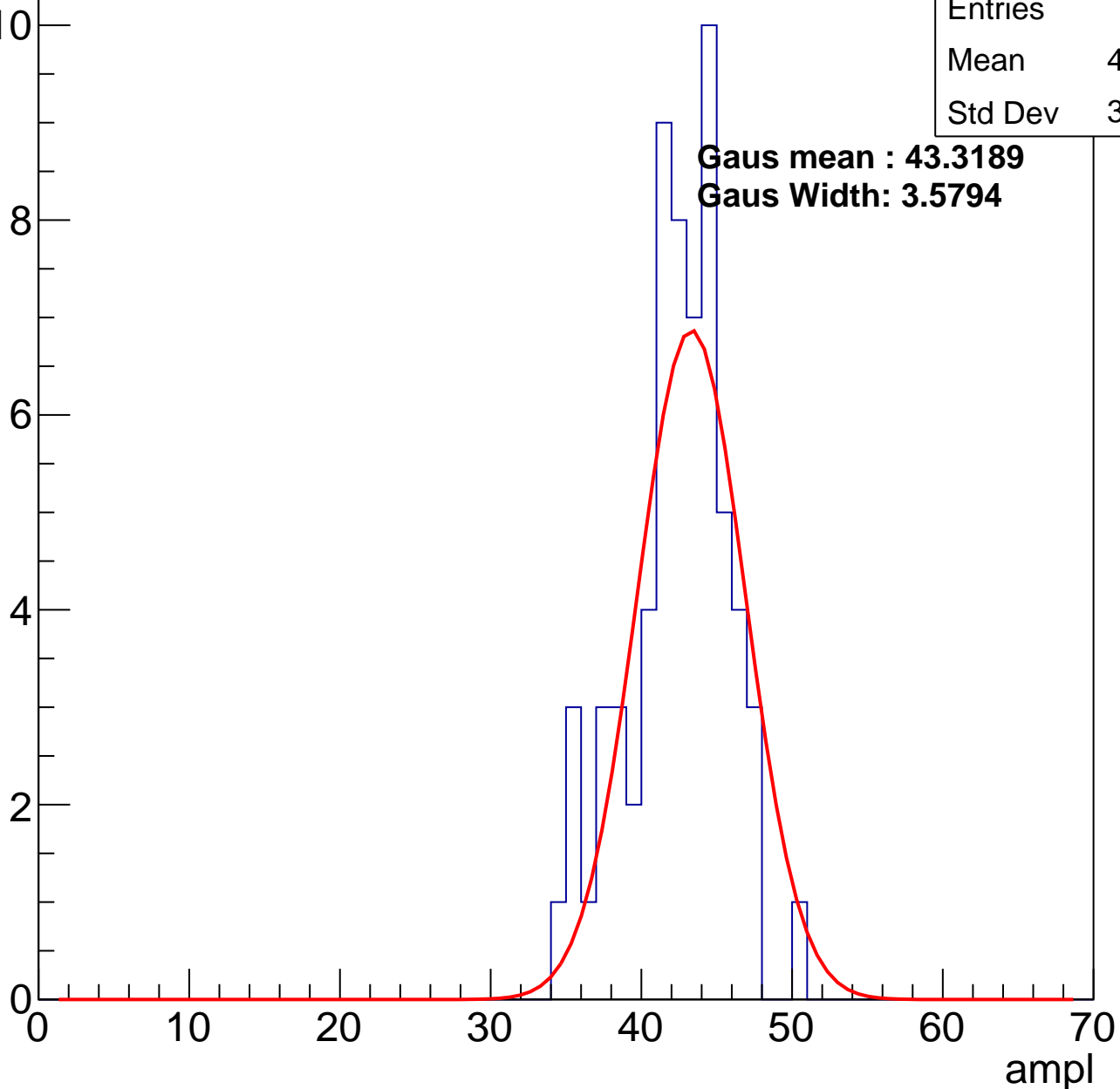
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.94
Std Dev	3.325

Gaus mean : 43.3189

Gaus Width: 3.5794

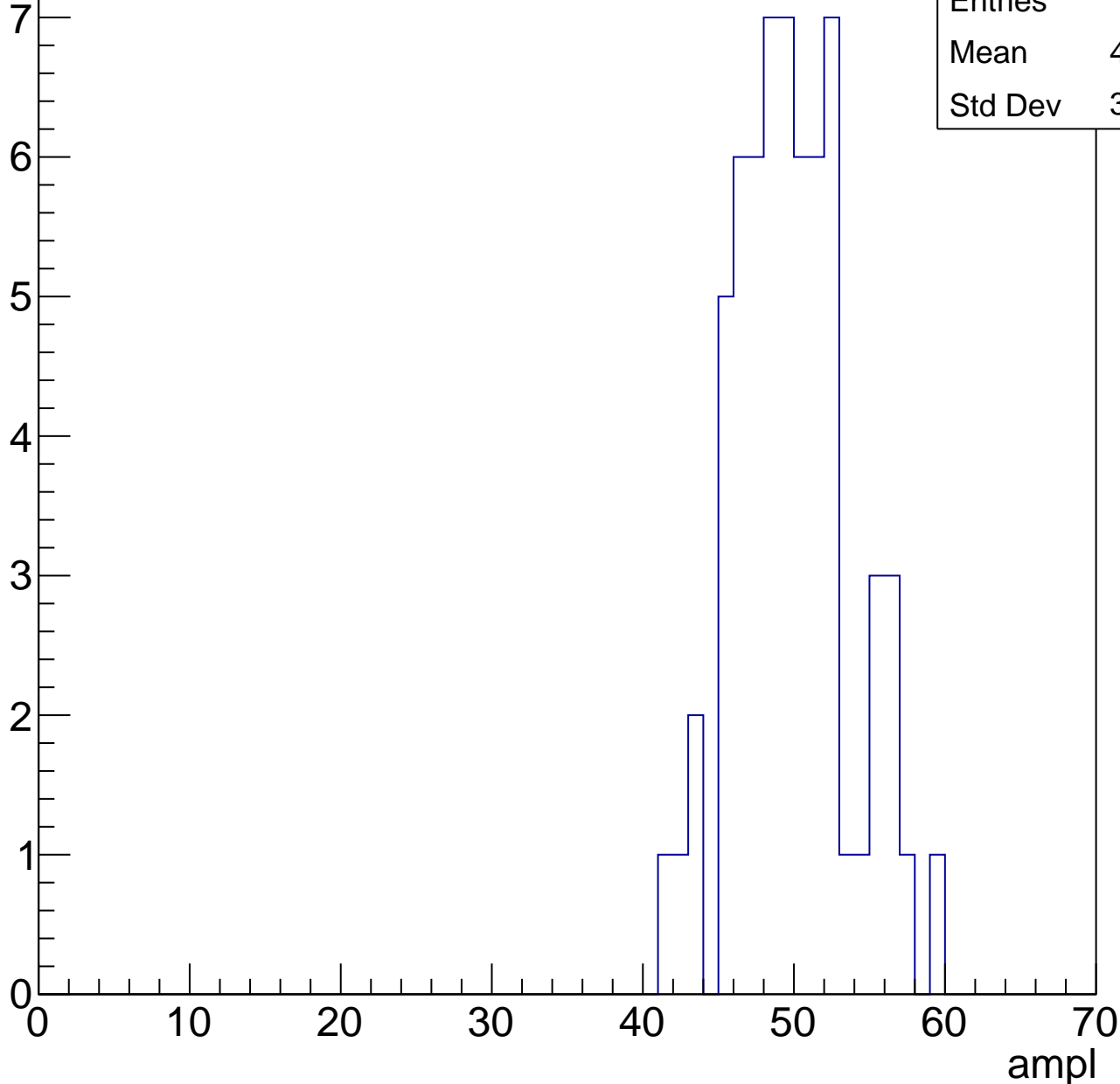


B1L102S, U18-ch85, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	49.33
Std Dev	3.742

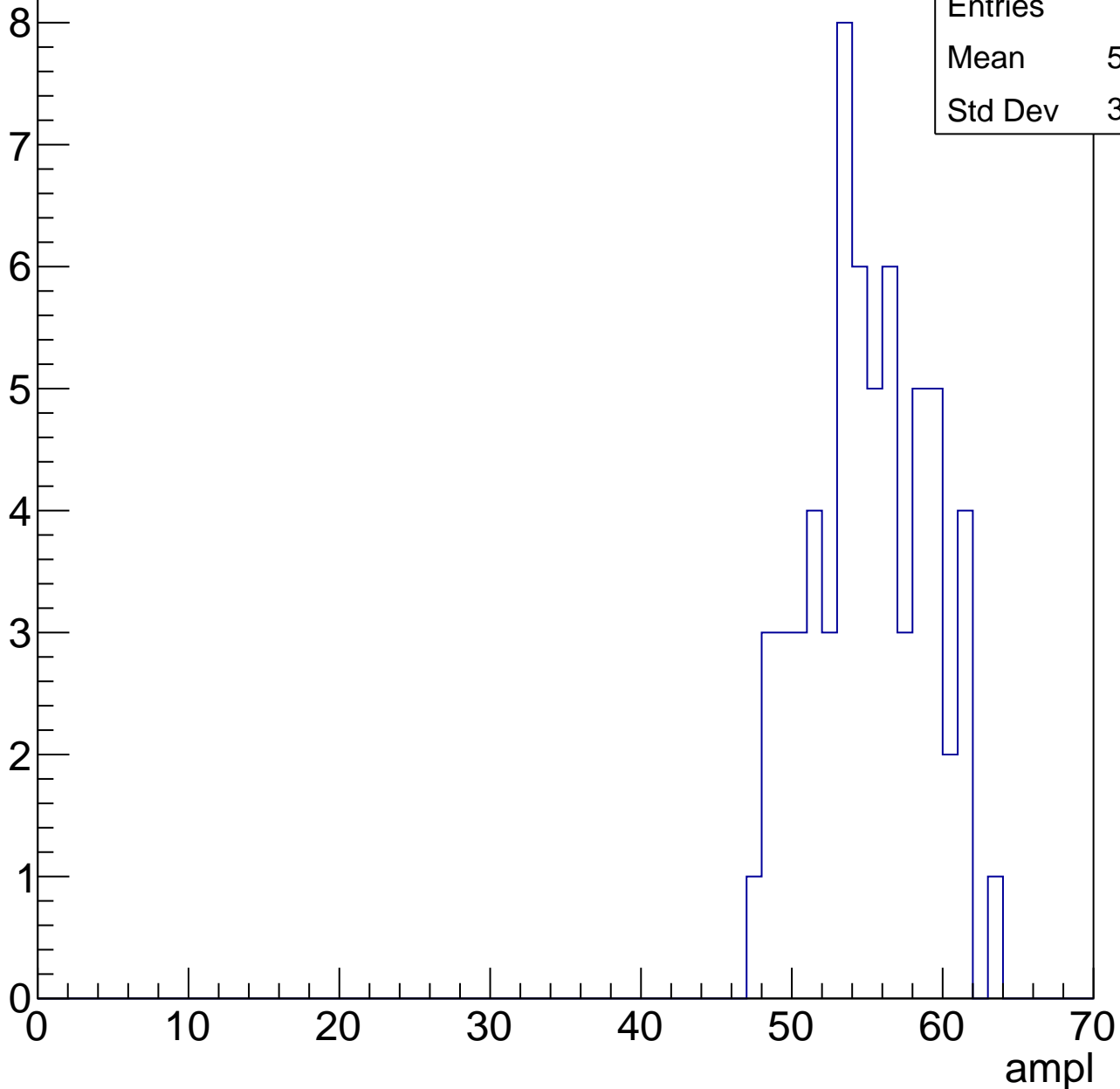


B1L102S, U18-ch85, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	54.68
Std Dev	3.847

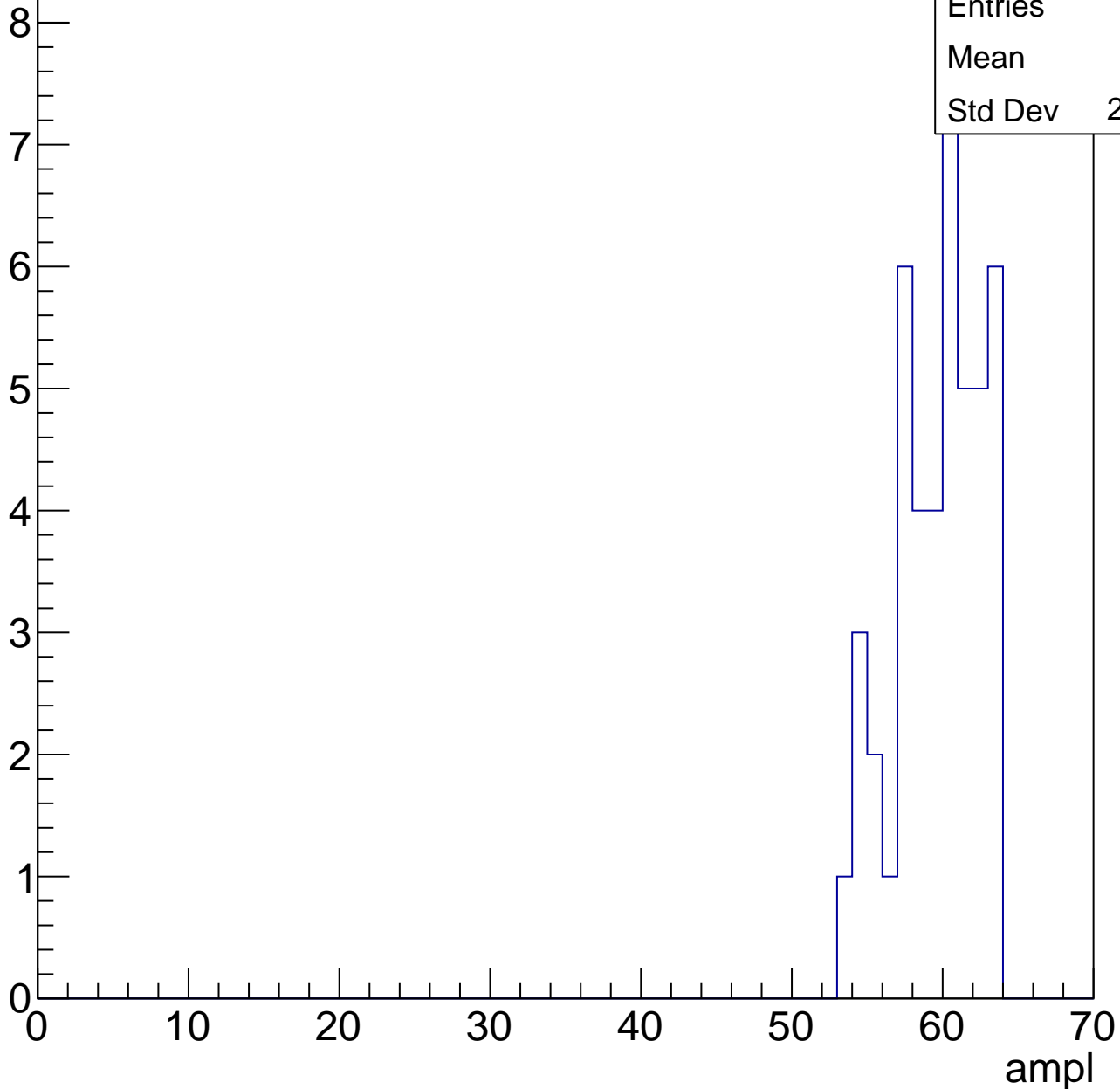


B1L102S, U18-ch85, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.2
Std Dev	2.778

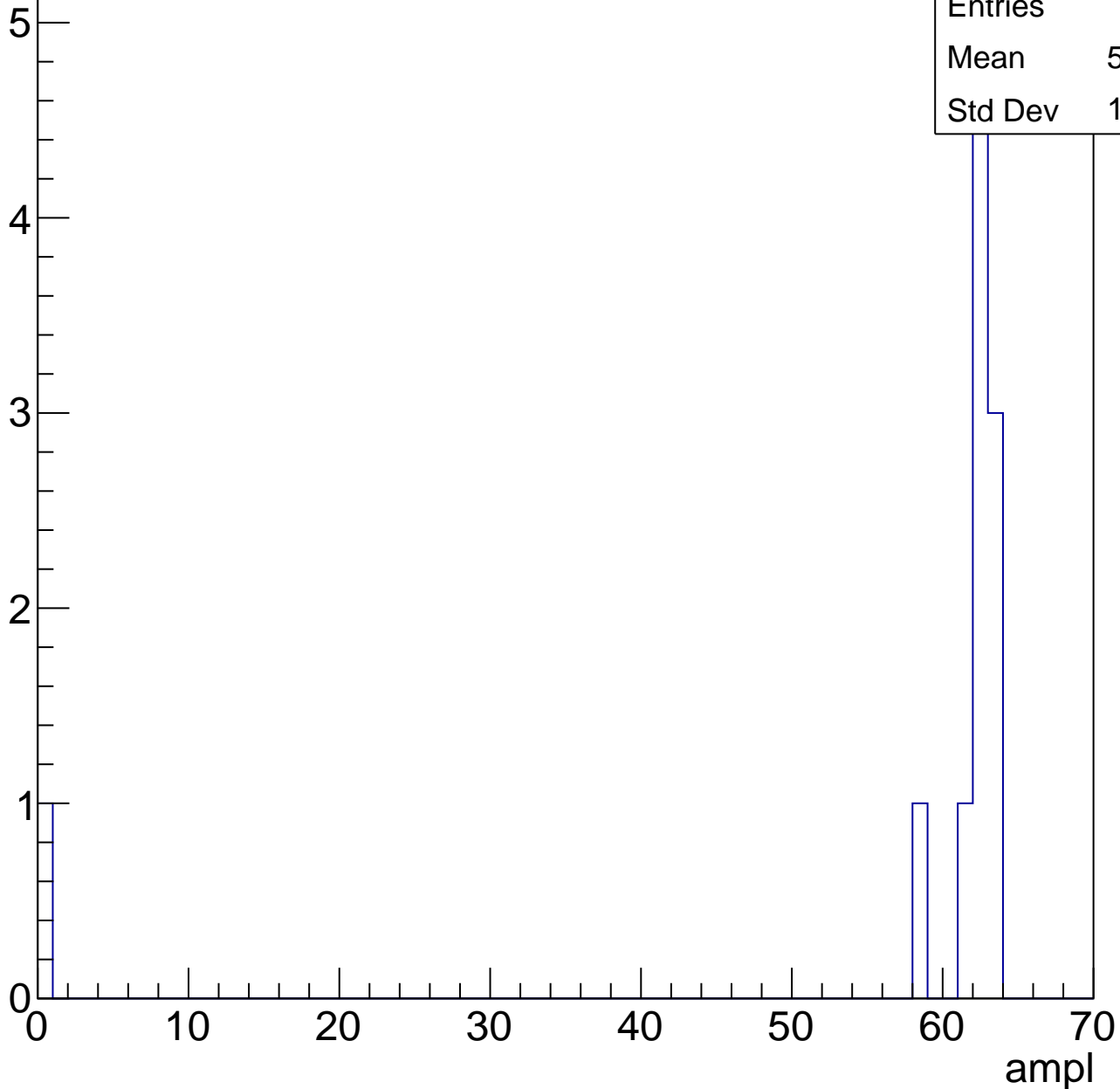


B1L102S, U18-ch85, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	11
Mean	56.18
Std Dev	17.82



B1L102S, U18-ch85, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch86, adc0

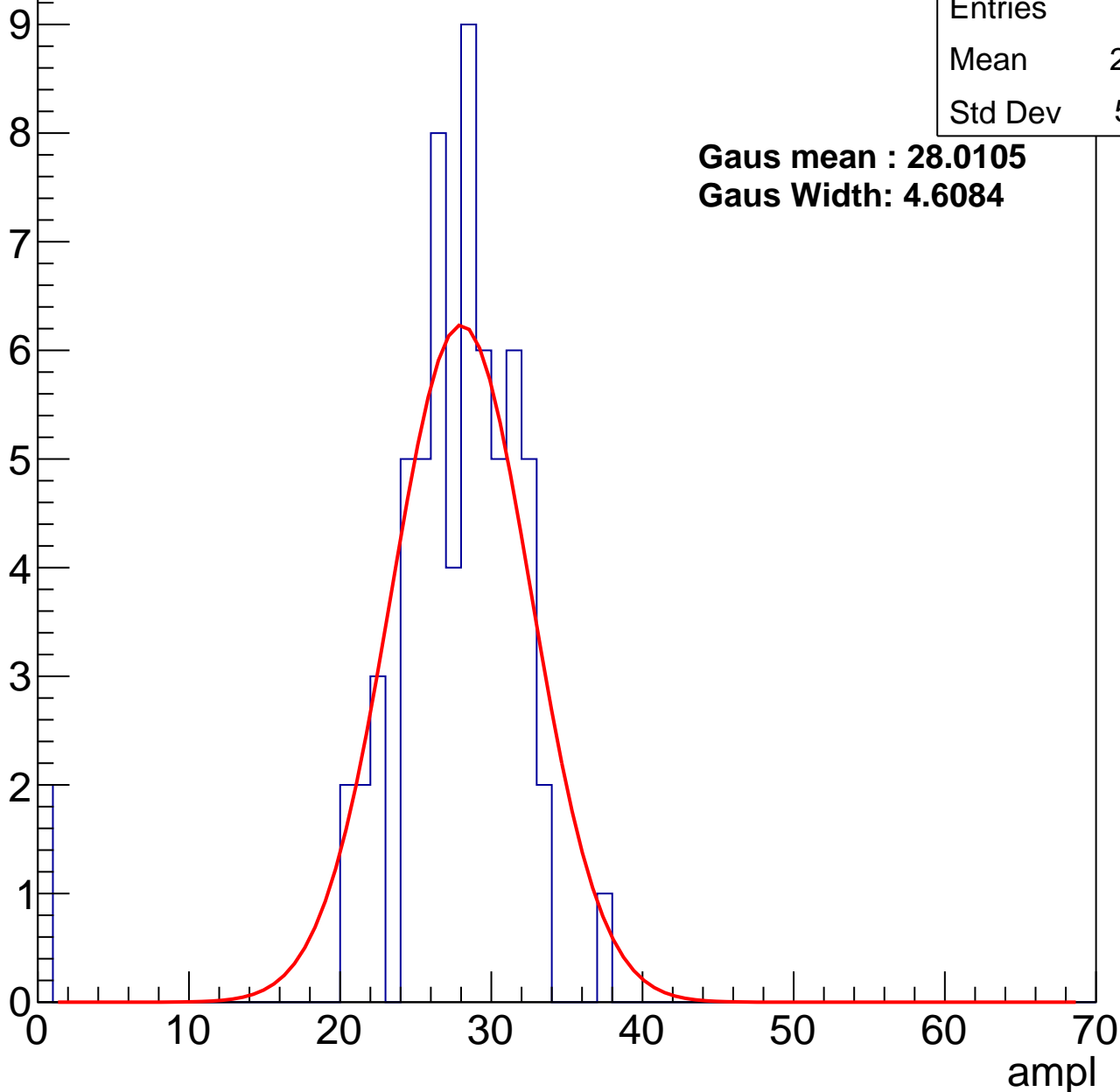
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	26.68
Std Dev	5.871

Gaus mean : 28.0105

Gaus Width: 4.6084



B1L102S, U18-ch86, adc1

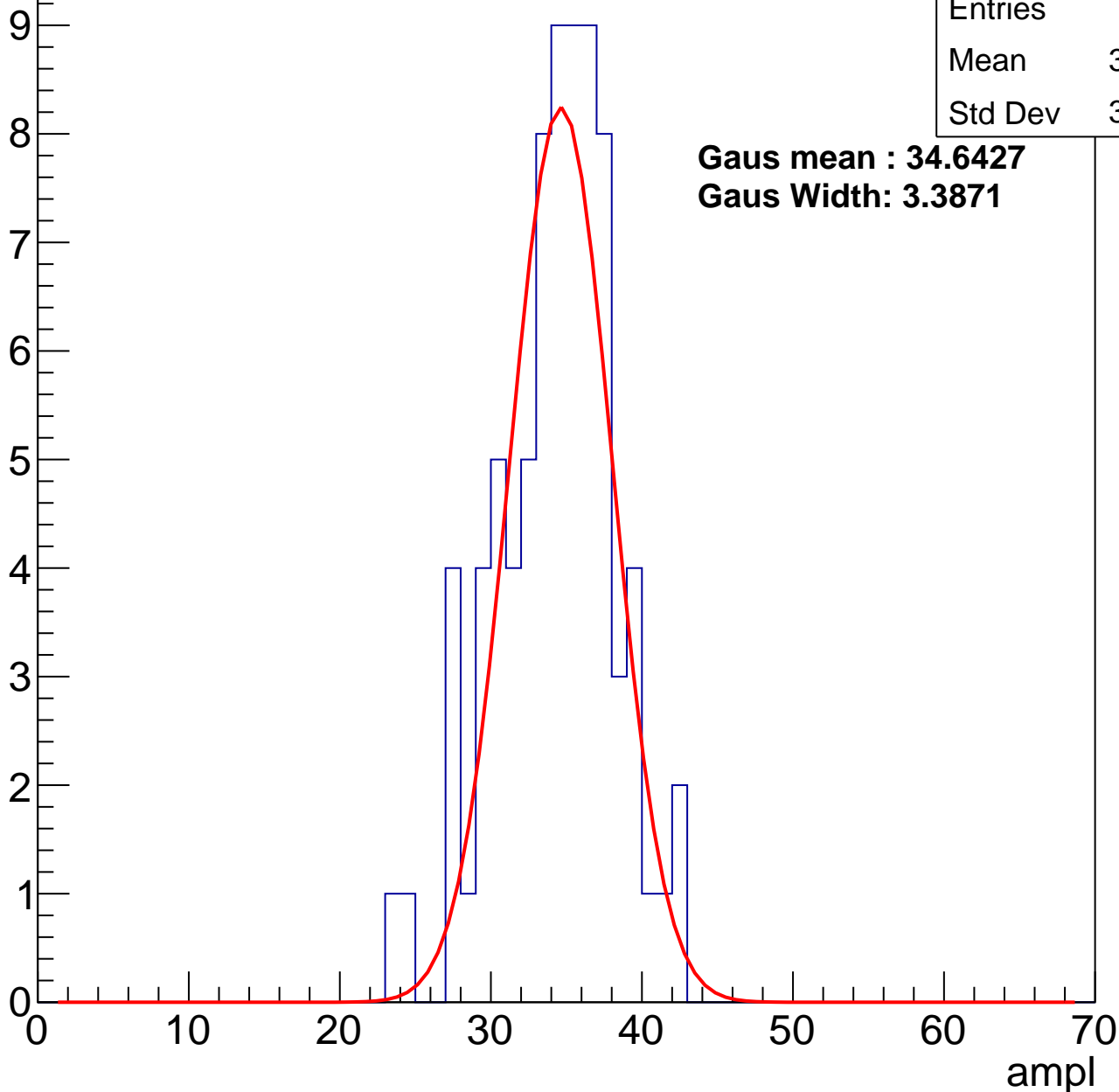
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	33.84
Std Dev	3.853

Gaus mean : 34.6427

Gaus Width: 3.3871



B1L102S, U18-ch86, adc2

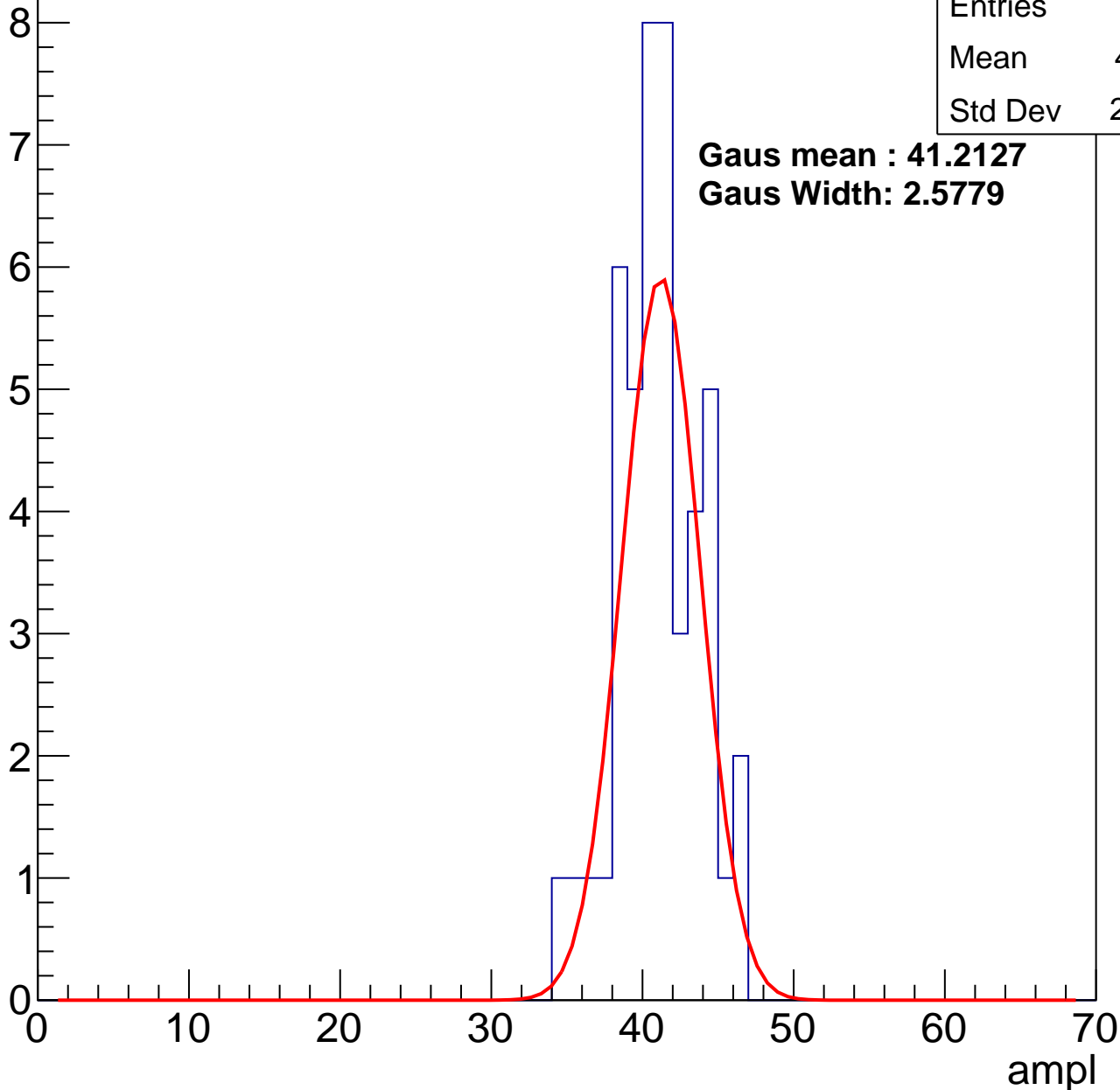
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	40.61
Std Dev	2.682

Gaus mean : 41.2127

Gaus Width: 2.5779

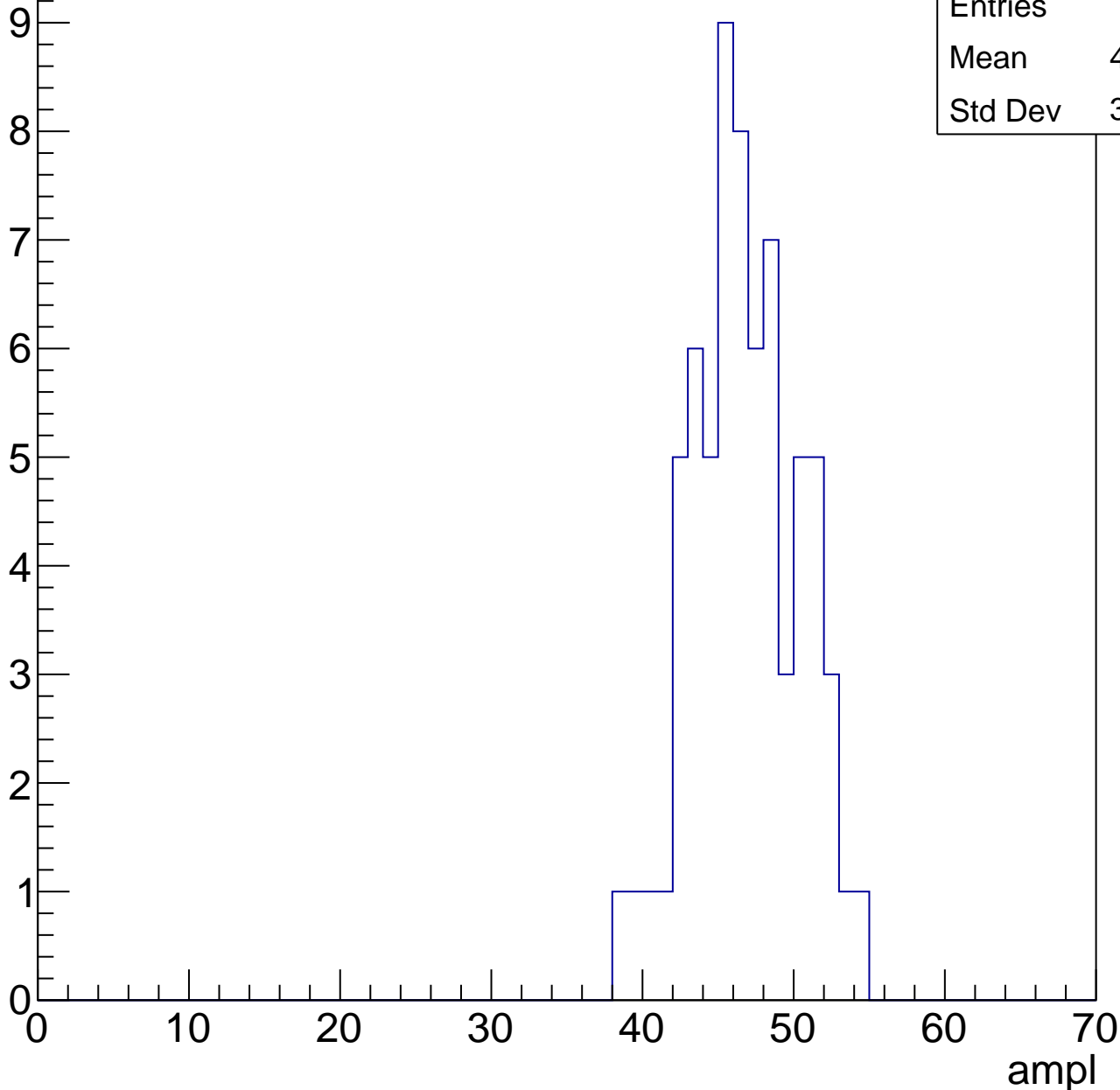


B1L102S, U18-ch86, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	46.35
Std Dev	3.463

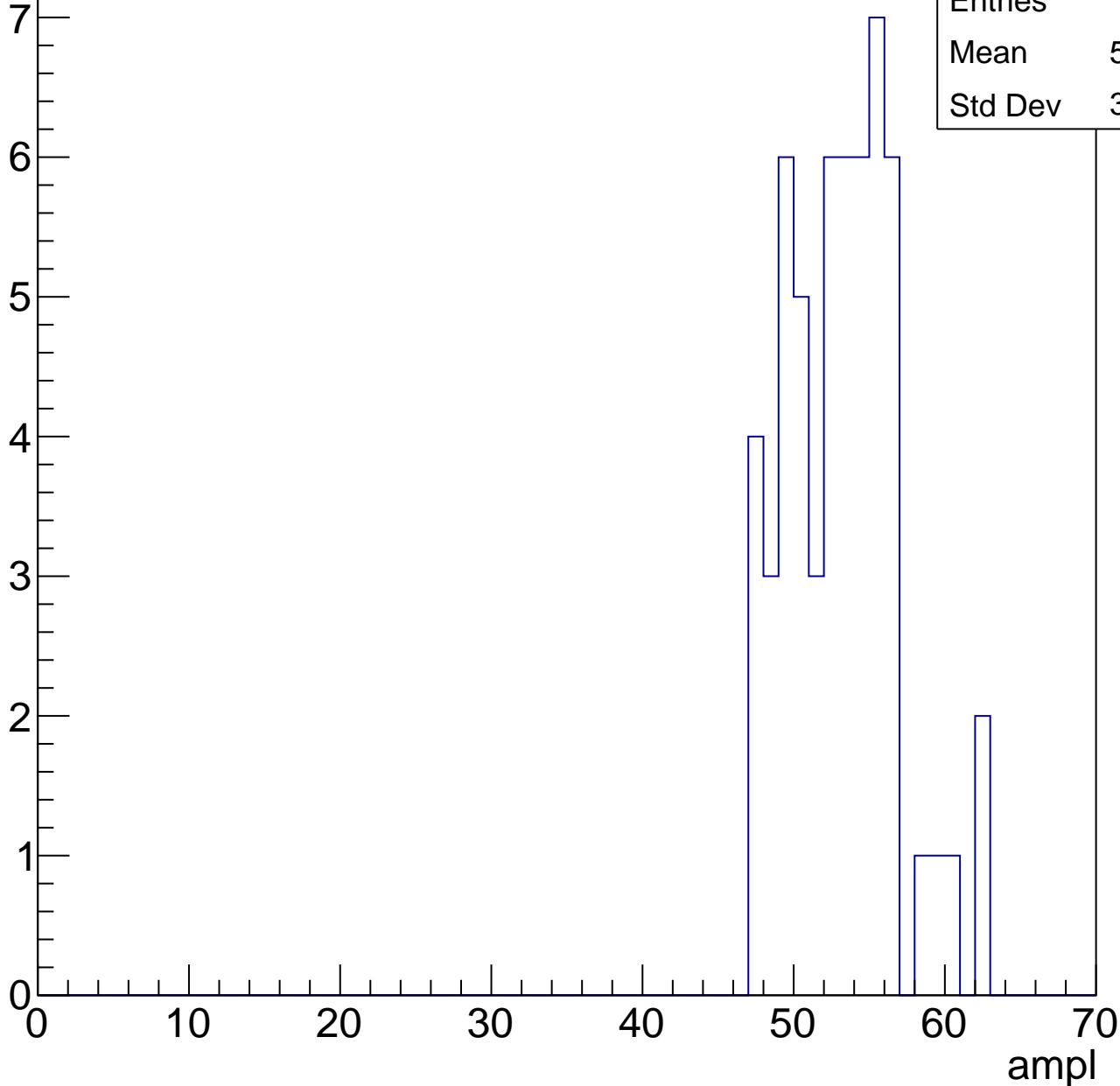


B1L102S, U18-ch86, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	52.72
Std Dev	3.587

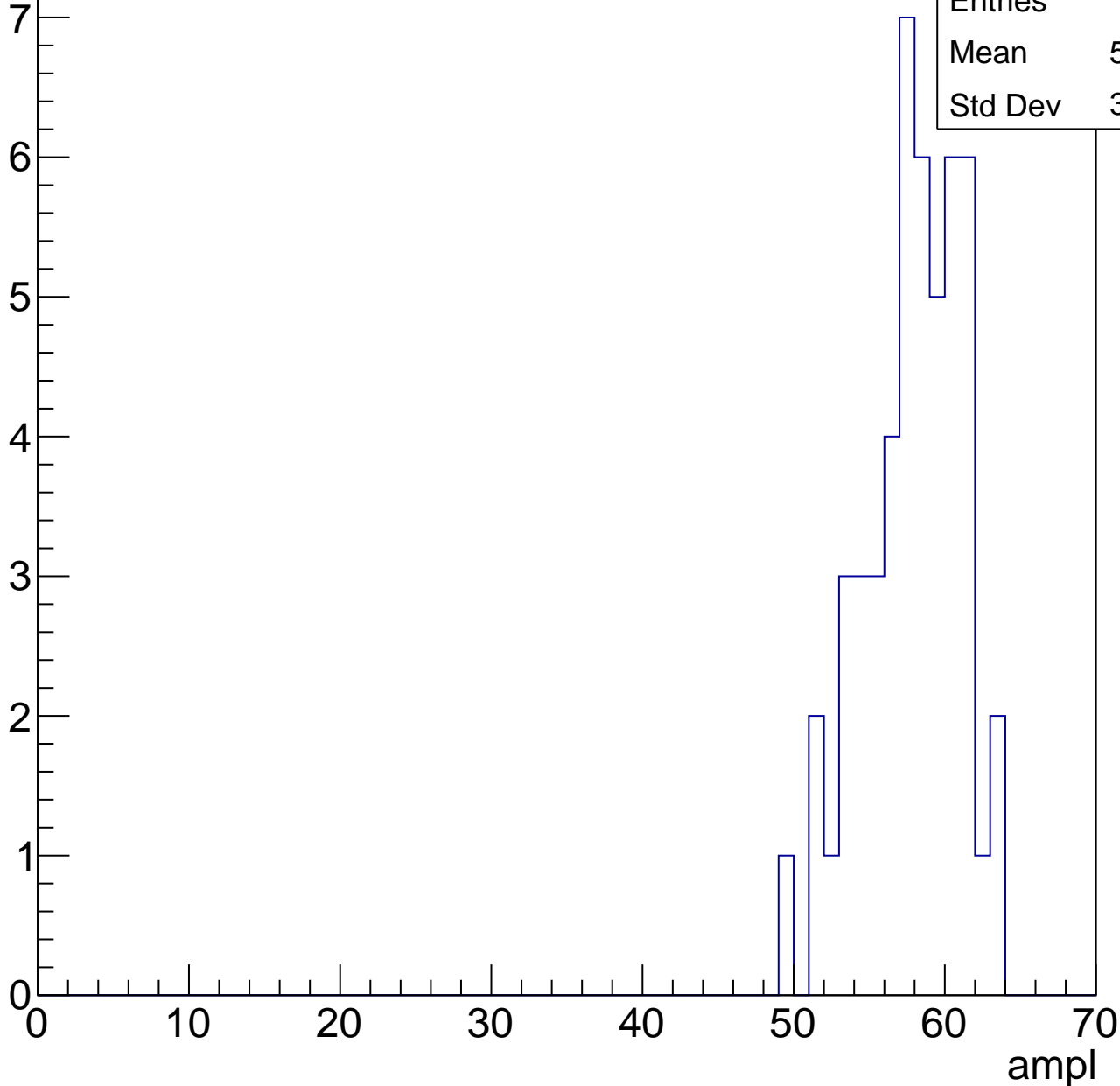


B1L102S, U18-ch86, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.38
Std Dev	3.218

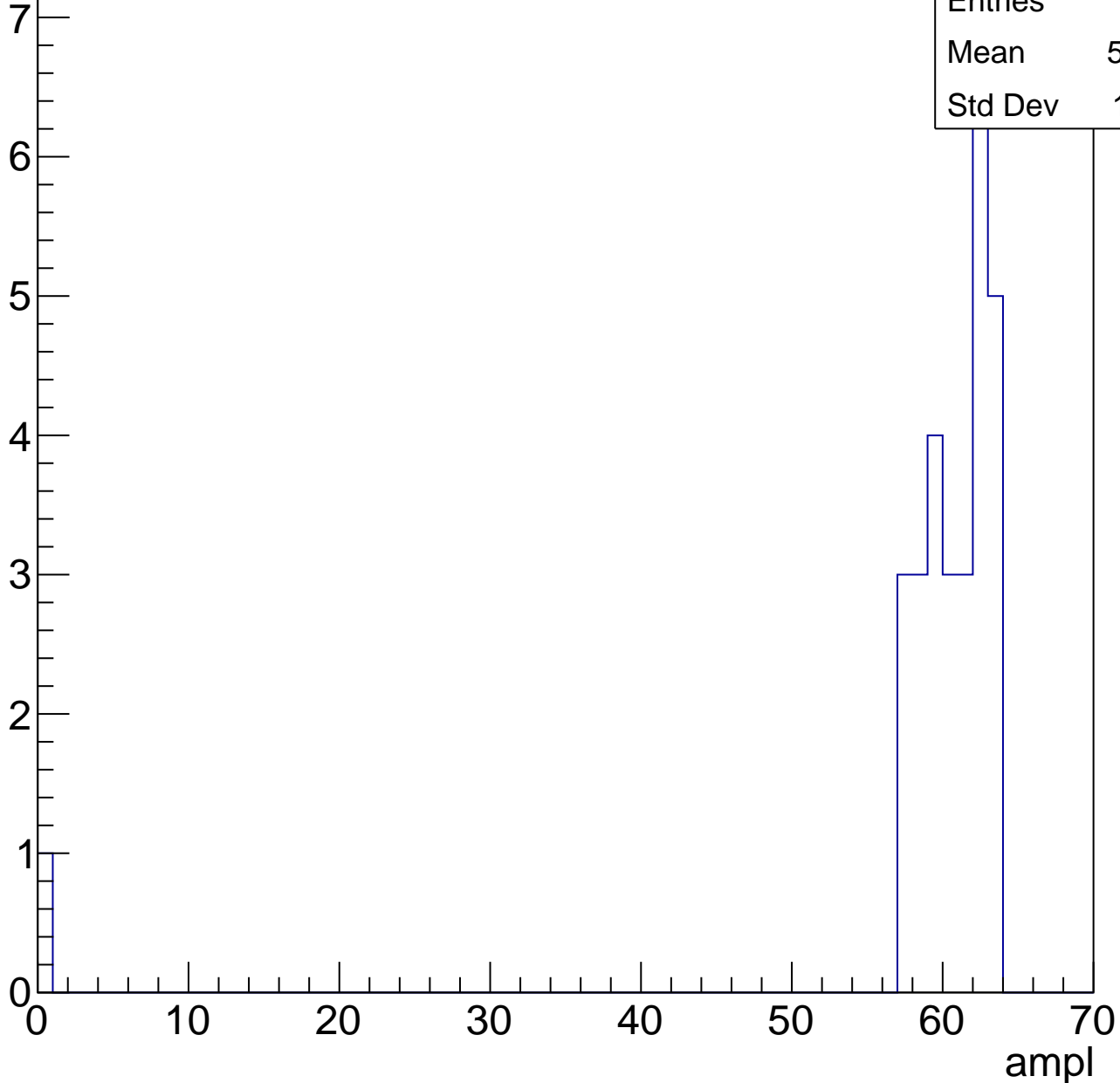


B1L102S, U18-ch86, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

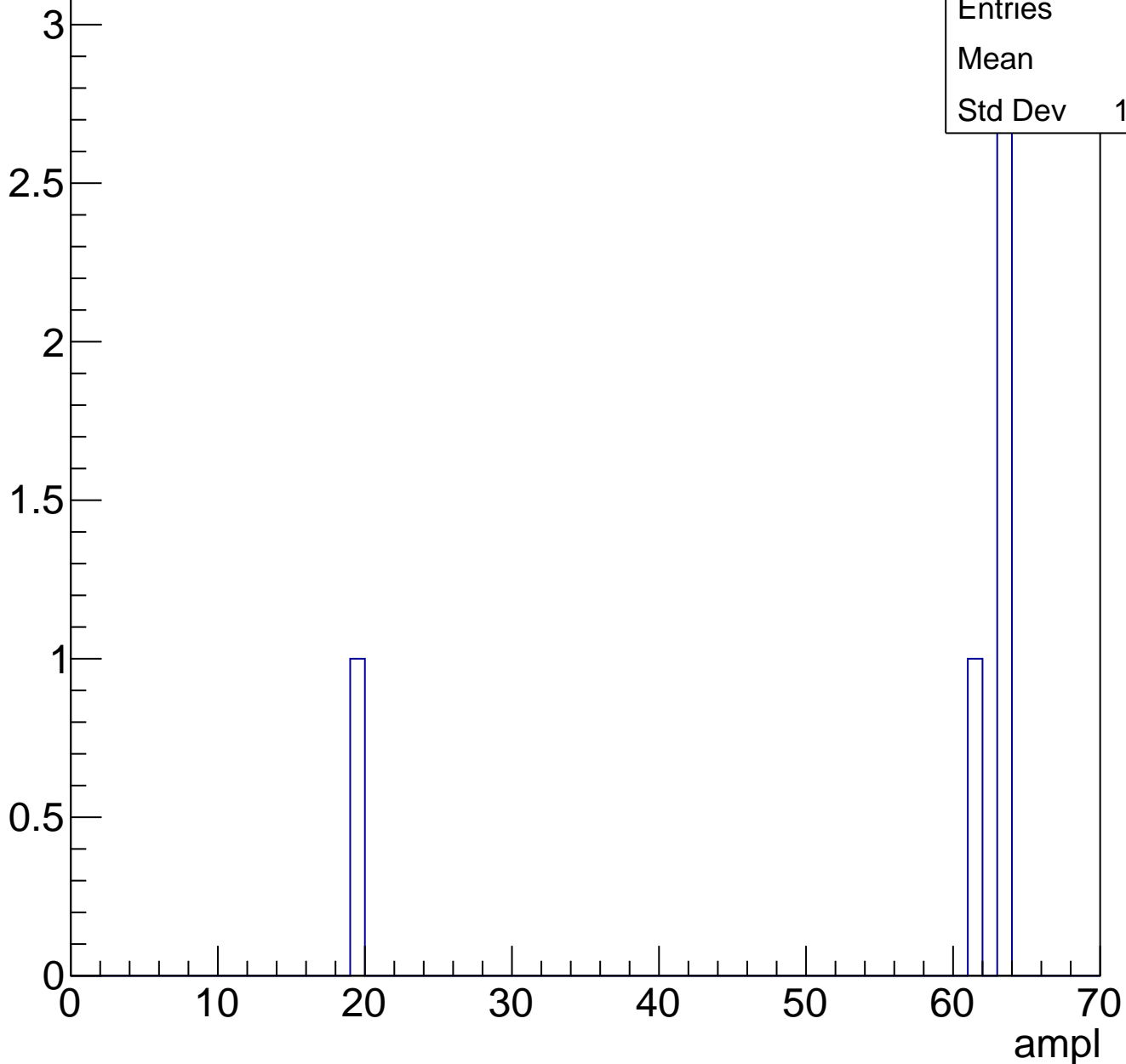
Entries	29
Mean	58.38
Std Dev	11.21



B1L102S, U18-ch86, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch87, adc0

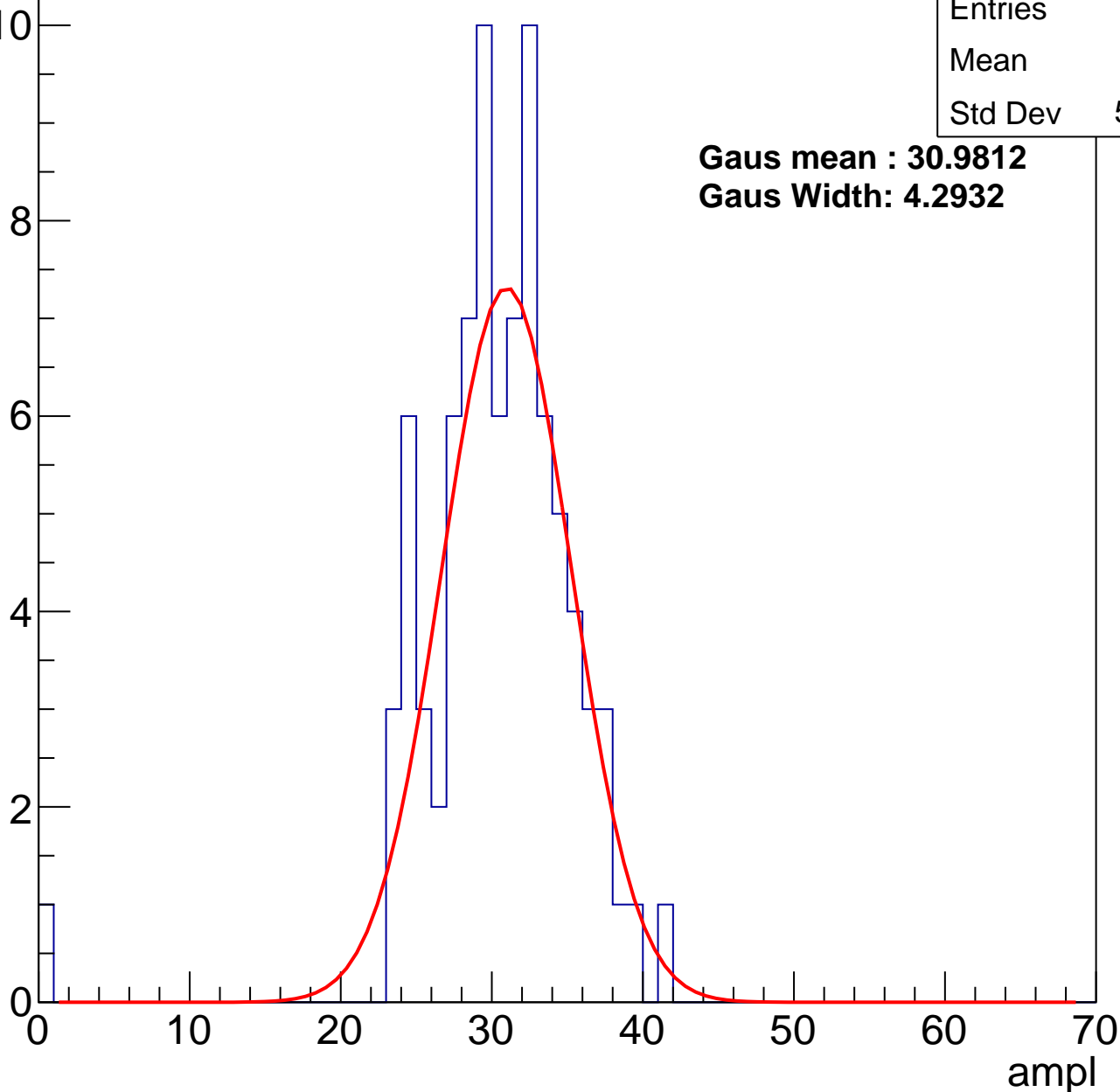
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	30
Std Dev	5.161

Gaus mean : 30.9812

Gaus Width: 4.2932



B1L102S, U18-ch87, adc1

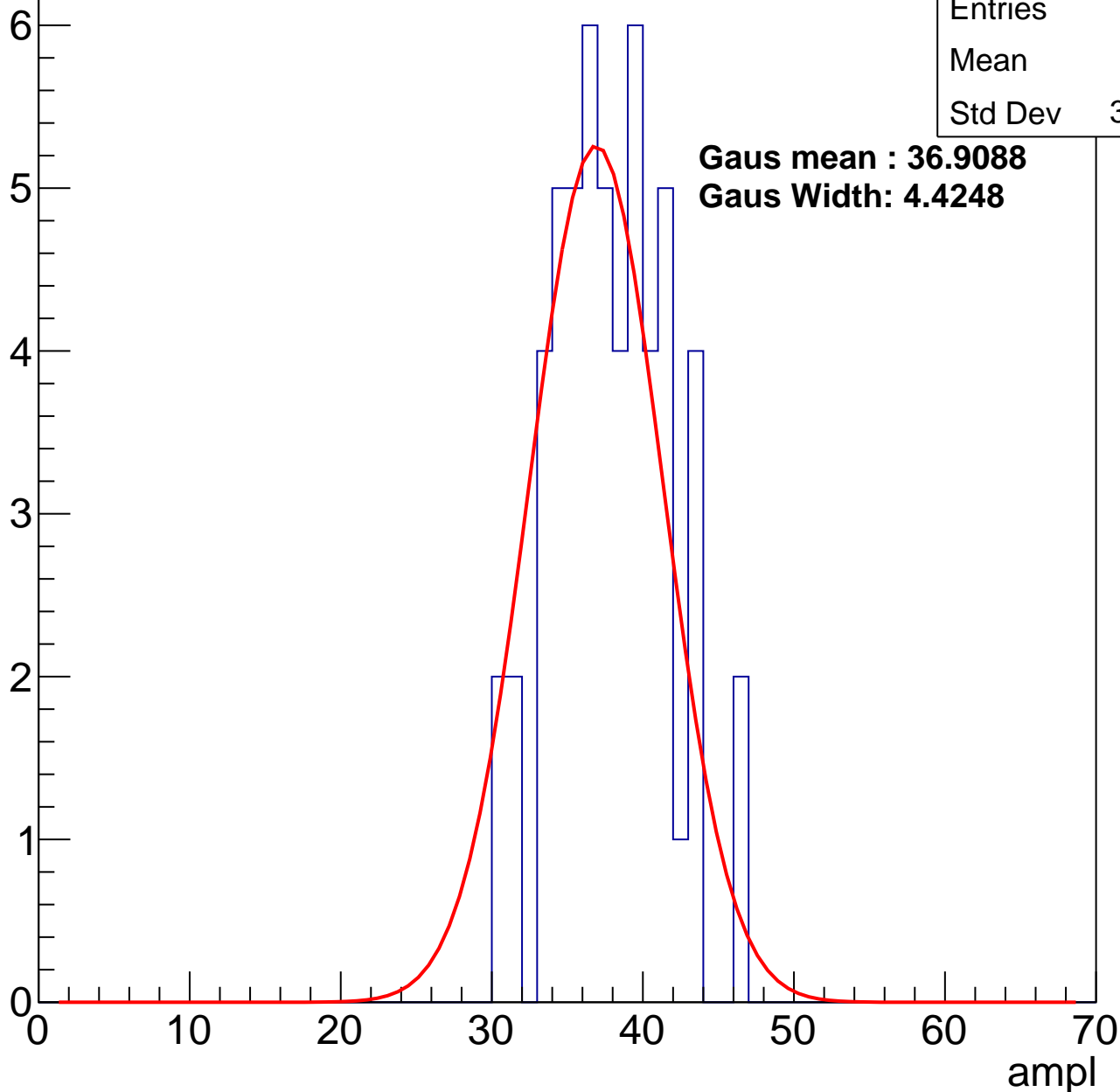
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	37.4
Std Dev	3.745

Gaus mean : 36.9088

Gaus Width: 4.4248



B1L102S, U18-ch87, adc2

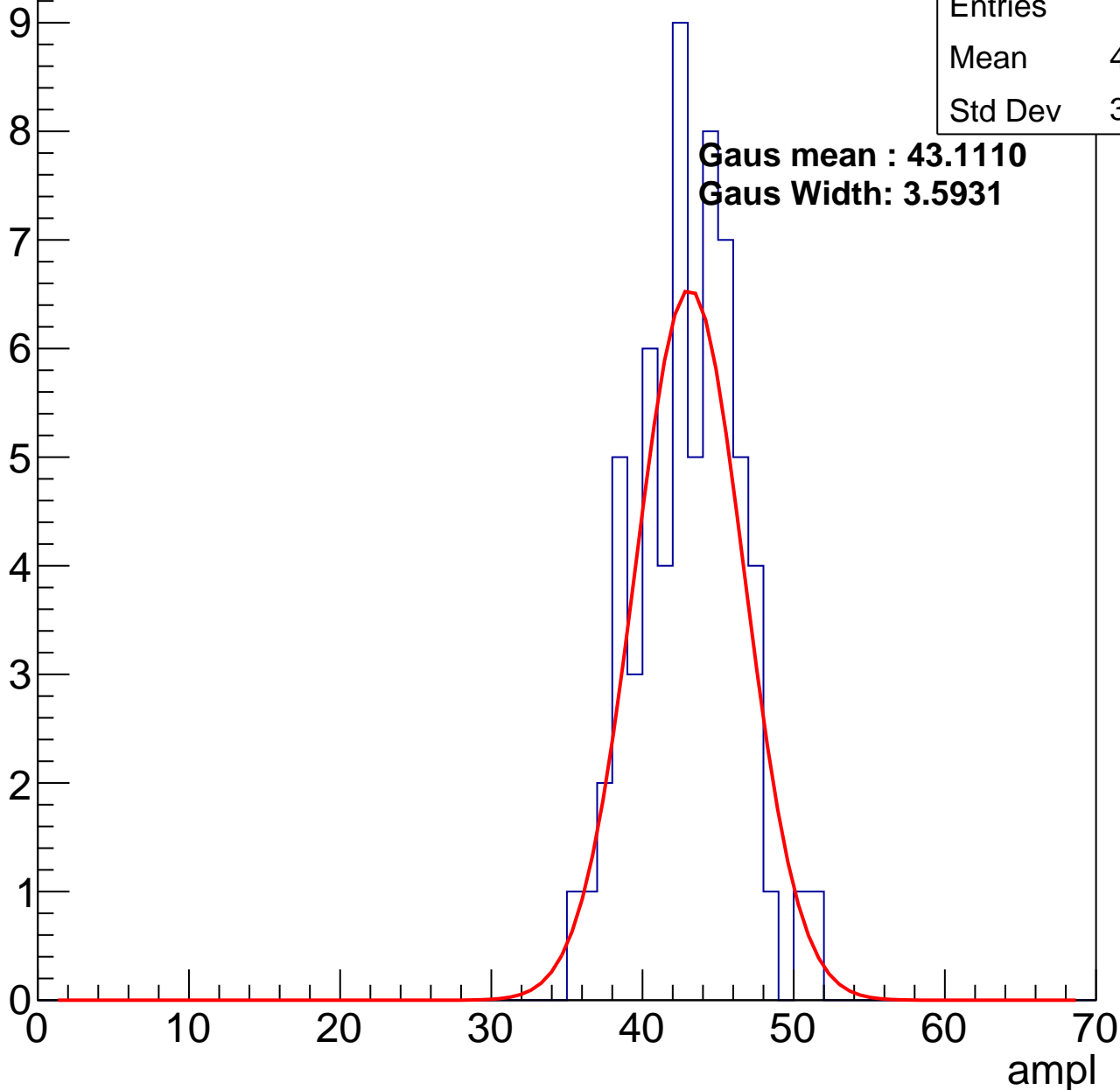
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.59
Std Dev	3.355

Gaus mean : 43.1110

Gaus Width: 3.5931

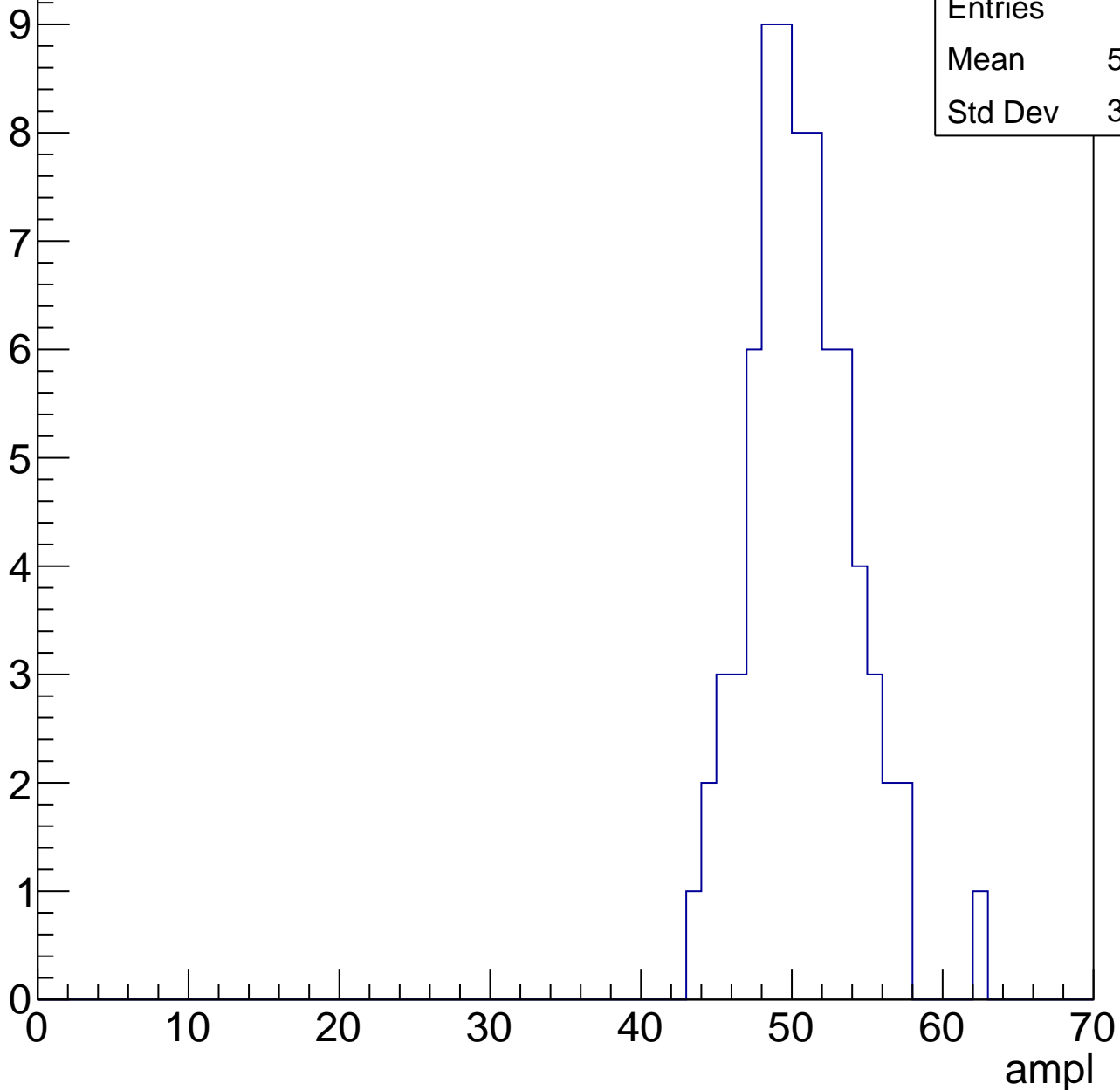


B1L102S, U18-ch87, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	50.22
Std Dev	3.469

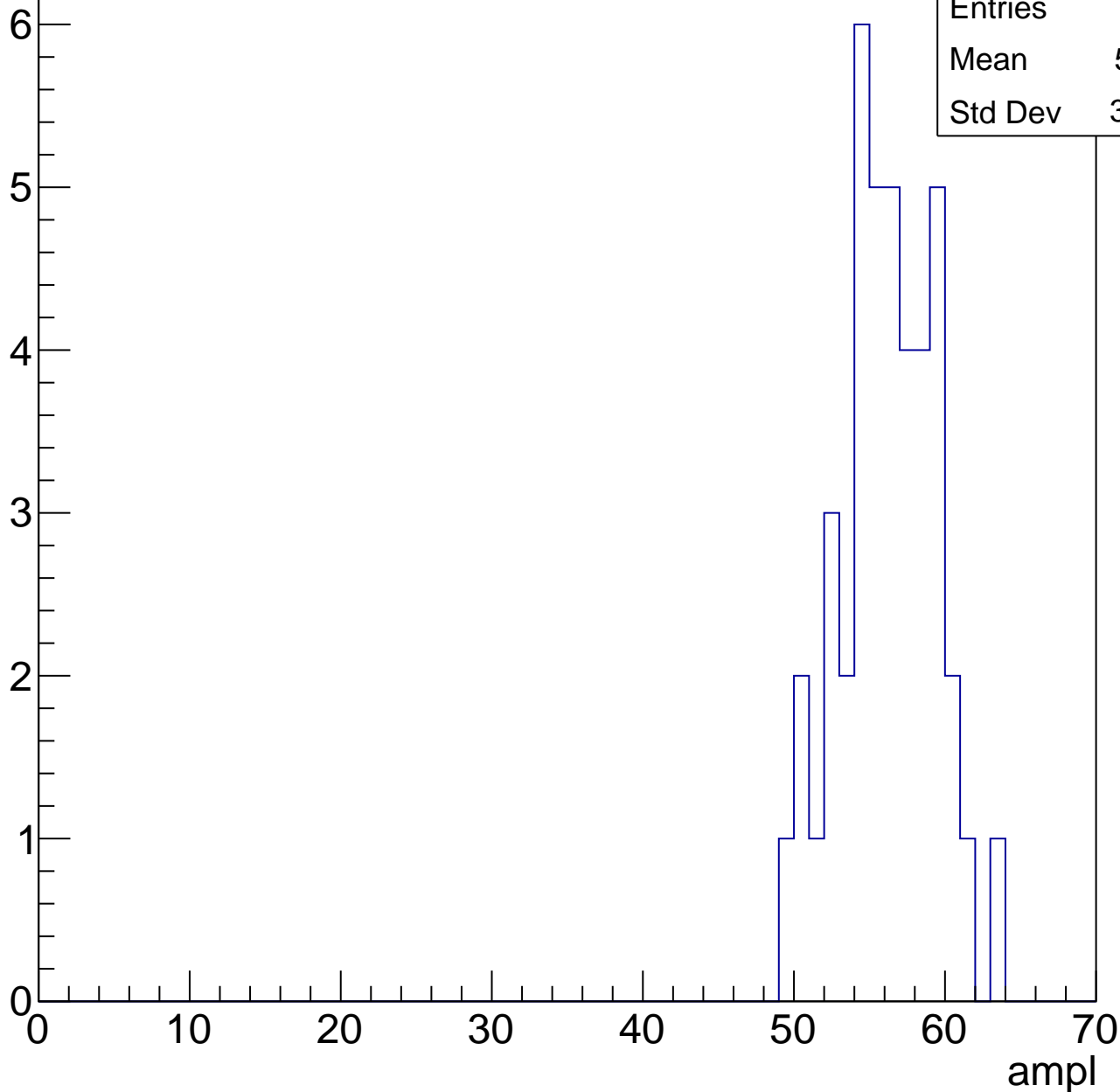


B1L102S, U18-ch87, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	55.71
Std Dev	3.119

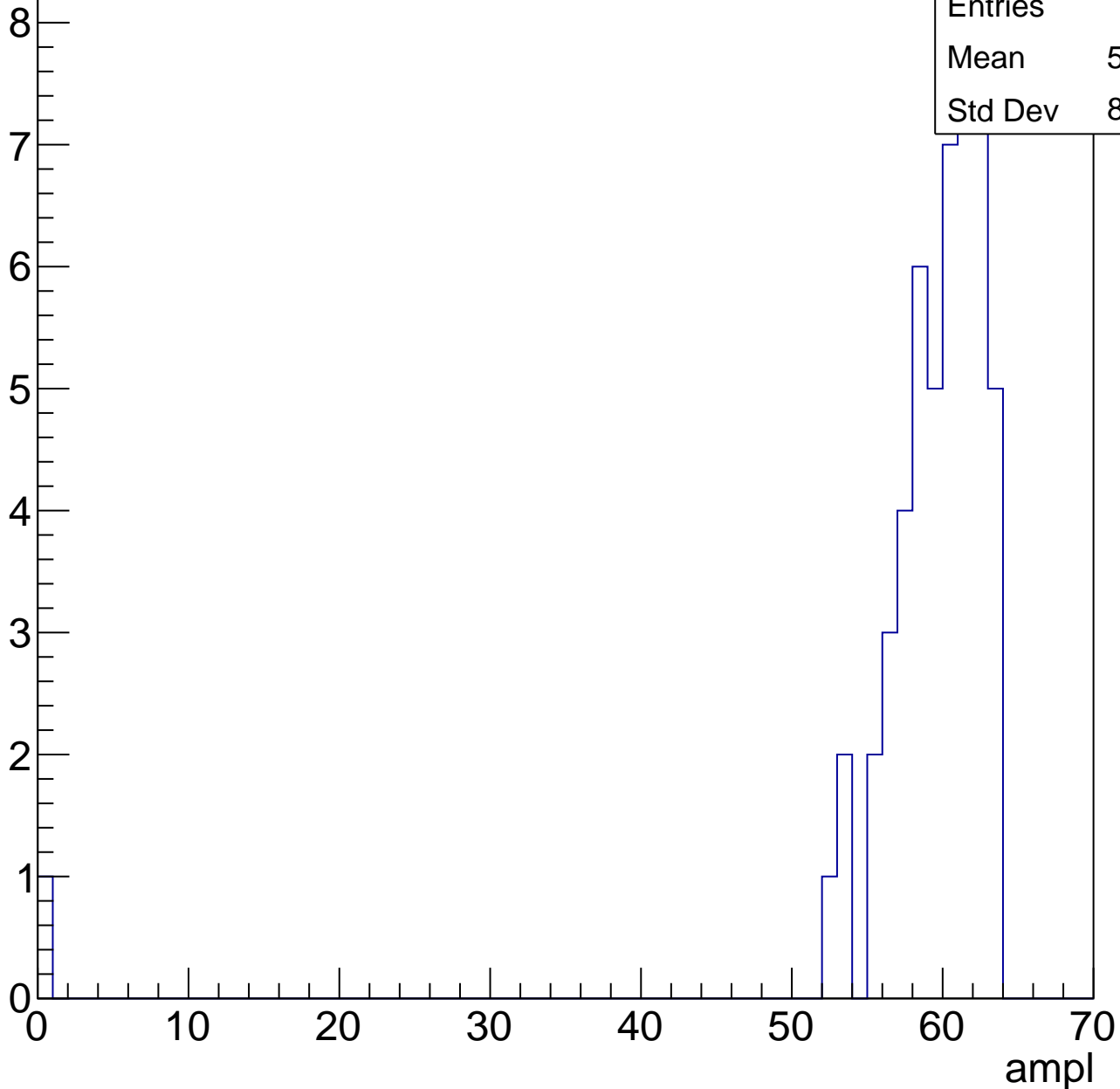


B1L102S, U18-ch87, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	58.19
Std Dev	8.593



B1L102S, U18-ch87, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch87, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch88, adc0

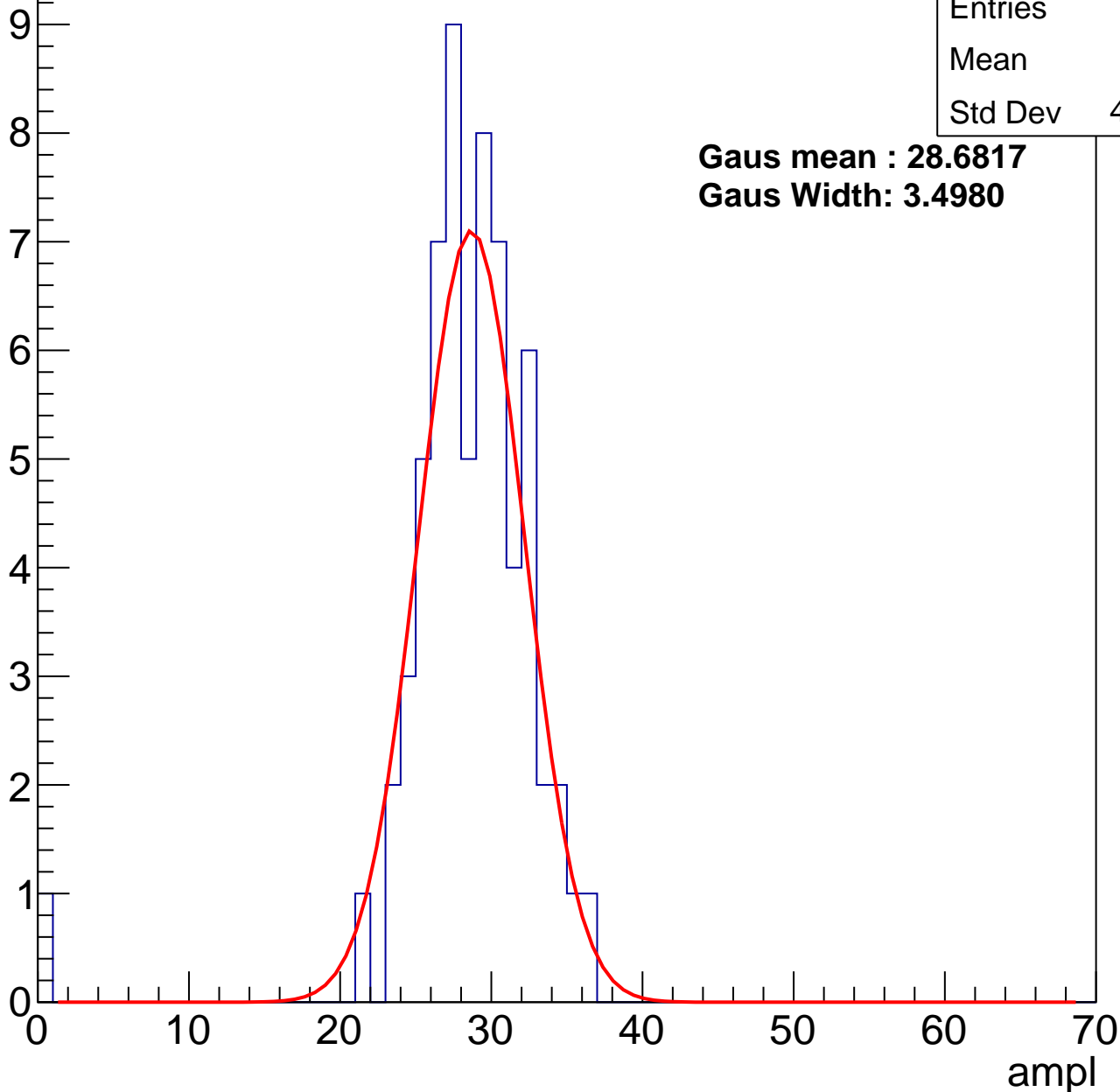
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	28
Std Dev	4.704

Gaus mean : 28.6817

Gaus Width: 3.4980



B1L102S, U18-ch88, adc1

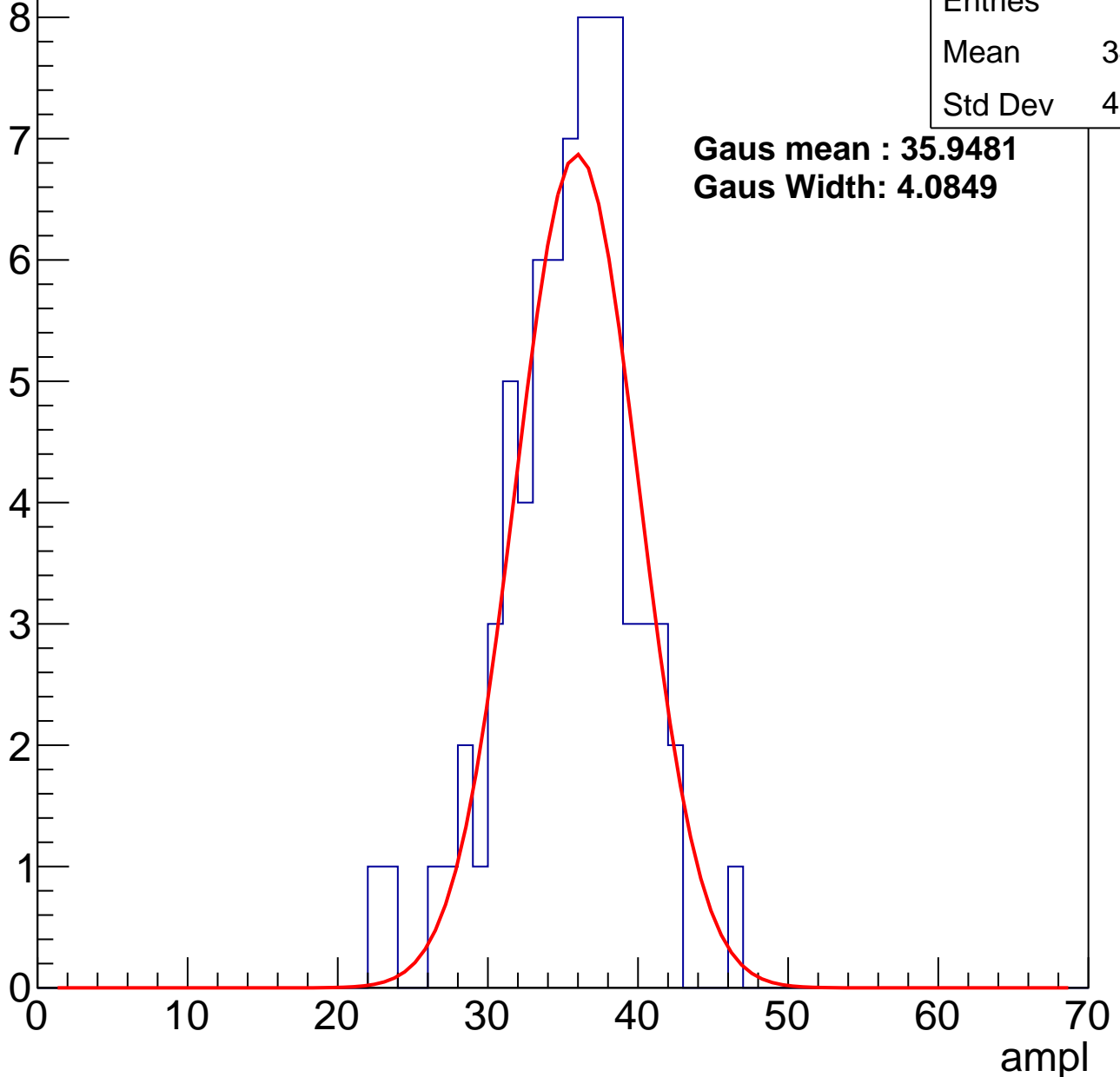
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	34.88
Std Dev	4.309

Gaus mean : 35.9481

Gaus Width: 4.0849



B1L102S, U18-ch88, adc2

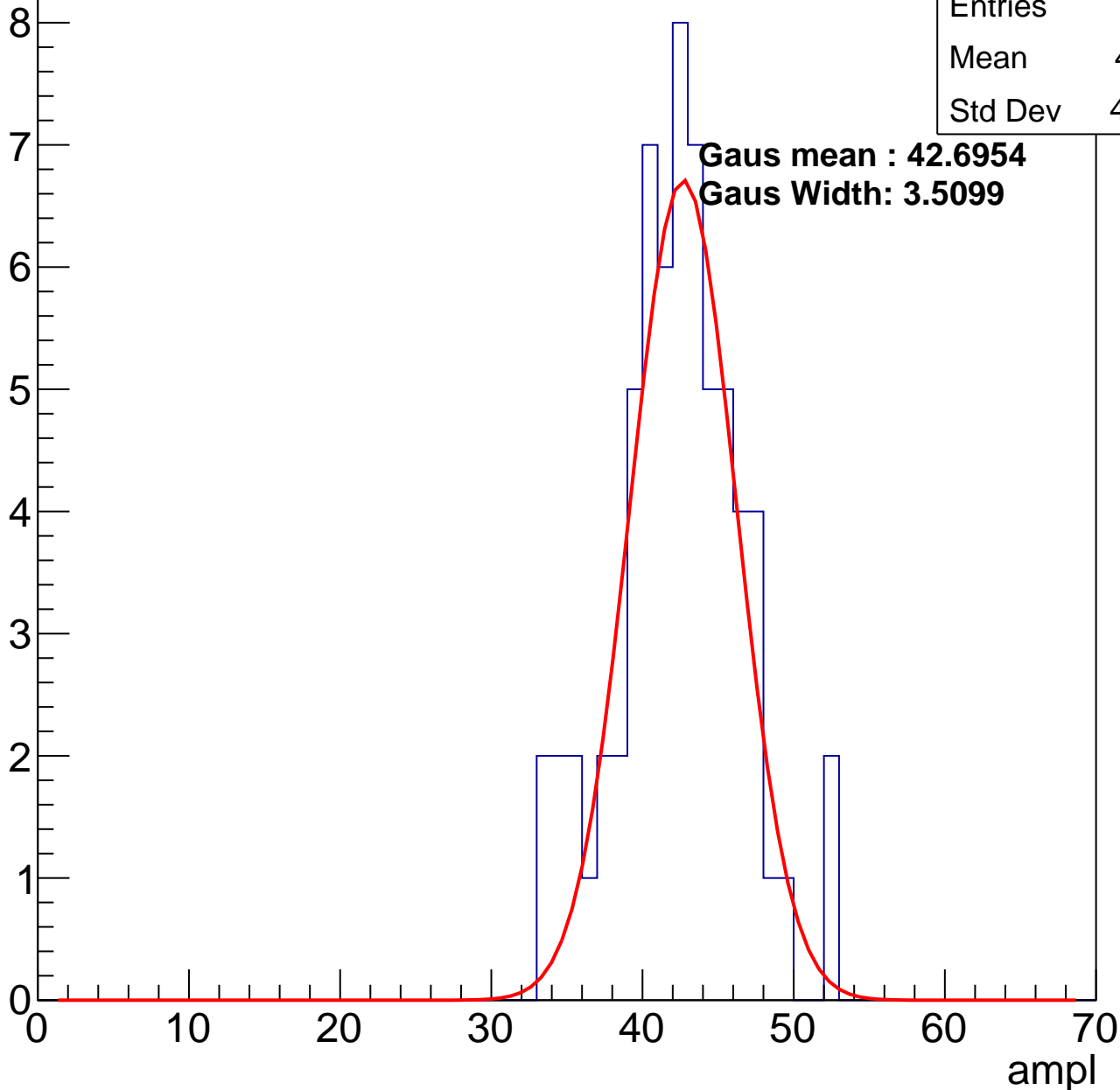
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	41.91
Std Dev	4.093

Gaus mean : 42.6954

Gaus Width: 3.5099

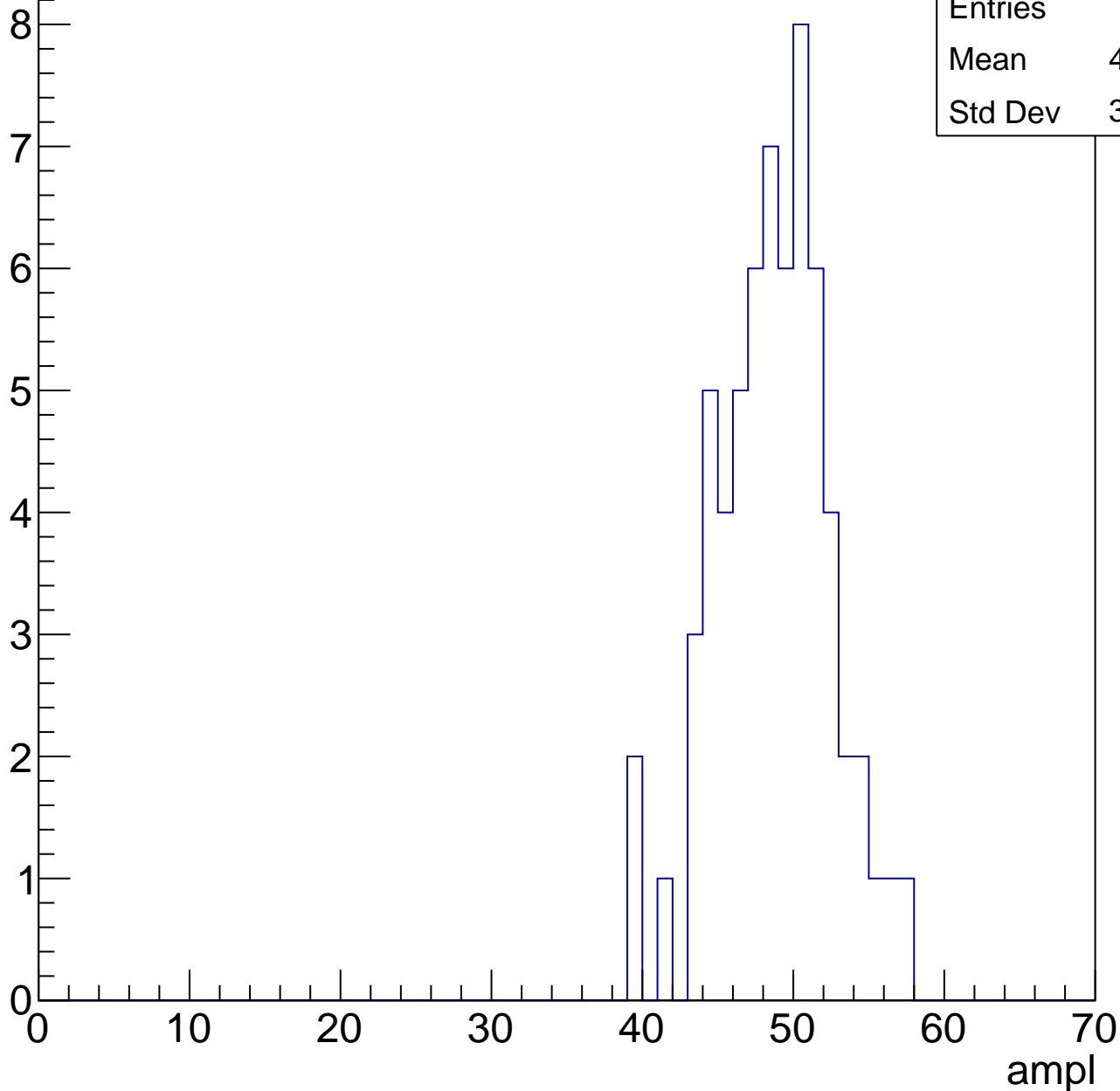


B1L102S, U18-ch88, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.22
Std Dev	3.744

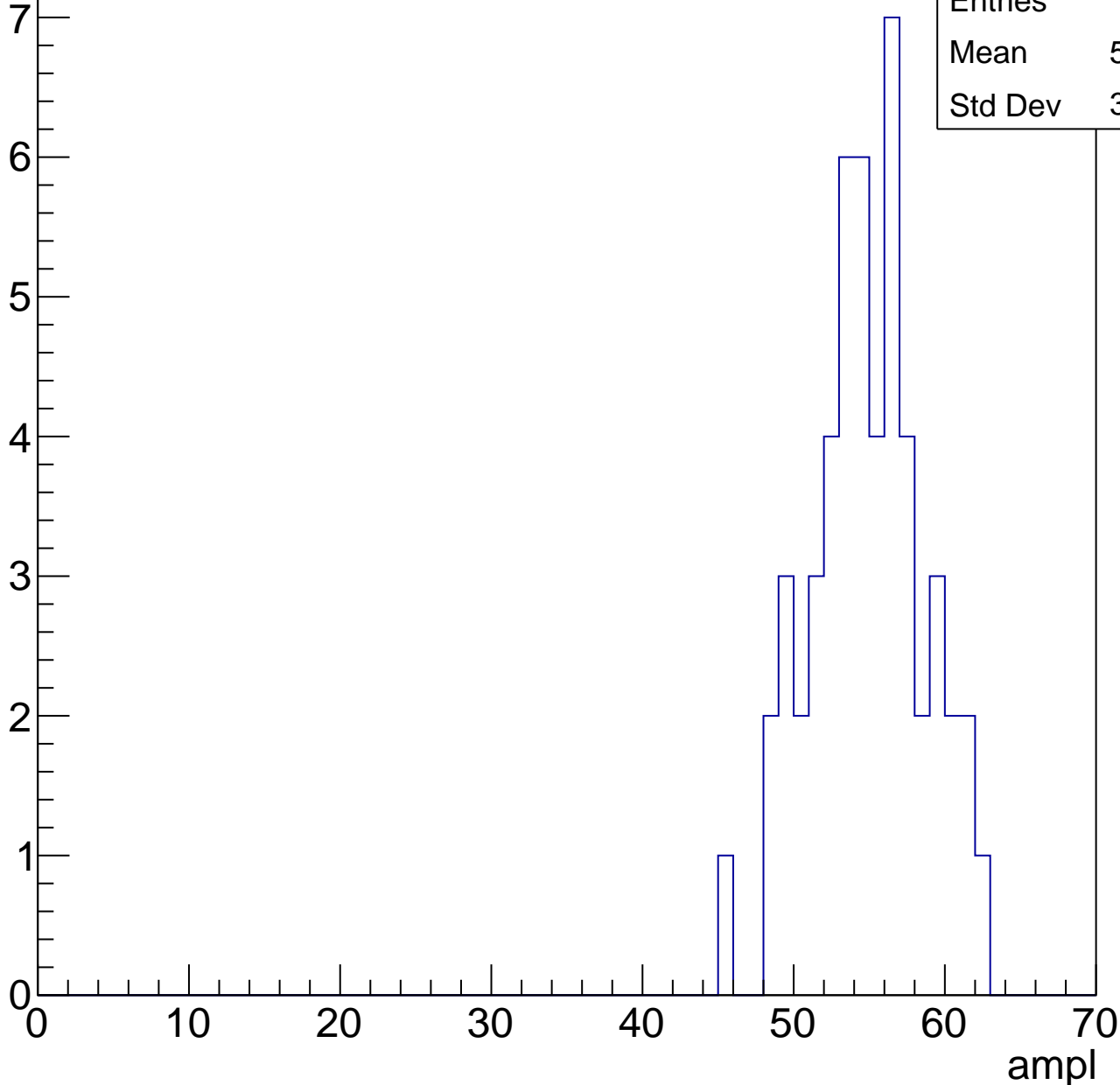


B1L102S, U18-ch88, adc4

calib_packv5_042523_0143.root, FC#11, port A2

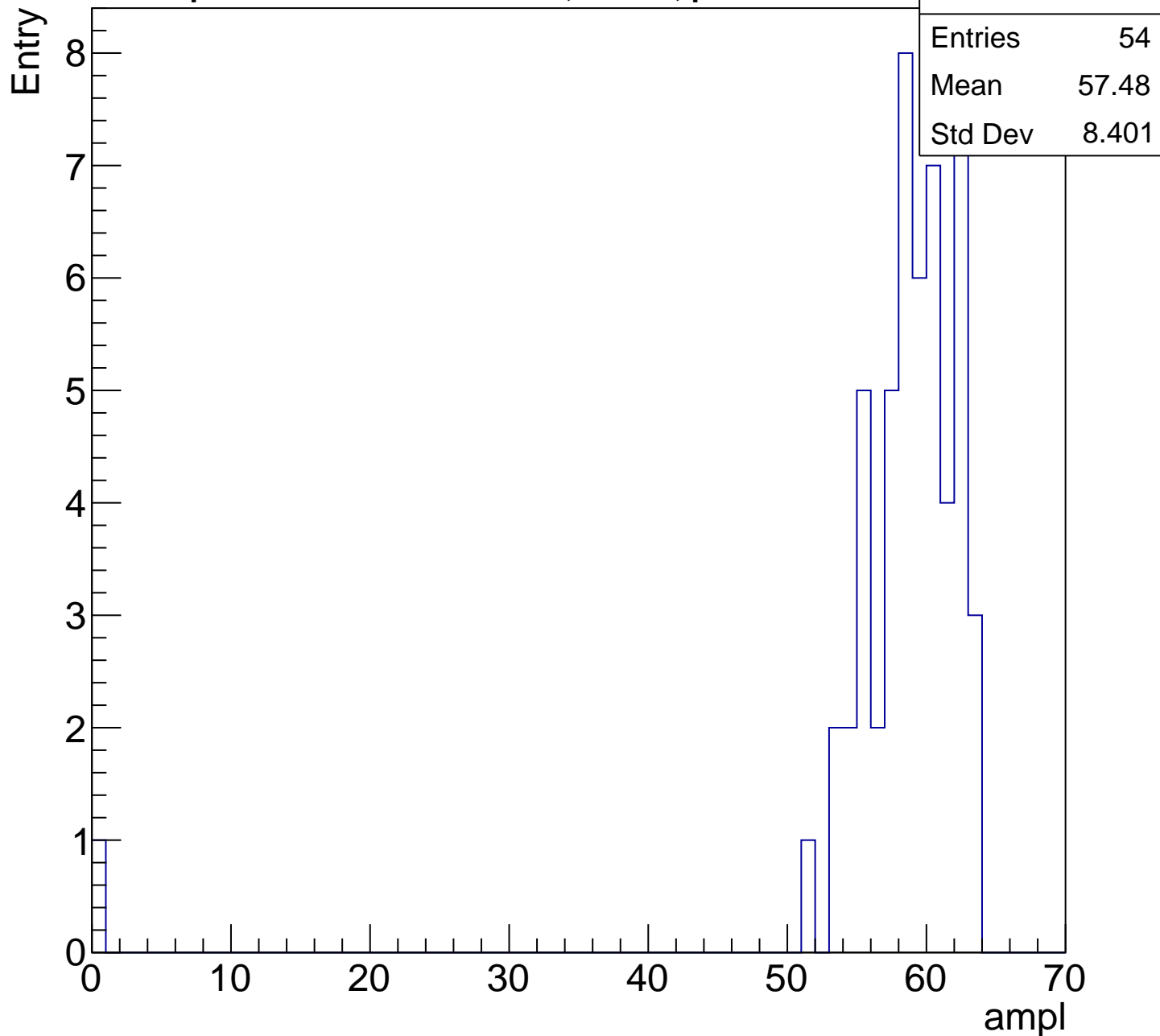
Entry

Entries	52
Mean	54.38
Std Dev	3.696



B1L102S, U18-ch88, adc5

calib_packv5_042523_0143.root, FC#11, port A2

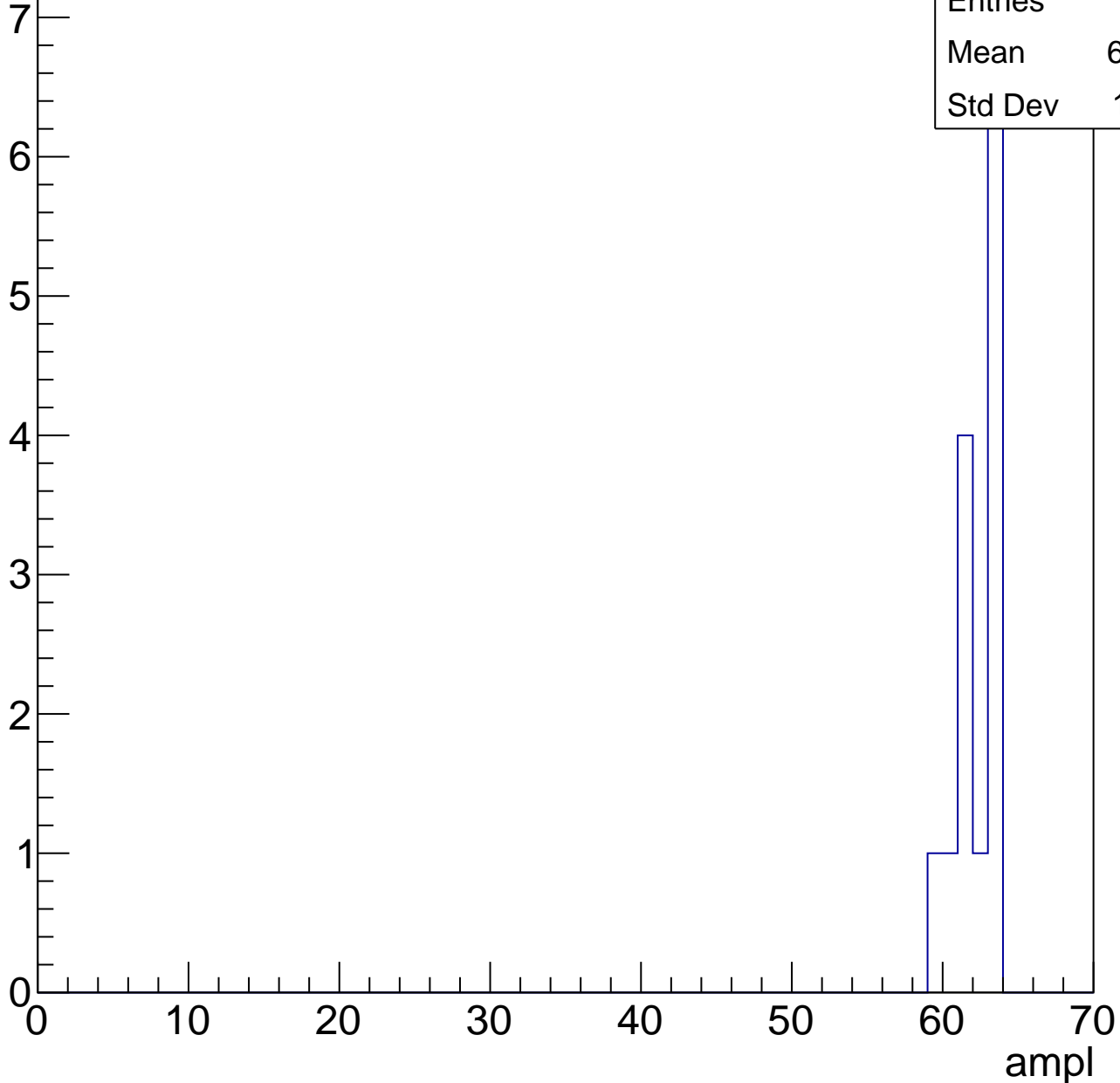


B1L102S, U18-ch88, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	14
Mean	61.86
Std Dev	1.301



B1L102S, U18-ch88, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch89, adc0

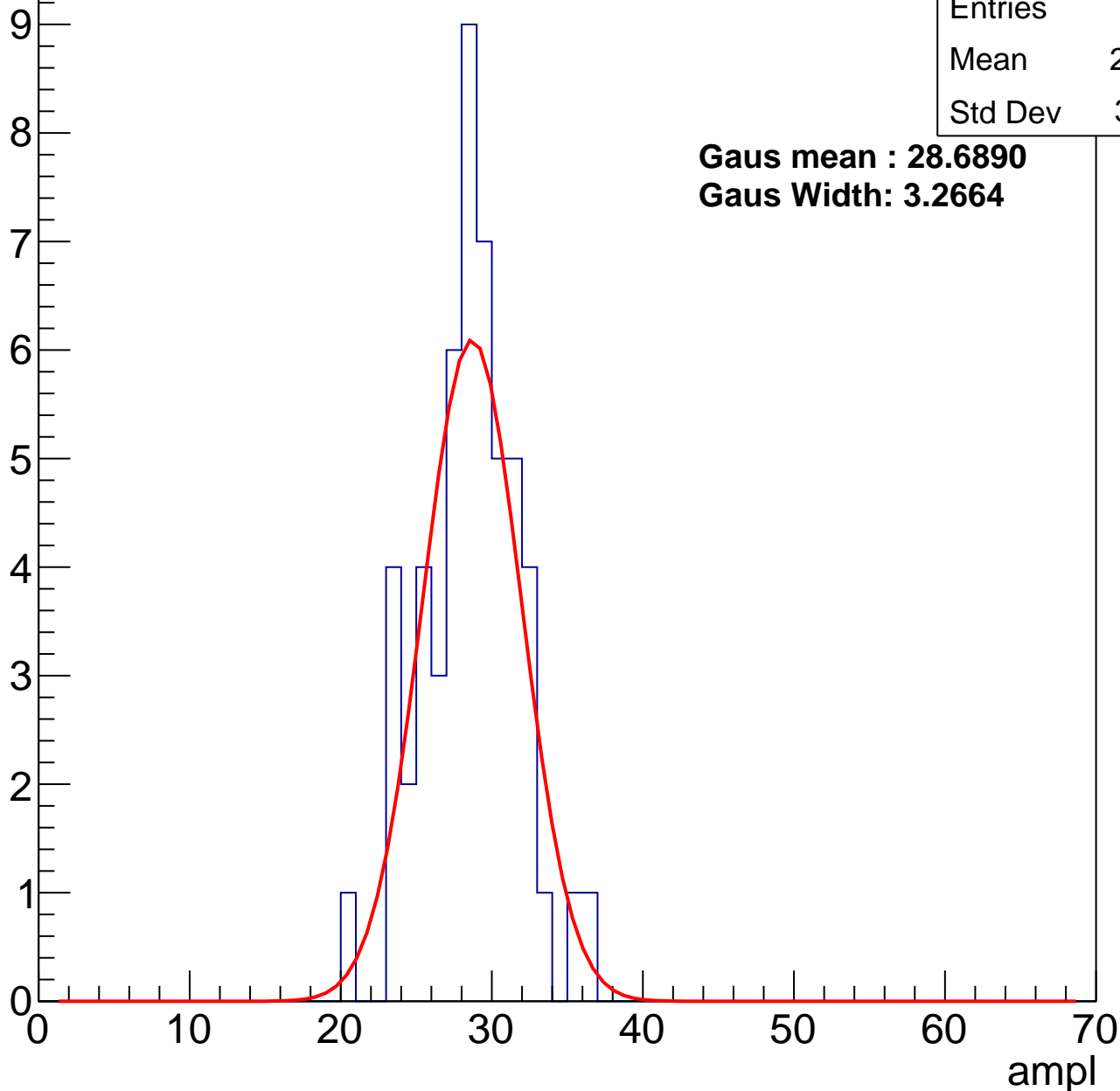
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	28.15
Std Dev	3.141

Gaus mean : 28.6890

Gaus Width: 3.2664



B1L102S, U18-ch89, adc1

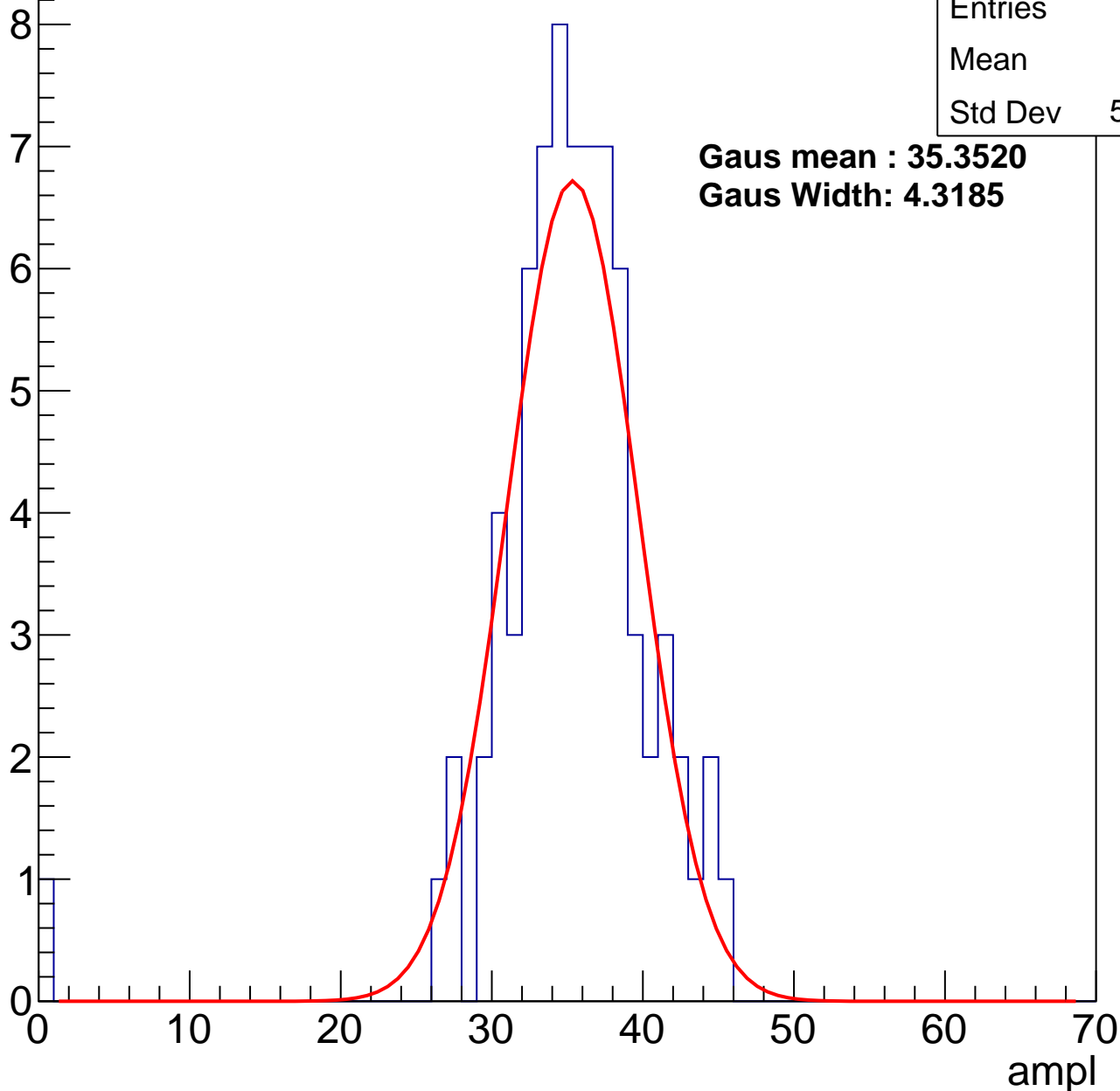
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	34.8
Std Dev	5.727

Gaus mean : 35.3520

Gaus Width: 4.3185



B1L102S, U18-ch89, adc2

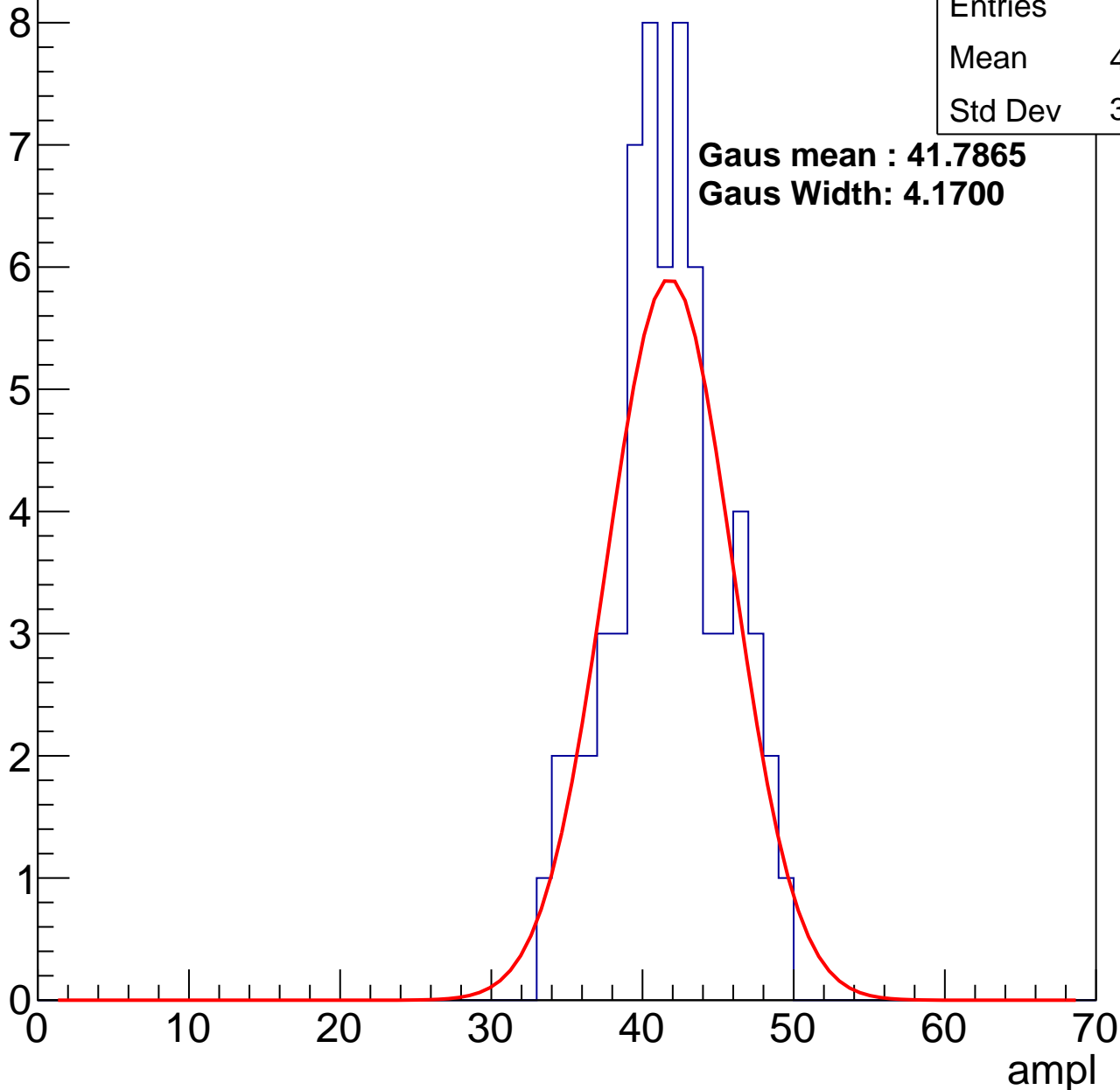
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	41.22
Std Dev	3.685

Gaus mean : 41.7865

Gaus Width: 4.1700

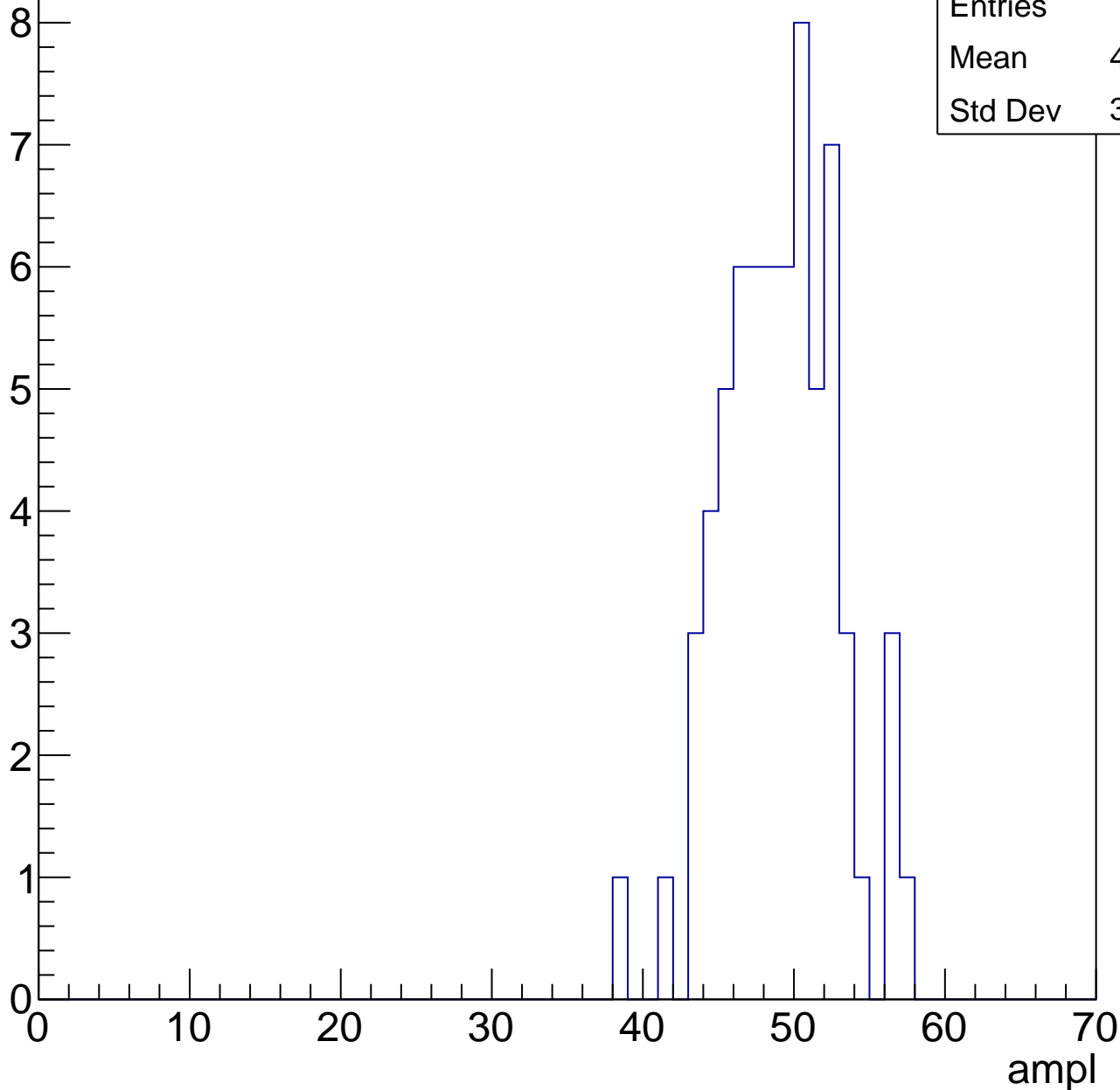


B1L102S, U18-ch89, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	48.58
Std Dev	3.734

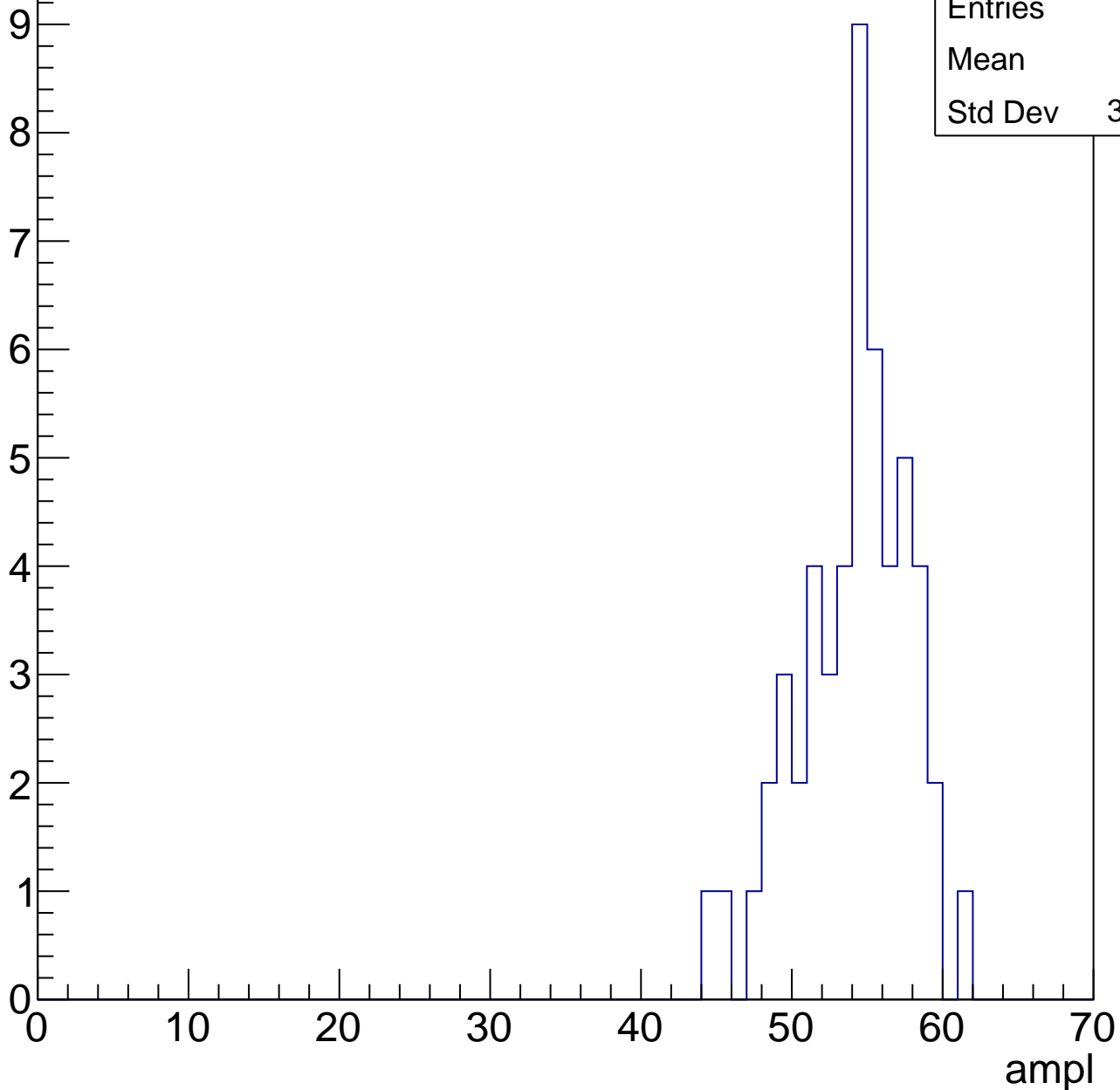


B1L102S, U18-ch89, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.6
Std Dev	3.623

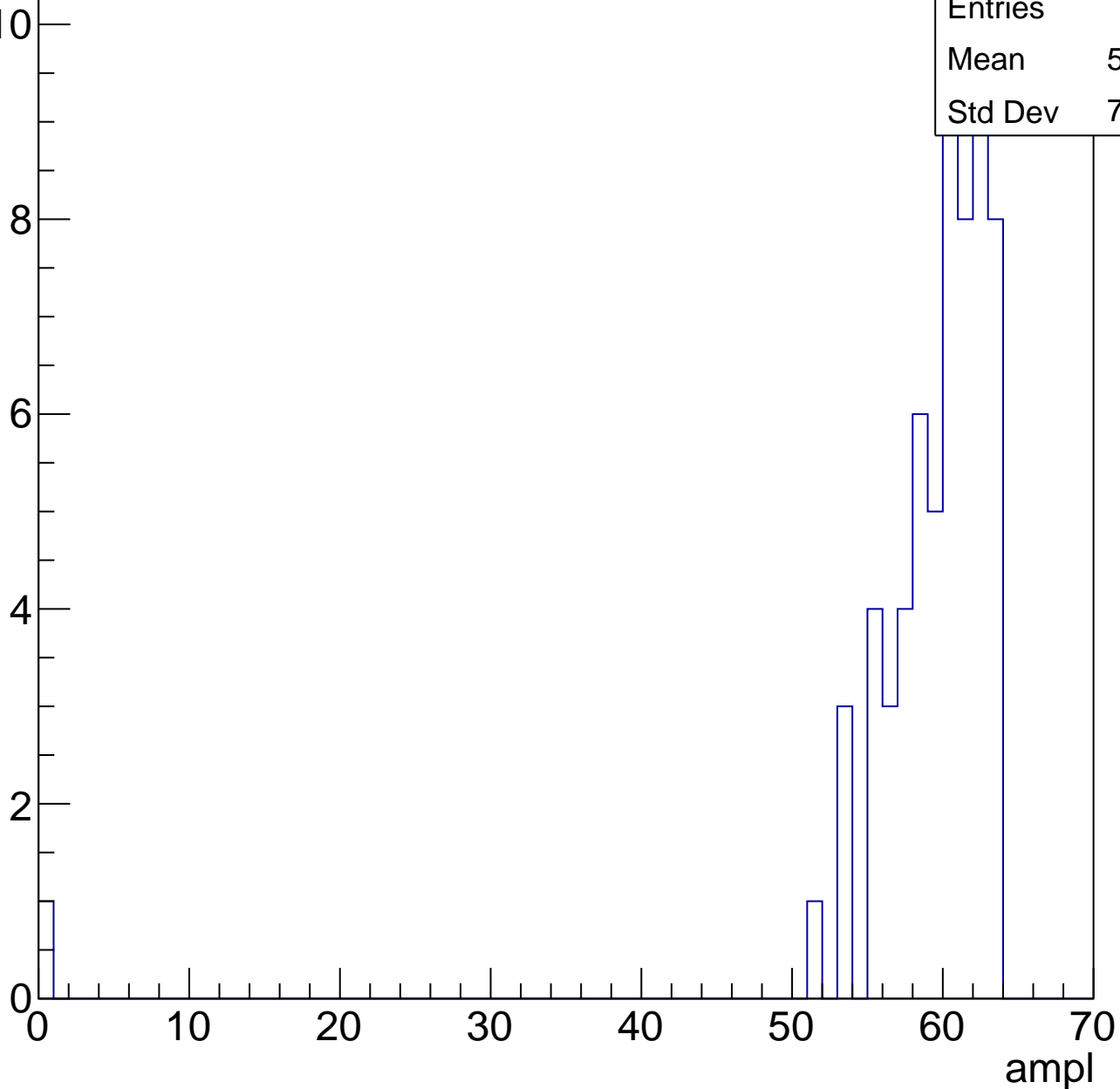


B1L102S, U18-ch89, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

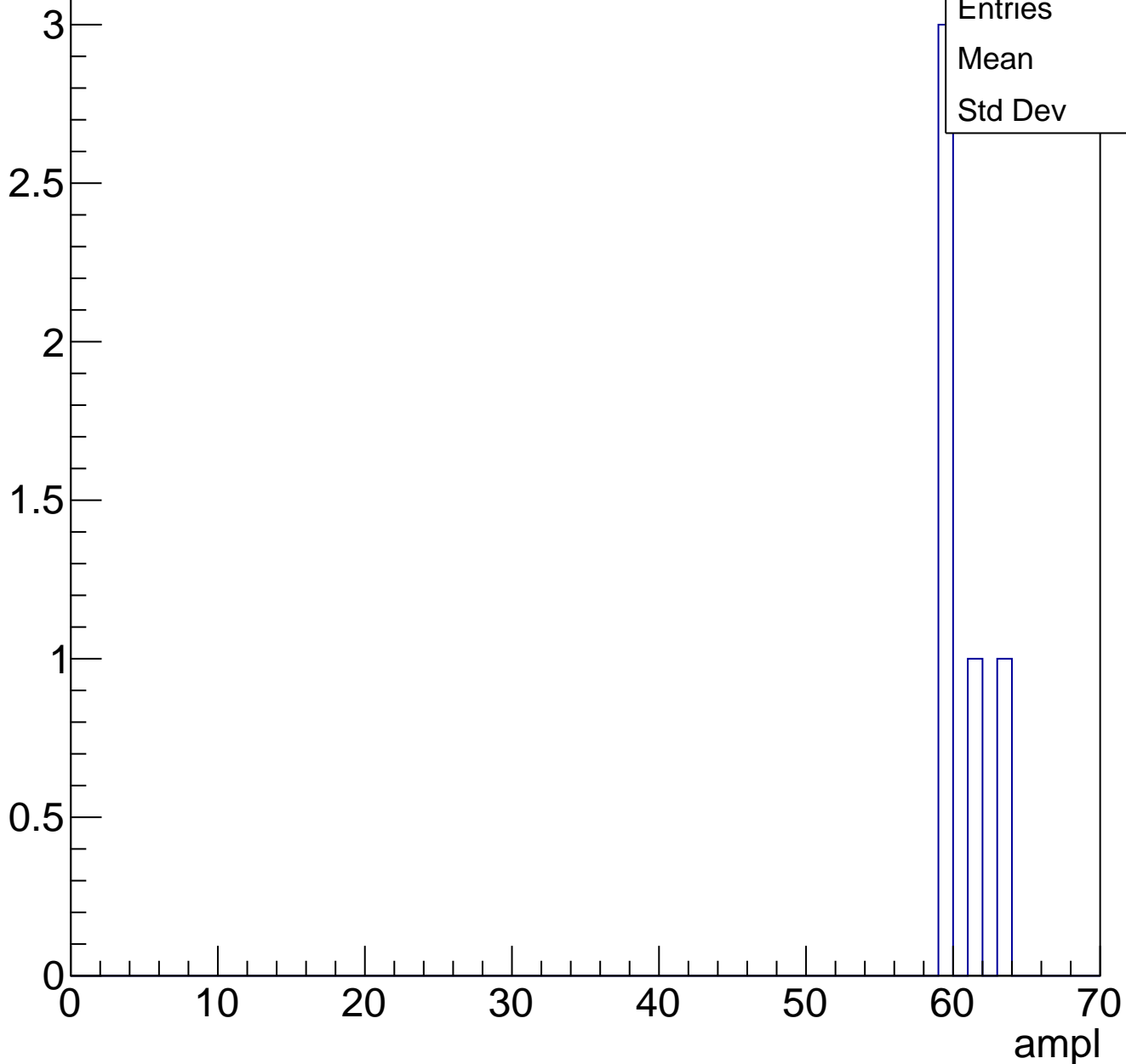
Entries	63
Mean	58.43
Std Dev	7.974



B1L102S, U18-ch89, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

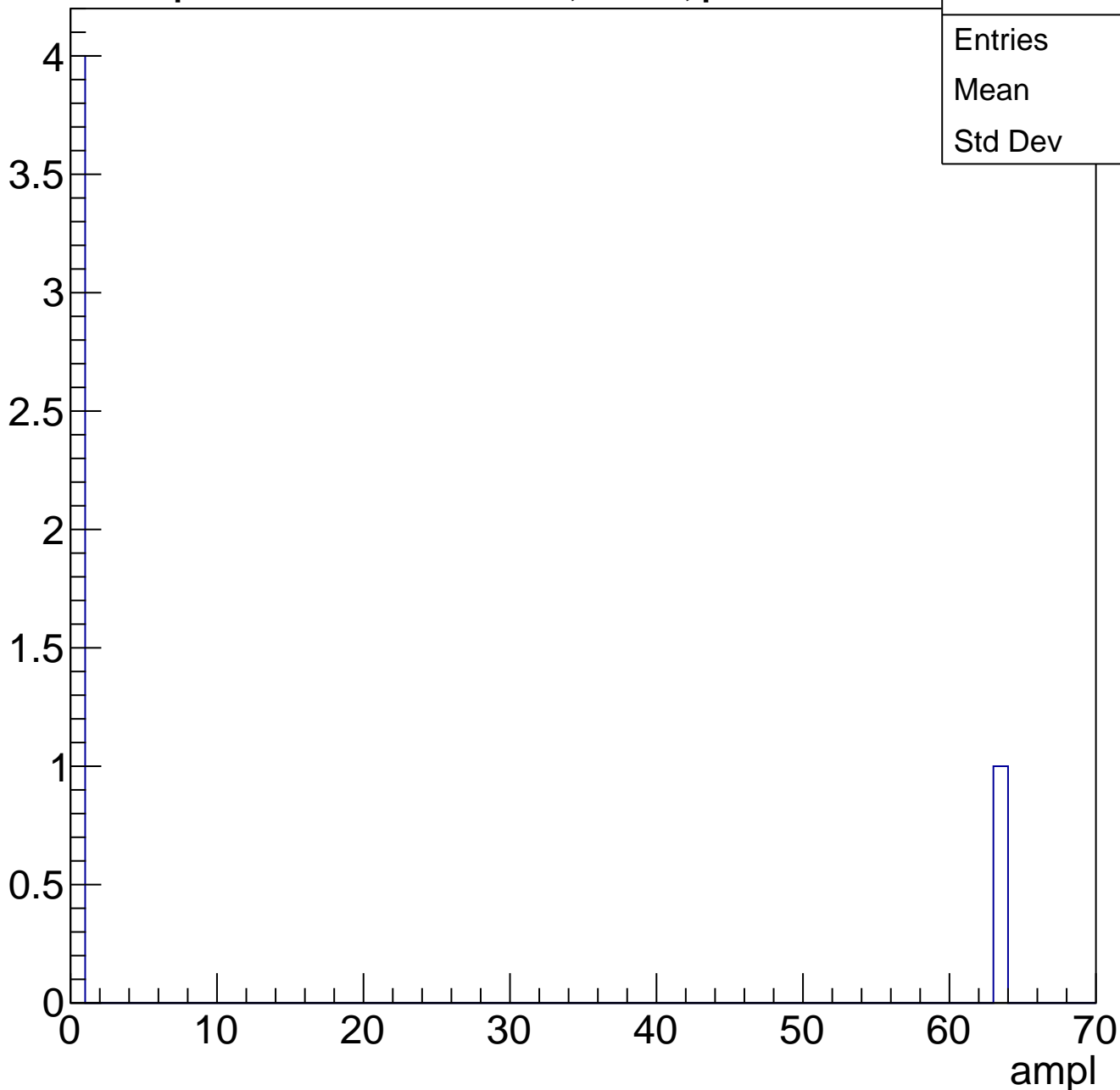


Entries	5
Mean	60.2
Std Dev	1.6

B1L102S, U18-ch89, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch90, adc0

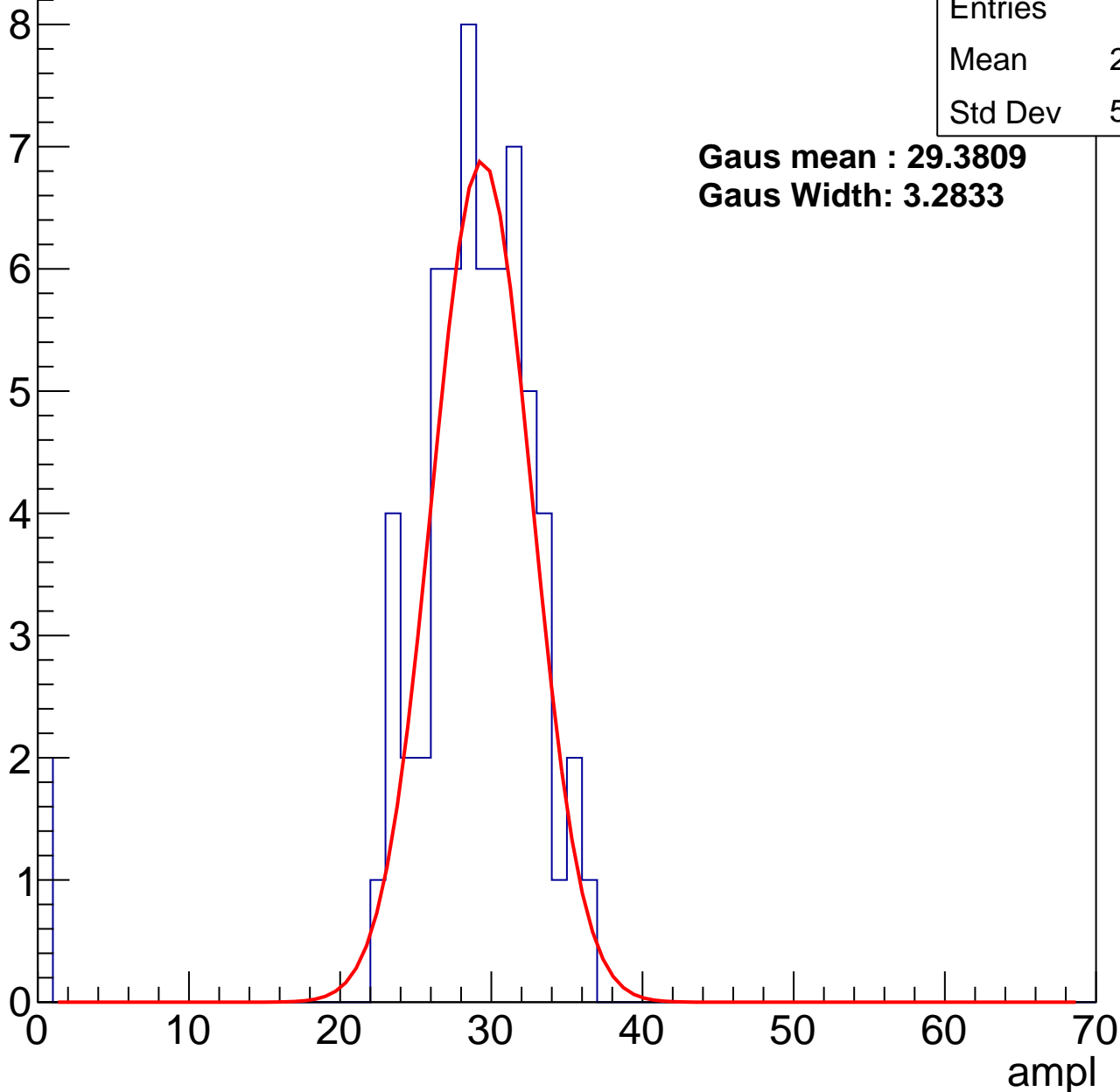
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	27.89
Std Dev	5.982

Gaus mean : 29.3809

Gaus Width: 3.2833



B1L102S, U18-ch90, adc1

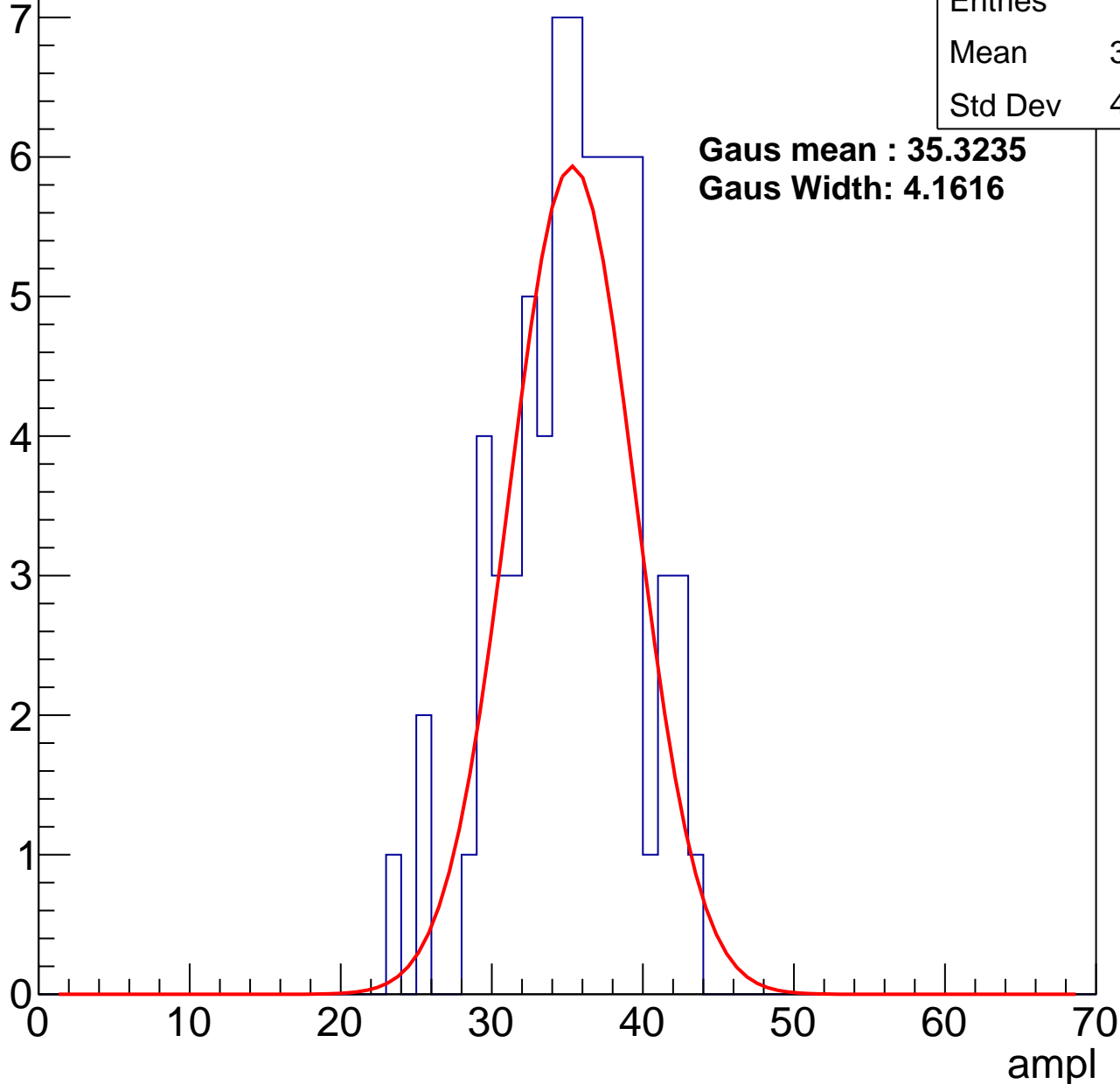
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	34.88
Std Dev	4.258

Gaus mean : 35.3235

Gaus Width: 4.1616



B1L102S, U18-ch90, adc2

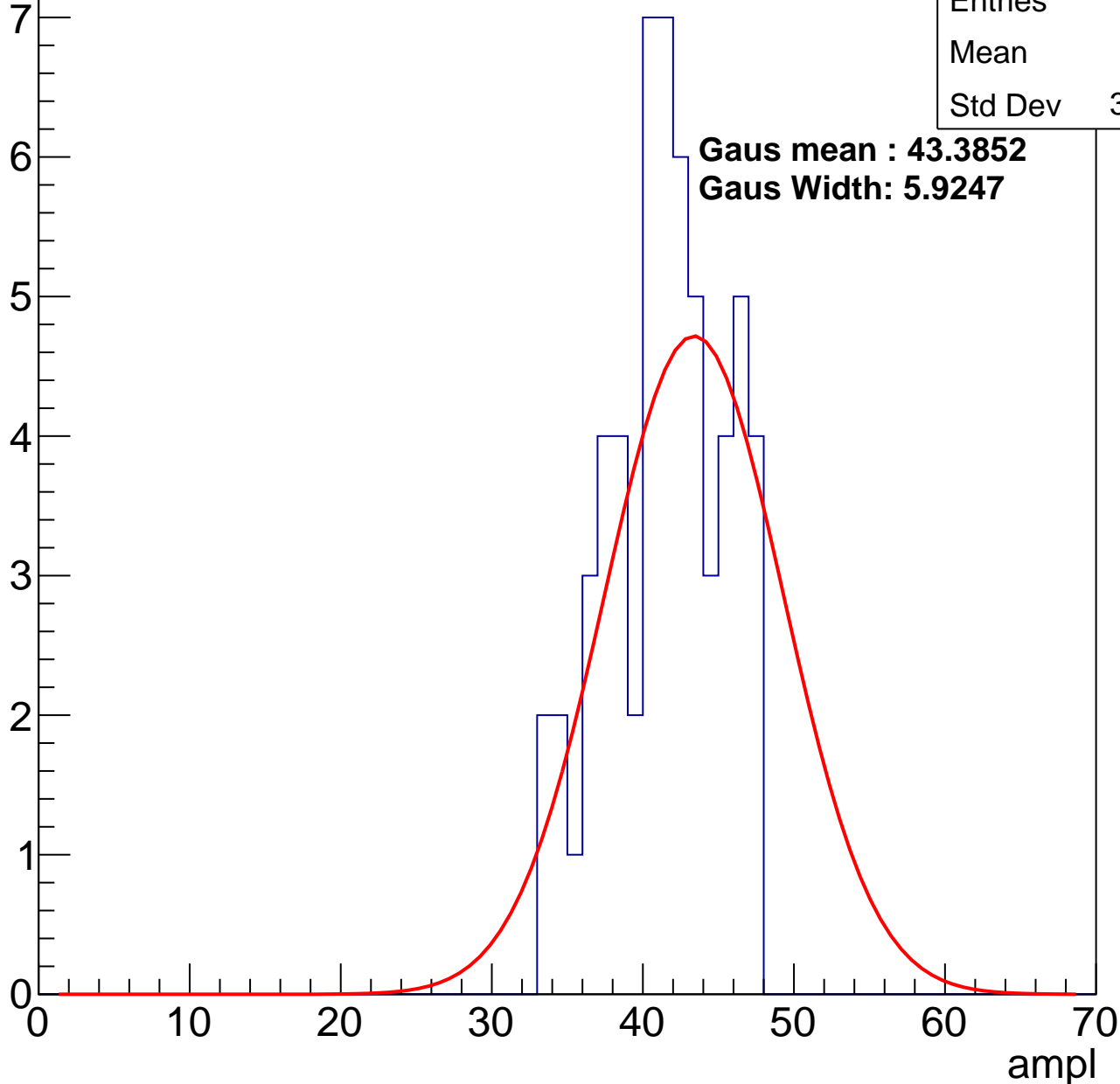
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41
Std Dev	3.769

Gaus mean : 43.3852

Gaus Width: 5.9247

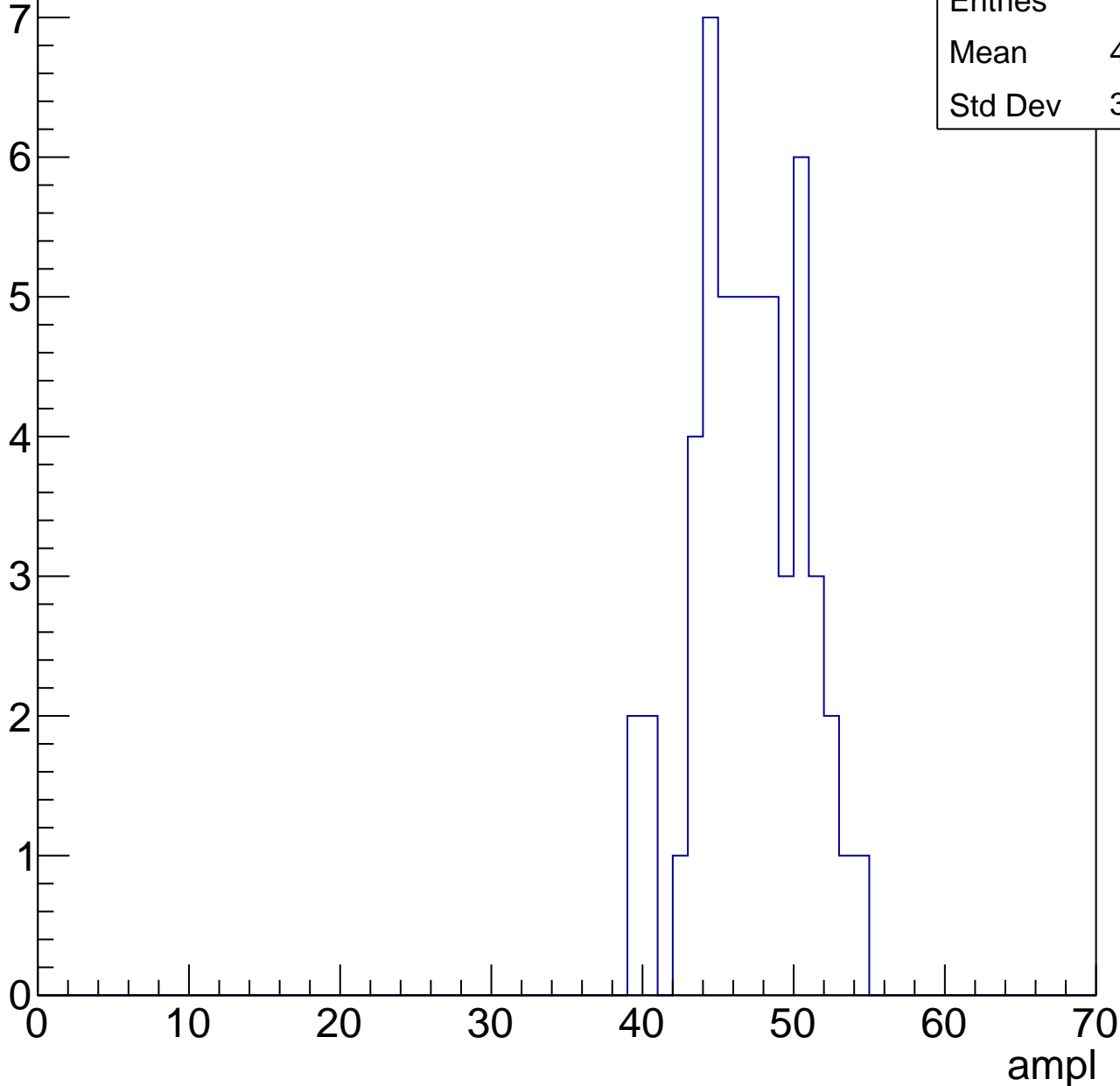


B1L102S, U18-ch90, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	46.56
Std Dev	3.527



B1L102S, U18-ch90, adc4

calib_packv5_042523_0143.root, FC#11, port A2

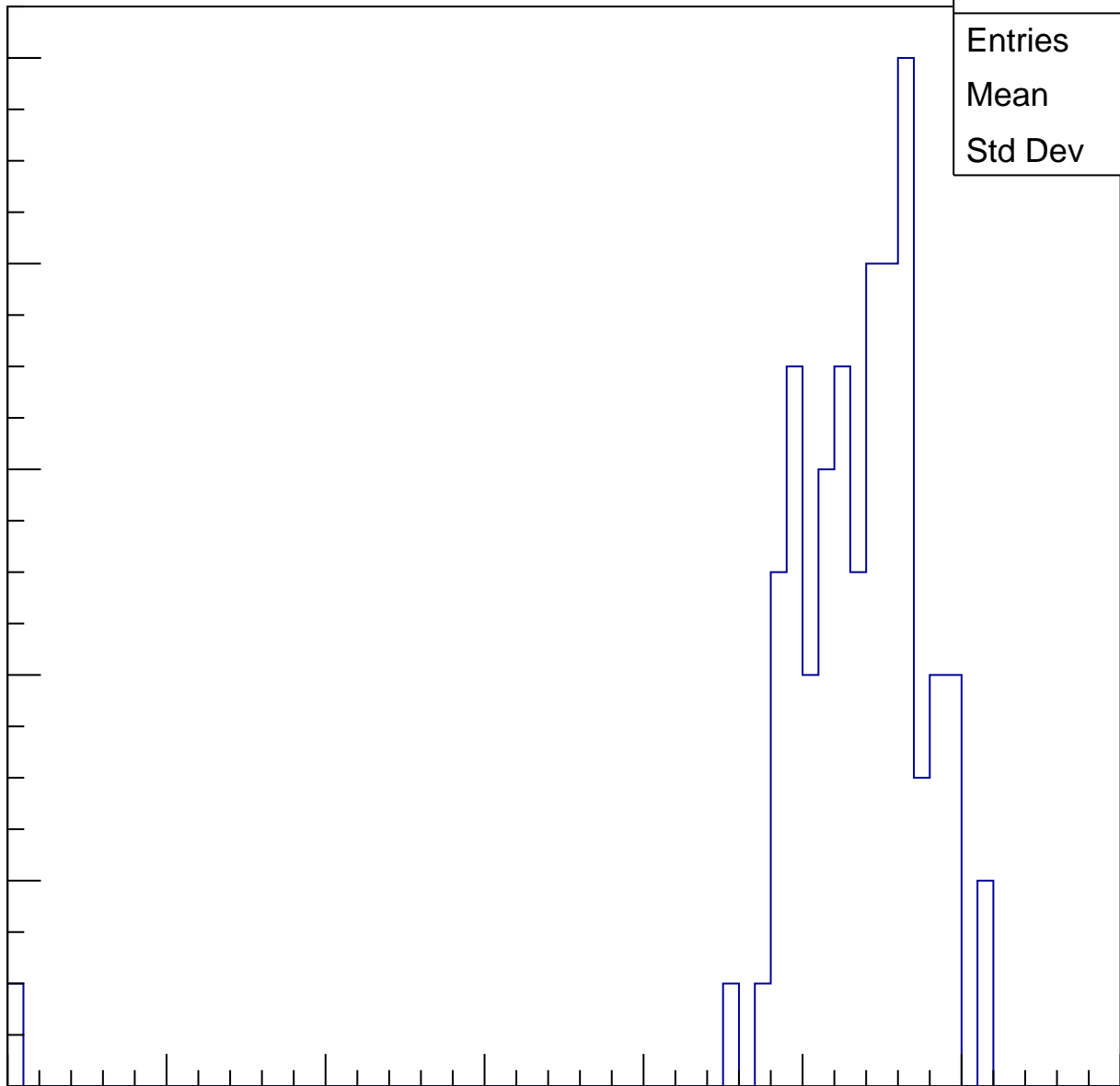
Entries	76
Mean	52.67
Std Dev	7.029

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

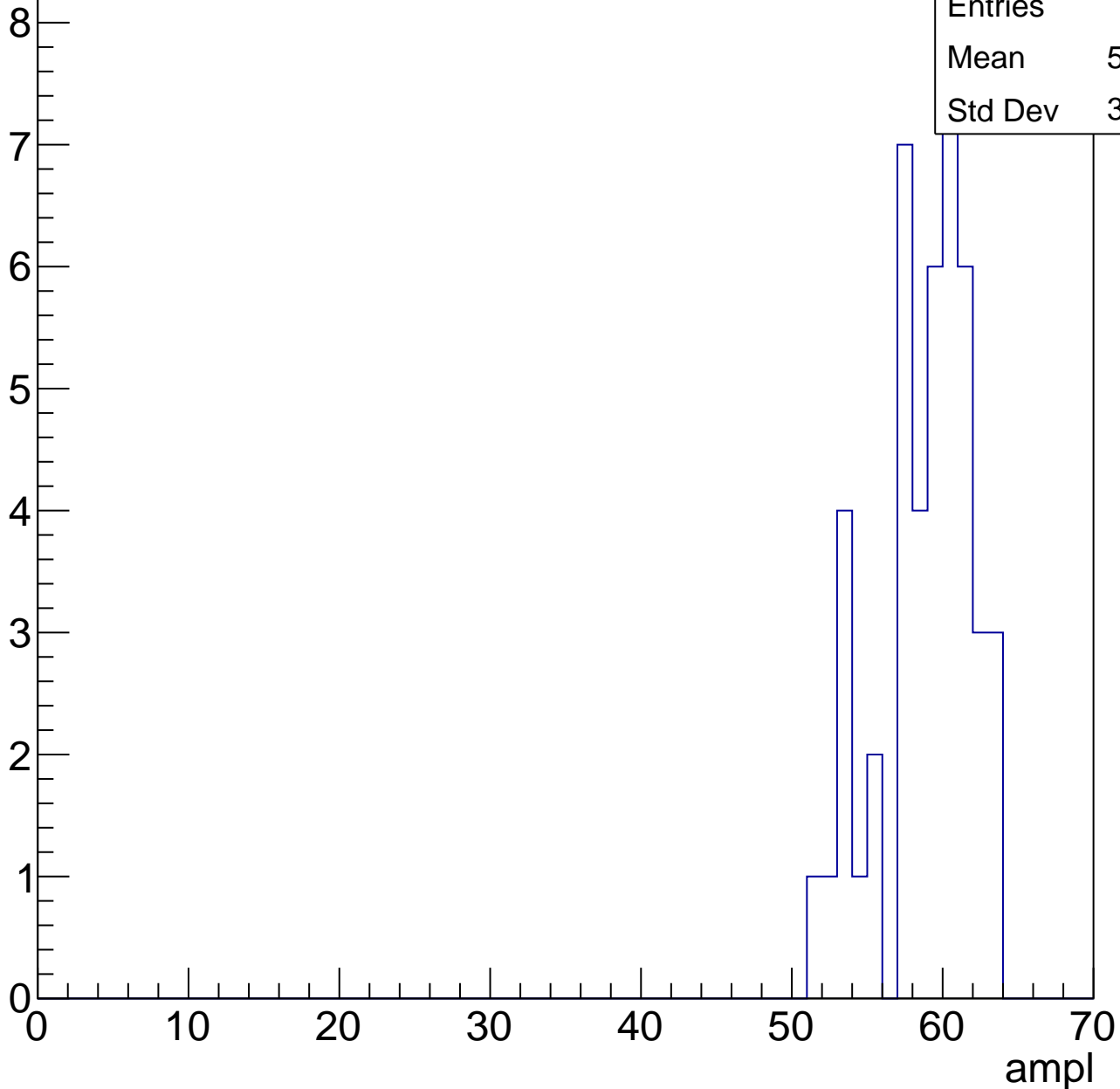


B1L102S, U18-ch90, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	46
Mean	58.37
Std Dev	3.074

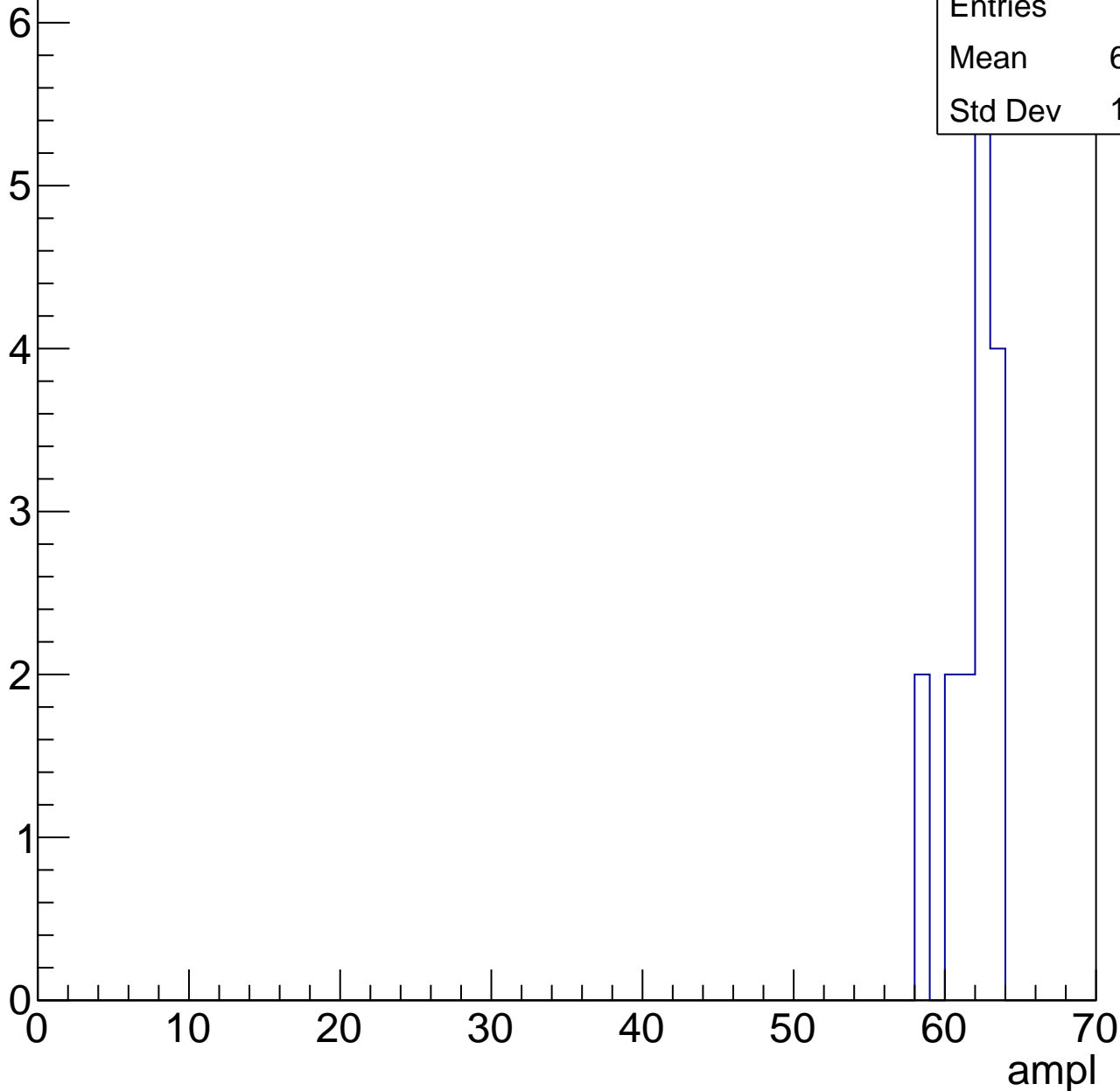


B1L102S, U18-ch90, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

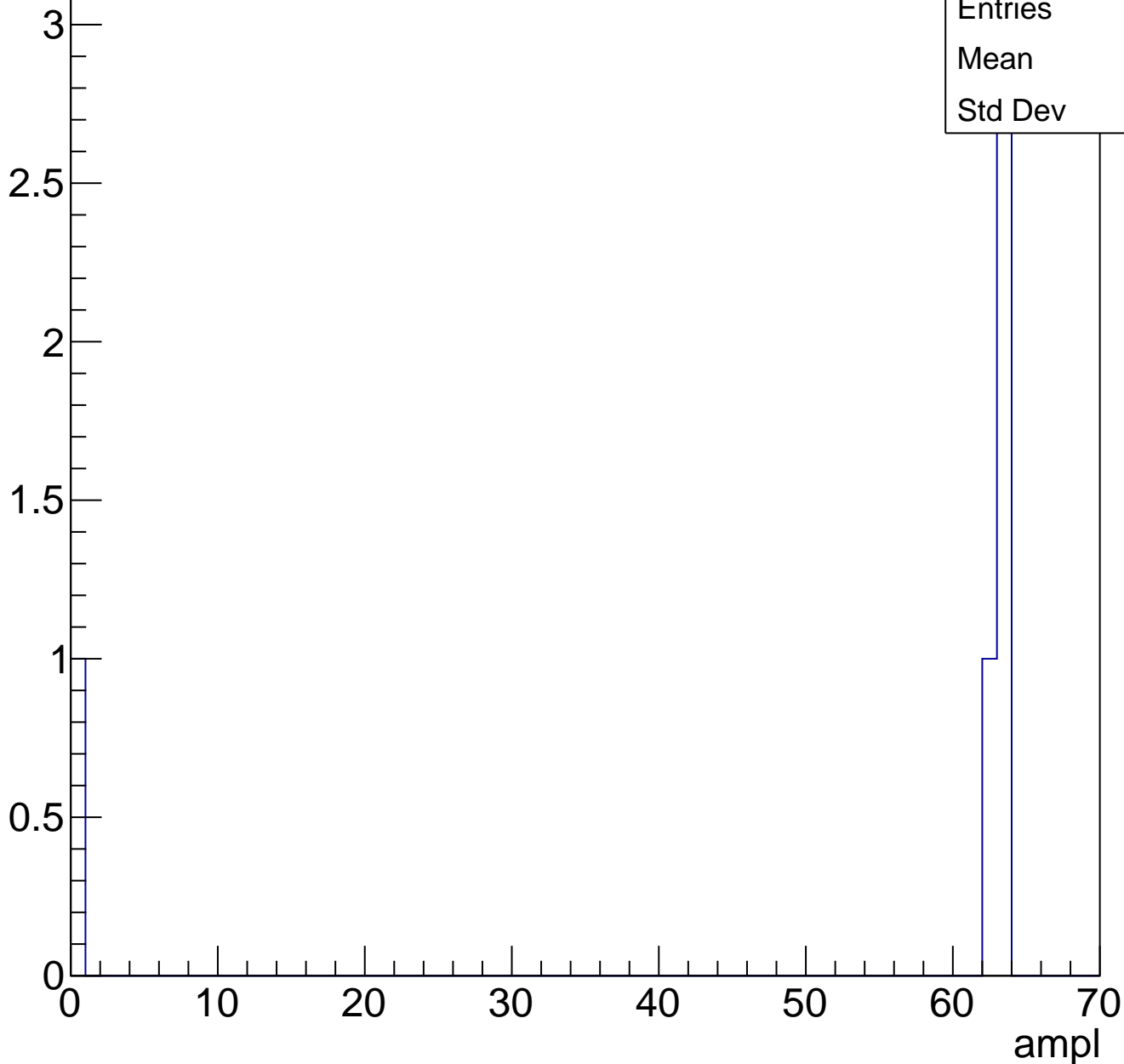
Entries	16
Mean	61.38
Std Dev	1.576



B1L102S, U18-ch90, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch91, adc0

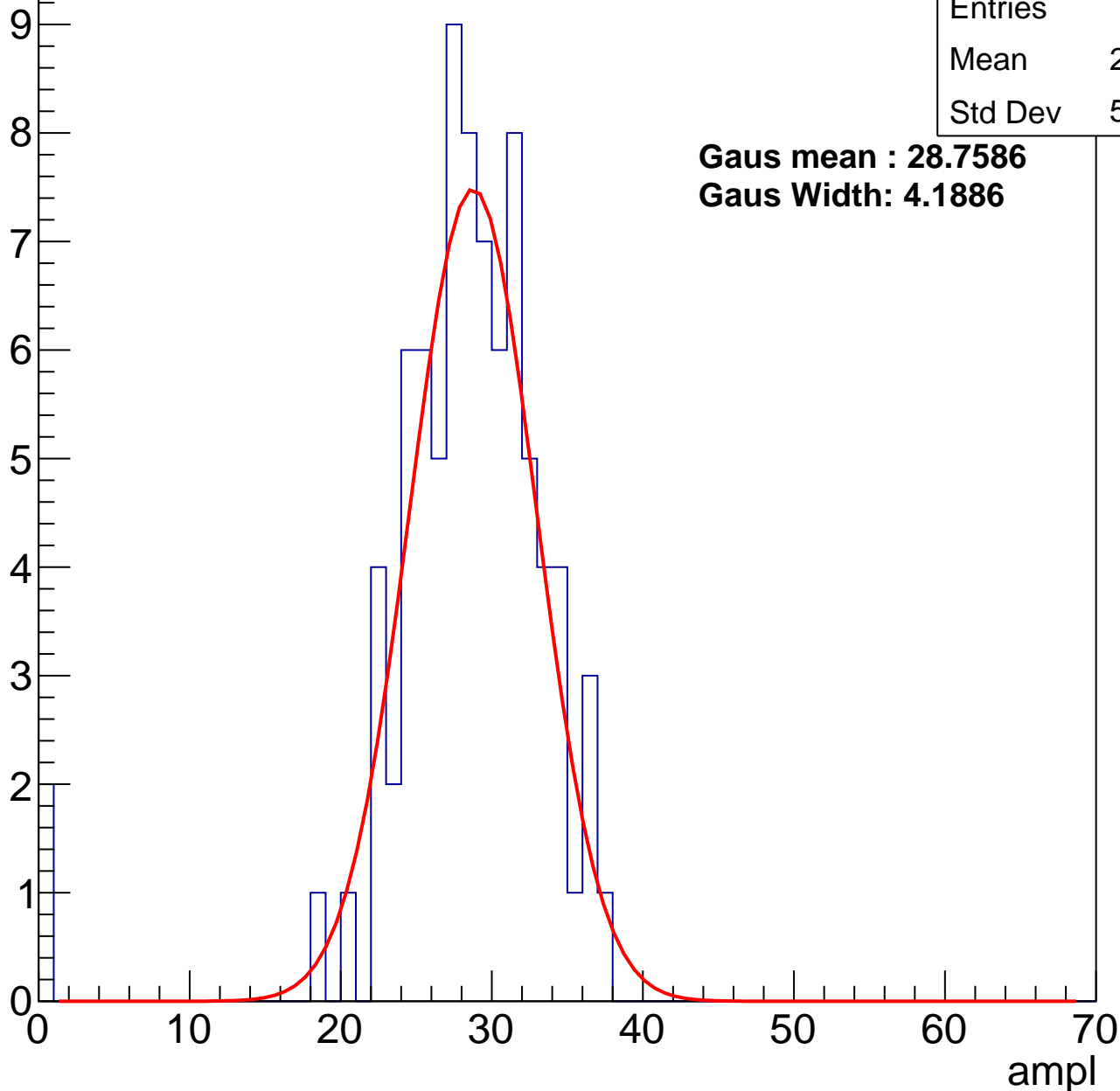
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.73
Std Dev	5.858

Gaus mean : 28.7586

Gaus Width: 4.1886



B1L102S, U18-ch91, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

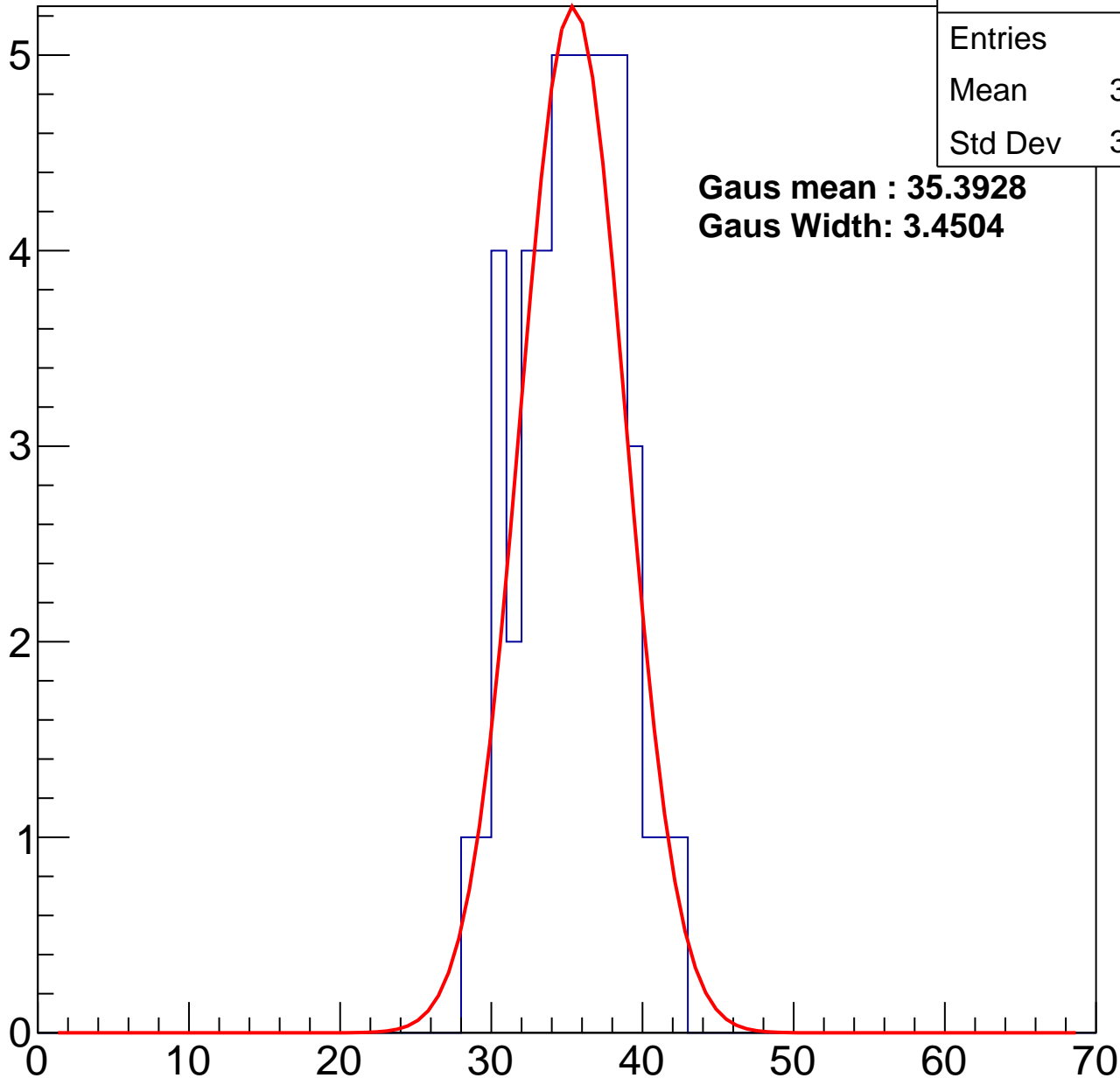
Entries	47
Mean	34.87
Std Dev	3.266

Gaus mean : 35.3928

Gaus Width: 3.4504

5
4
3
2
1
0

ampl



B1L102S, U18-ch91, adc2

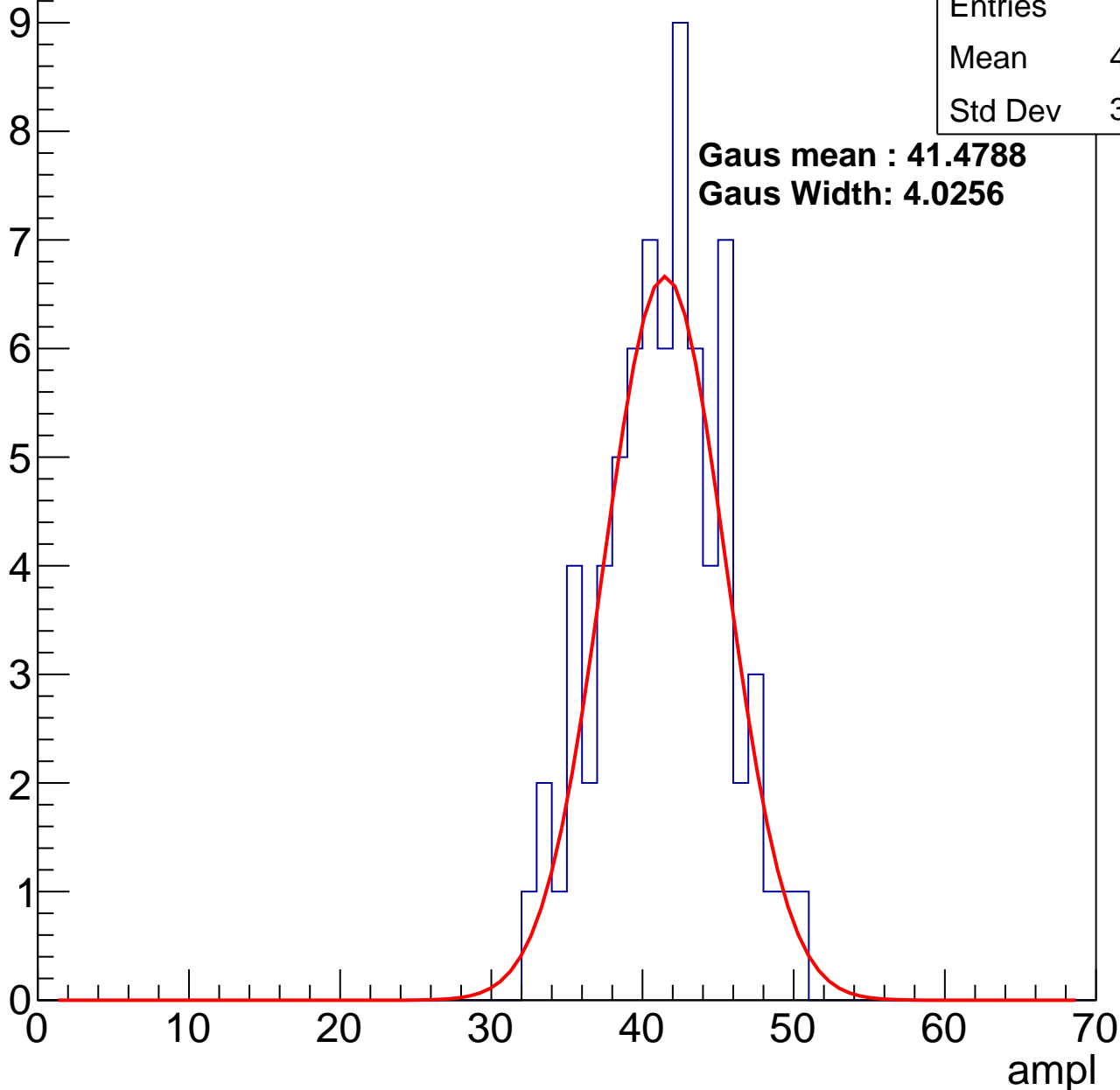
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	40.96
Std Dev	3.953

Gaus mean : 41.4788

Gaus Width: 4.0256

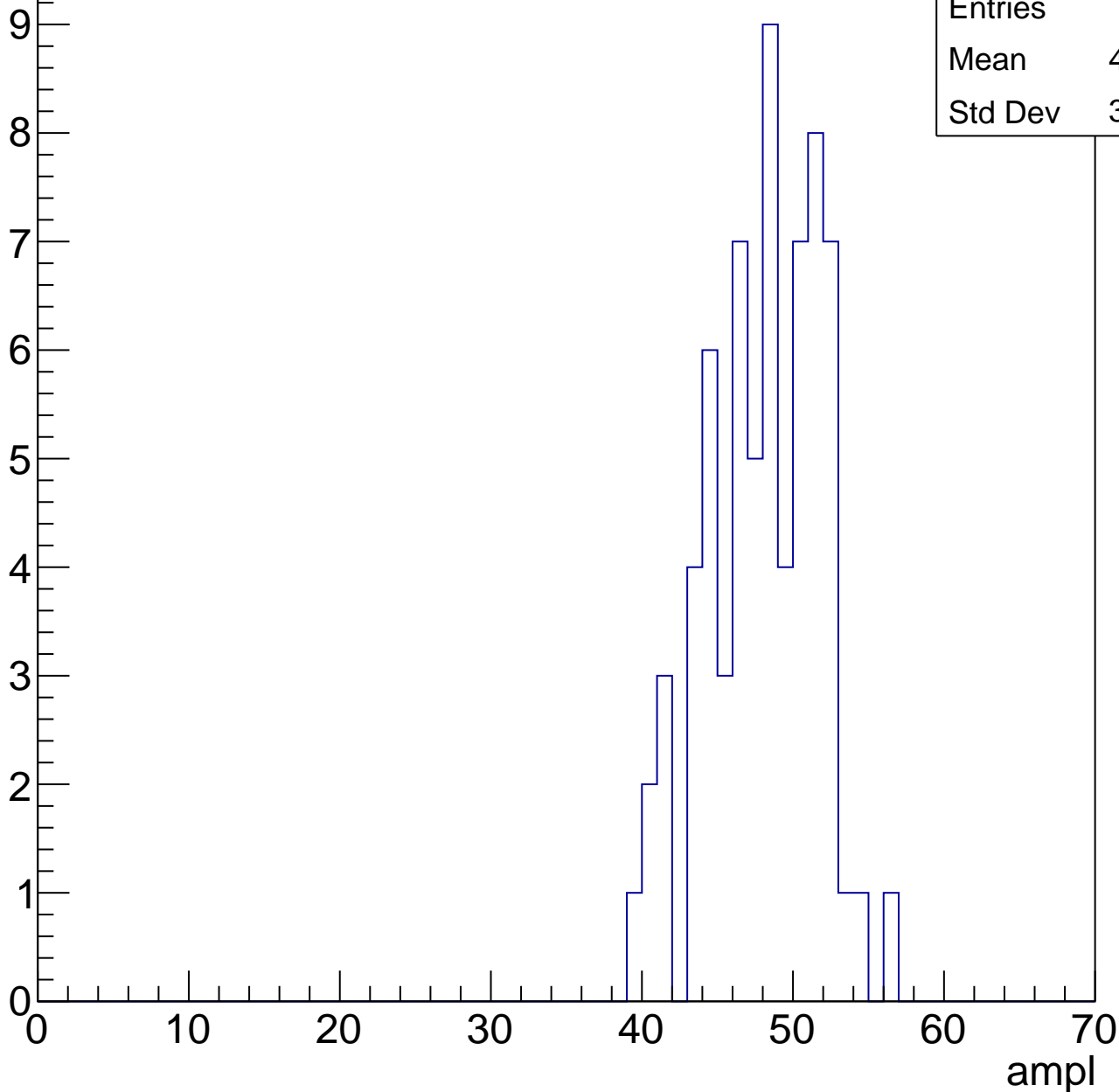


B1L102S, U18-ch91, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	47.58
Std Dev	3.693

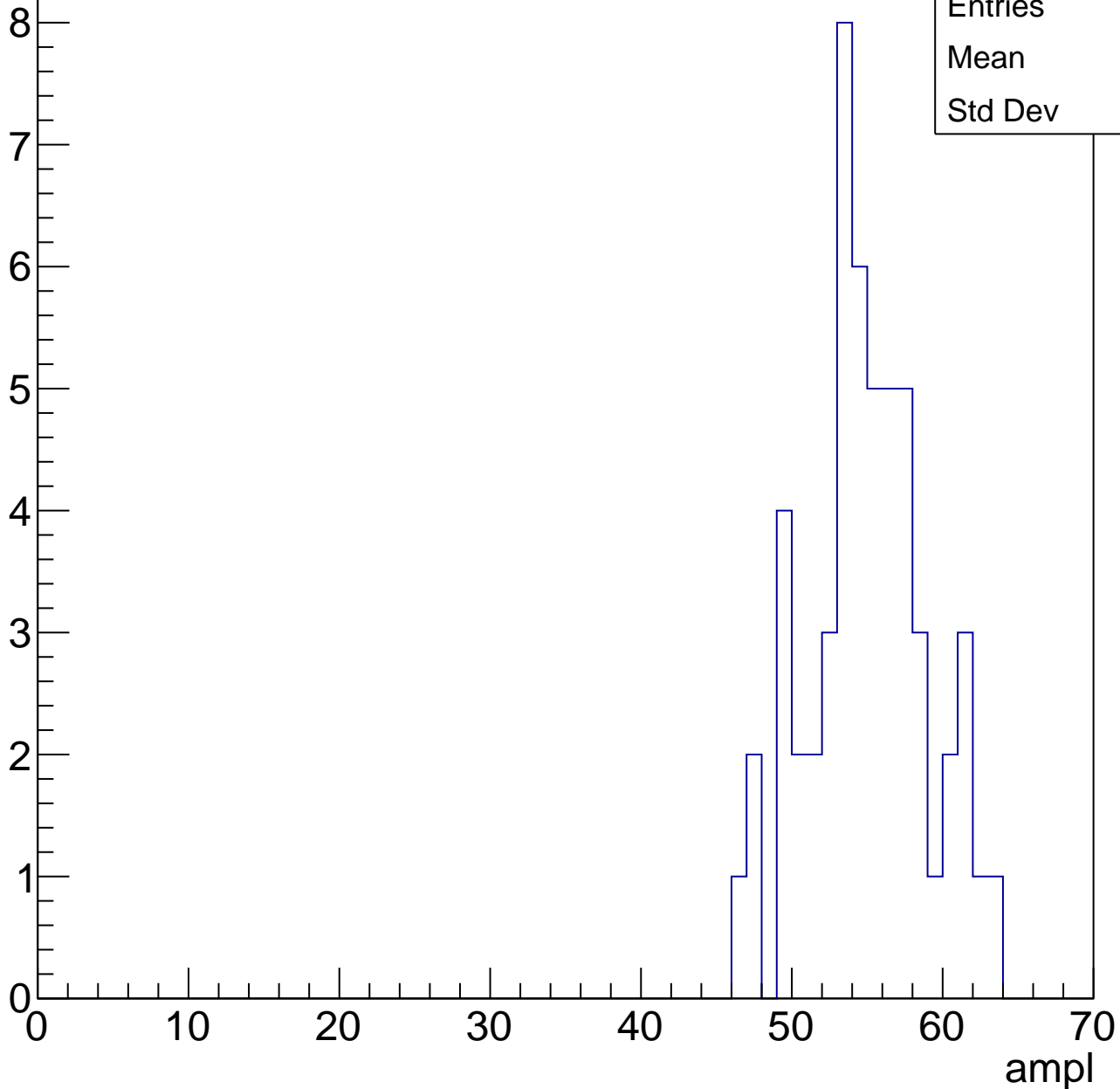


B1L102S, U18-ch91, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

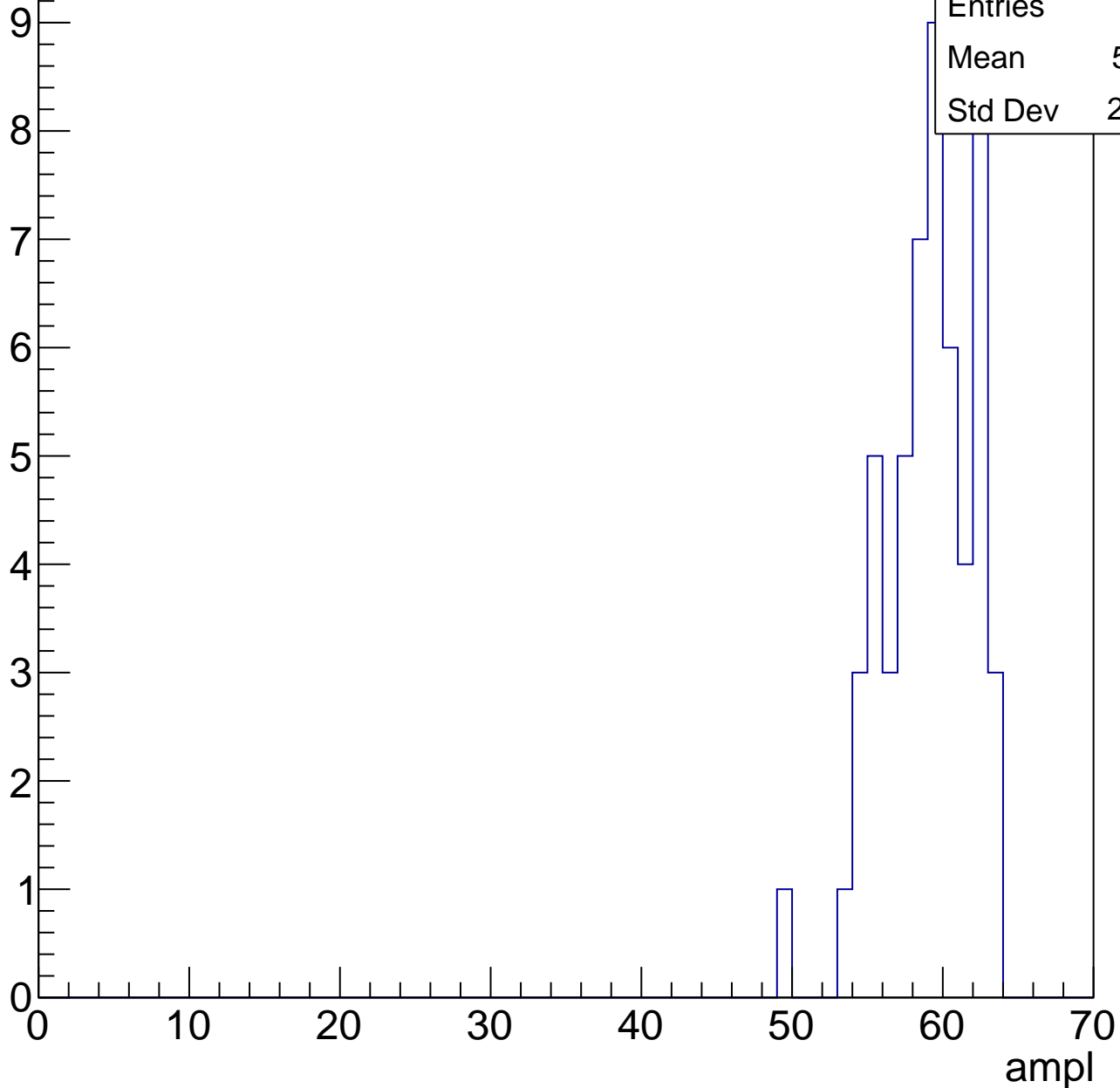
Entries	54
Mean	54.5
Std Dev	3.91



B1L102S, U18-ch91, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

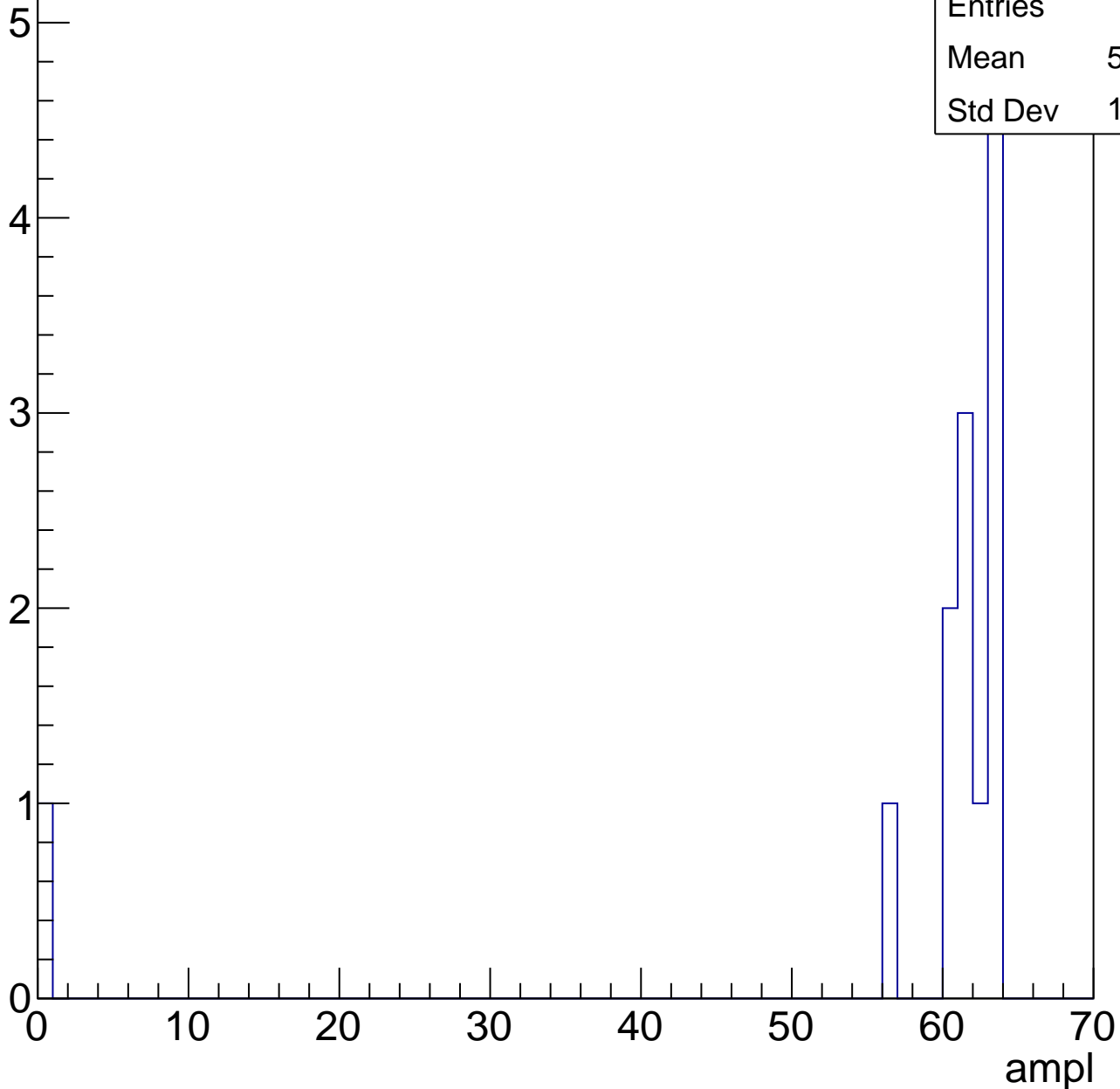


B1L102S, U18-ch91, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	56.62
Std Dev	16.45



B1L102S, U18-ch91, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L102S, U18-ch92, adc0

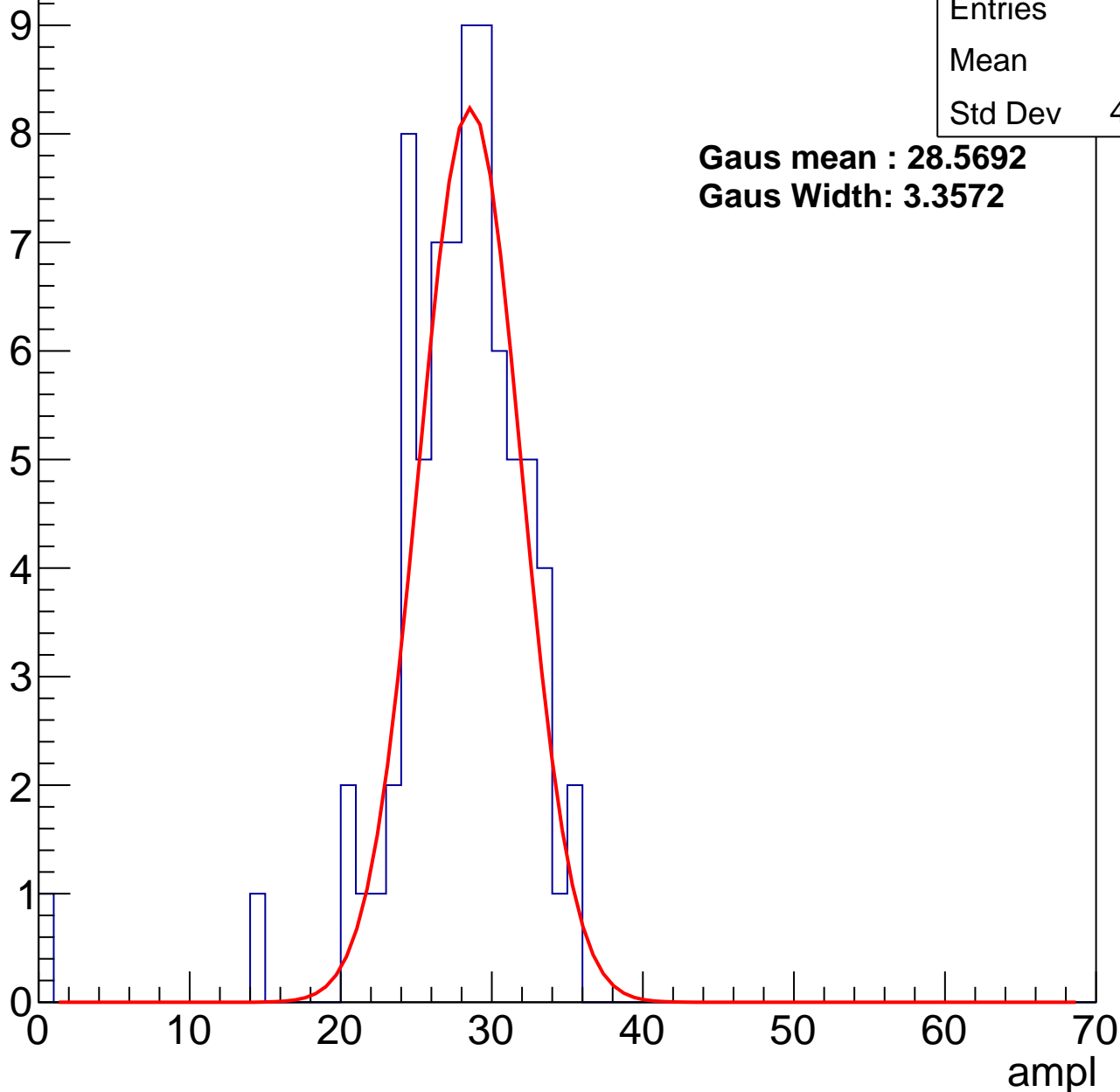
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.3
Std Dev	4.875

Gaus mean : 28.5692

Gaus Width: 3.3572



B1L102S, U18-ch92, adc1

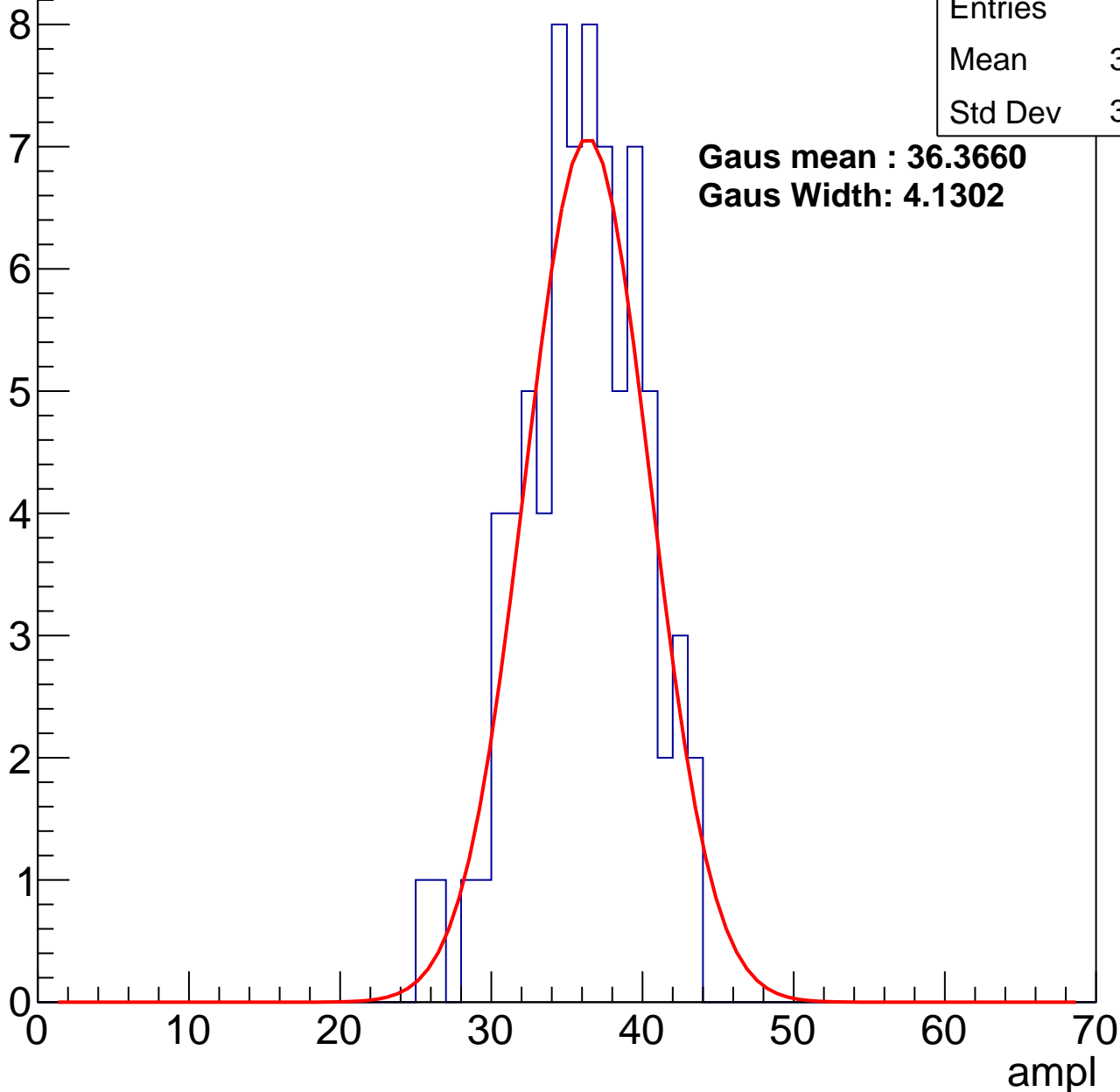
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.53
Std Dev	3.903

Gaus mean : 36.3660

Gaus Width: 4.1302



B1L102S, U18-ch92, adc2

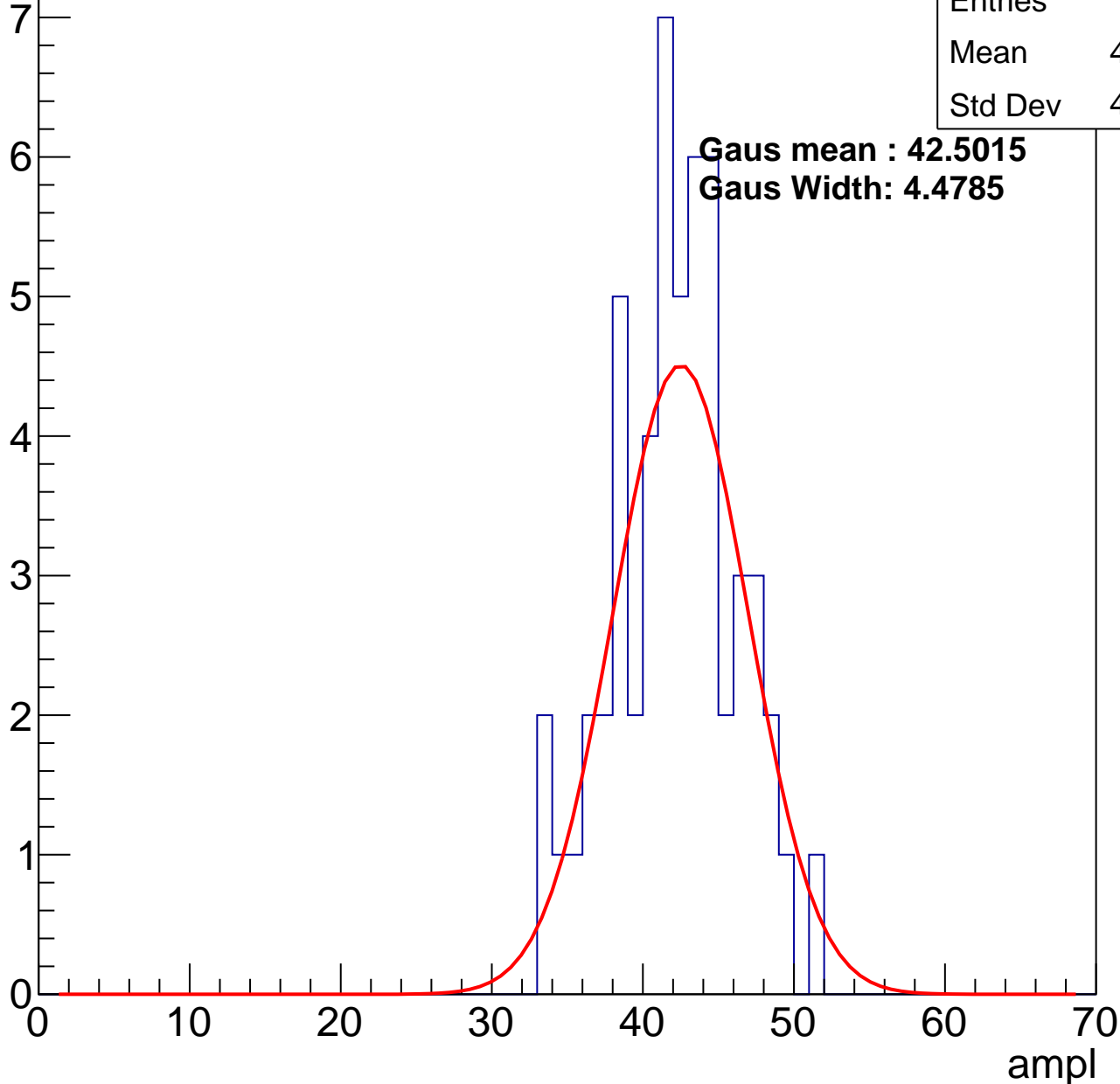
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	41.69
Std Dev	4.013

Gaus mean : 42.5015

Gaus Width: 4.4785

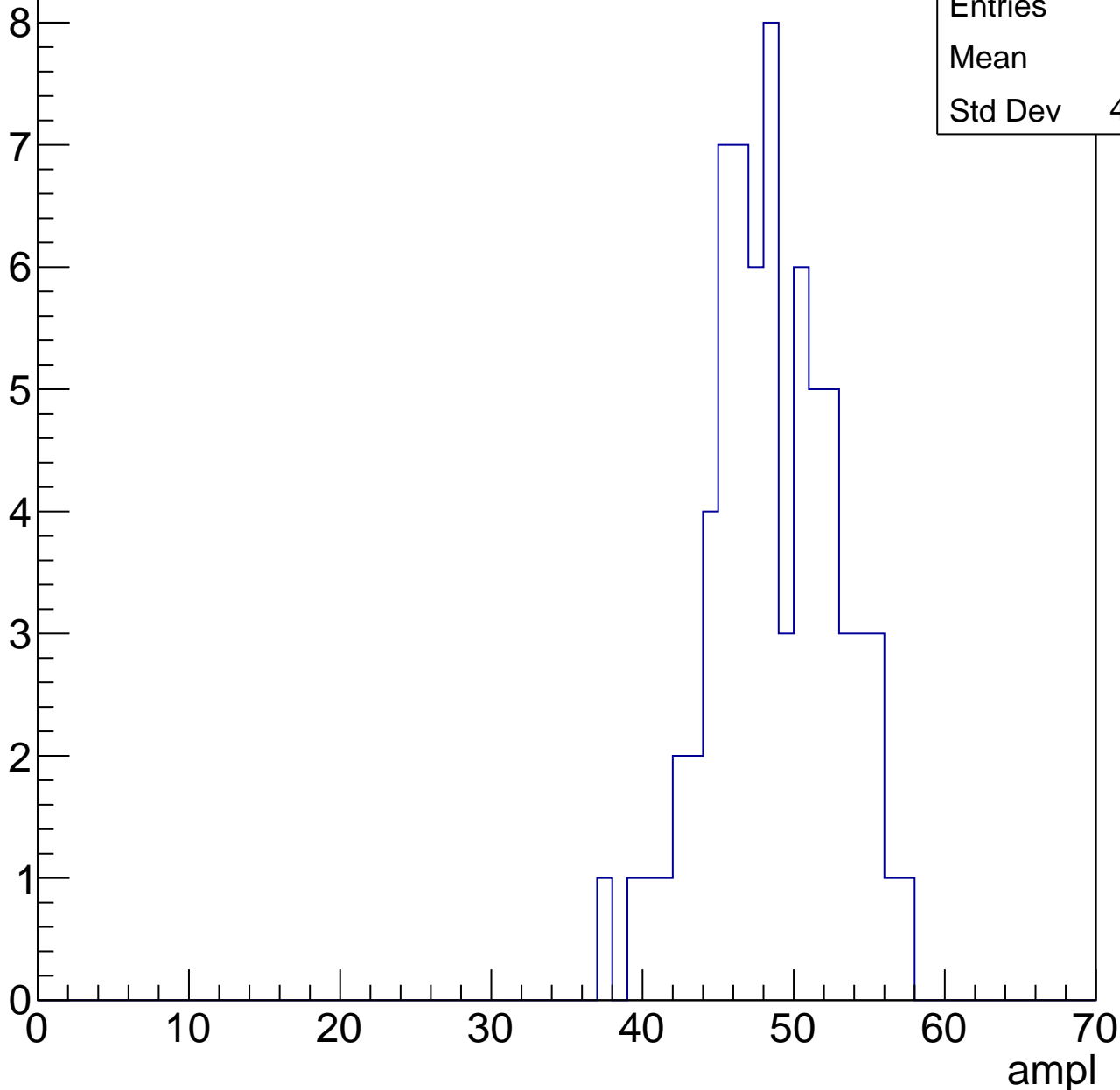


B1L102S, U18-ch92, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	48.1
Std Dev	4.182

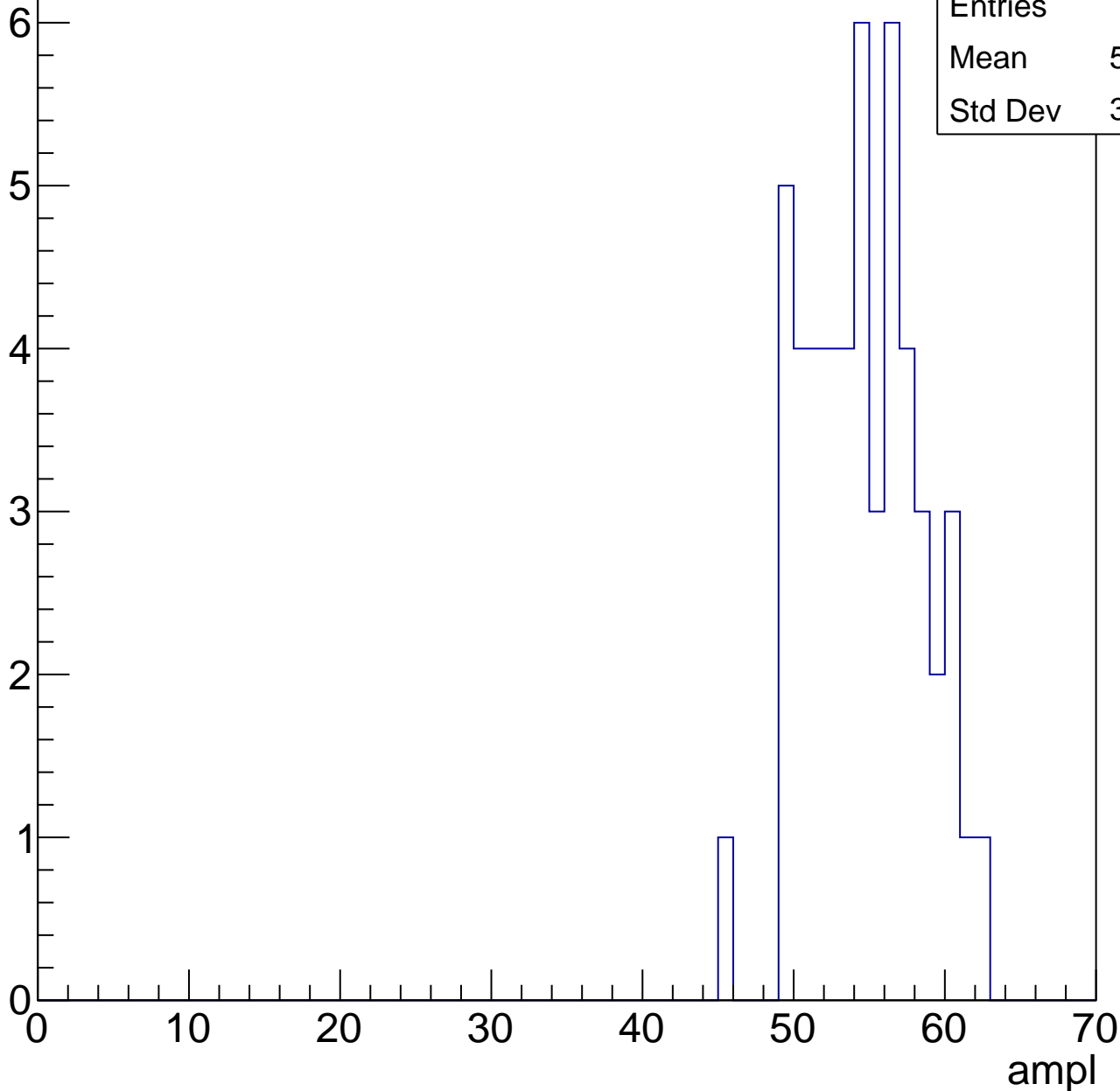


B1L102S, U18-ch92, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	54.16
Std Dev	3.723

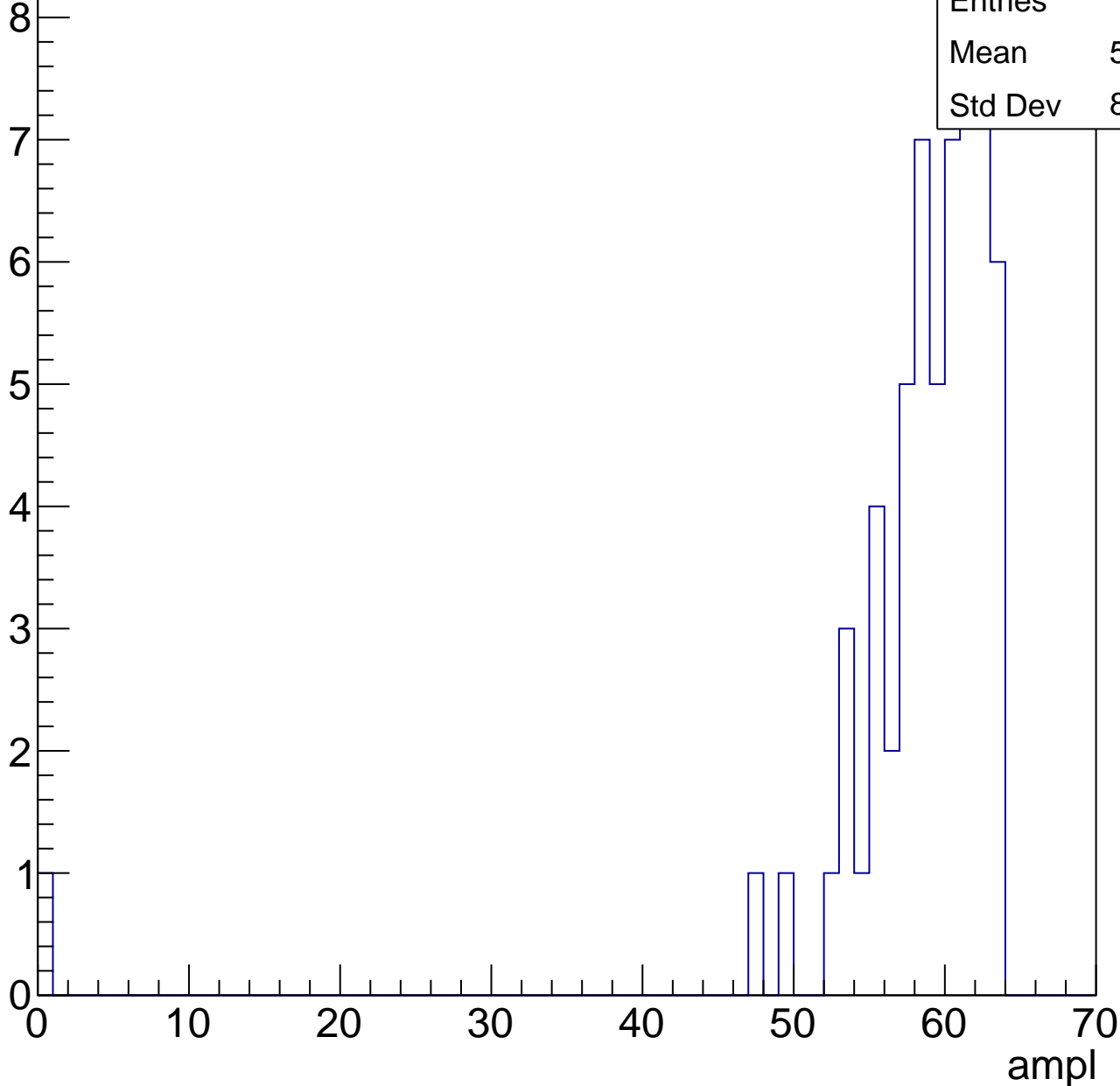


B1L102S, U18-ch92, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

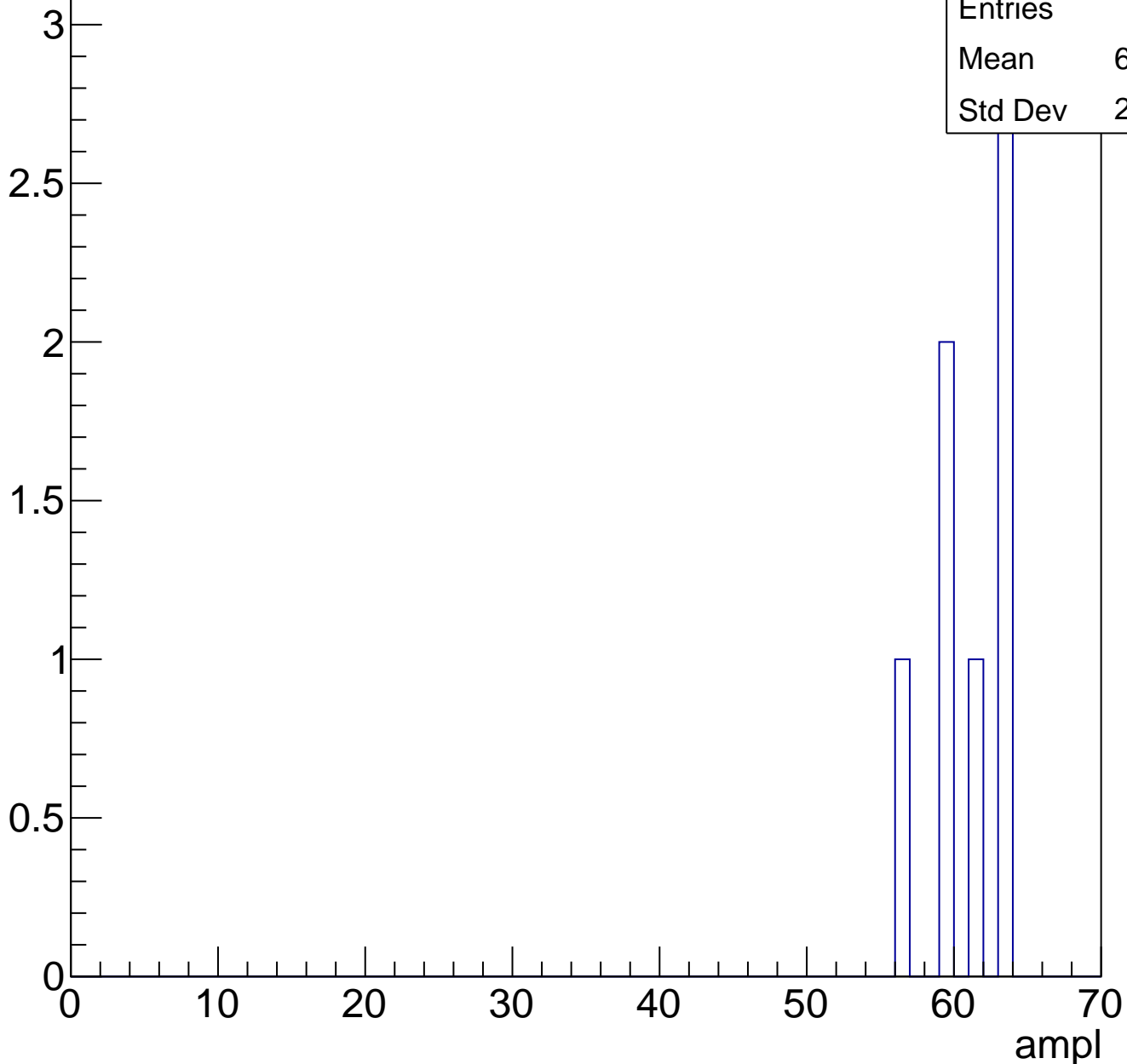
Entries	60
Mean	57.68
Std Dev	8.286



B1L102S, U18-ch92, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

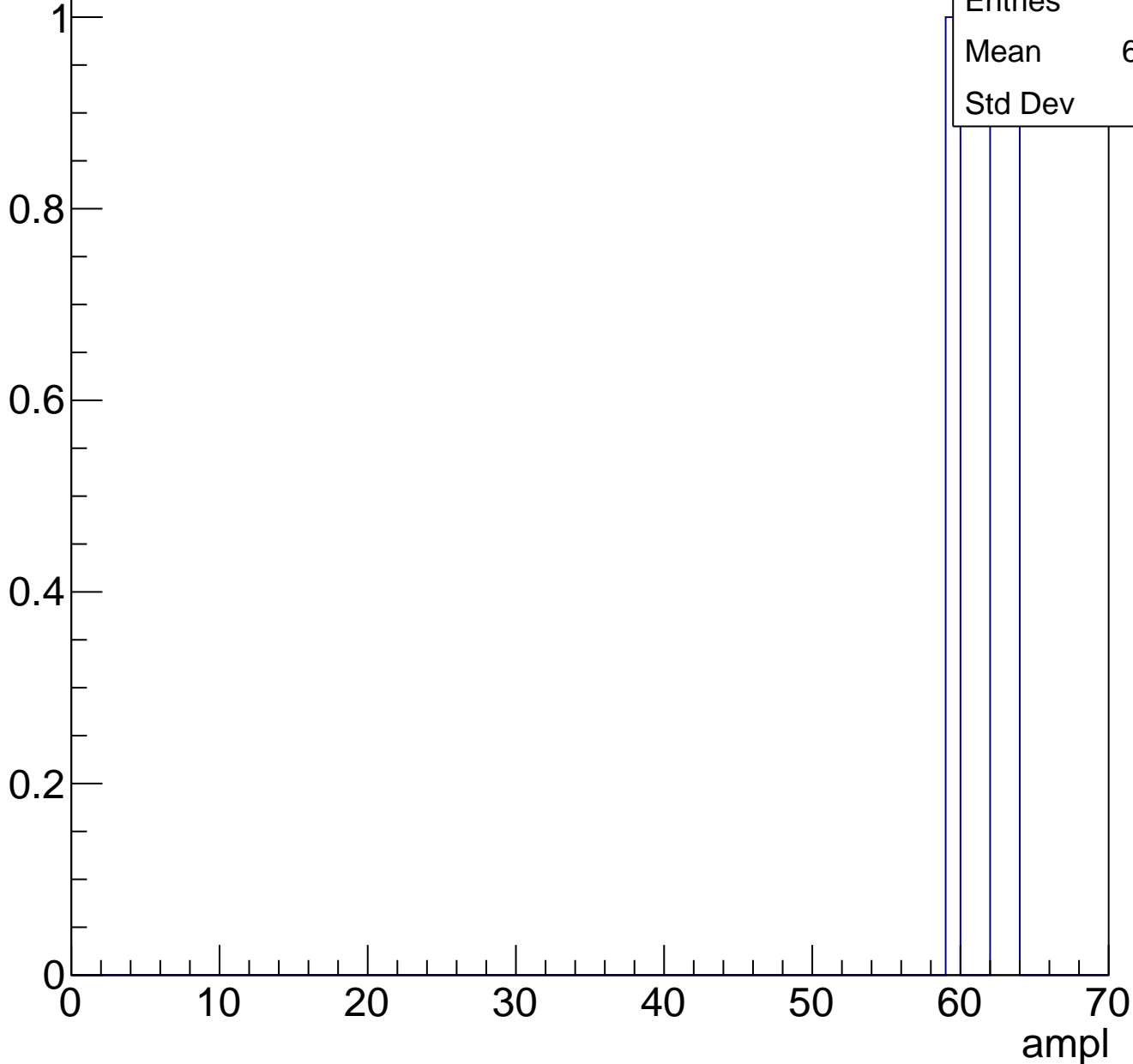


Entries	7
Mean	60.57
Std Dev	2.499

B1L102S, U18-ch92, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch93, adc0

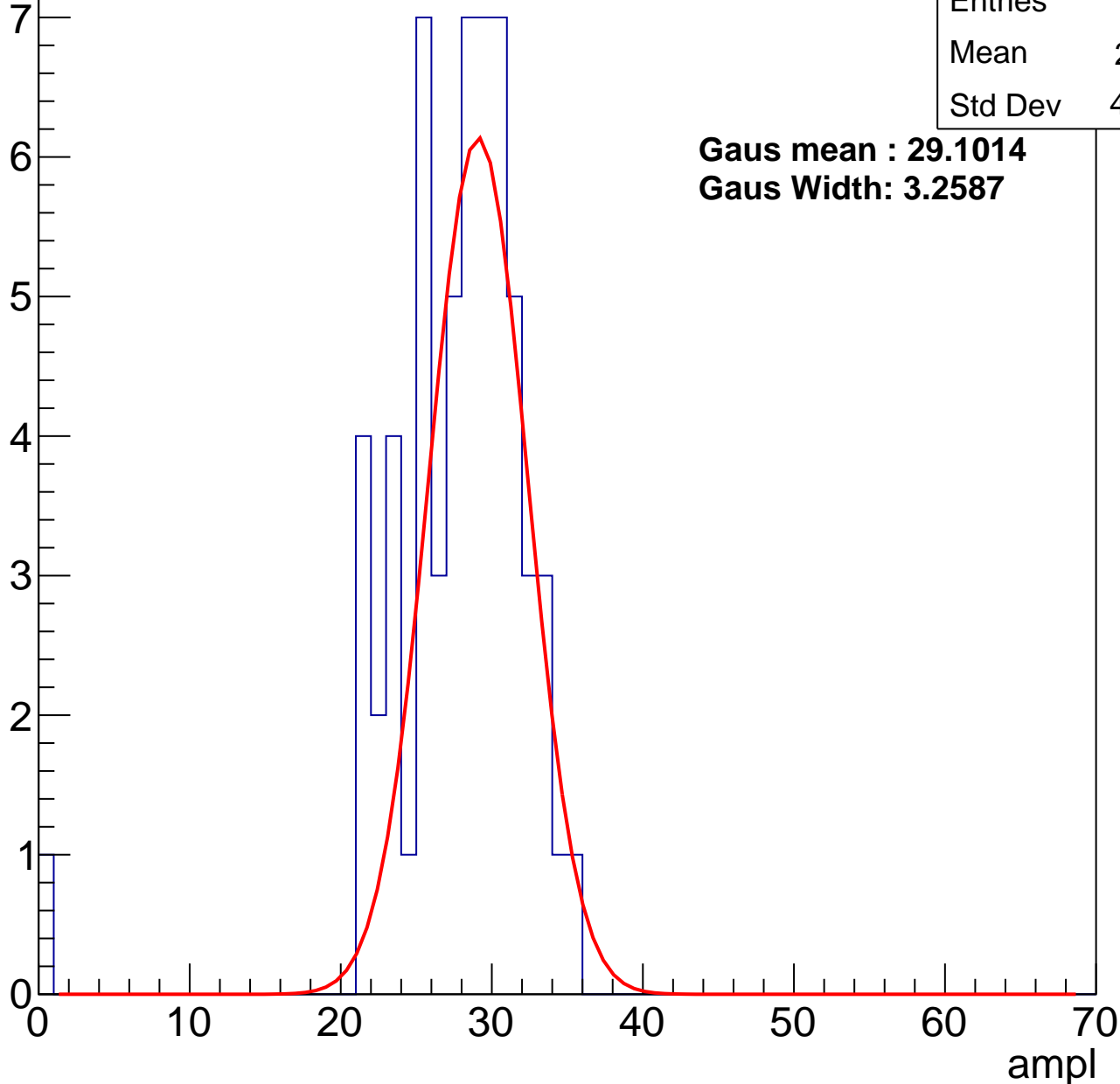
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	27.21
Std Dev	4.956

Gaus mean : 29.1014

Gaus Width: 3.2587



B1L102S, U18-ch93, adc1

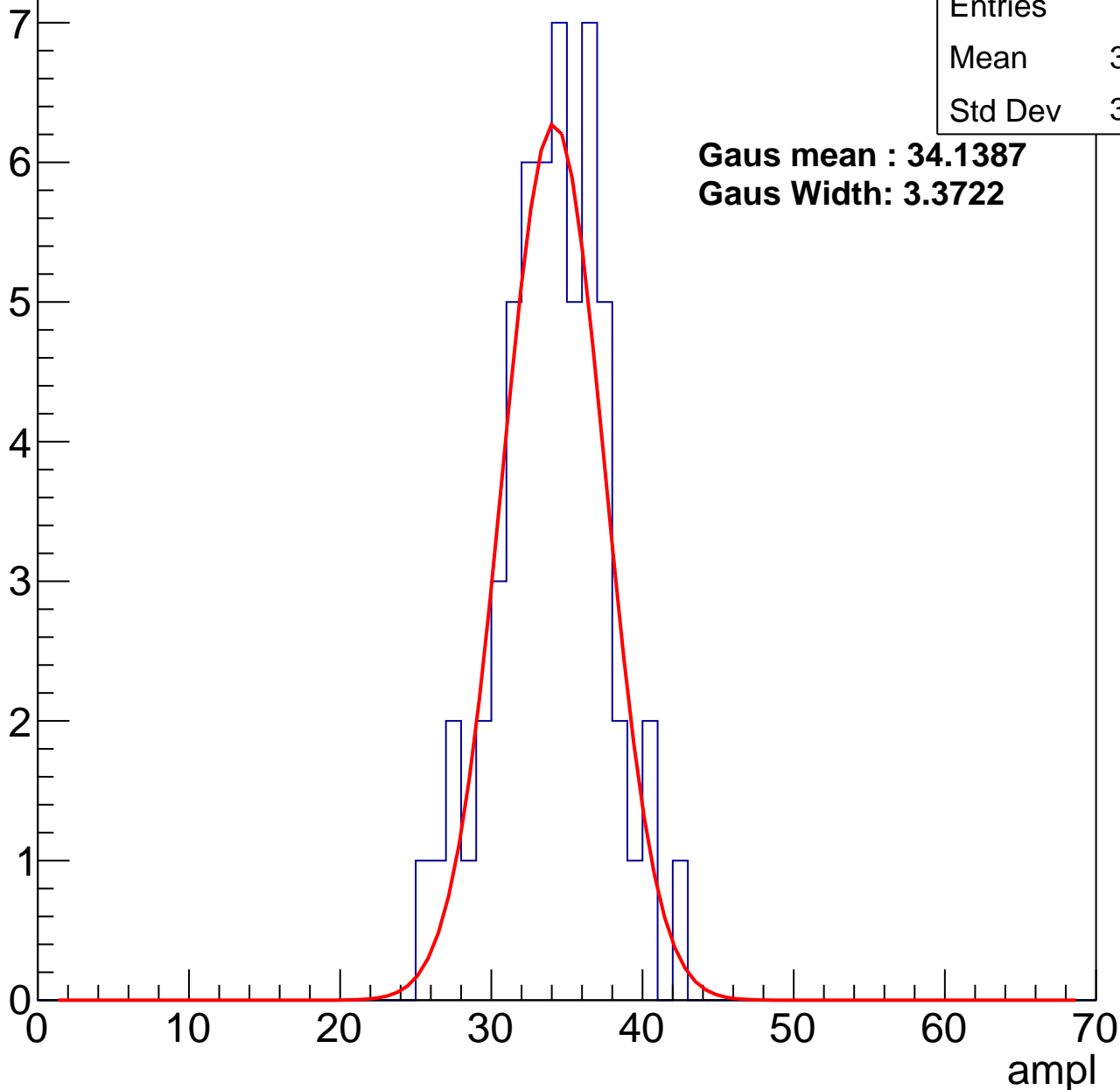
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	33.56
Std Dev	3.534

Gaus mean : 34.1387

Gaus Width: 3.3722



B1L102S, U18-ch93, adc2

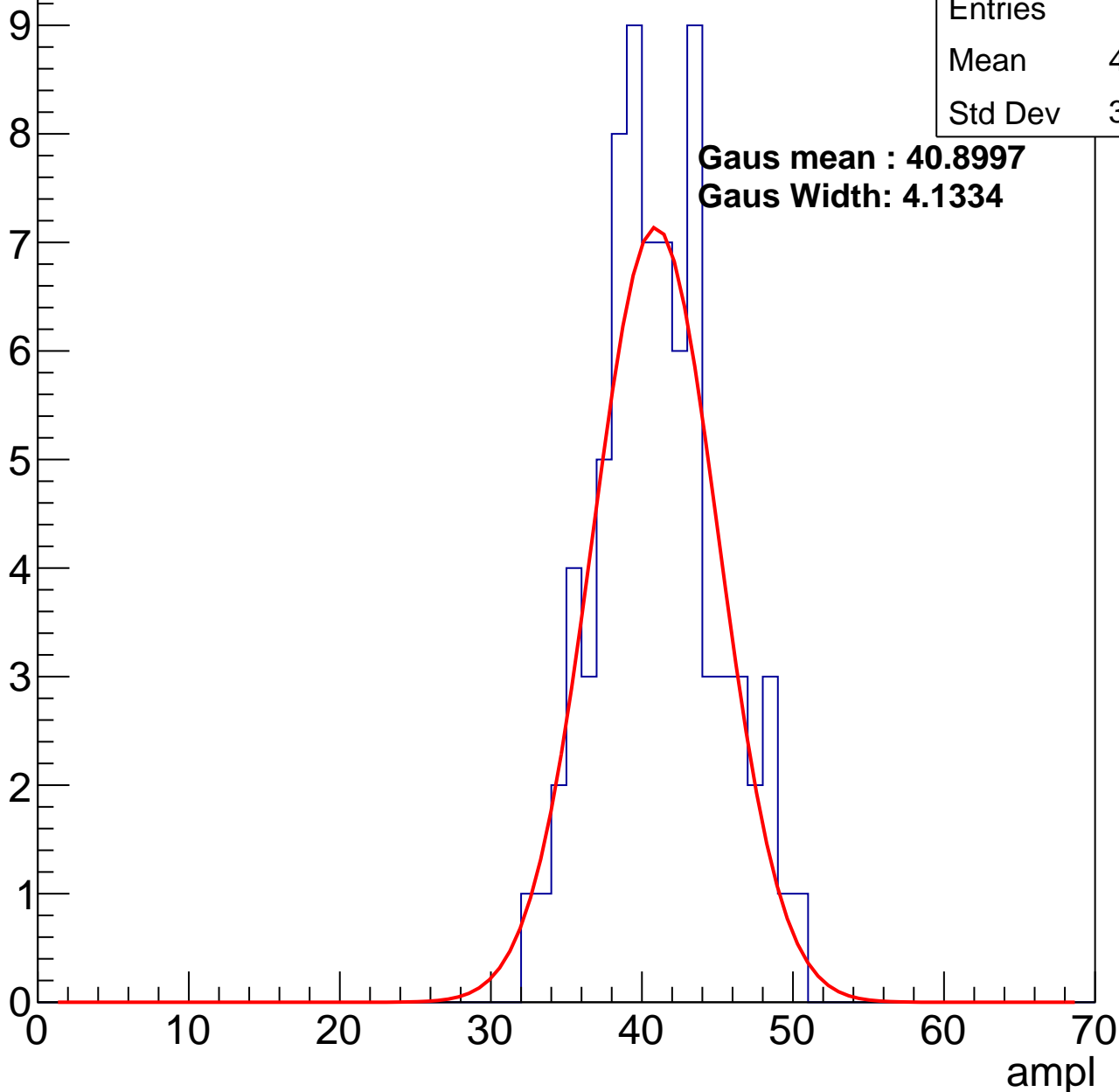
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	40.63
Std Dev	3.926

Gaus mean : 40.8997

Gaus Width: 4.1334

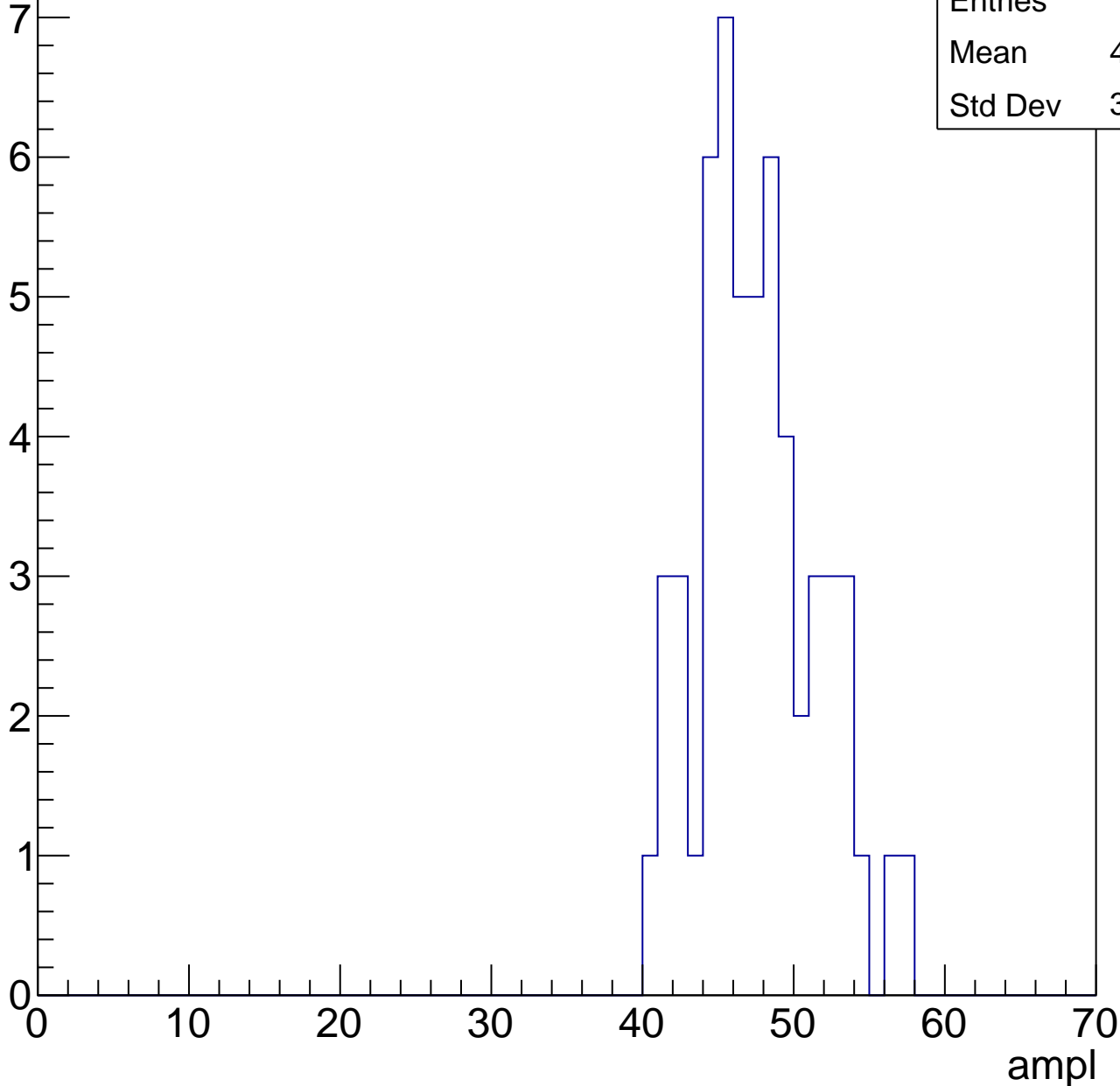


B1L102S, U18-ch93, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	47.18
Std Dev	3.894

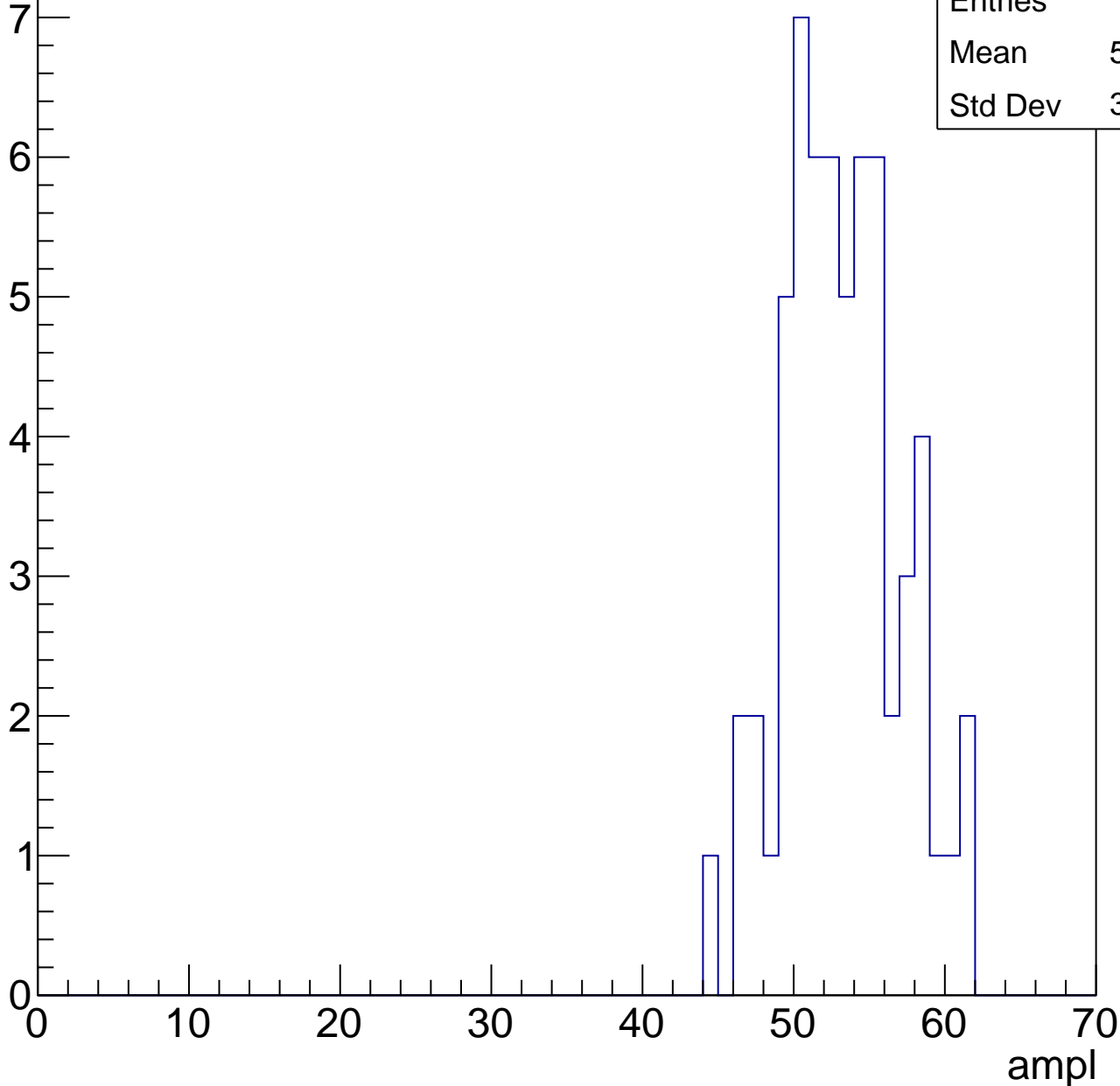


B1L102S, U18-ch93, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	52.77
Std Dev	3.774

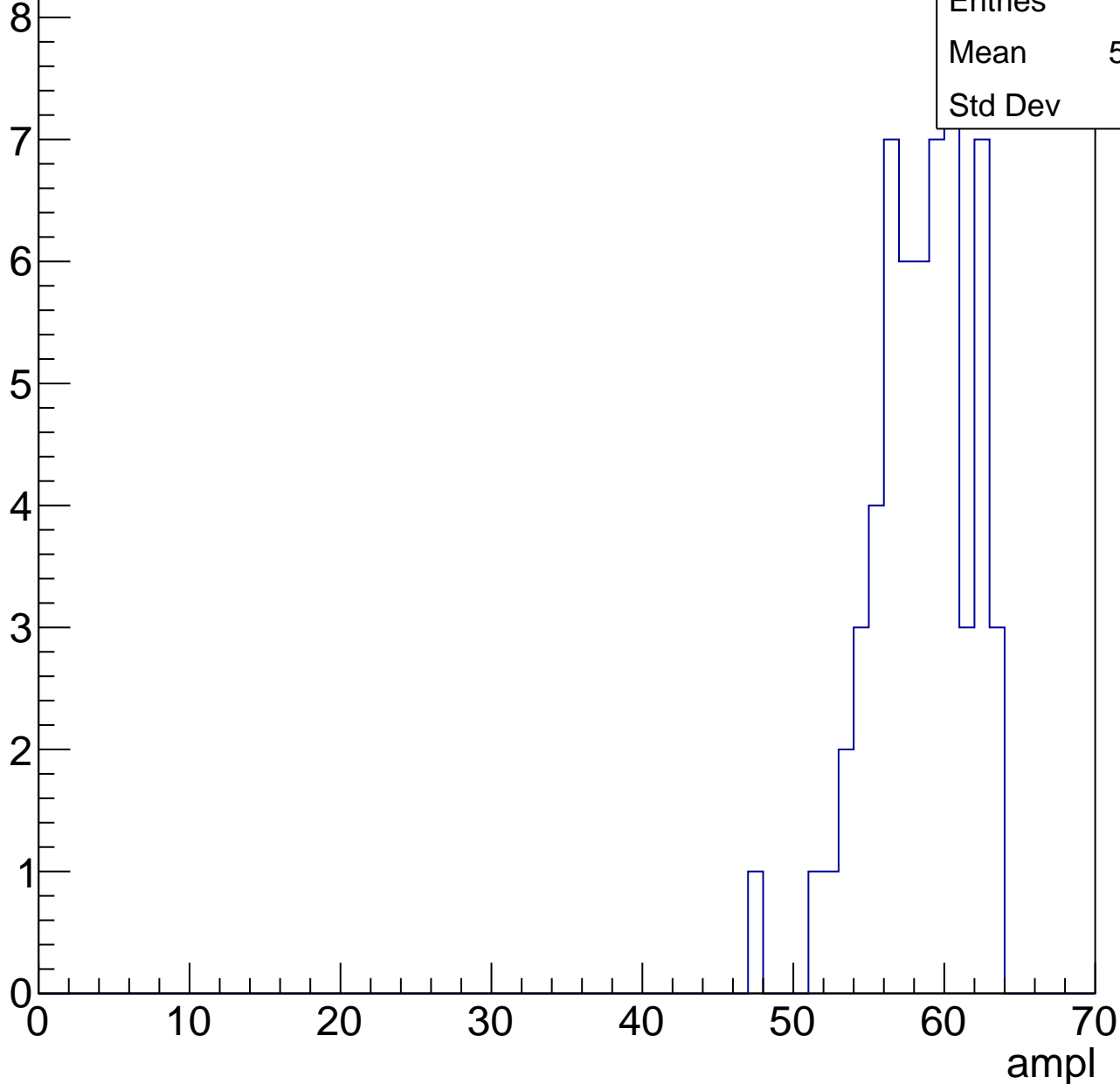


B1L102S, U18-ch93, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.95
Std Dev	3.26

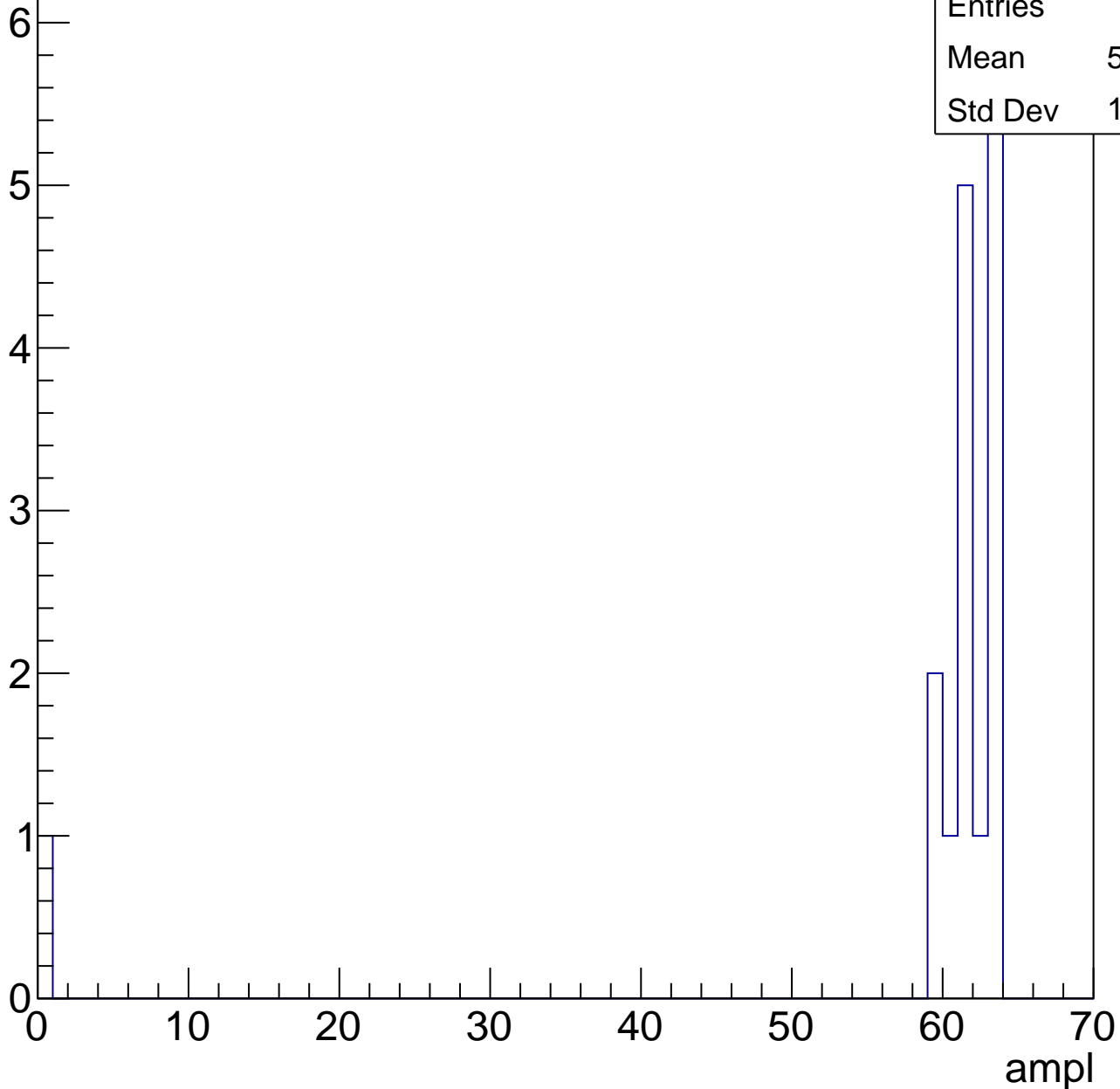


B1L102S, U18-ch93, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

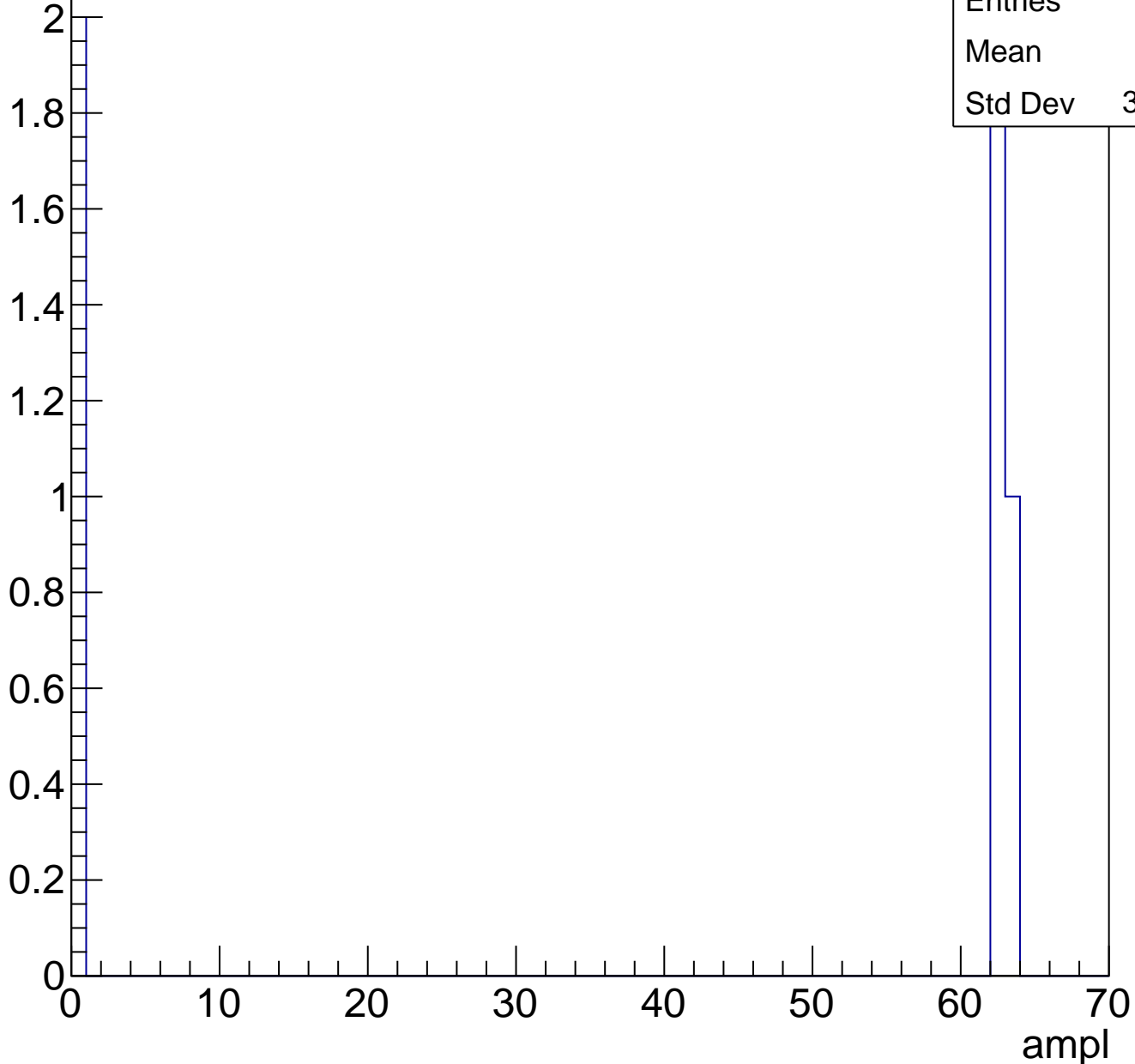
Entries	16
Mean	57.69
Std Dev	14.96



B1L102S, U18-ch93, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	5
Mean	37.4
Std Dev	30.54

B1L102S, U18-ch94, adc0

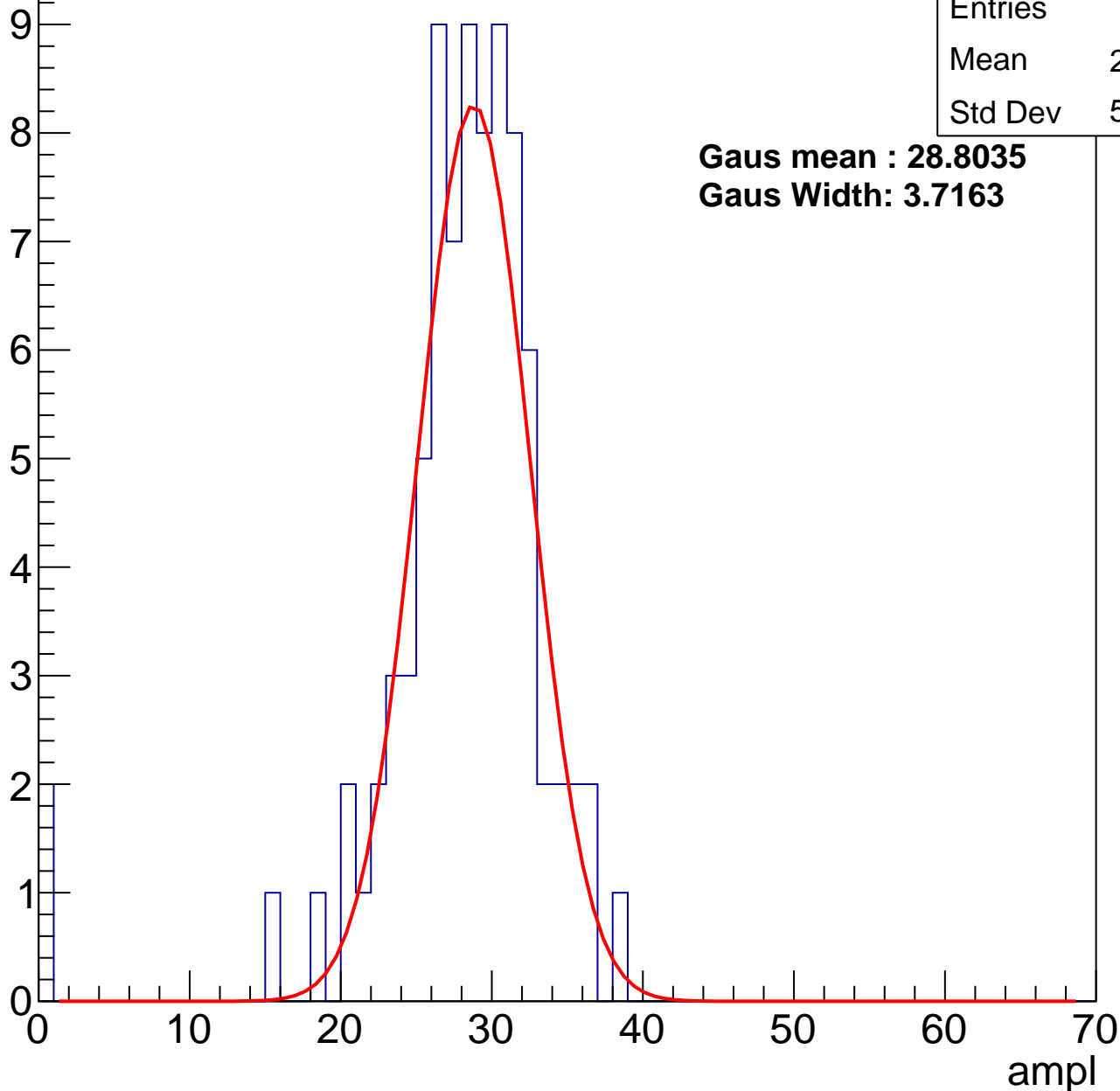
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	27.47
Std Dev	5.874

Gaus mean : 28.8035

Gaus Width: 3.7163



B1L102S, U18-ch94, adc1

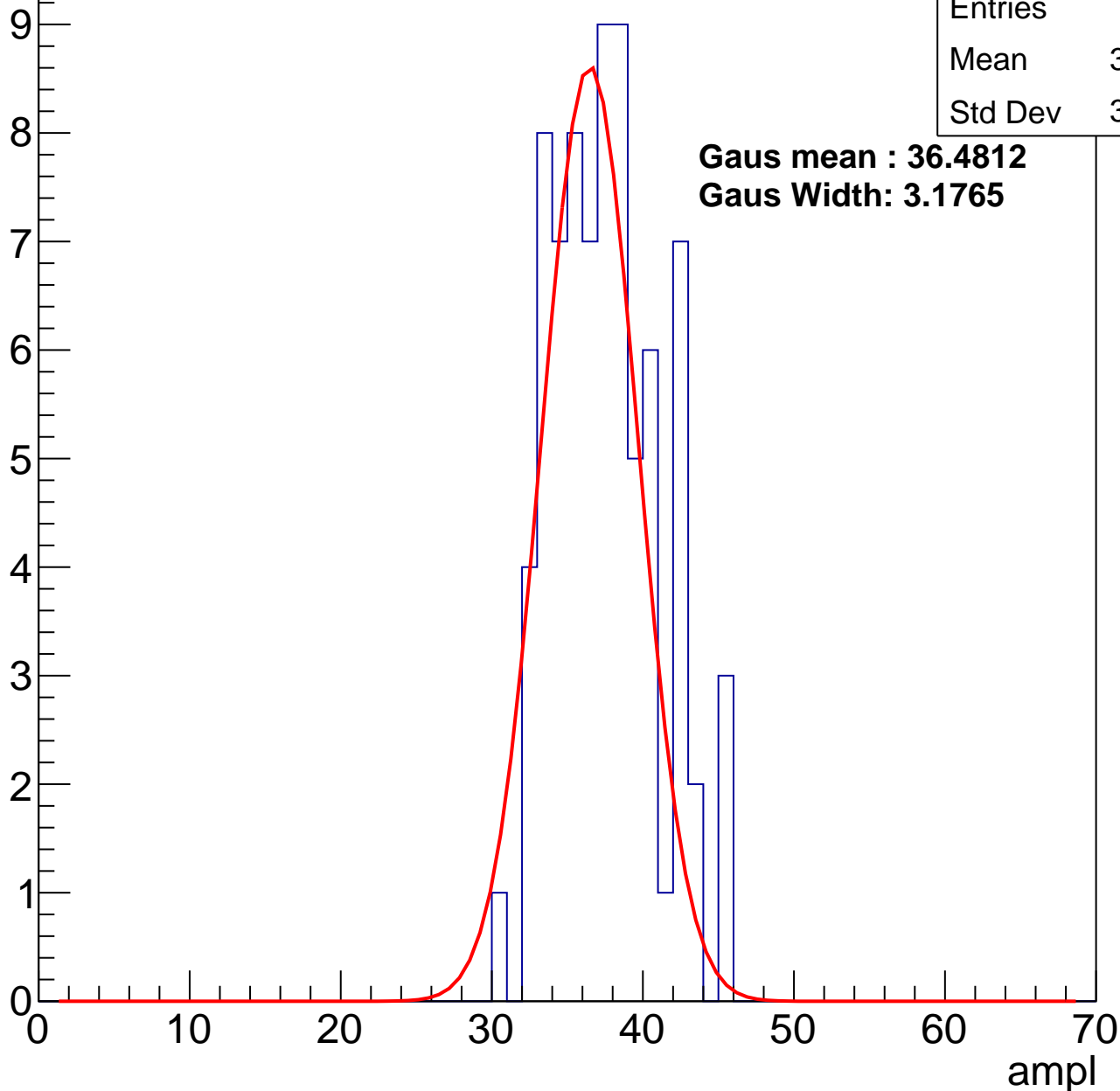
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	37.12
Std Dev	3.449

Gaus mean : 36.4812

Gaus Width: 3.1765



B1L102S, U18-ch94, adc2

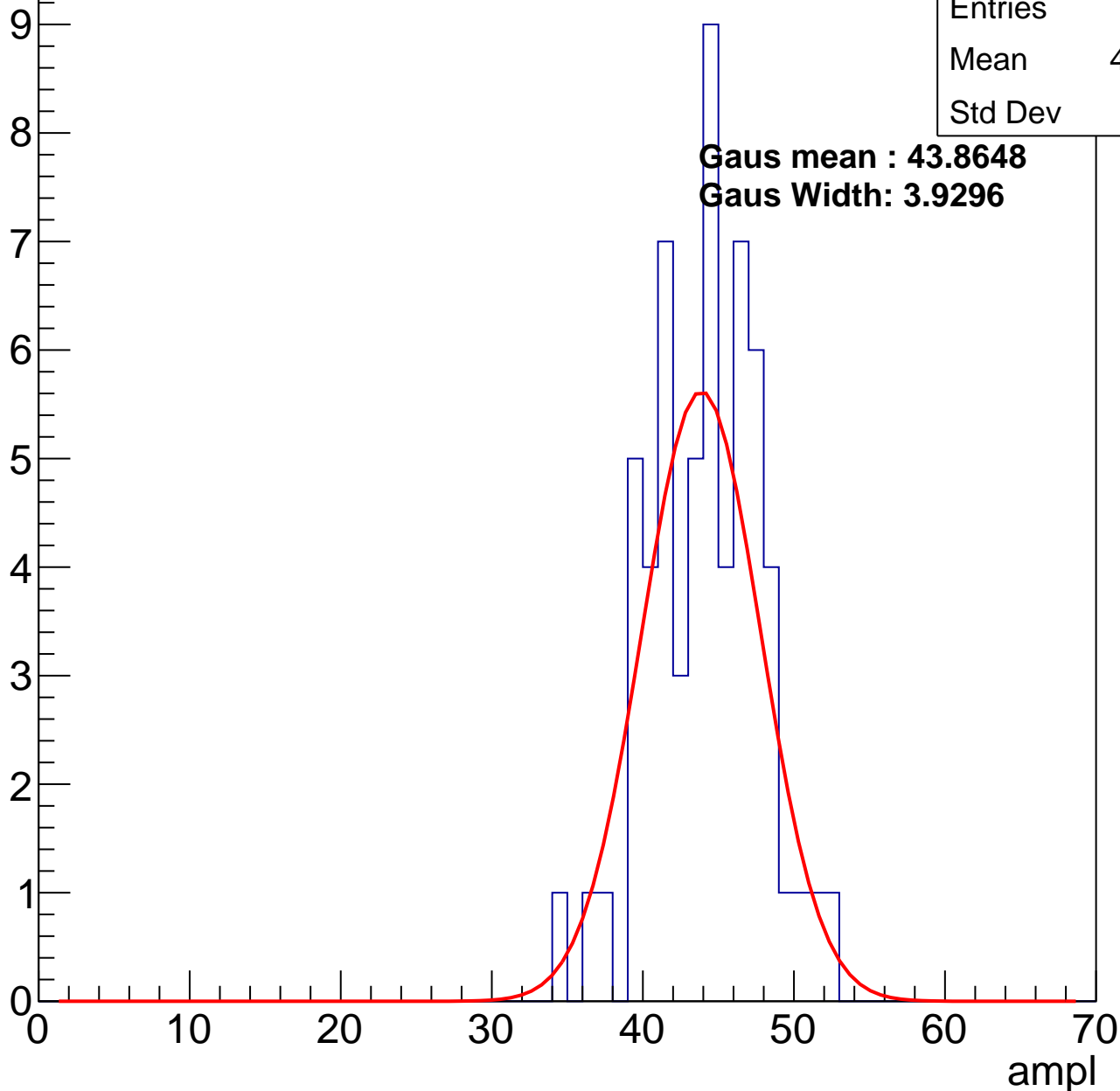
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	43.67
Std Dev	3.62

Gaus mean : 43.8648

Gaus Width: 3.9296

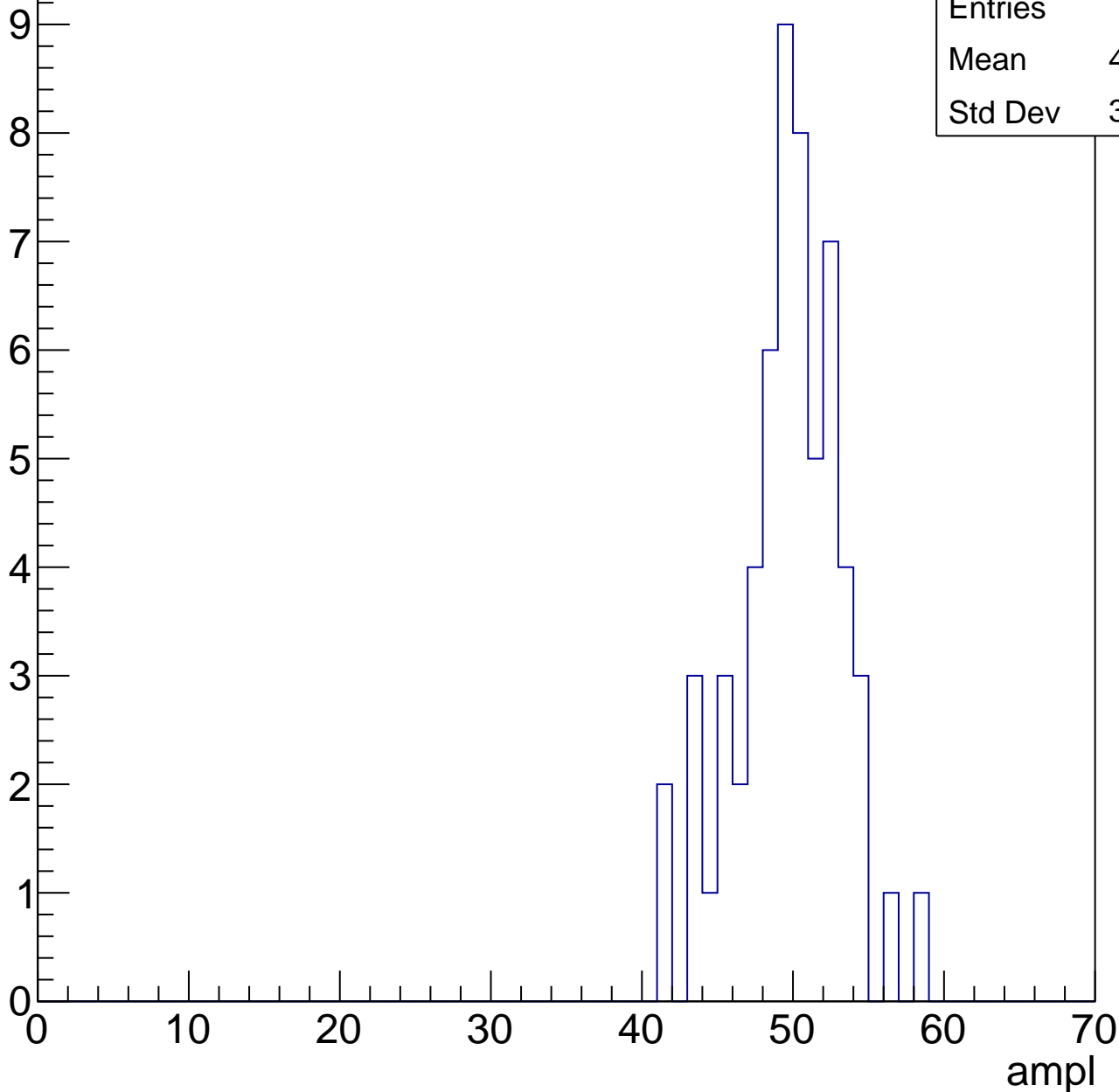


B1L102S, U18-ch94, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	49.25
Std Dev	3.457

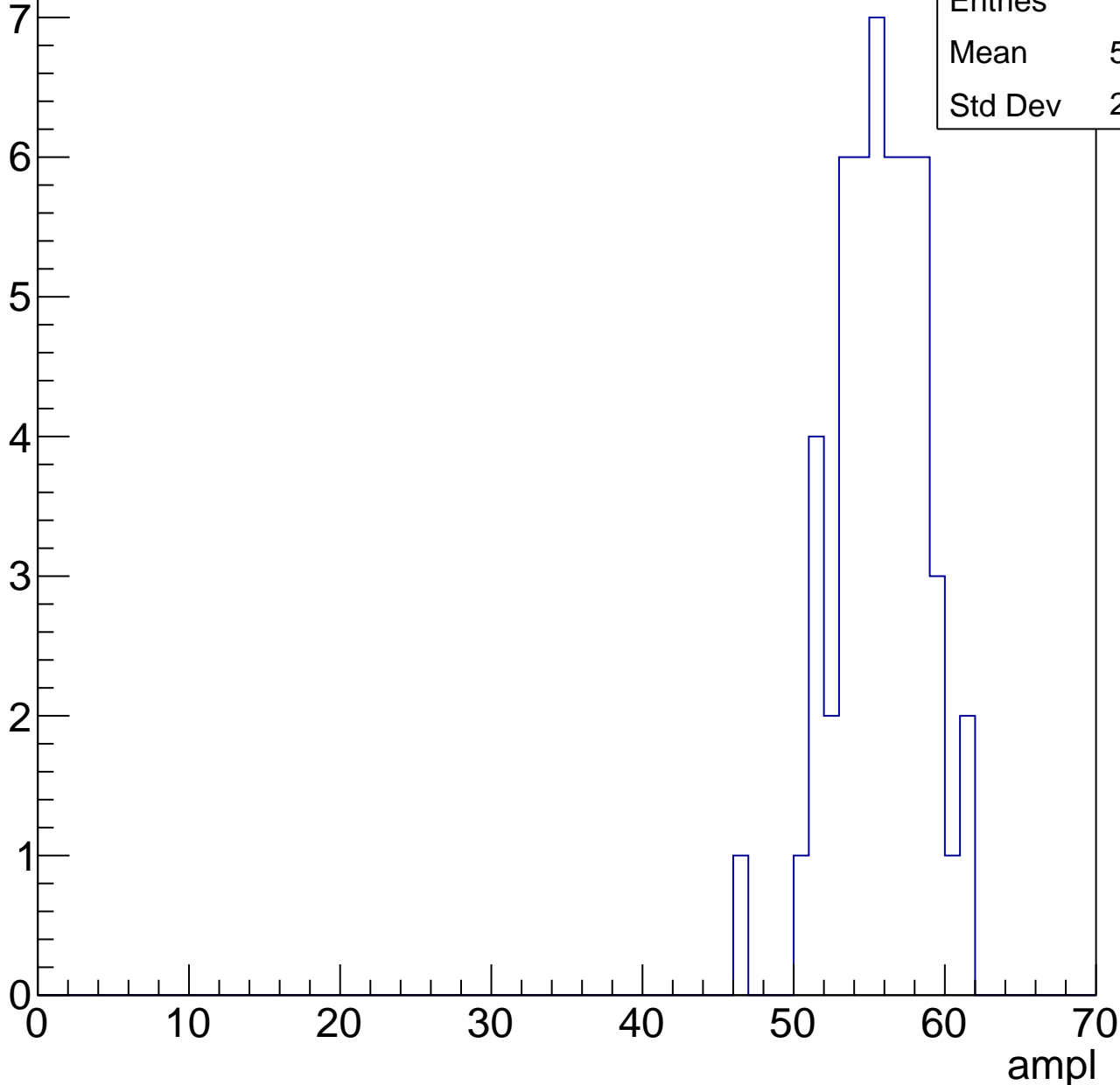


B1L102S, U18-ch94, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

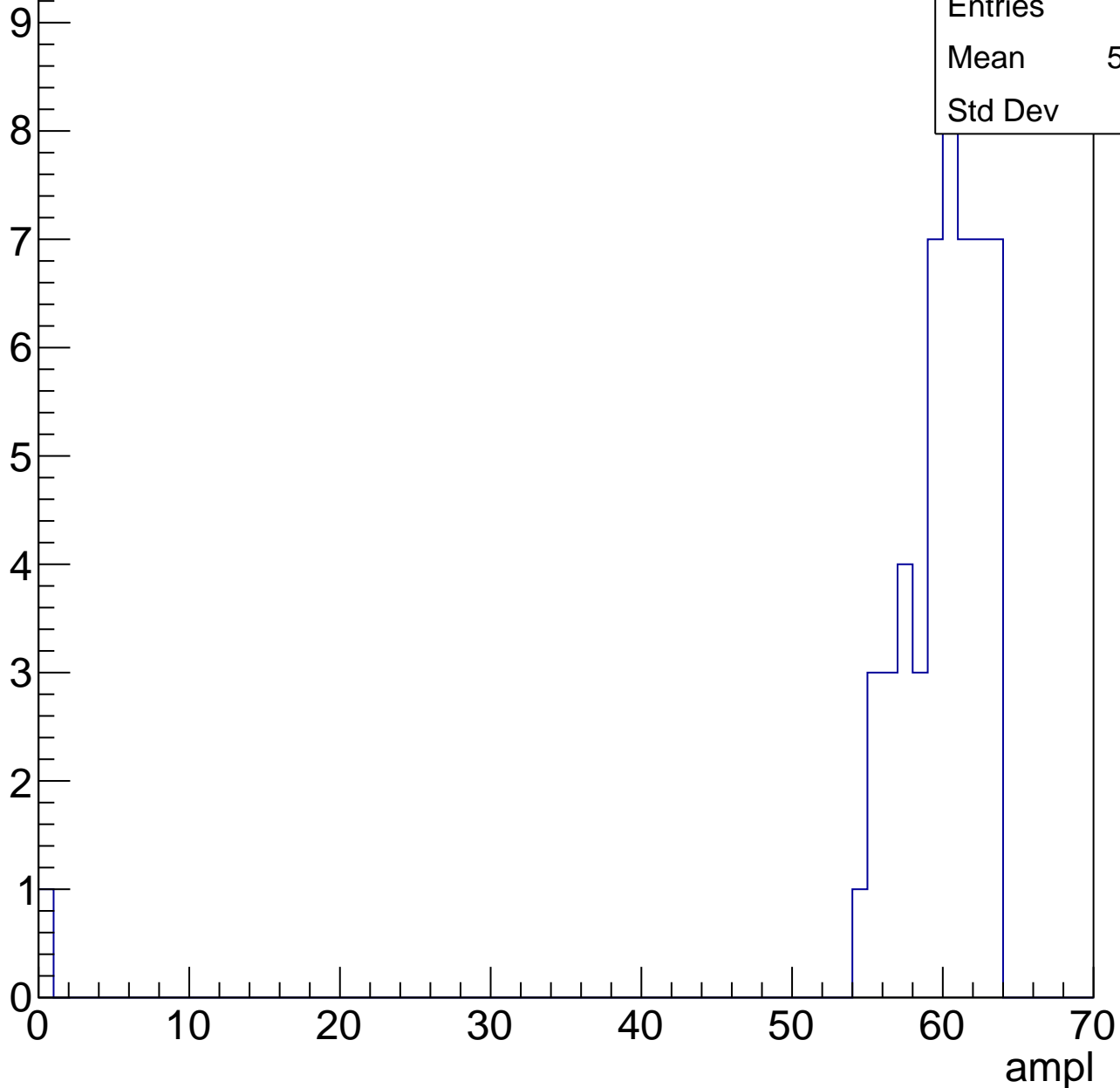
Entries	51
Mean	55.22
Std Dev	2.953



B1L102S, U18-ch94, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch94, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch94, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch95, adc0

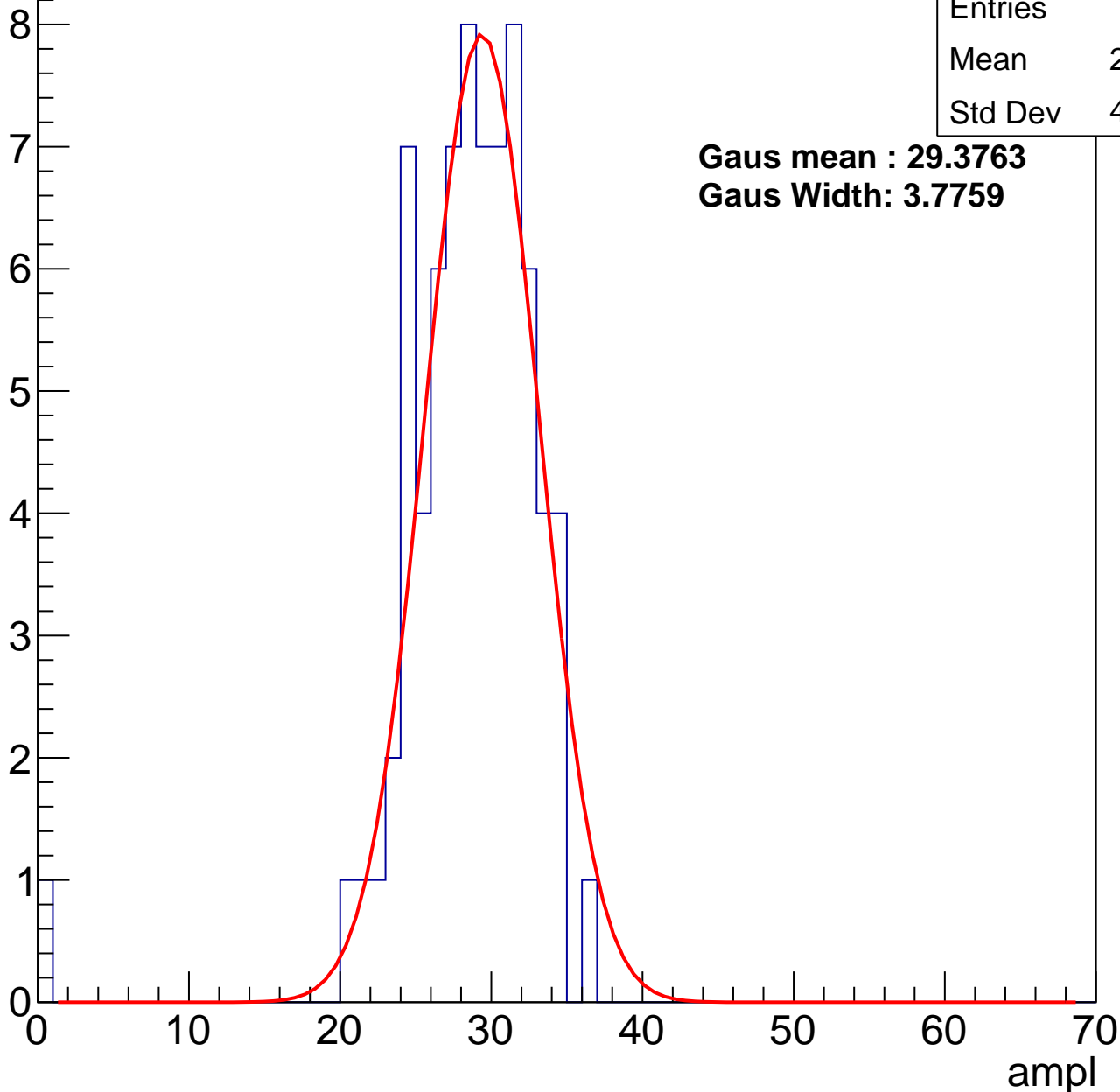
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	28.04
Std Dev	4.723

Gaus mean : 29.3763

Gaus Width: 3.7759



B1L102S, U18-ch95, adc1

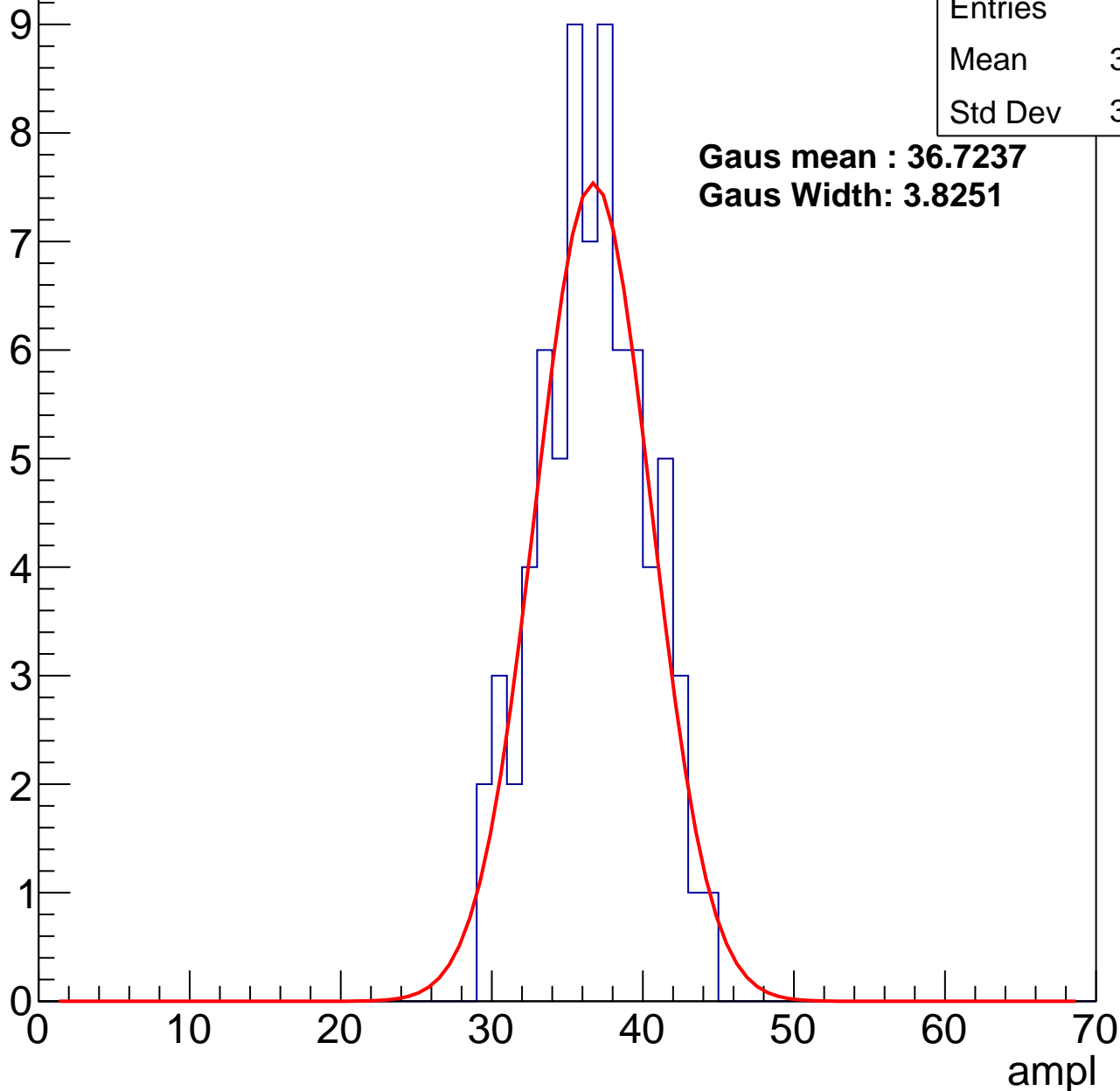
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.25
Std Dev	3.503

Gaus mean : 36.7237

Gaus Width: 3.8251



B1L102S, U18-ch95, adc2

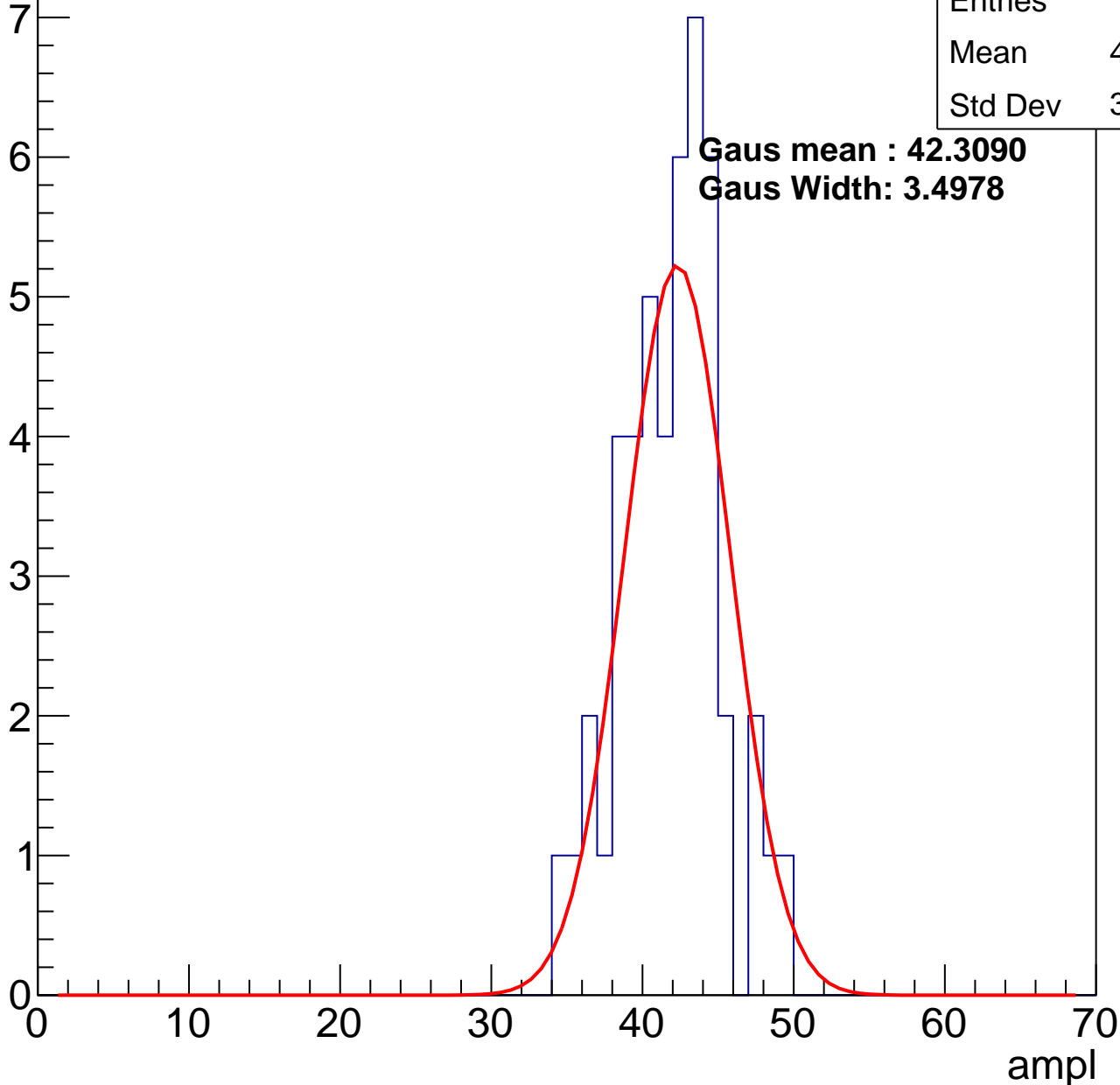
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	41.45
Std Dev	3.273

Gaus mean : 42.3090

Gaus Width: 3.4978



B1L102S, U18-ch95, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	79
Mean	48.28
Std Dev	3.337

Entry

10

8

6

4

2

0

0

10

20

30

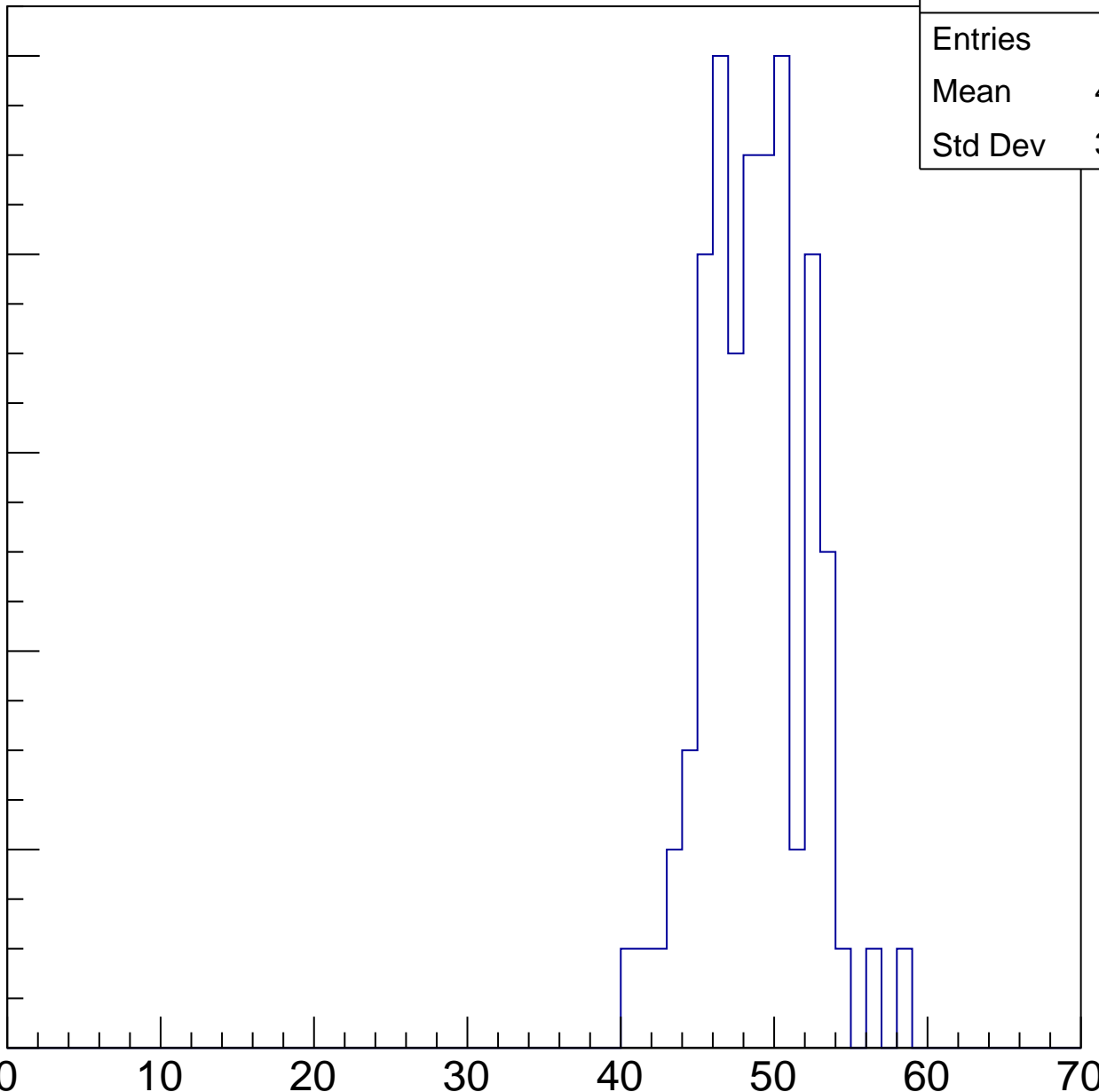
40

50

60

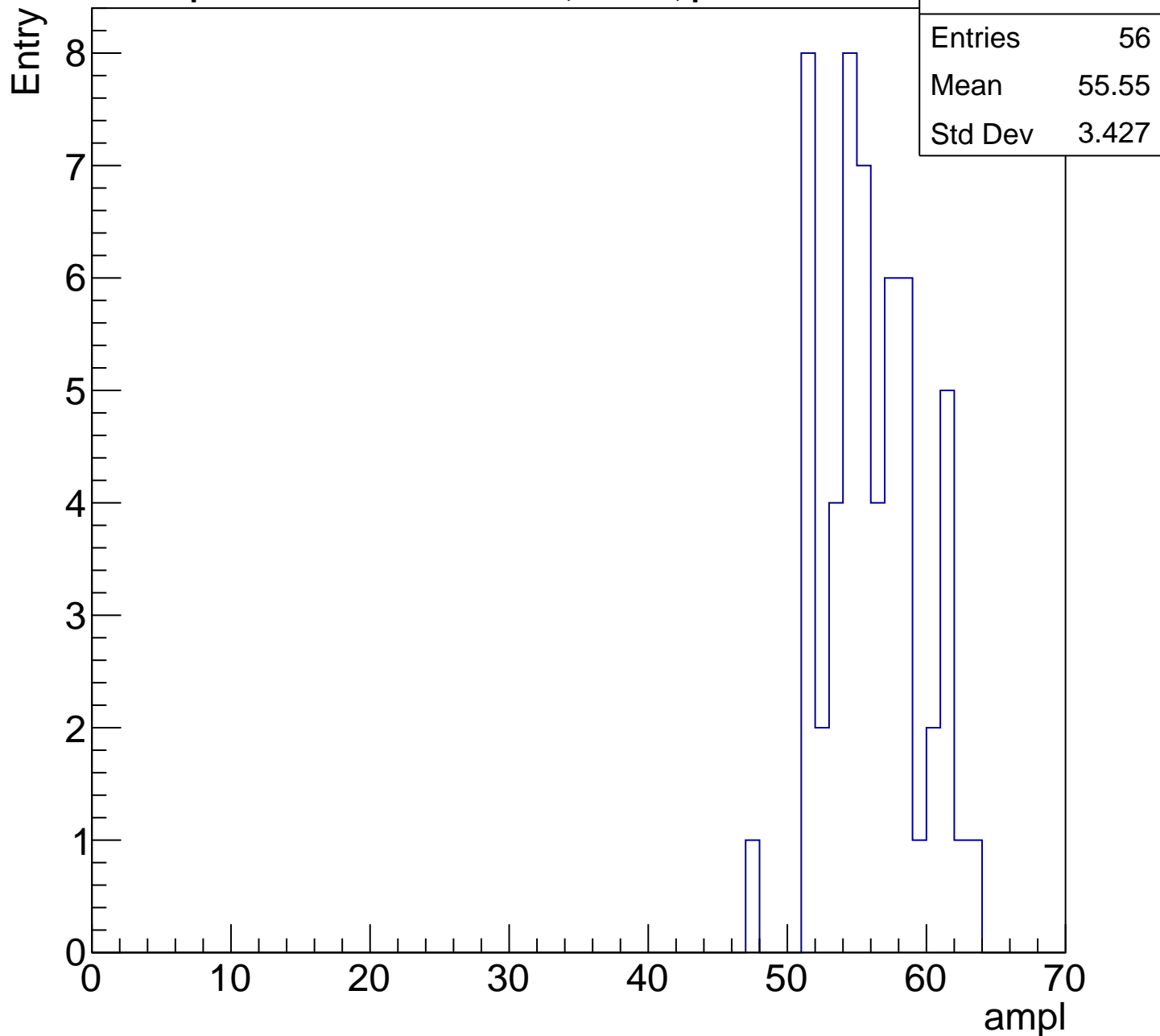
ampl

70



B1L102S, U18-ch95, adc4

calib_packv5_042523_0143.root, FC#11, port A2

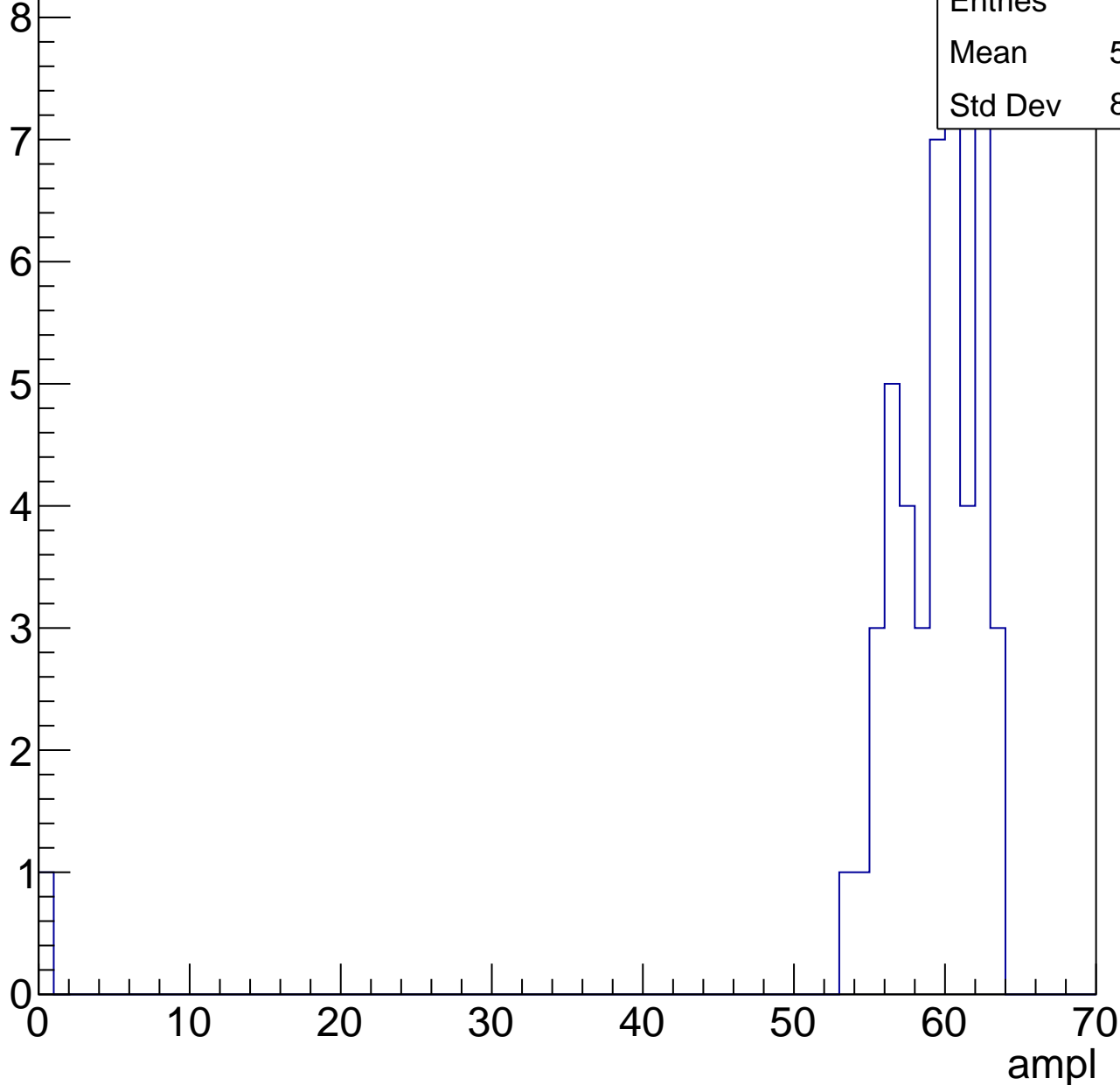


B1L102S, U18-ch95, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	57.83
Std Dev	8.818

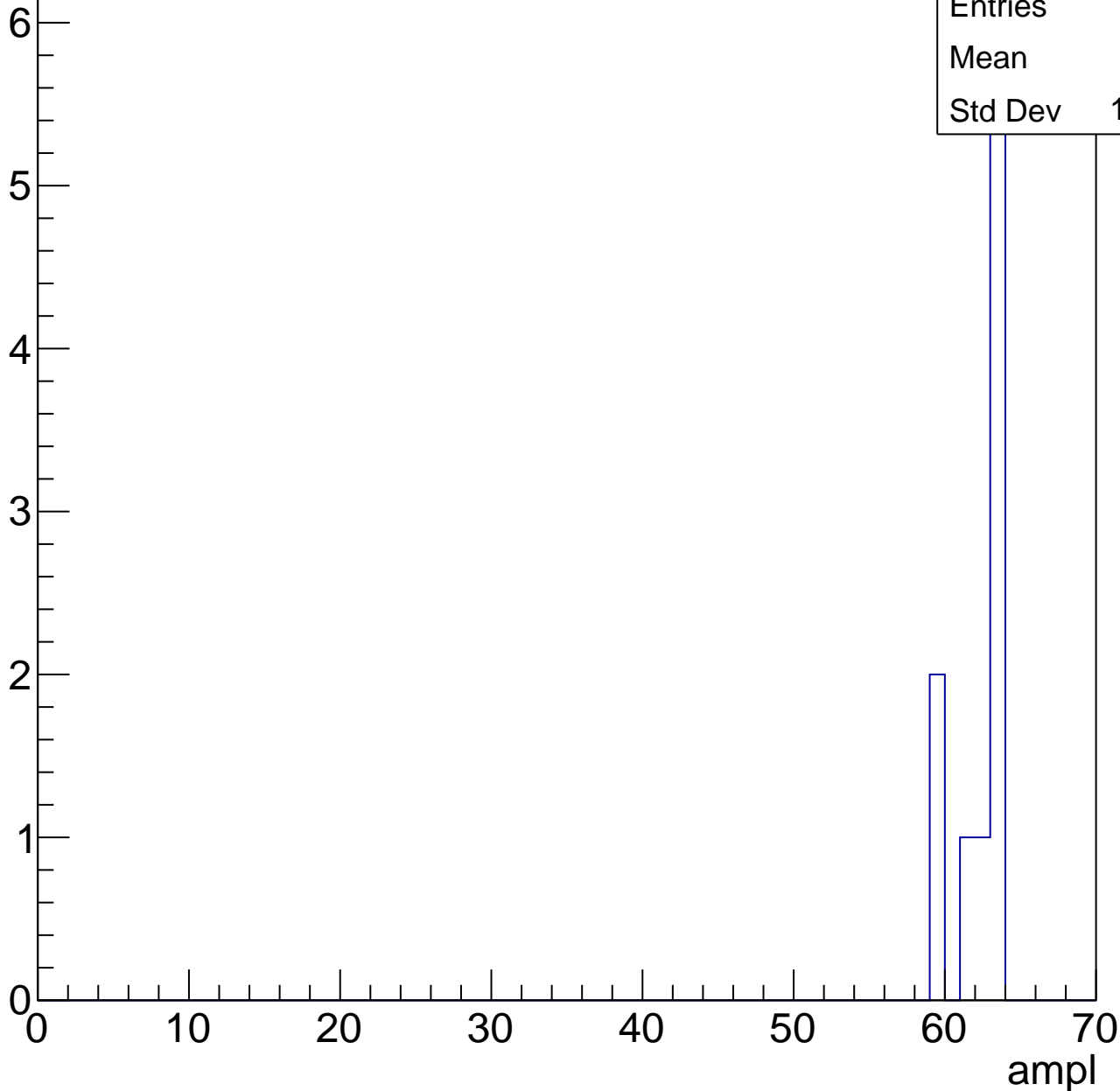


B1L102S, U18-ch95, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	10
Mean	61.9
Std Dev	1.578



B1L102S, U18-ch95, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U18-ch96, adc0

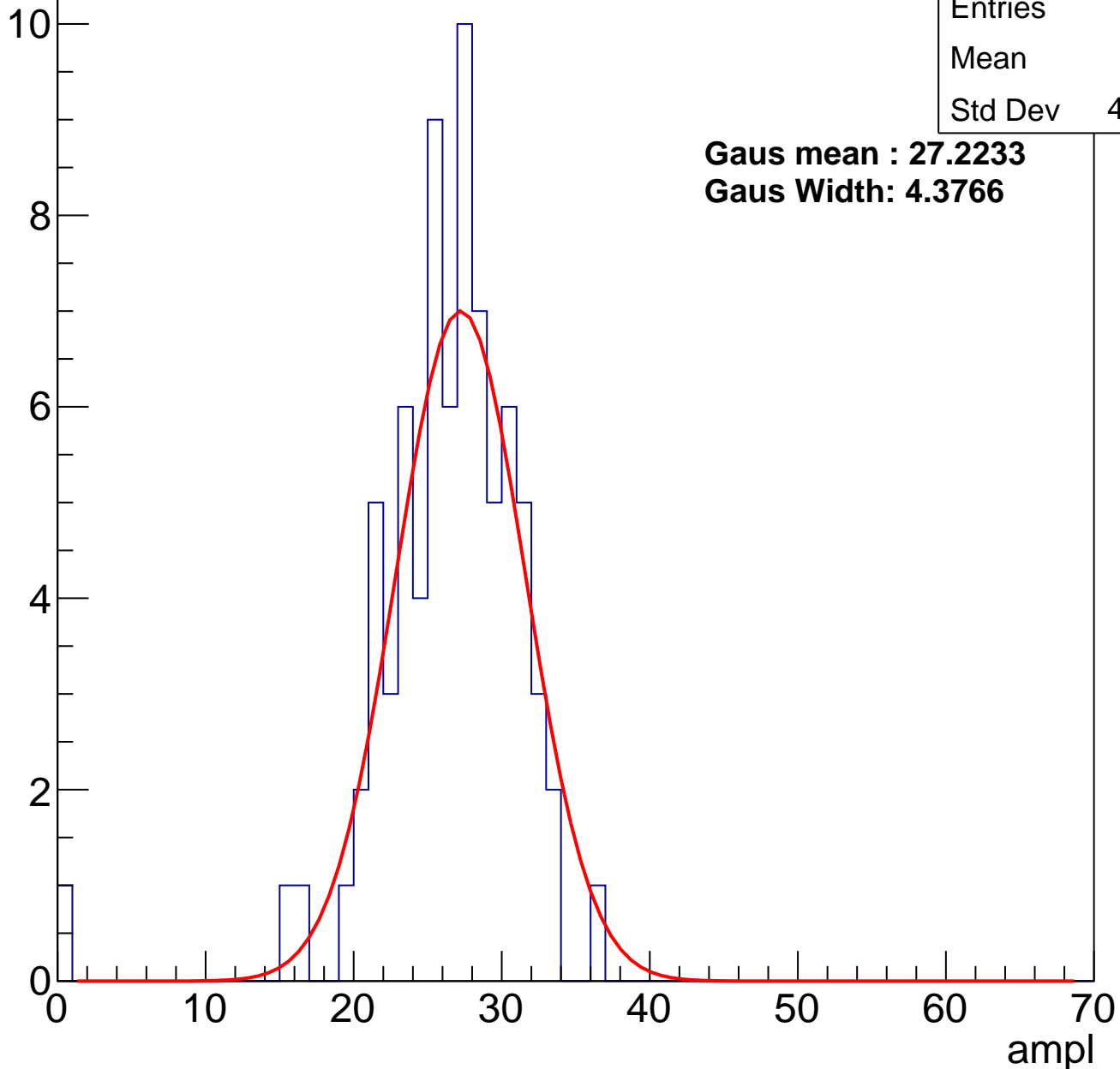
calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	25.9
Std Dev	4.908

Gaus mean : 27.2233

Gaus Width: 4.3766

Entry



B1L102S, U18-ch96, adc1

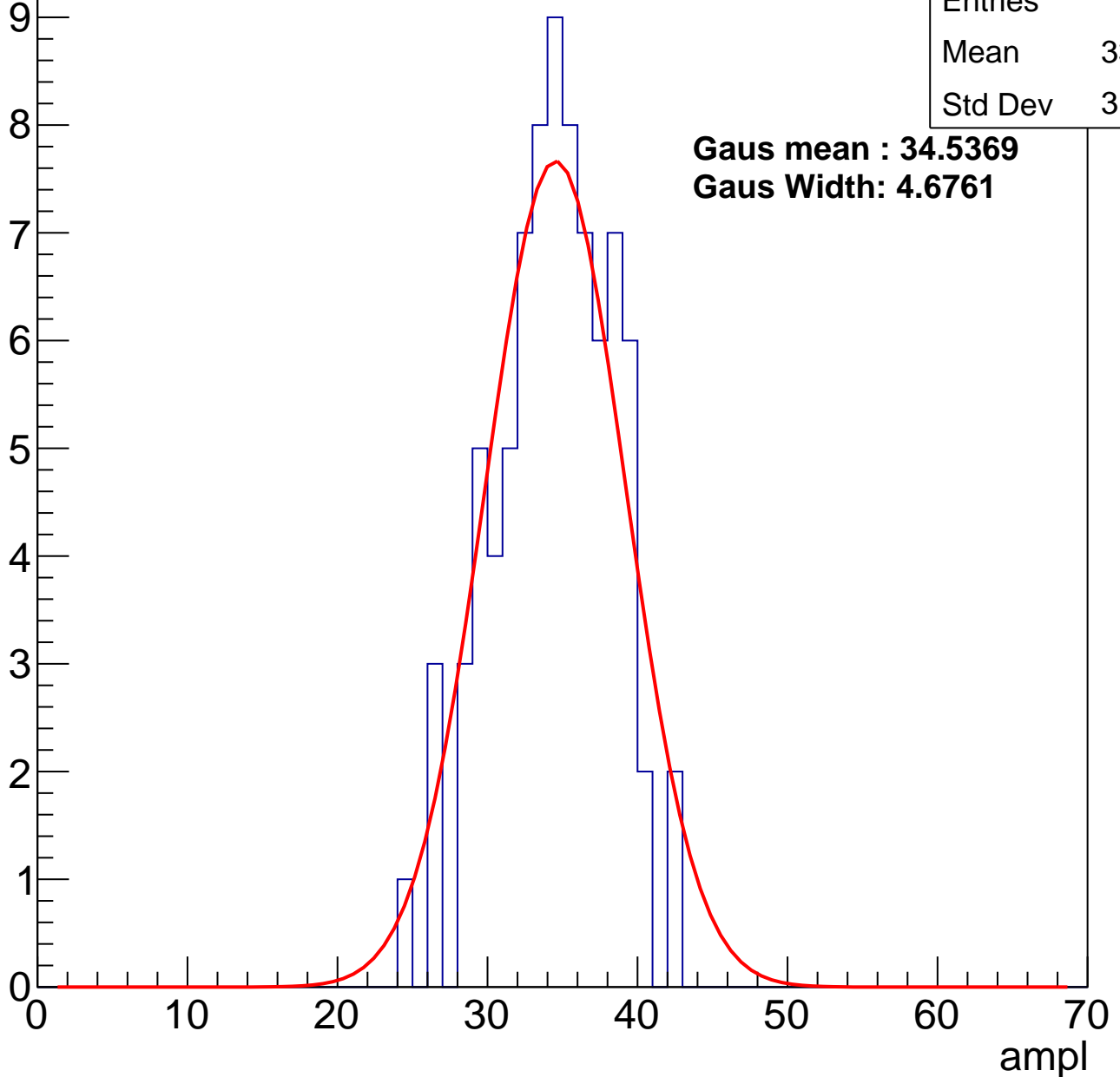
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	33.95
Std Dev	3.837

Gaus mean : 34.5369

Gaus Width: 4.6761



B1L102S, U18-ch96, adc2

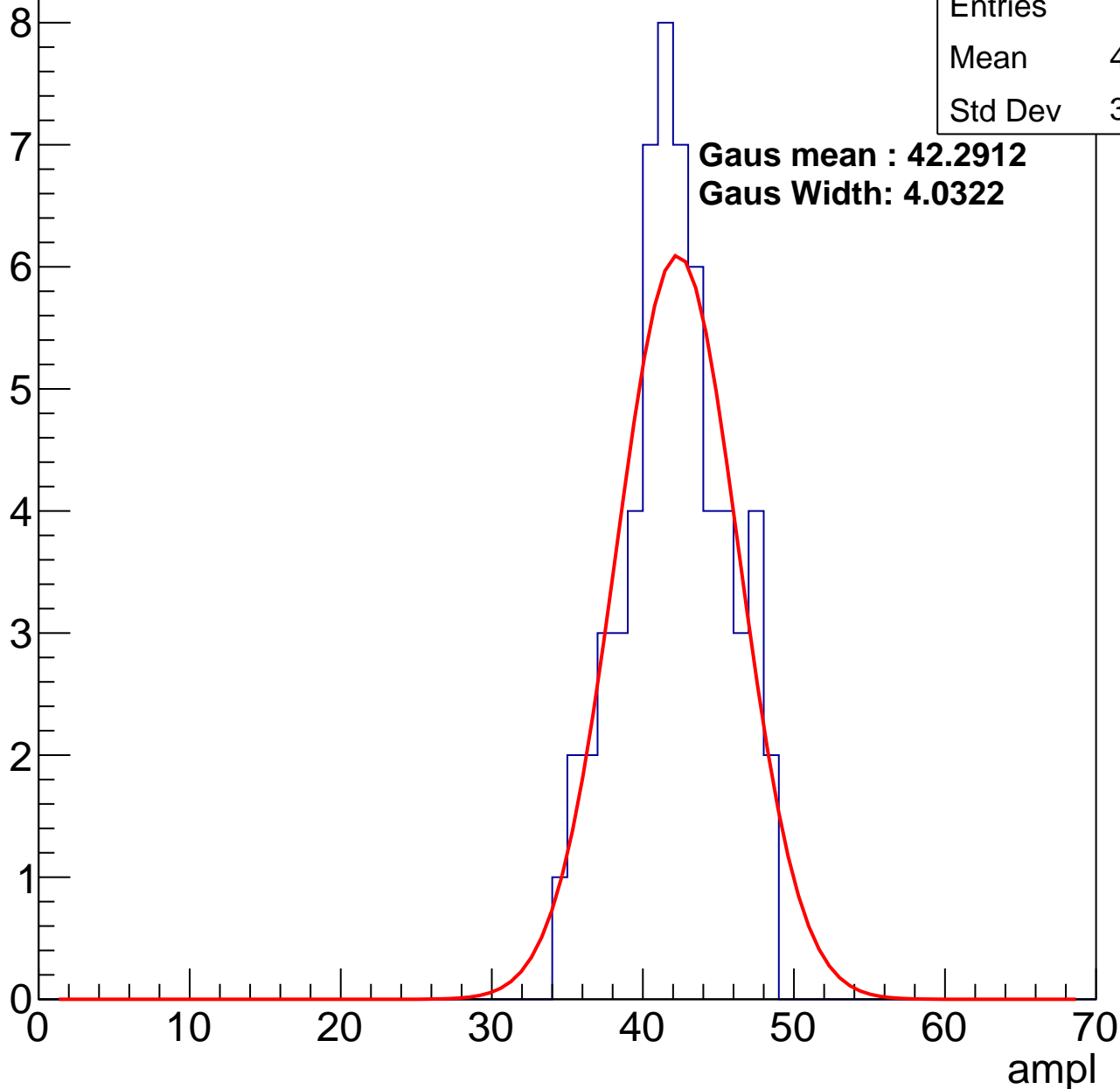
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.58
Std Dev	3.407

Gaus mean : 42.2912

Gaus Width: 4.0322

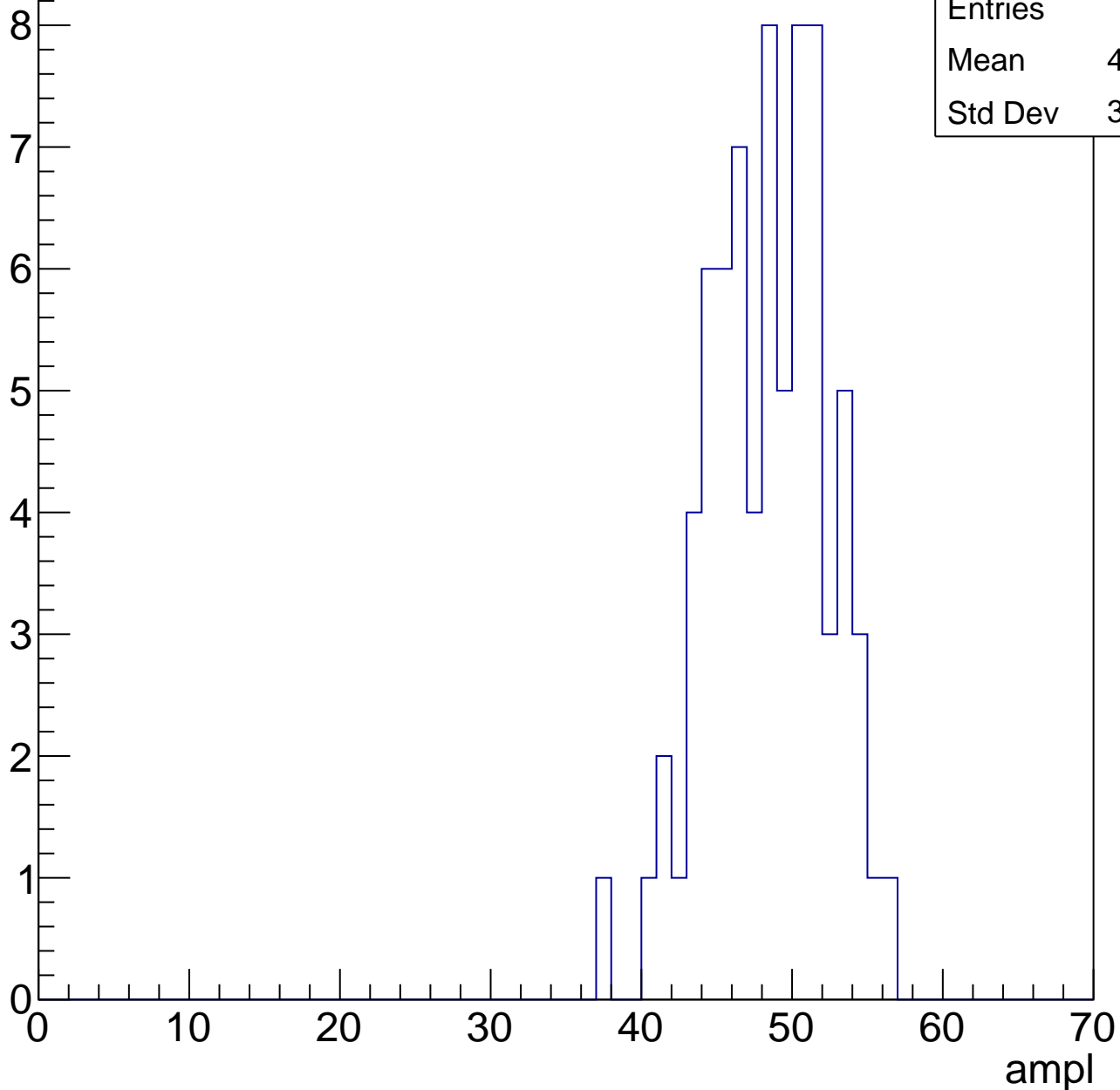


B1L102S, U18-ch96, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	47.95
Std Dev	3.862

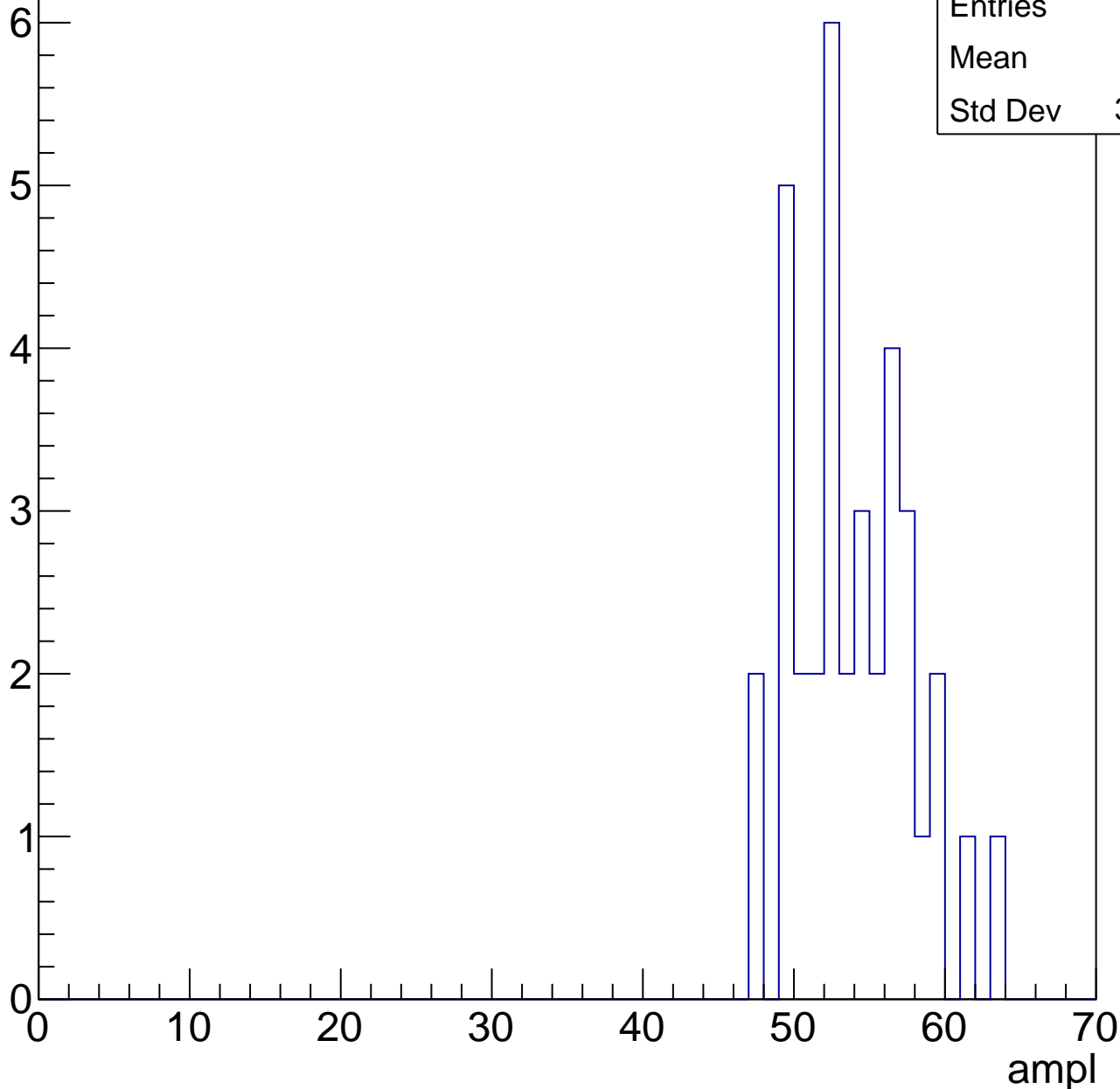


B1L102S, U18-ch96, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	36
Mean	53.5
Std Dev	3.841

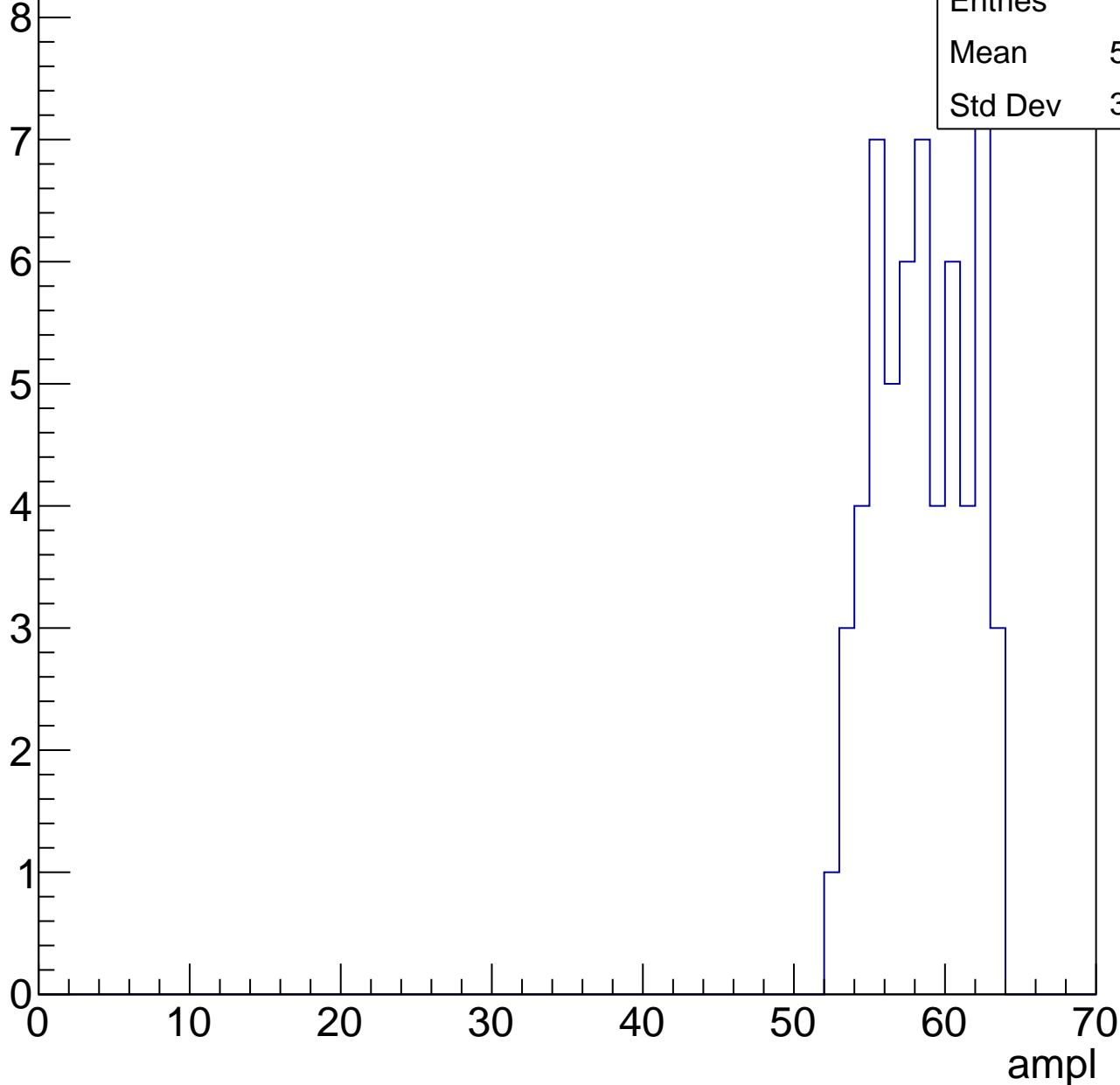


B1L102S, U18-ch96, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

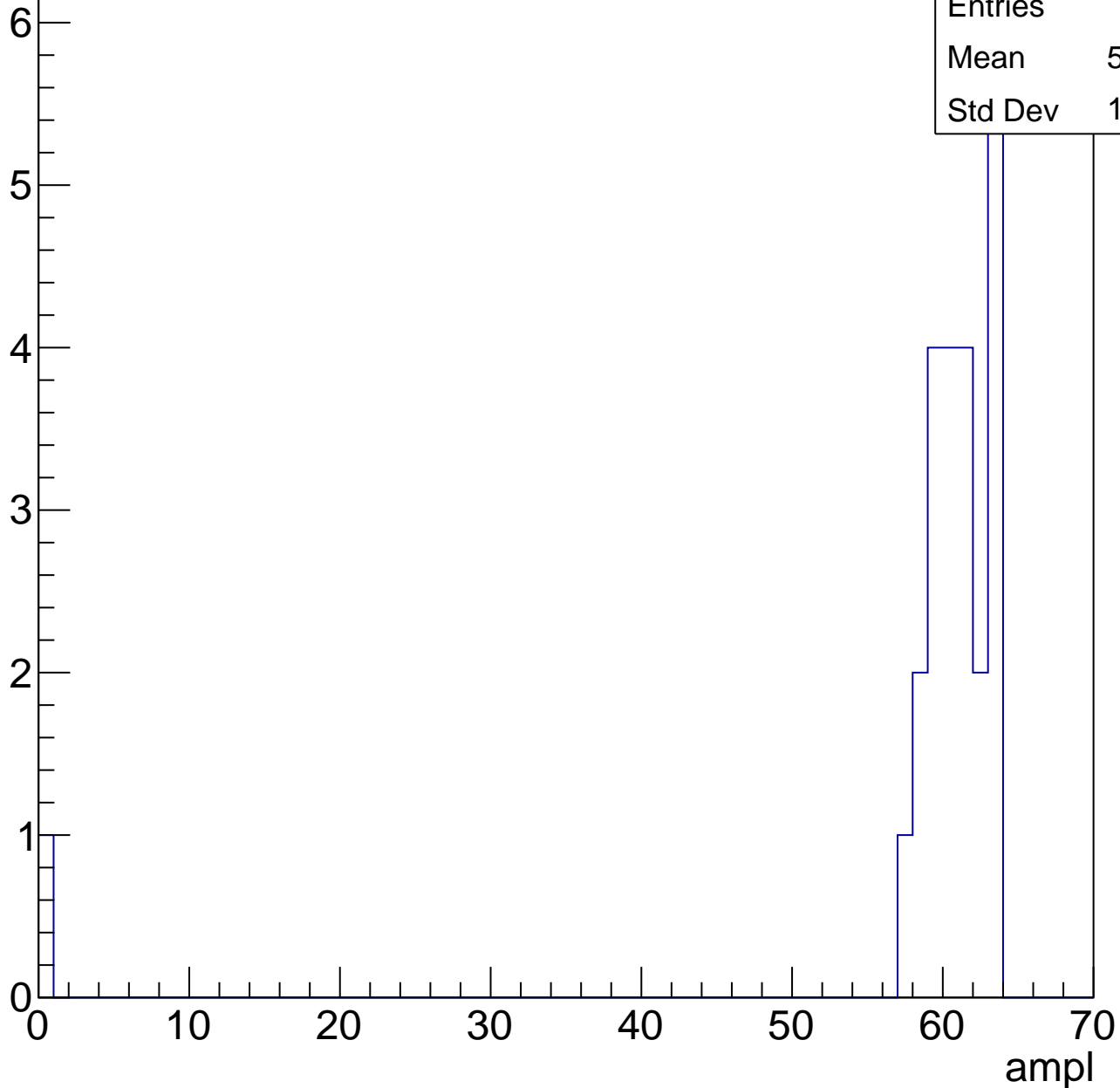
Entries	58
Mean	58.02
Std Dev	3.026



B1L102S, U18-ch96, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch96, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch97, adc0

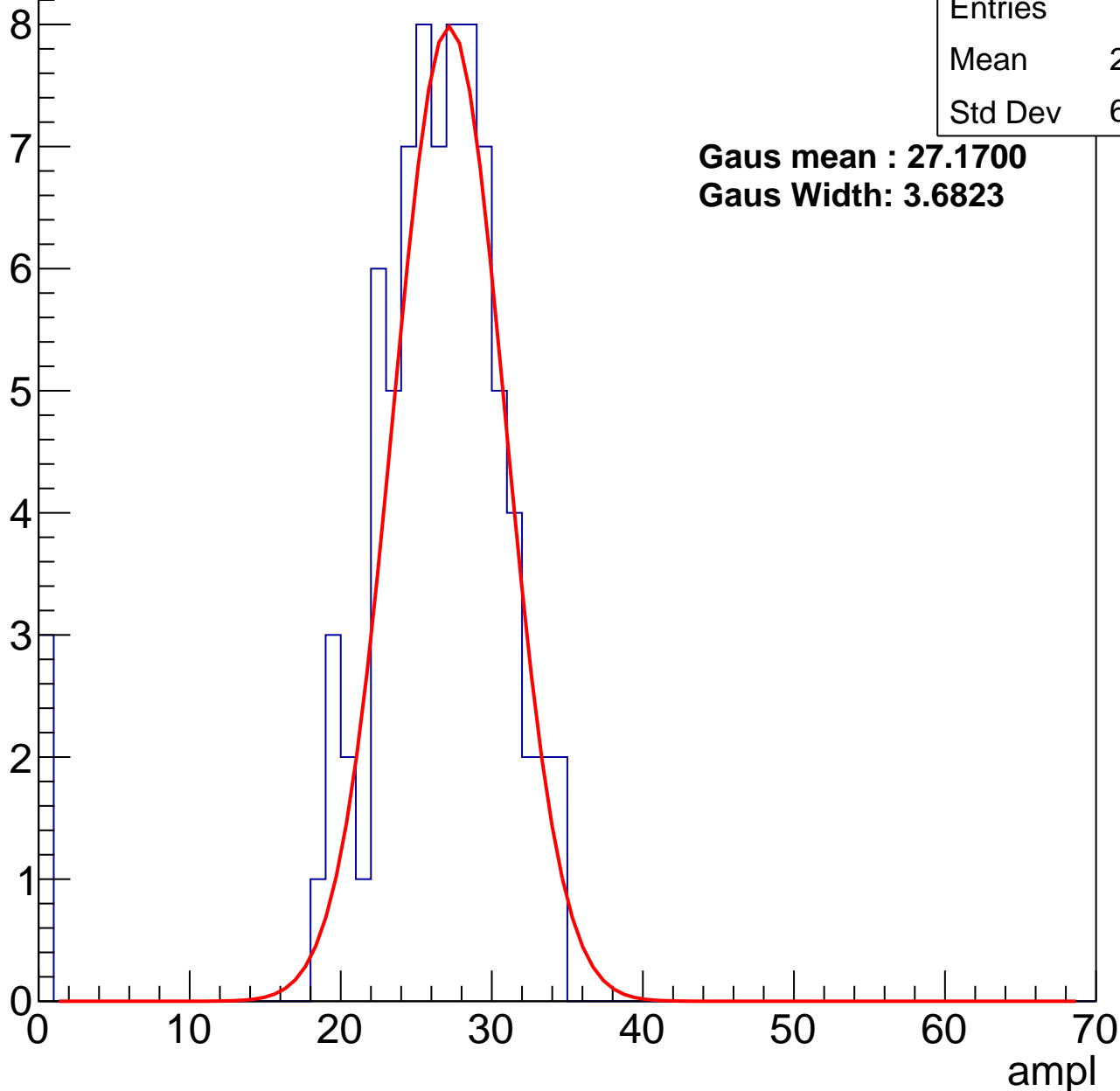
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	25.28
Std Dev	6.139

Gaus mean : 27.1700

Gaus Width: 3.6823



B1L102S, U18-ch97, adc1

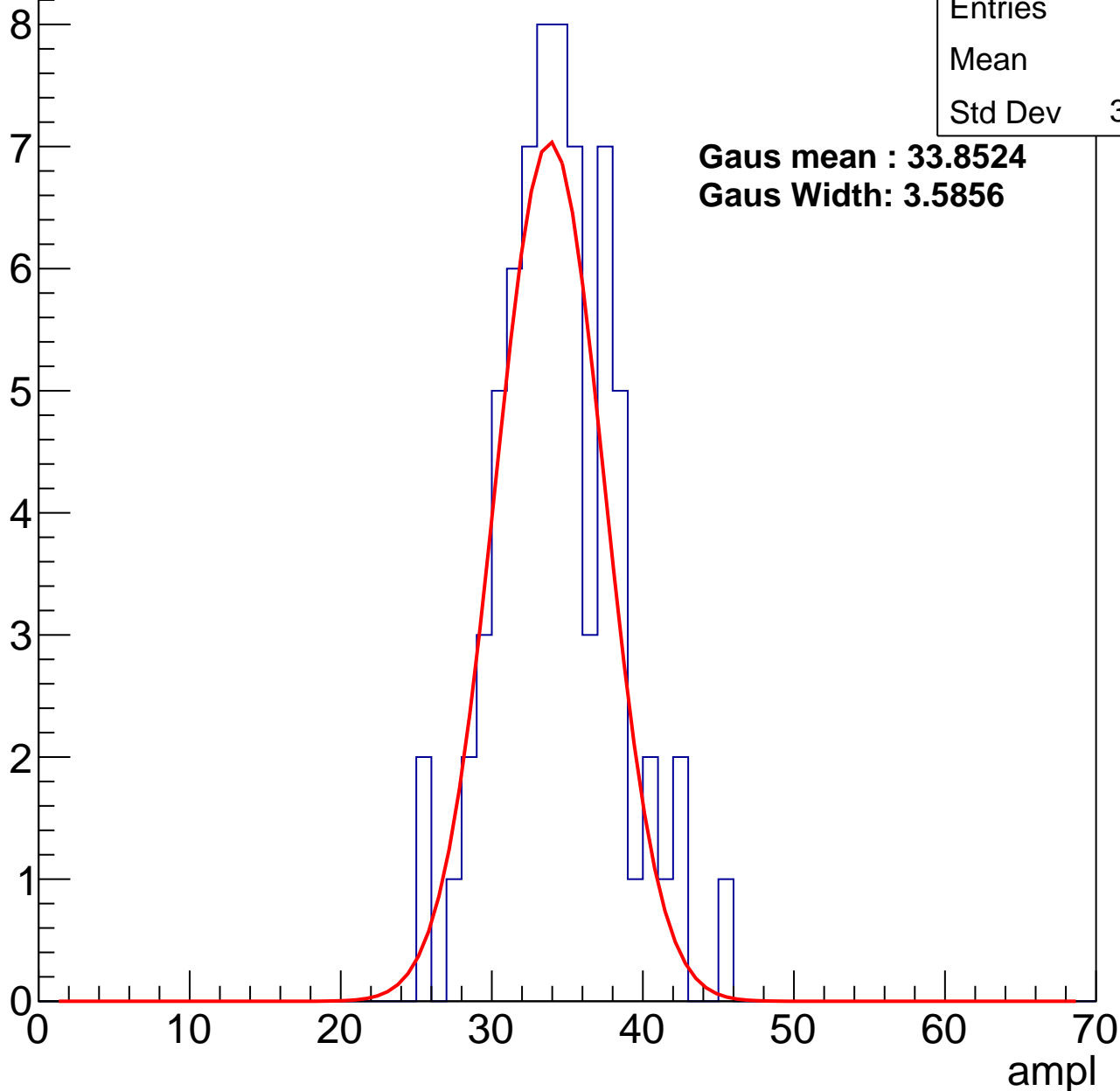
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	33.9
Std Dev	3.908

Gaus mean : 33.8524

Gaus Width: 3.5856



B1L102S, U18-ch97, adc2

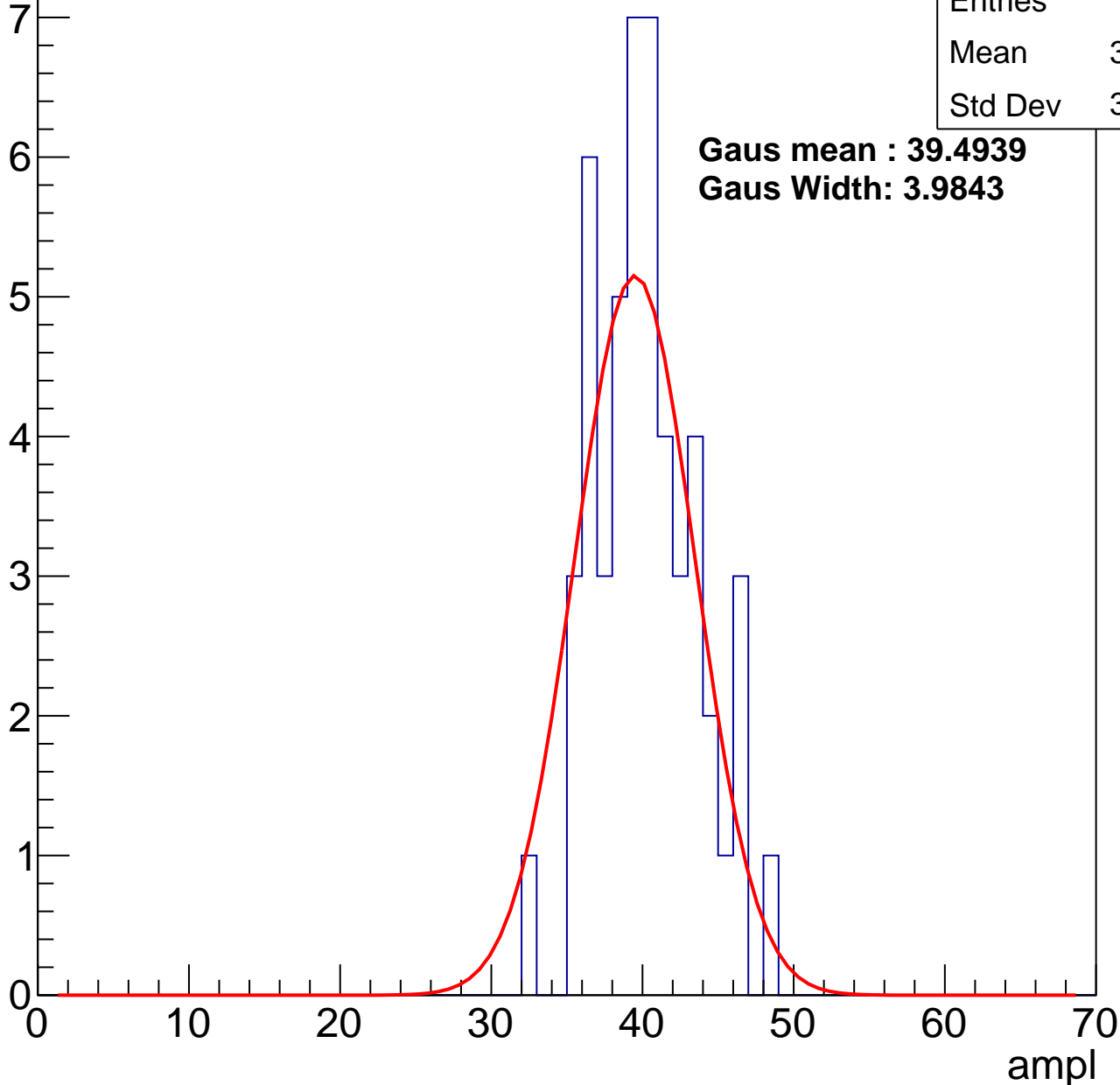
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	39.76
Std Dev	3.368

Gaus mean : 39.4939

Gaus Width: 3.9843

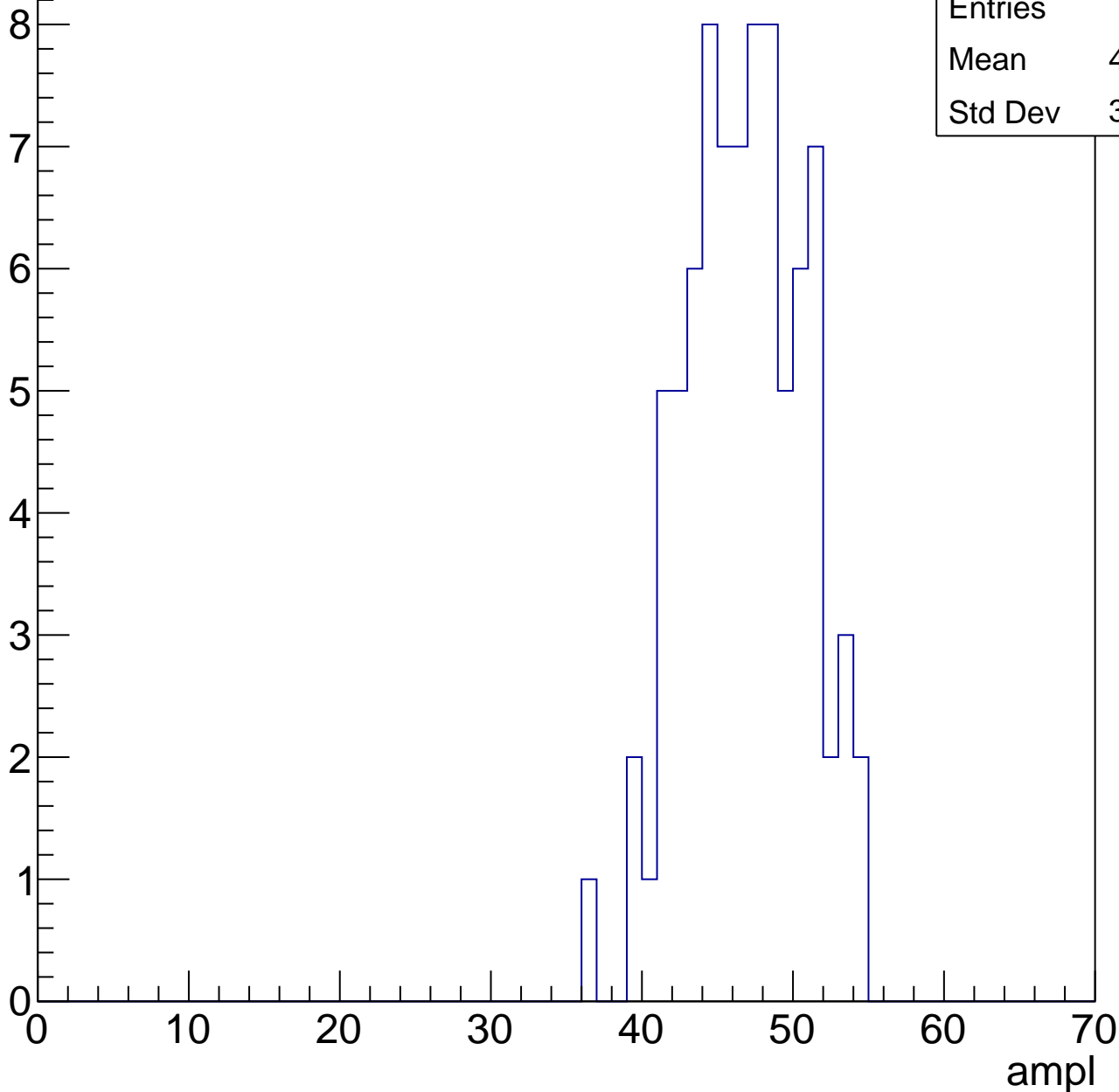


B1L102S, U18-ch97, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	46.37
Std Dev	3.839

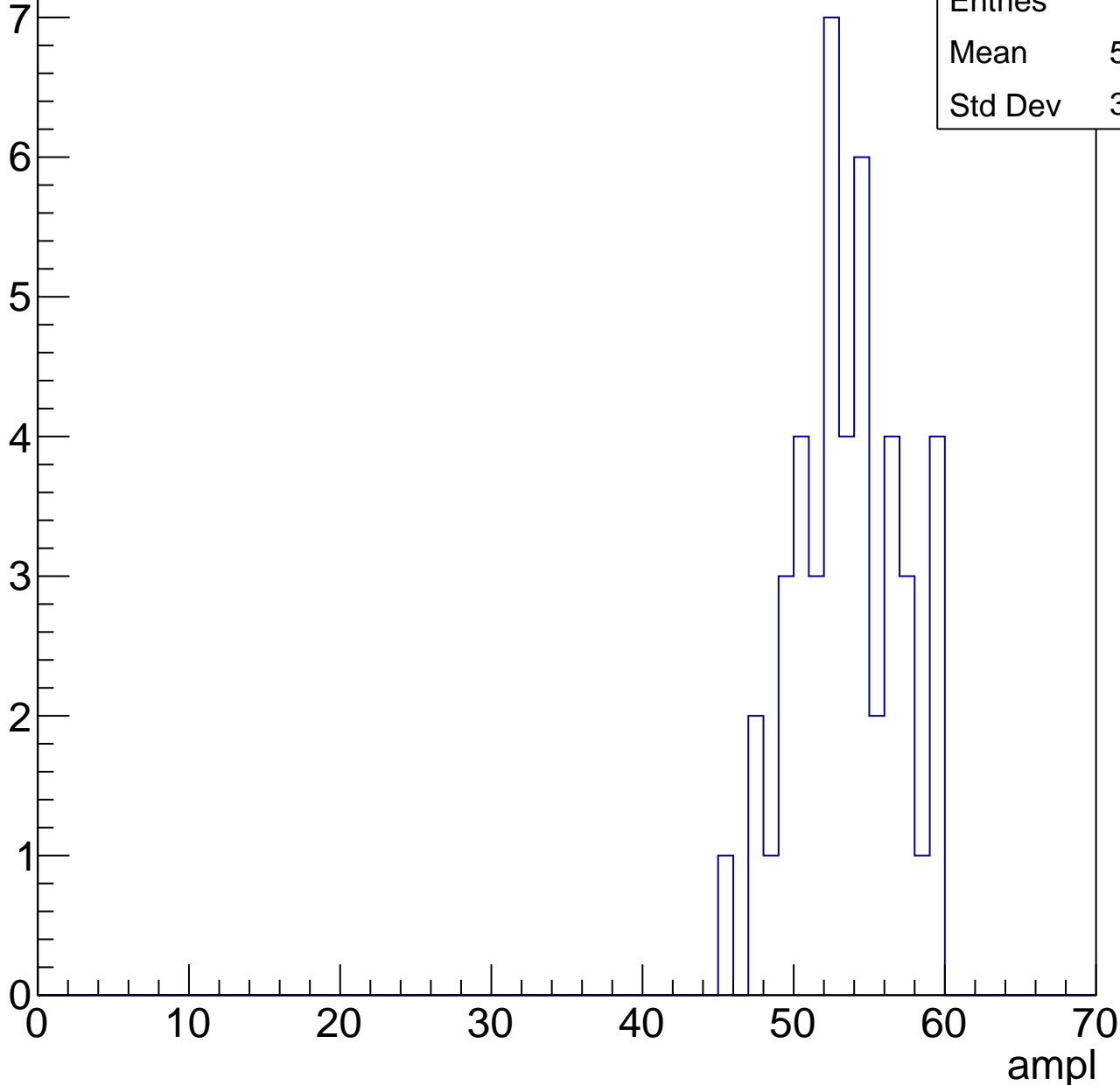


B1L102S, U18-ch97, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	53.02
Std Dev	3.435

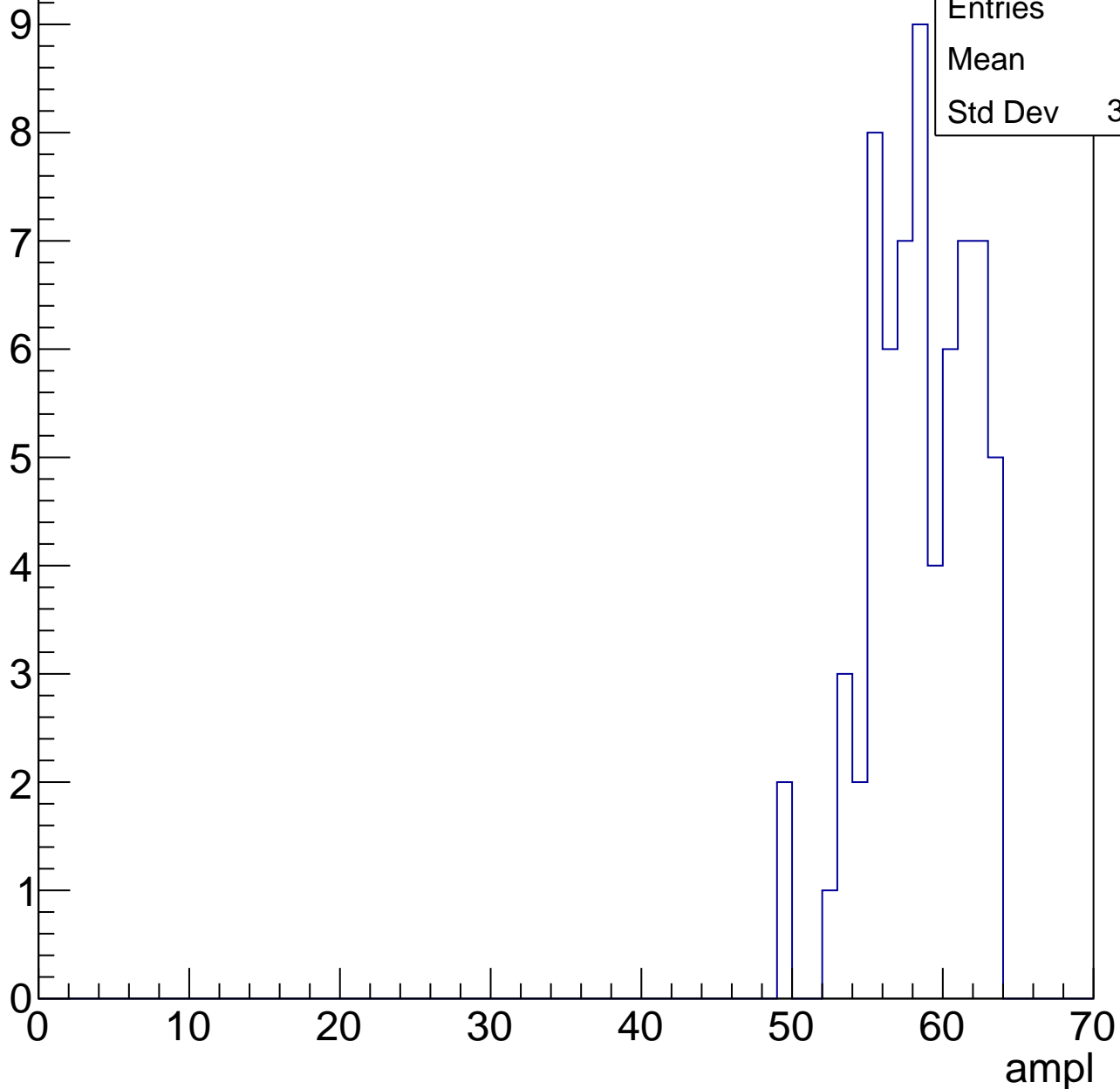


B1L102S, U18-ch97, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

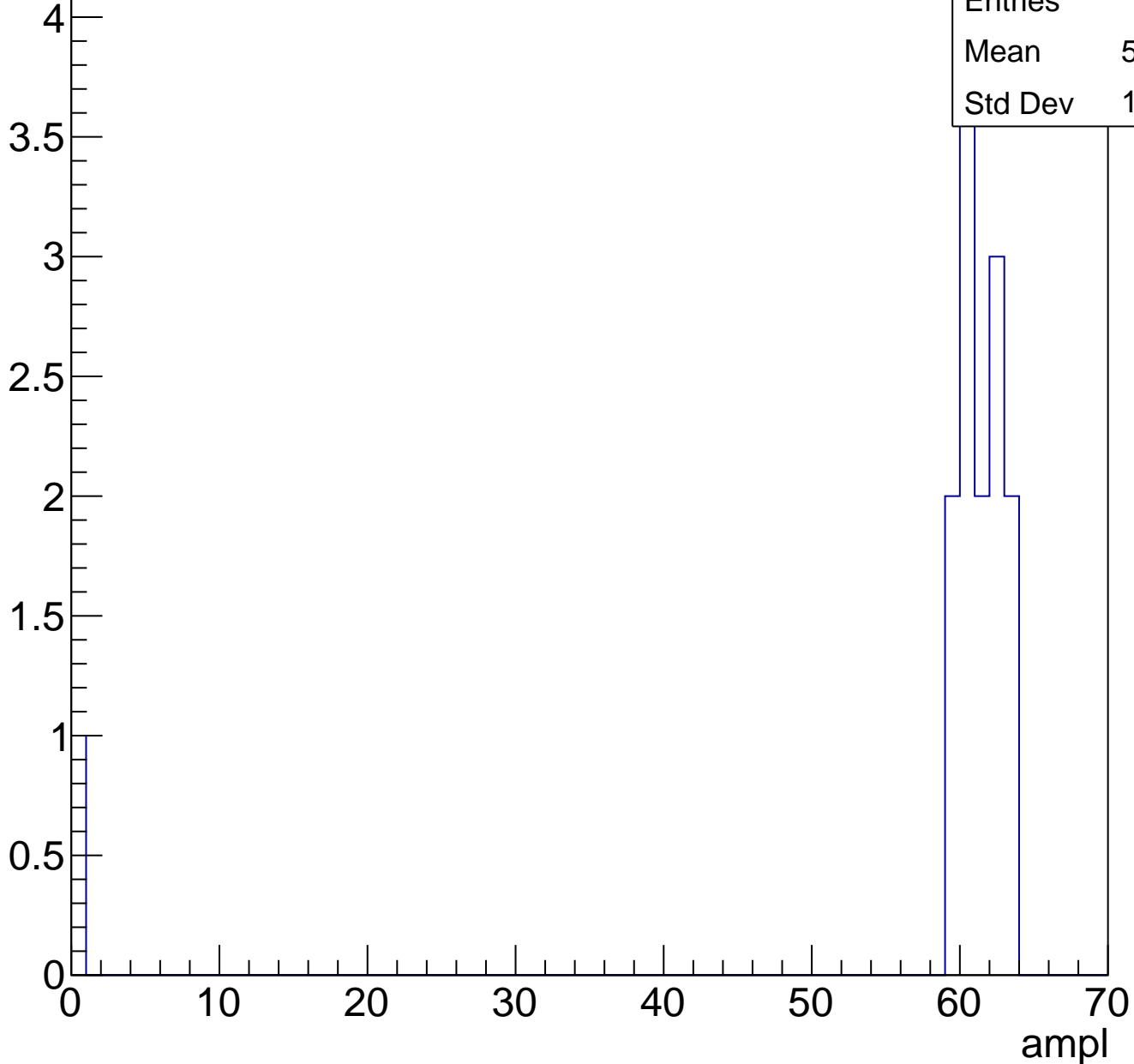
Entries	67
Mean	58
Std Dev	3.314



B1L102S, U18-ch97, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

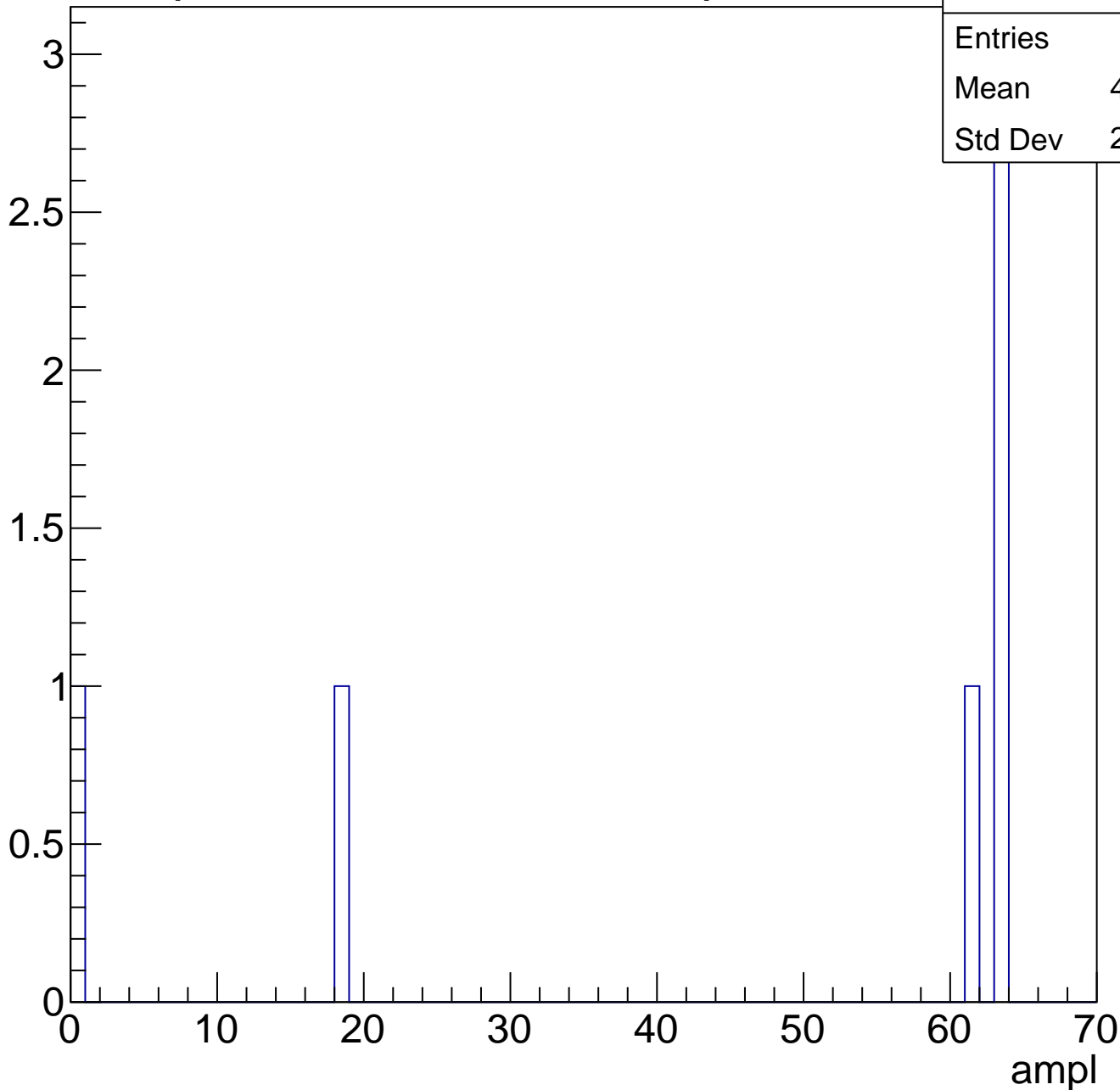


Entries	14
Mean	56.57
Std Dev	15.74

B1L102S, U18-ch97, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	44.67
Std Dev	25.76

B1L102S, U18-ch98, adc0

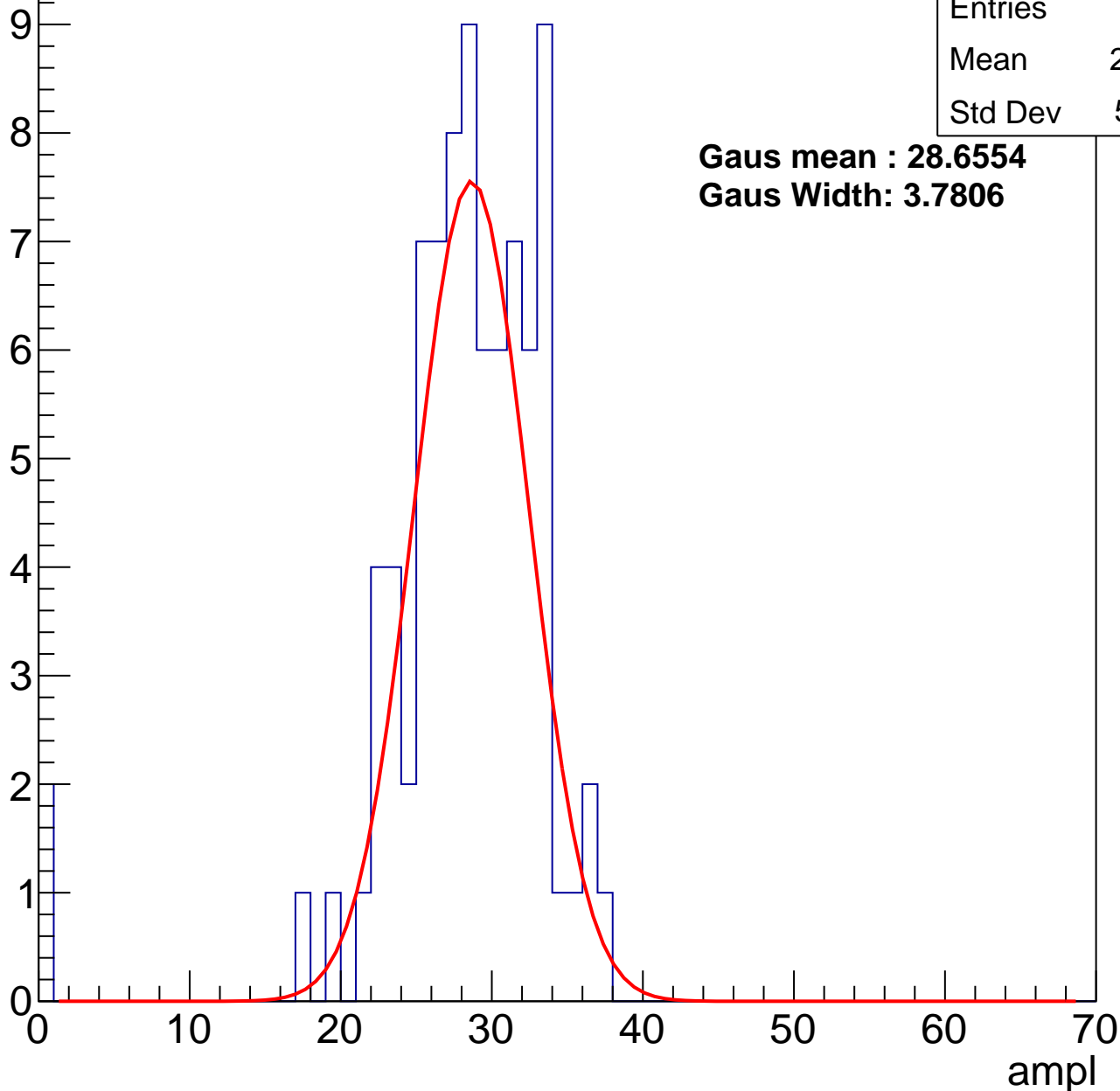
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	85
Mean	27.62
Std Dev	5.831

Gaus mean : 28.6554

Gaus Width: 3.7806



B1L102S, U18-ch98, adc1

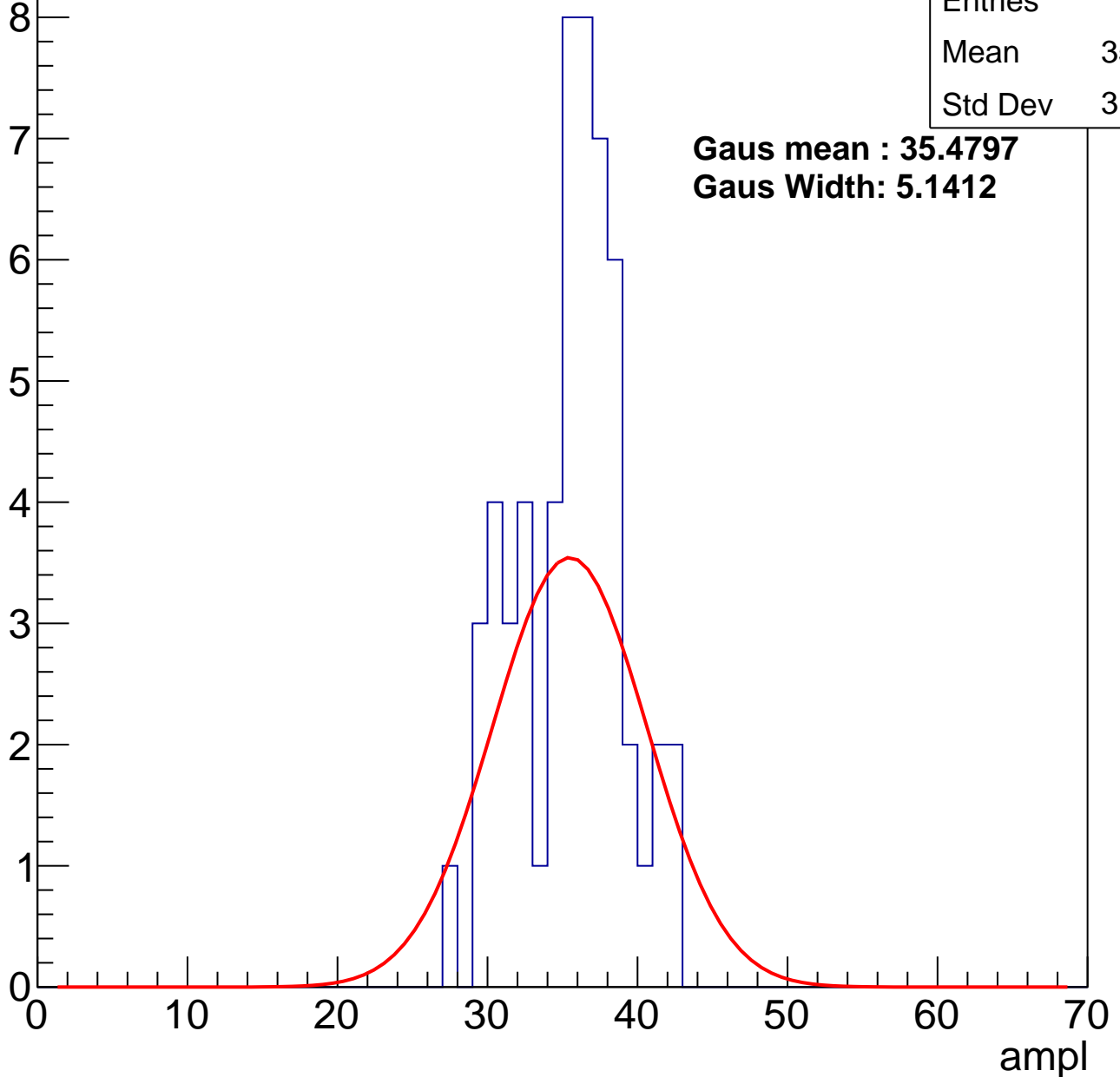
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	35.05
Std Dev	3.487

Gaus mean : 35.4797

Gaus Width: 5.1412



B1L102S, U18-ch98, adc2

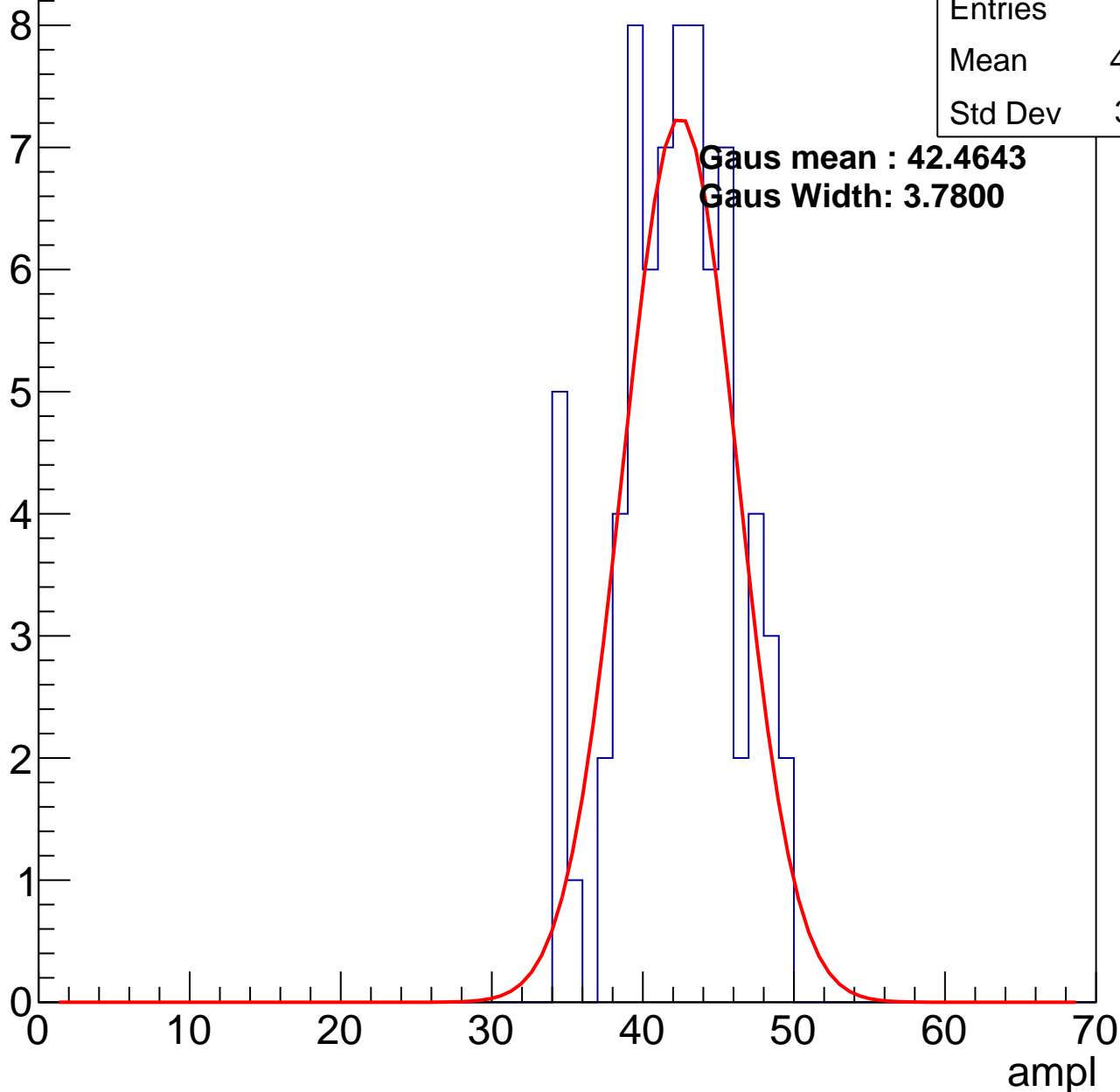
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	41.79
Std Dev	3.731

Gaus mean : 42.4643

Gaus Width: 3.7800

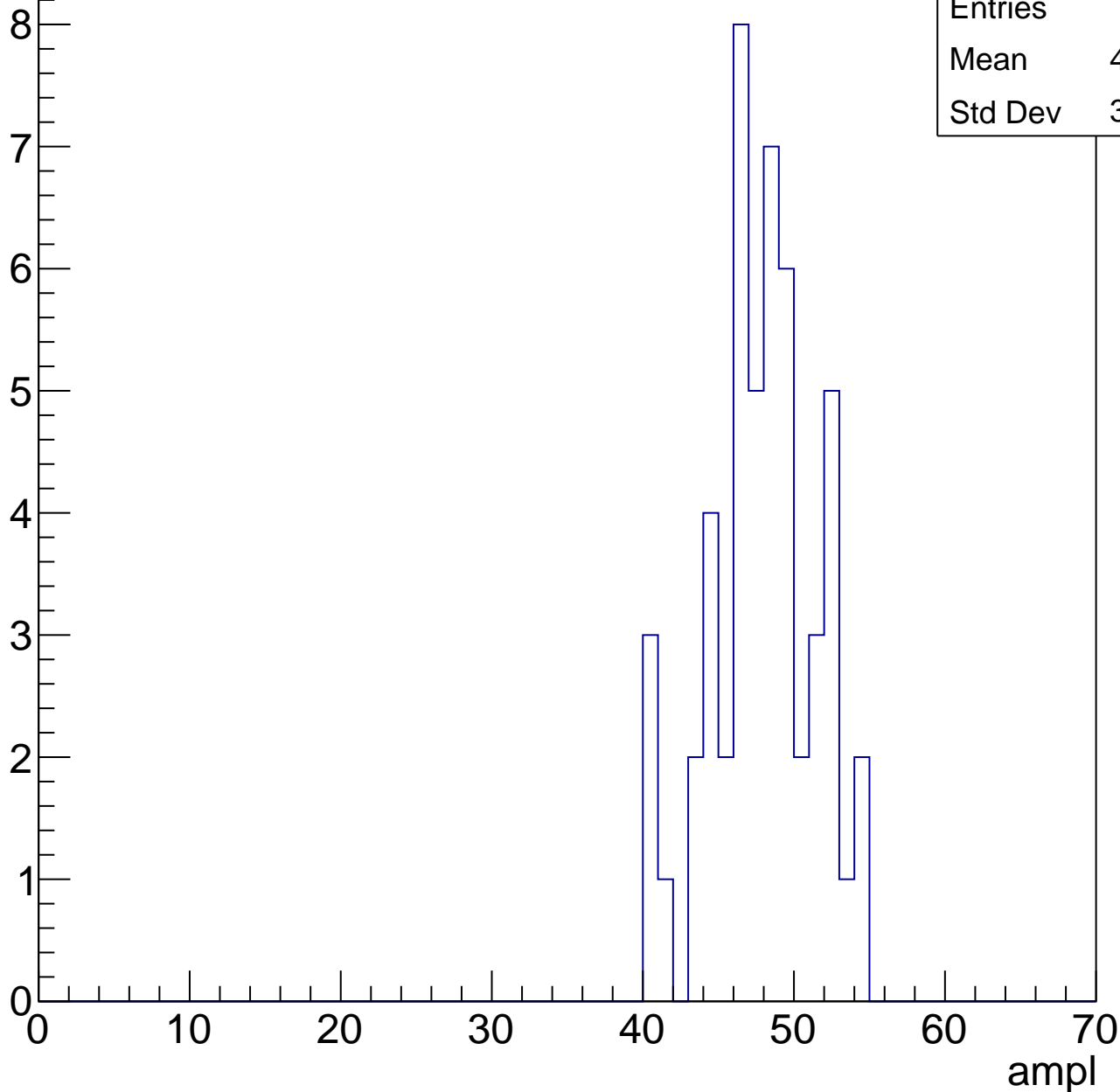


B1L102S, U18-ch98, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	47.45
Std Dev	3.466

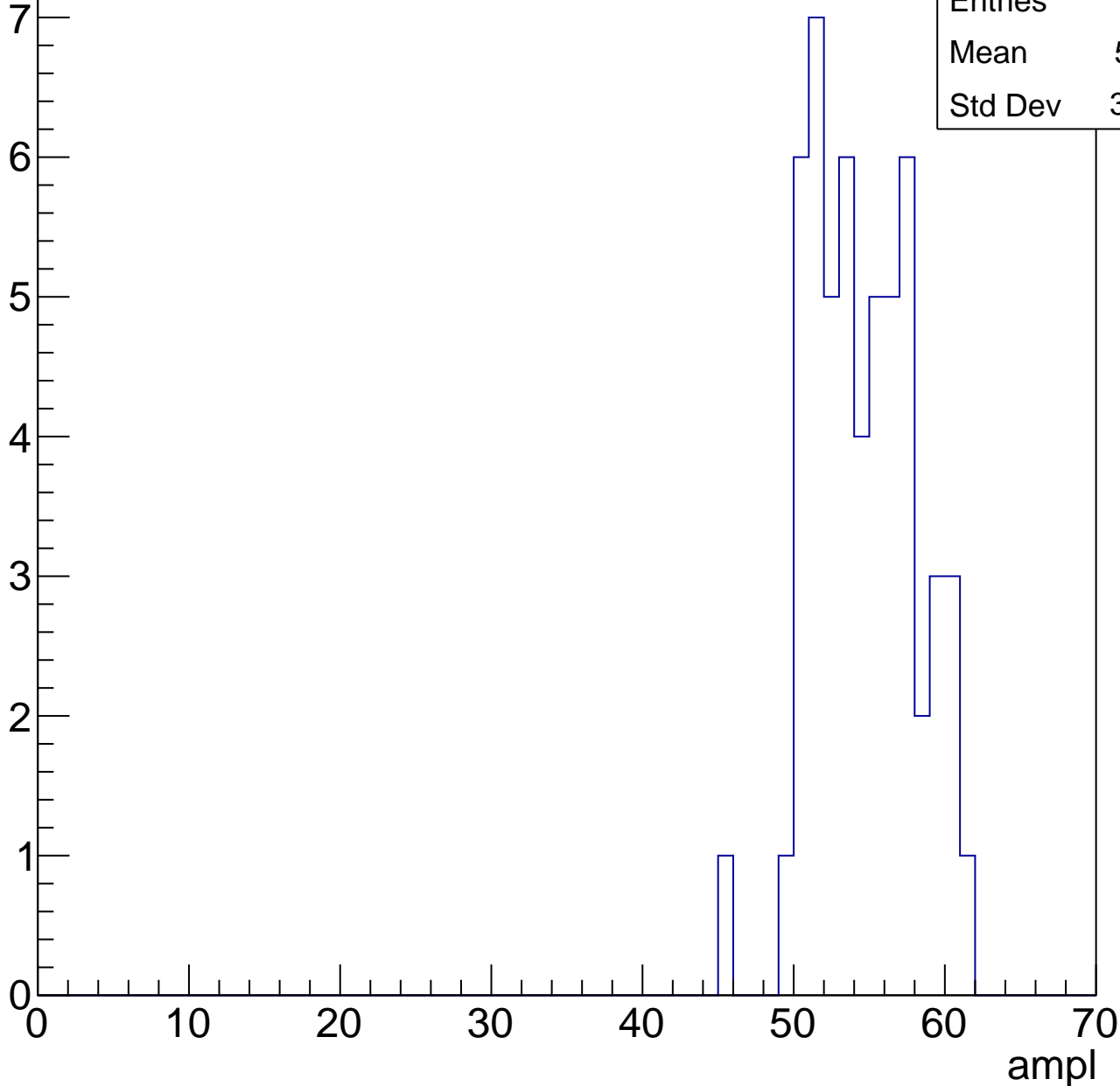


B1L102S, U18-ch98, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	54.11
Std Dev	3.393

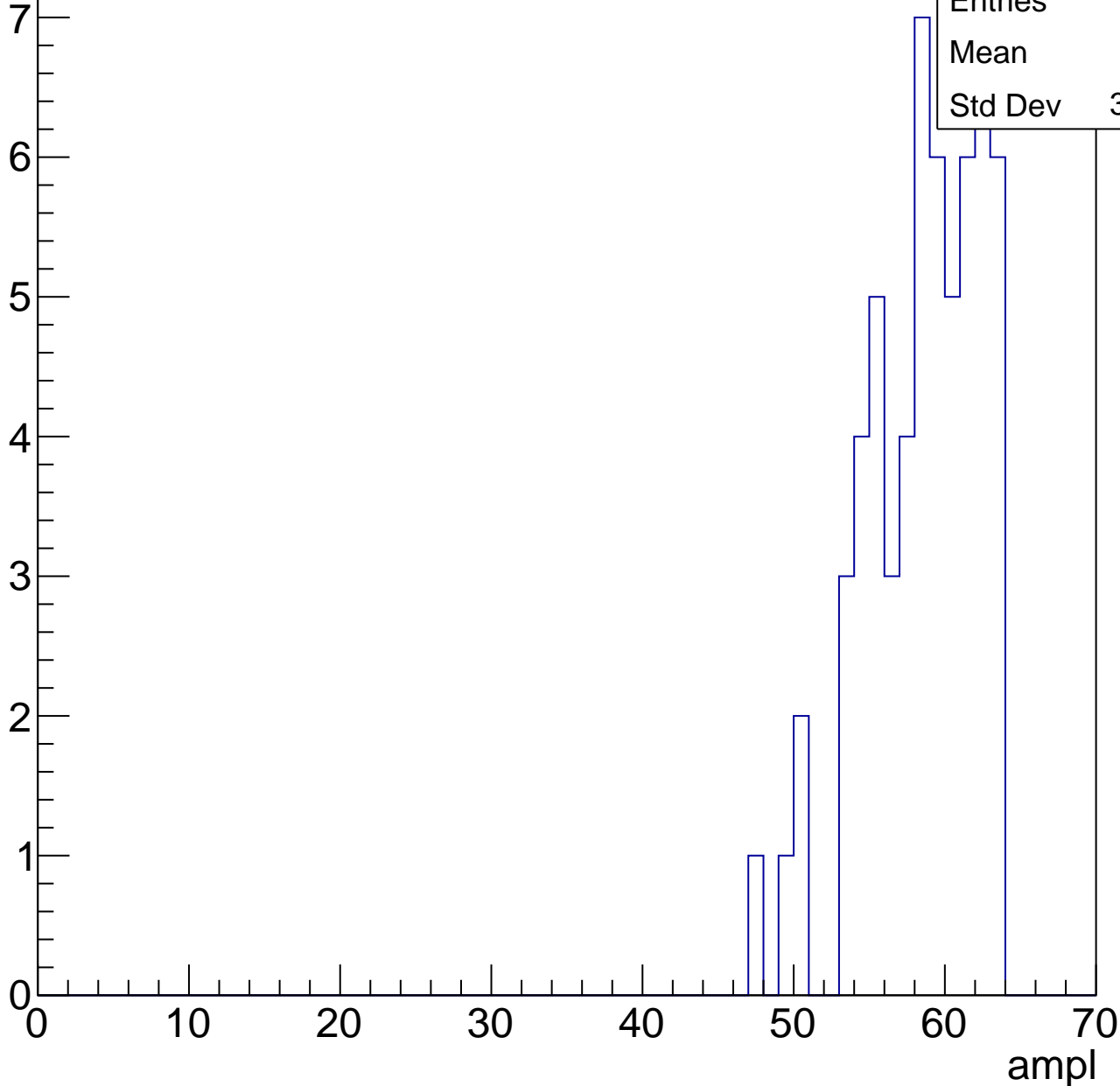


B1L102S, U18-ch98, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

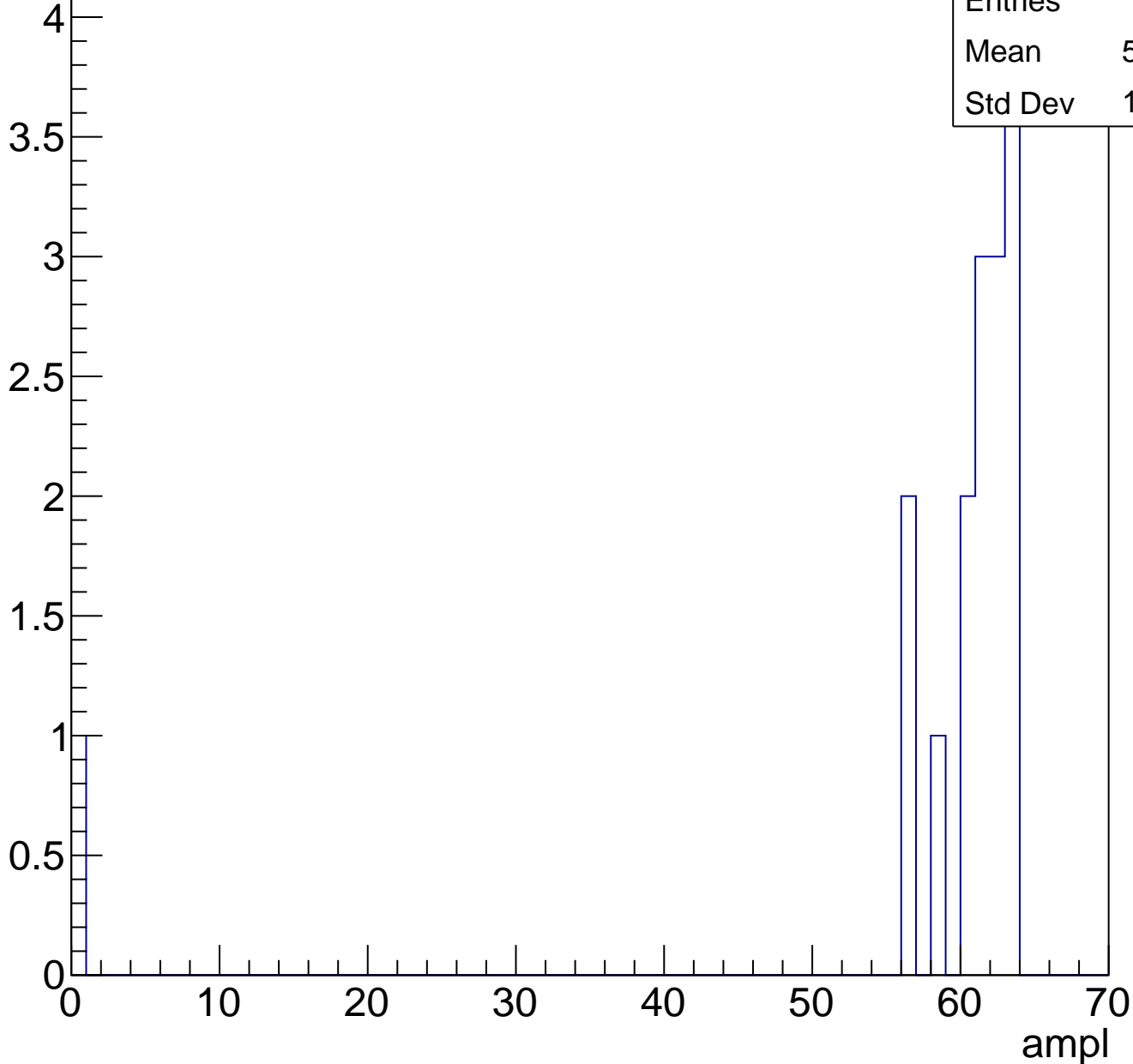
Entries	60
Mean	58
Std Dev	3.812



B1L102S, U18-ch98, adc6

calib_packv5_042523_0143.root, FC#11, port A2

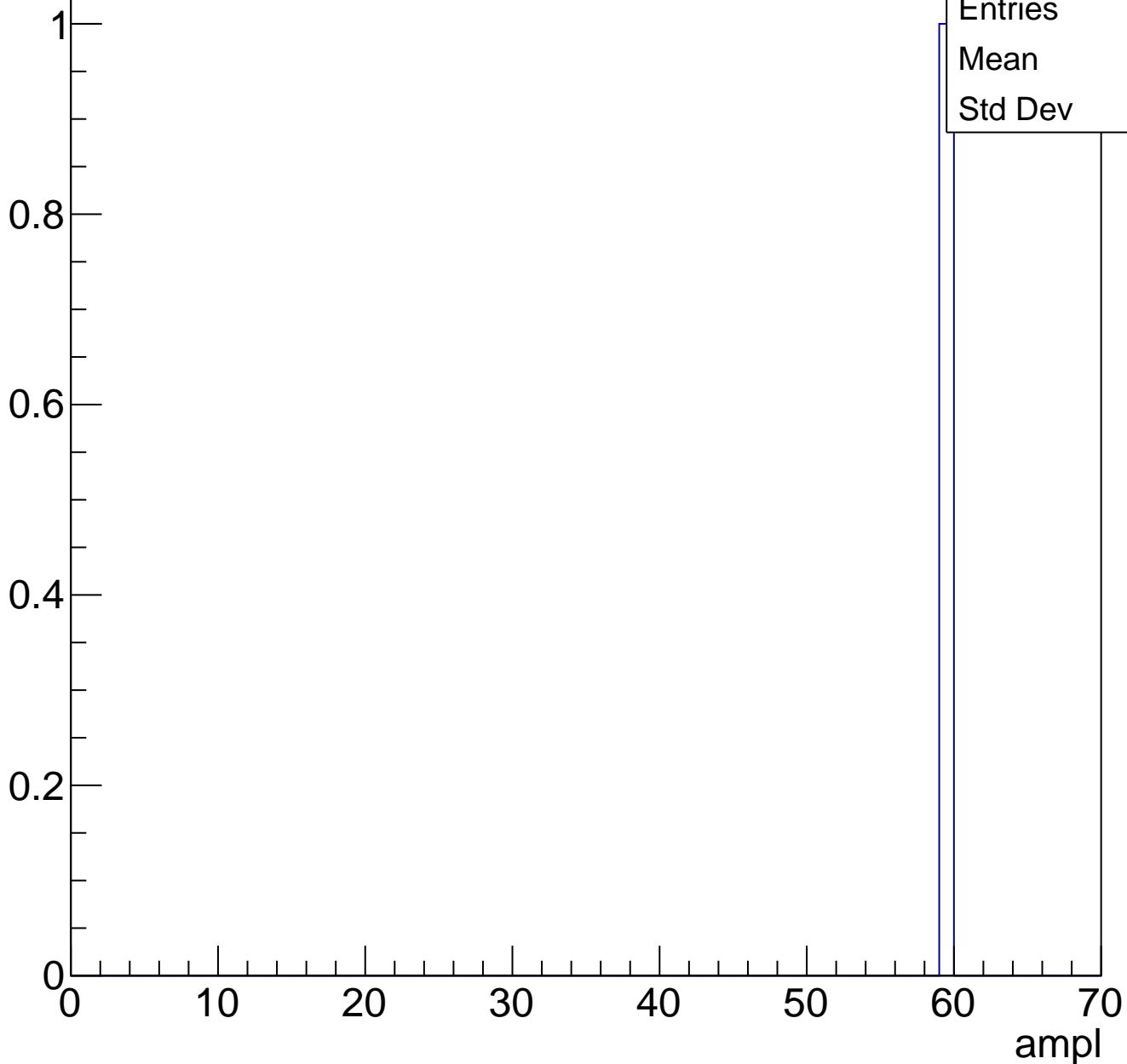
Entry



B1L102S, U18-ch98, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	59
Std Dev	0

B1L102S, U18-ch99, adc0

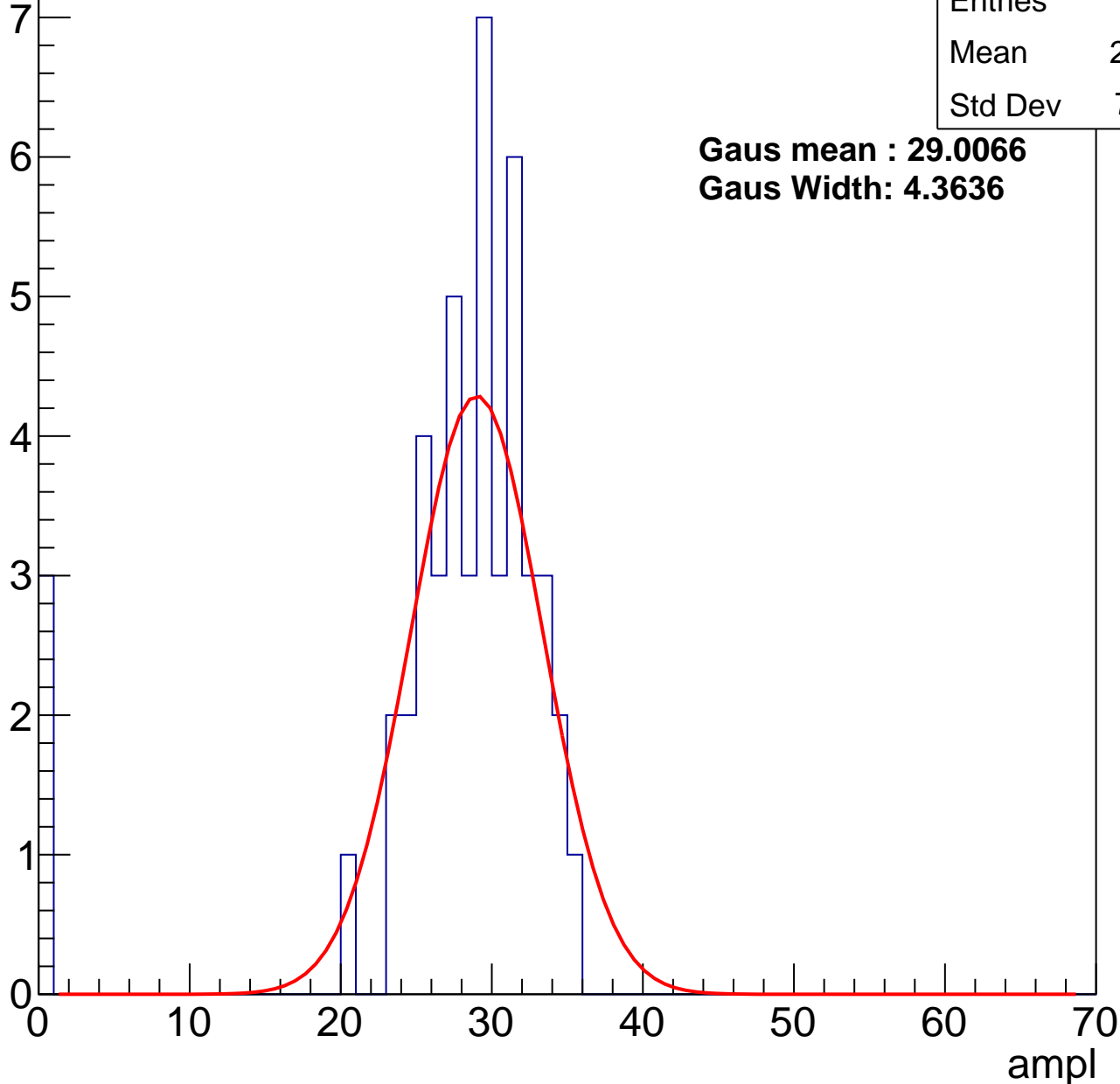
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	26.83
Std Dev	7.641

Gaus mean : 29.0066

Gaus Width: 4.3636



B1L102S, U18-ch99, adc1

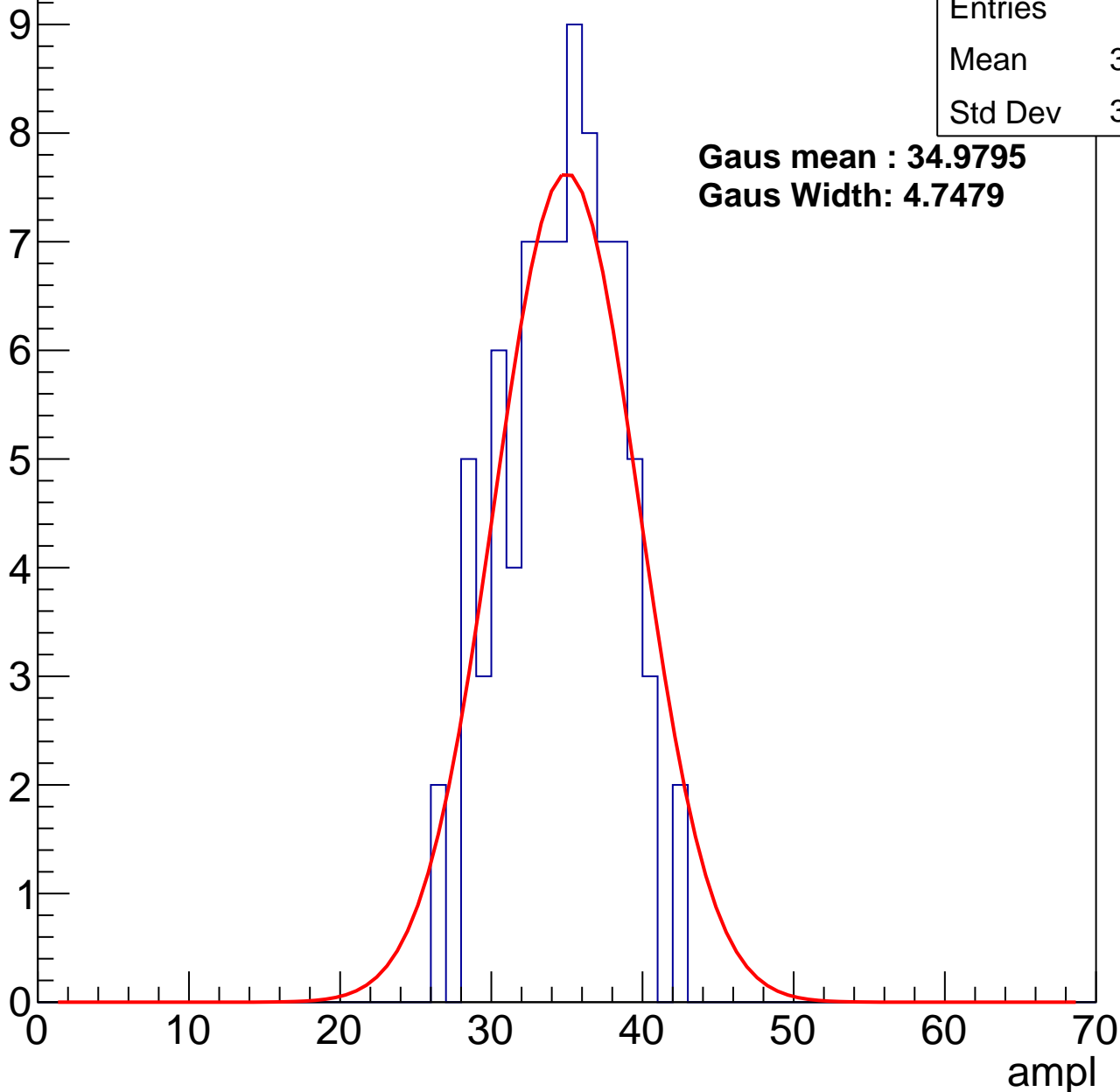
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.18
Std Dev	3.703

Gaus mean : 34.9795

Gaus Width: 4.7479



B1L102S, U18-ch99, adc2

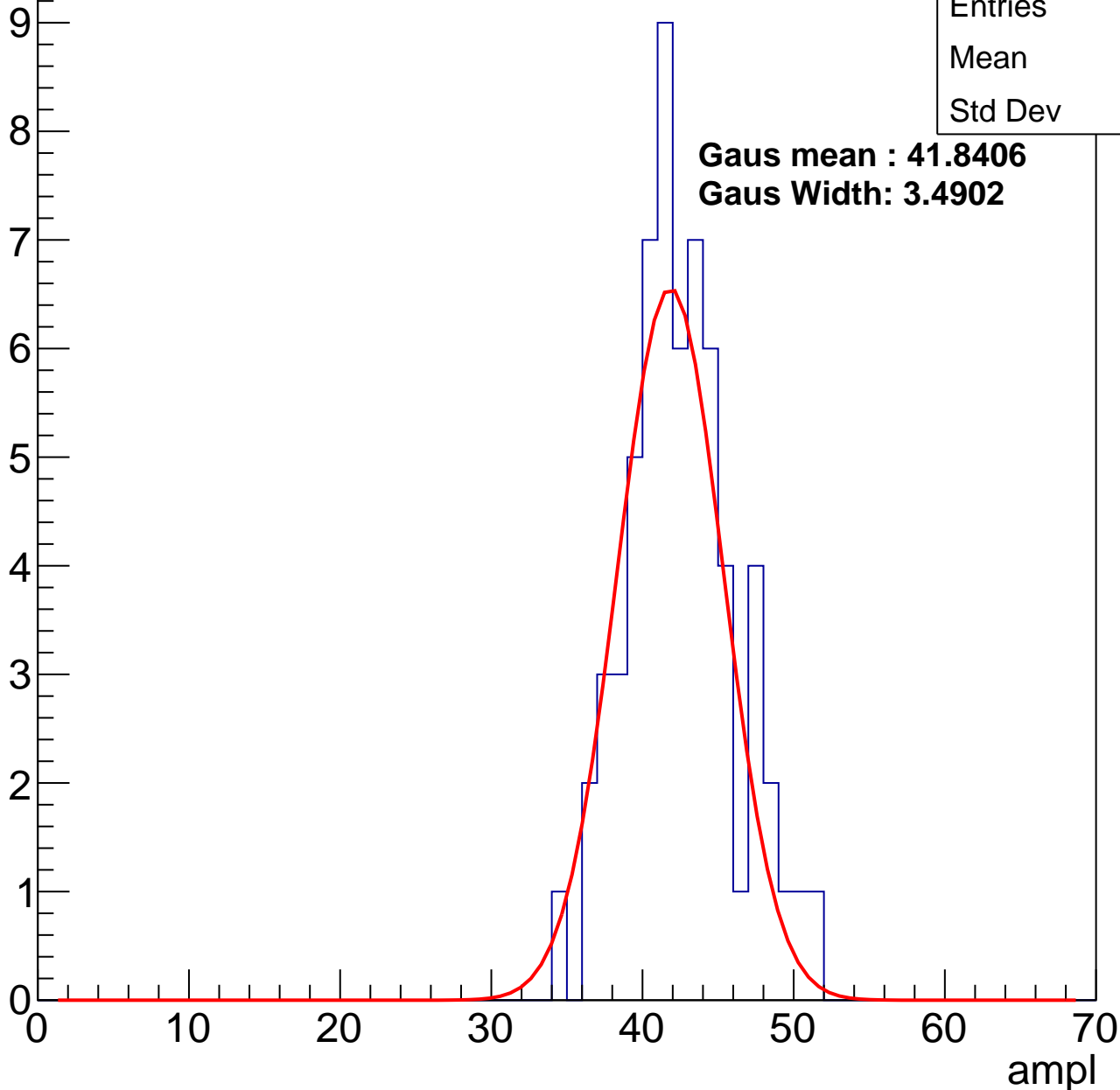
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	42.1
Std Dev	3.54

Gaus mean : 41.8406

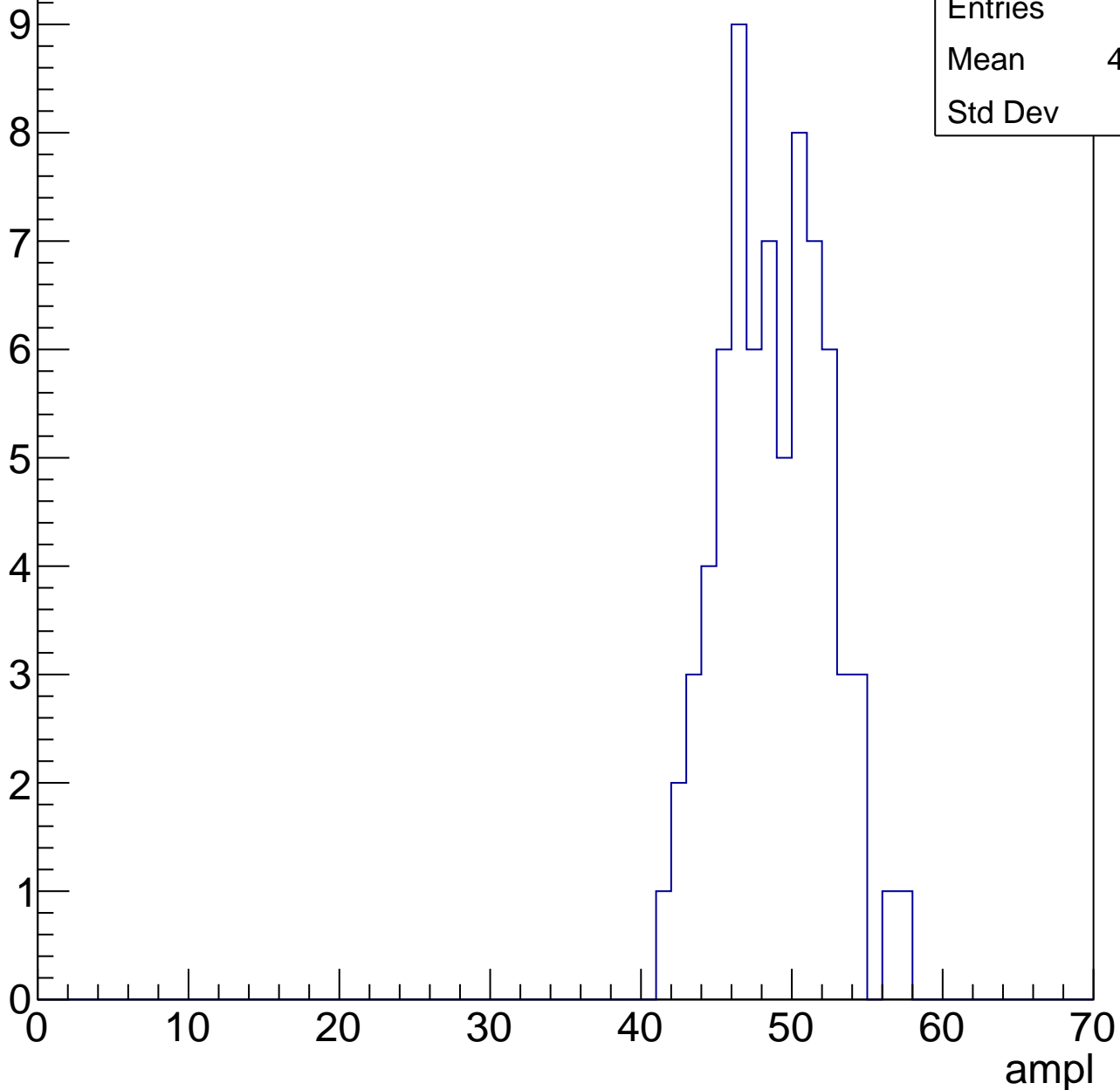
Gaus Width: 3.4902



B1L102S, U18-ch99, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



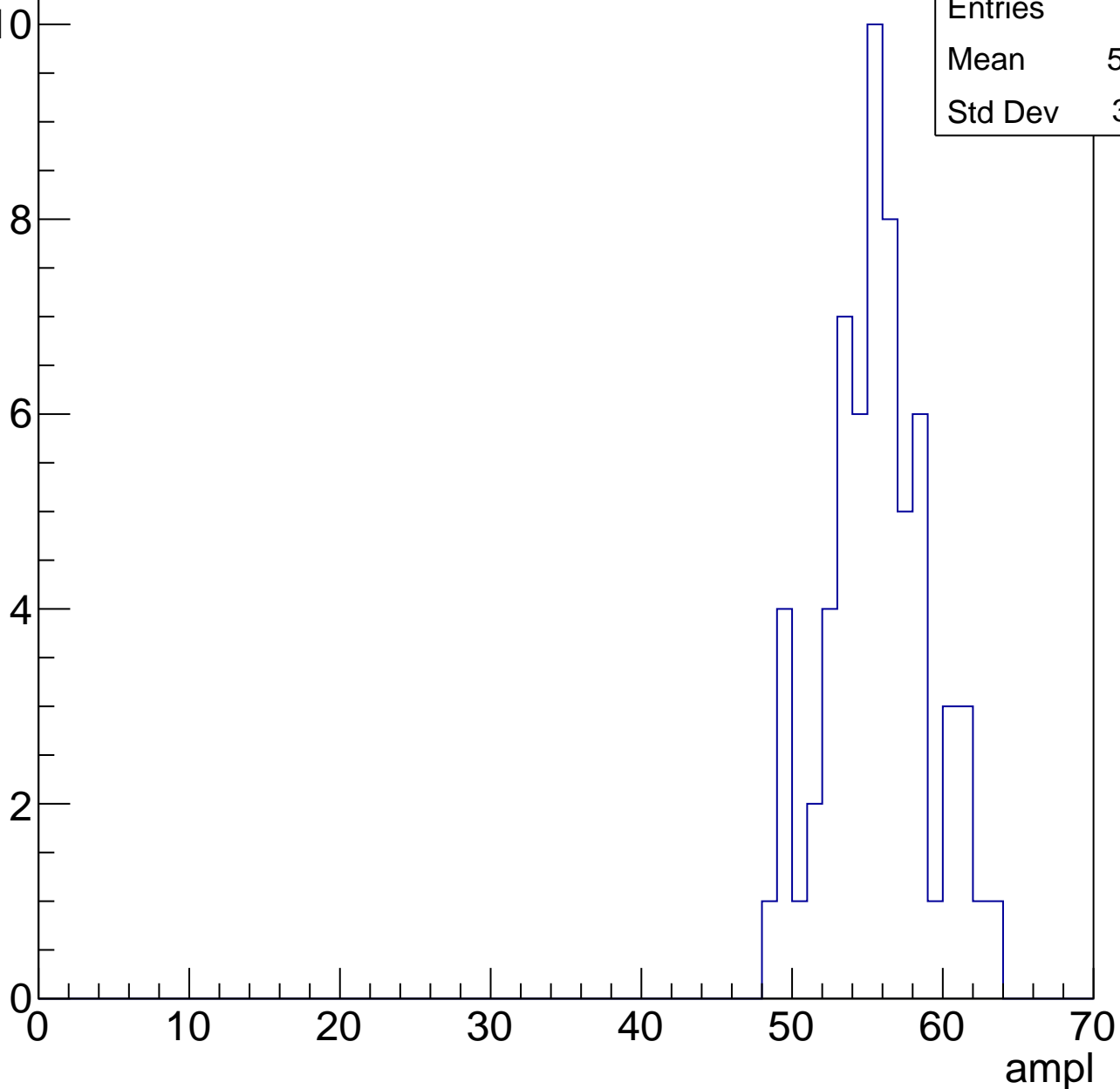
Entries	72
Mean	48.33
Std Dev	3.48

B1L102S, U18-ch99, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	55.19
Std Dev	3.361

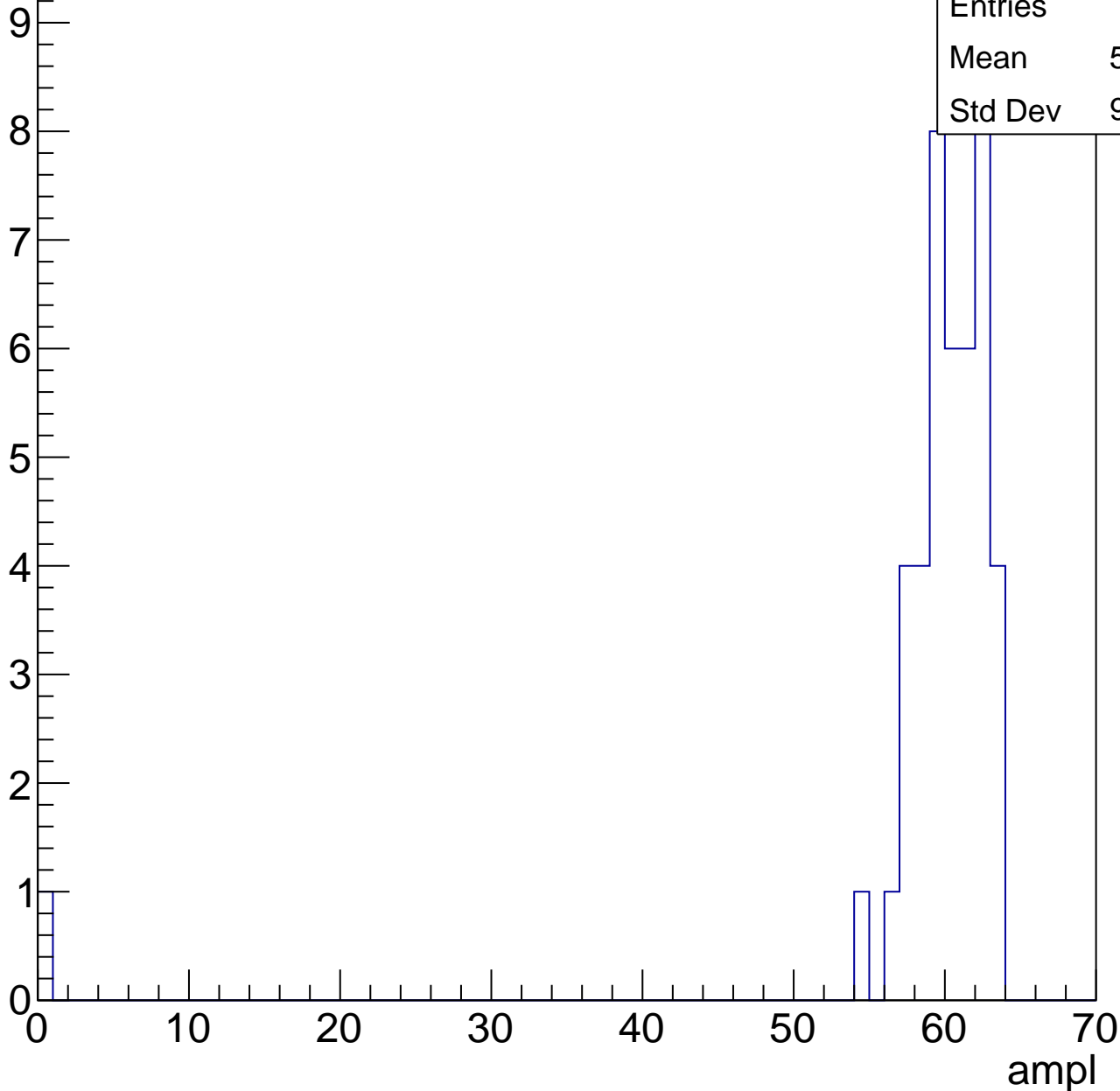


B1L102S, U18-ch99, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	58.59
Std Dev	9.173

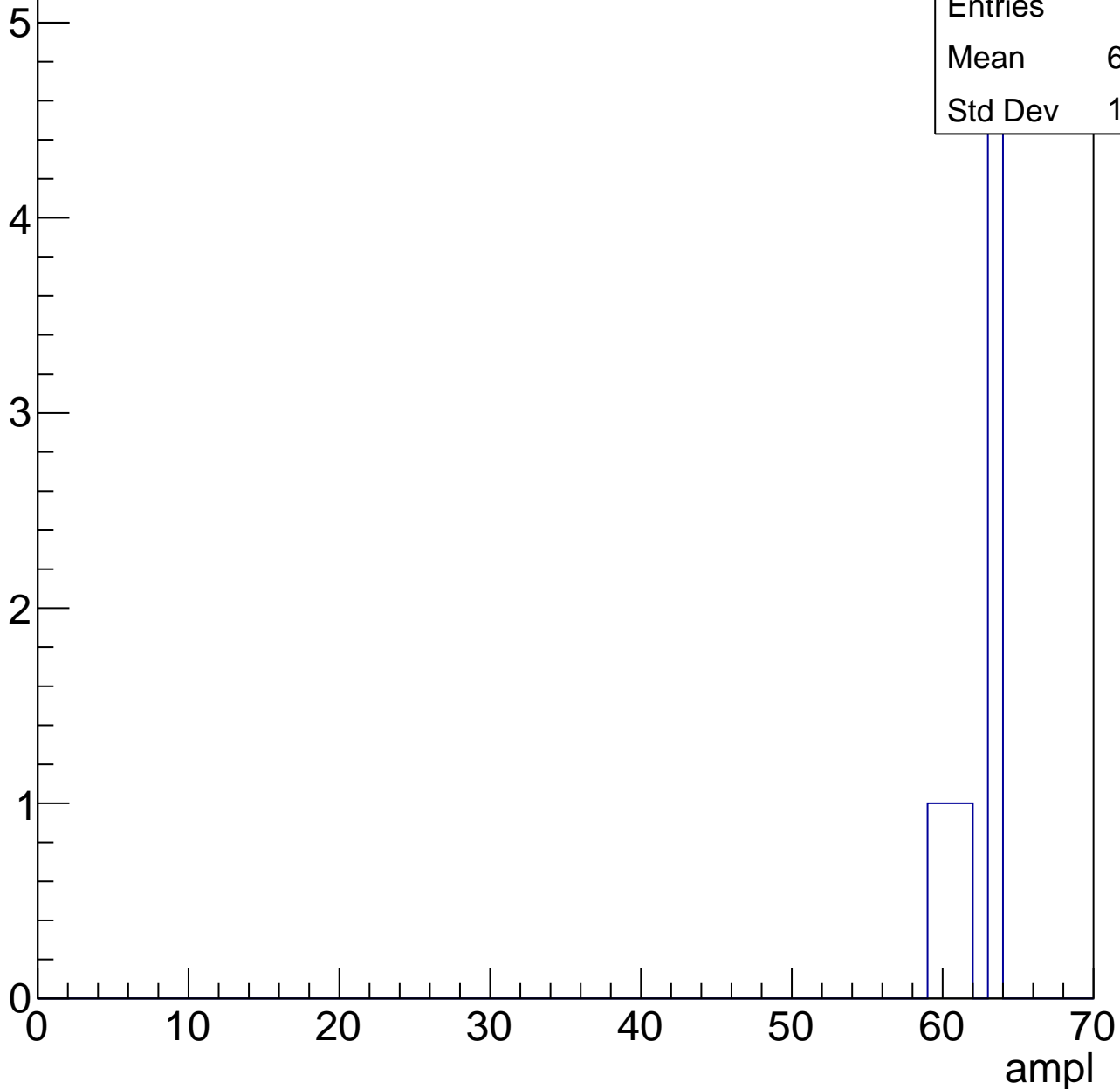


B1L102S, U18-ch99, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	61.88
Std Dev	1.536



B1L102S, U18-ch99, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U18-ch100, adc0

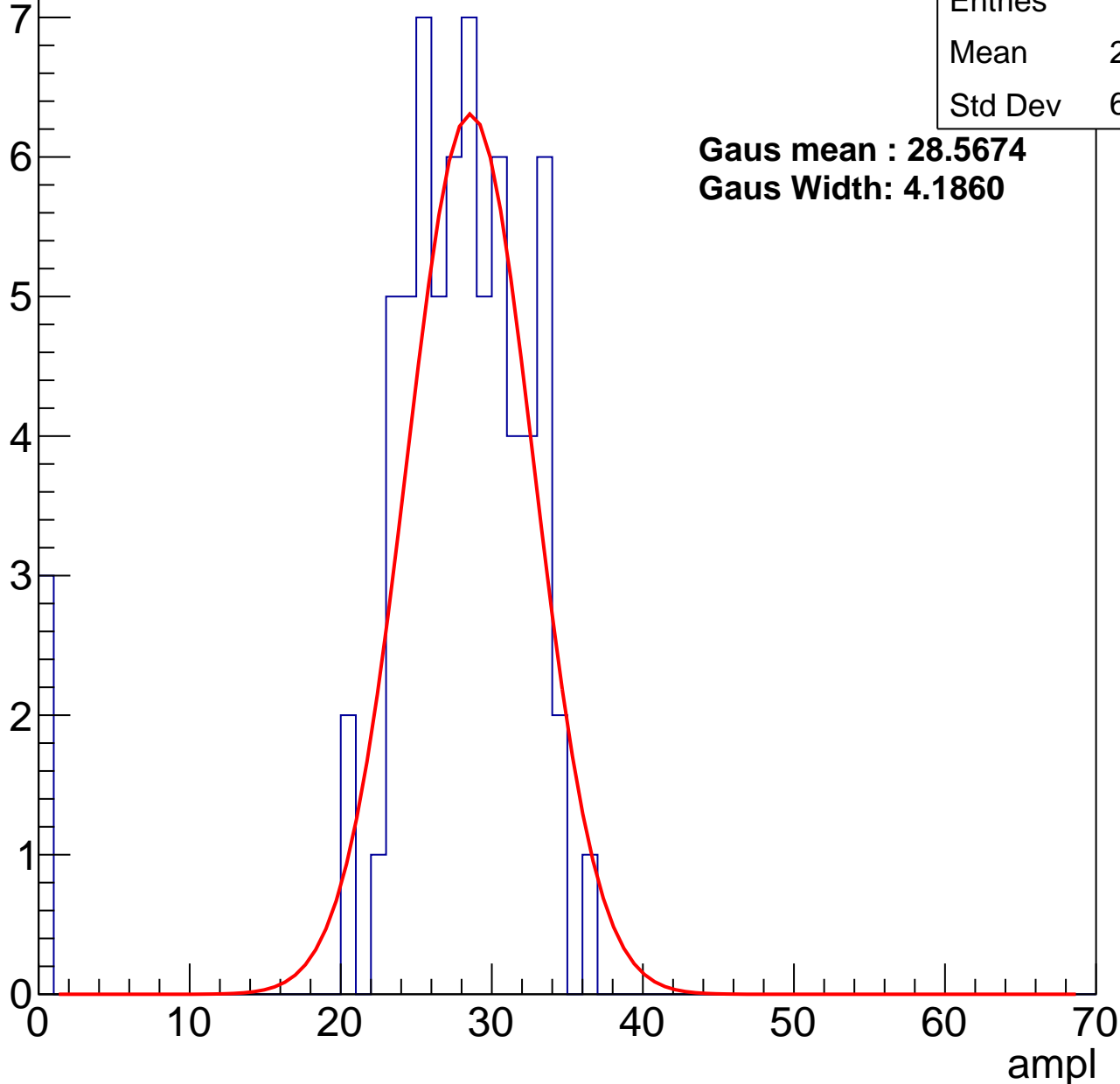
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	26.65
Std Dev	6.702

Gaus mean : 28.5674

Gaus Width: 4.1860



B1L102S, U18-ch100, adc1

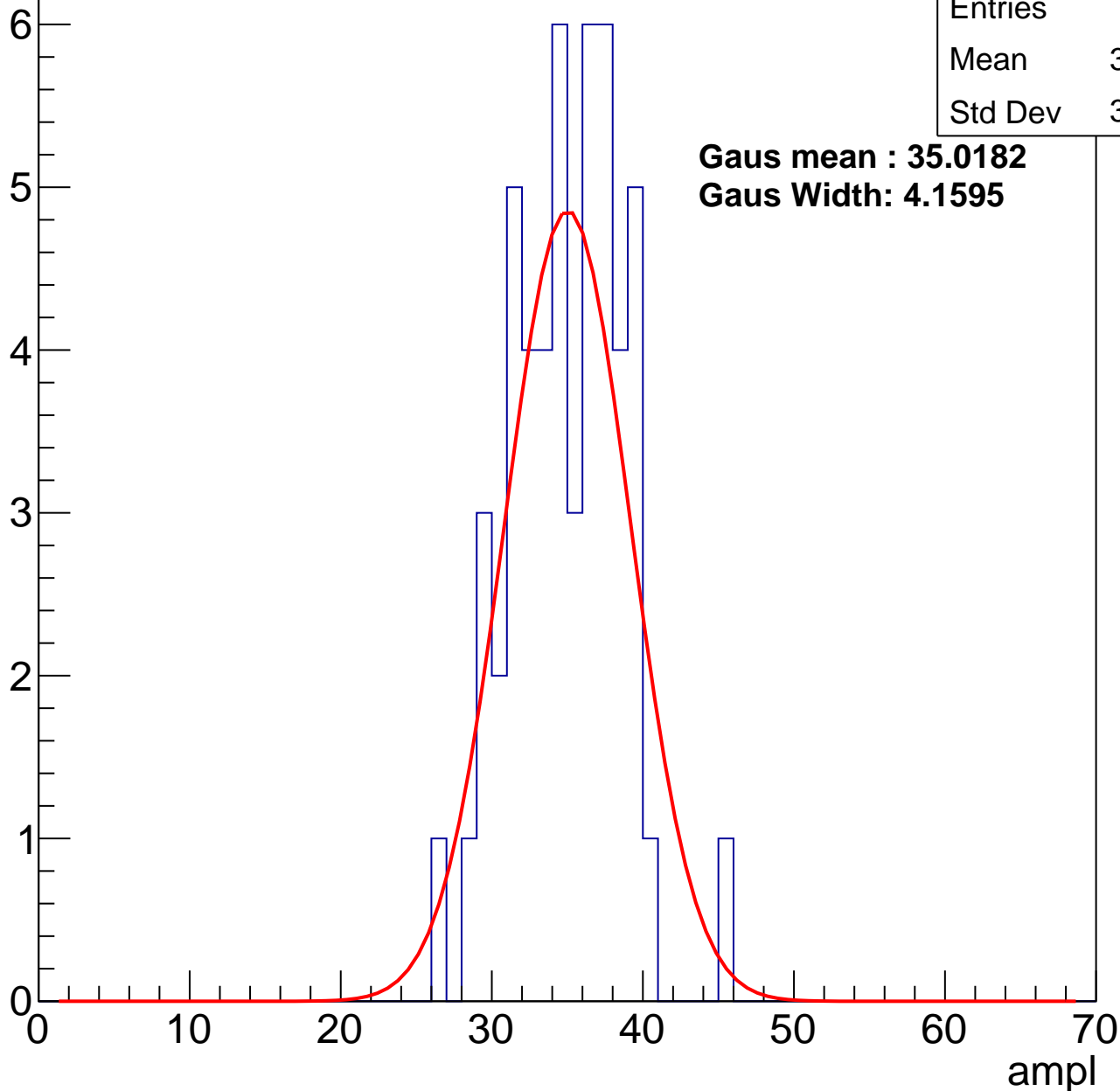
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	34.52
Std Dev	3.629

Gaus mean : 35.0182

Gaus Width: 4.1595



B1L102S, U18-ch100, adc2

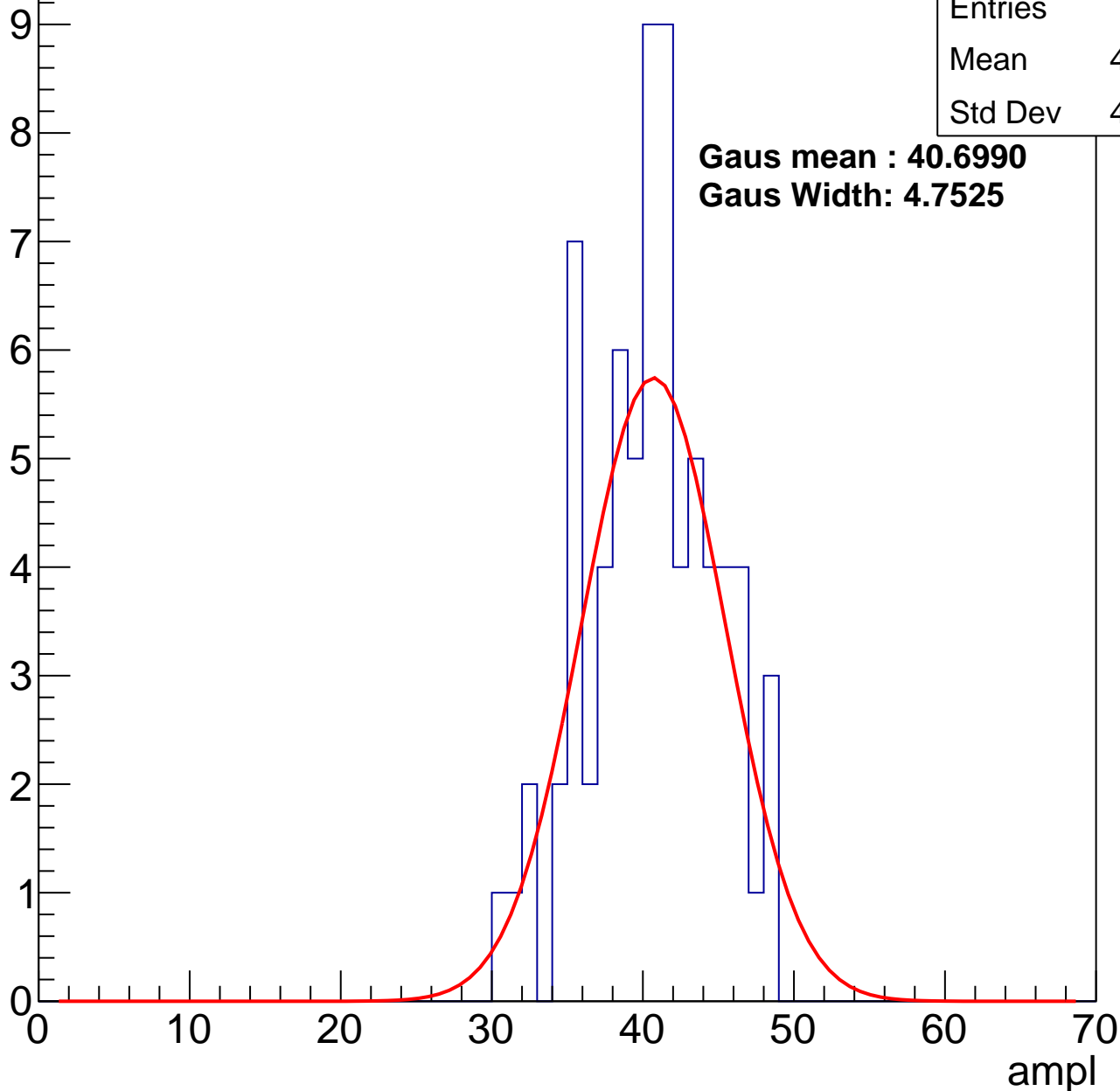
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	40.05
Std Dev	4.174

Gaus mean : 40.6990

Gaus Width: 4.7525

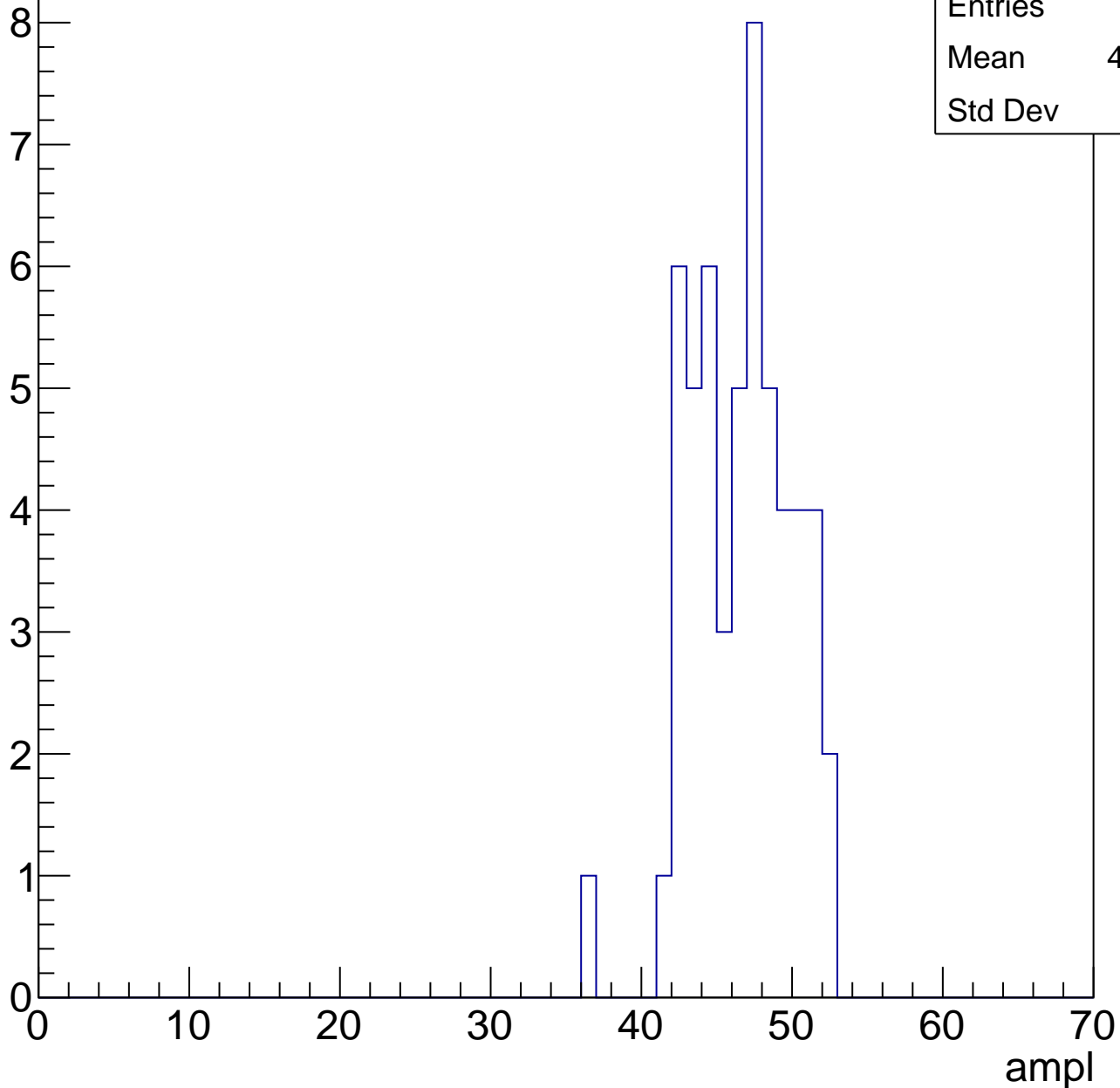


B1L102S, U18-ch100, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	46.17
Std Dev	3.31

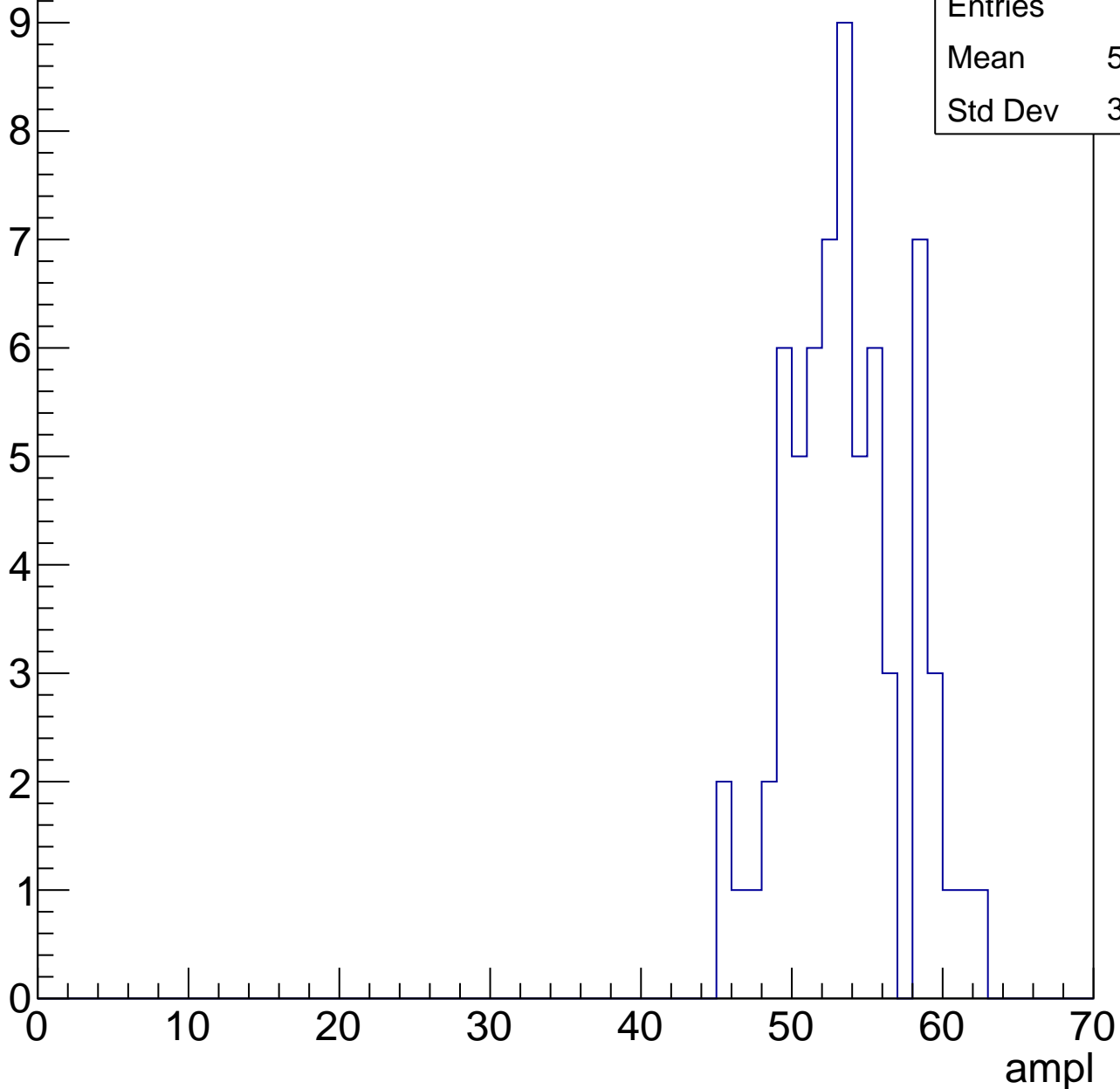


B1L102S, U18-ch100, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	53.09
Std Dev	3.829



B1L102S, U18-ch100, adc5

calib_packv5_042523_0143.root, FC#11, port A2

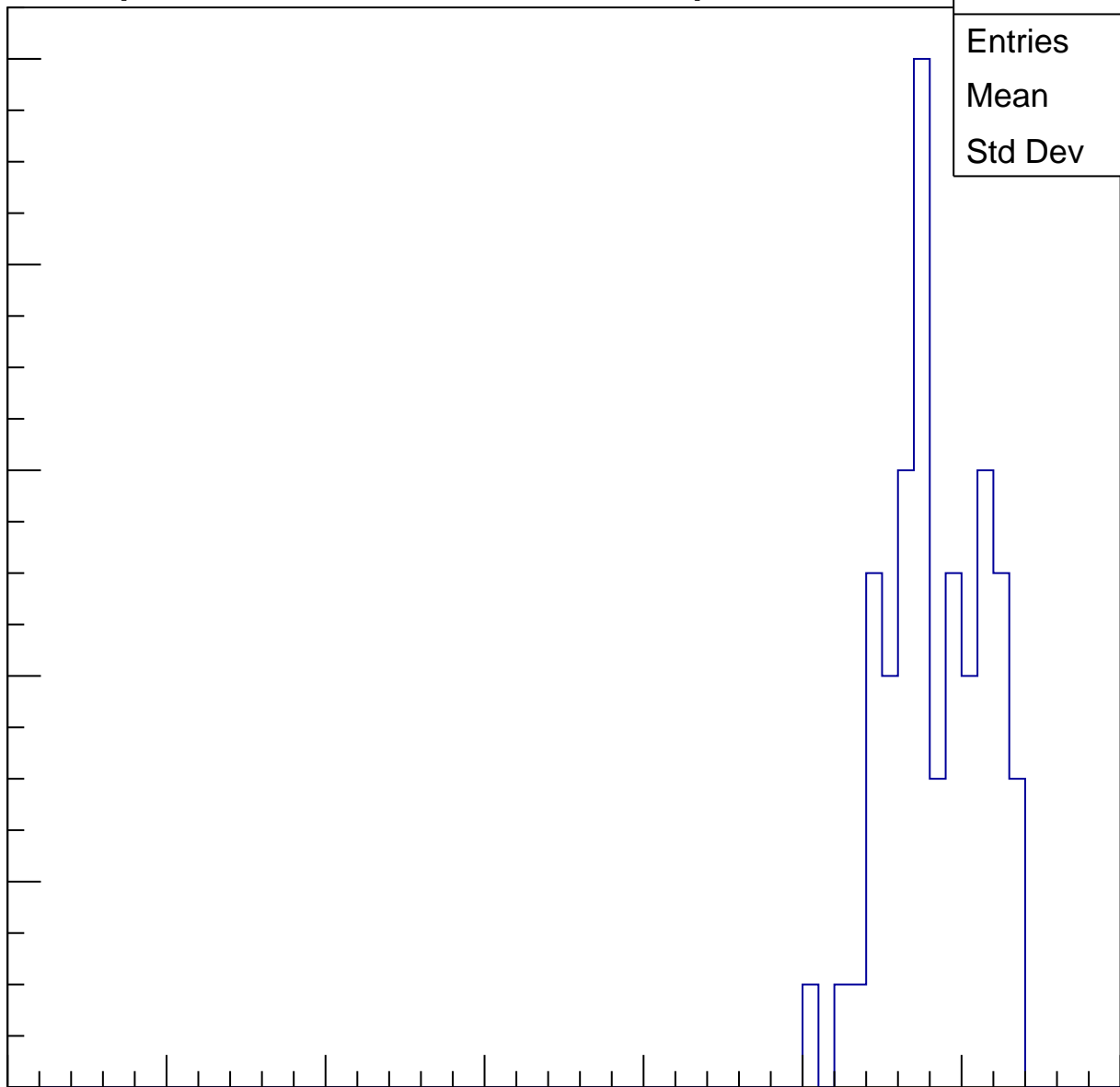
Entries	54
Mean	57.87
Std Dev	3.055

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

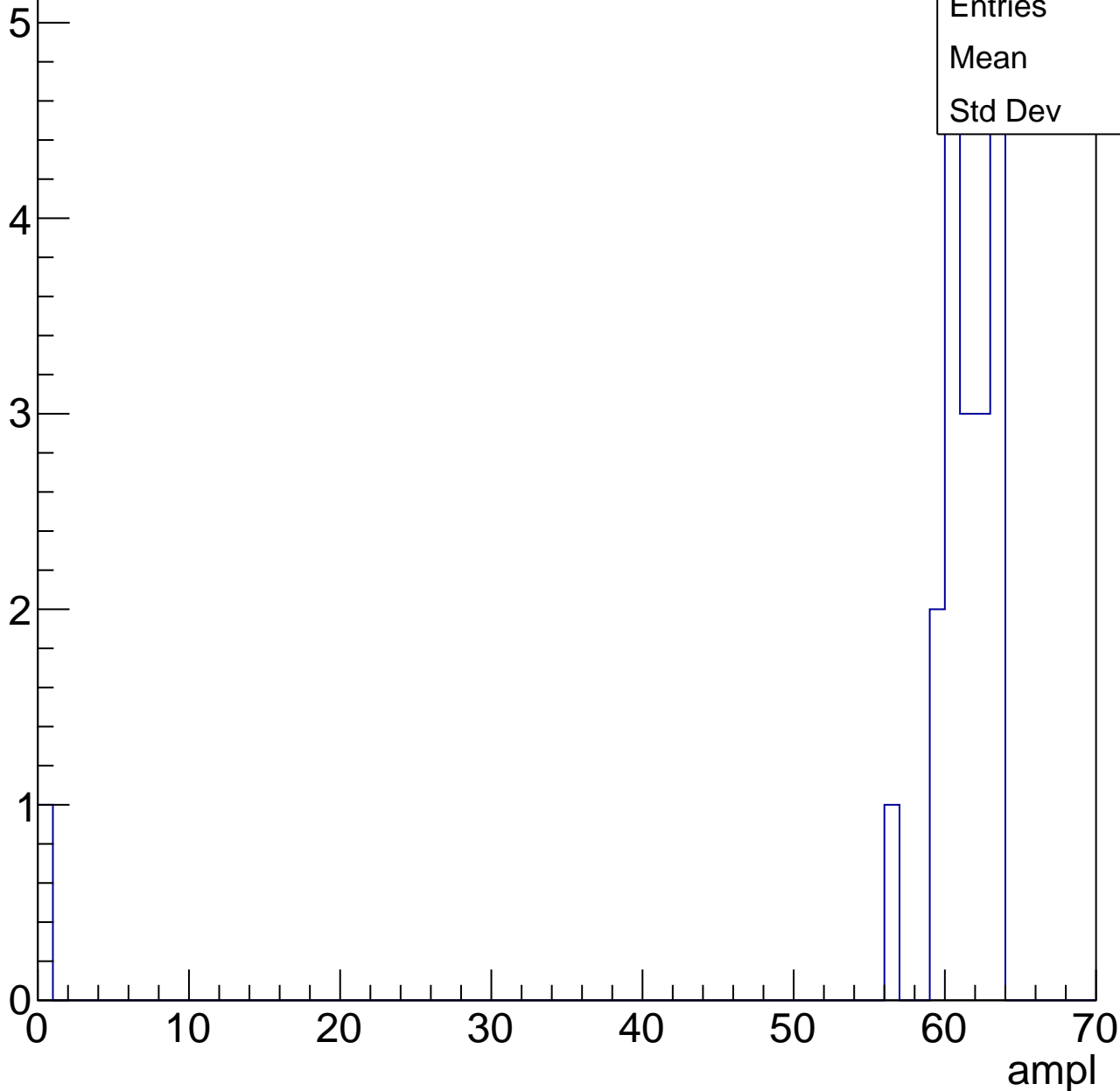


B1L102S, U18-ch100, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	20
Mean	57.9
Std Dev	13.4



B1L102S, U18-ch100, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch101, adc0

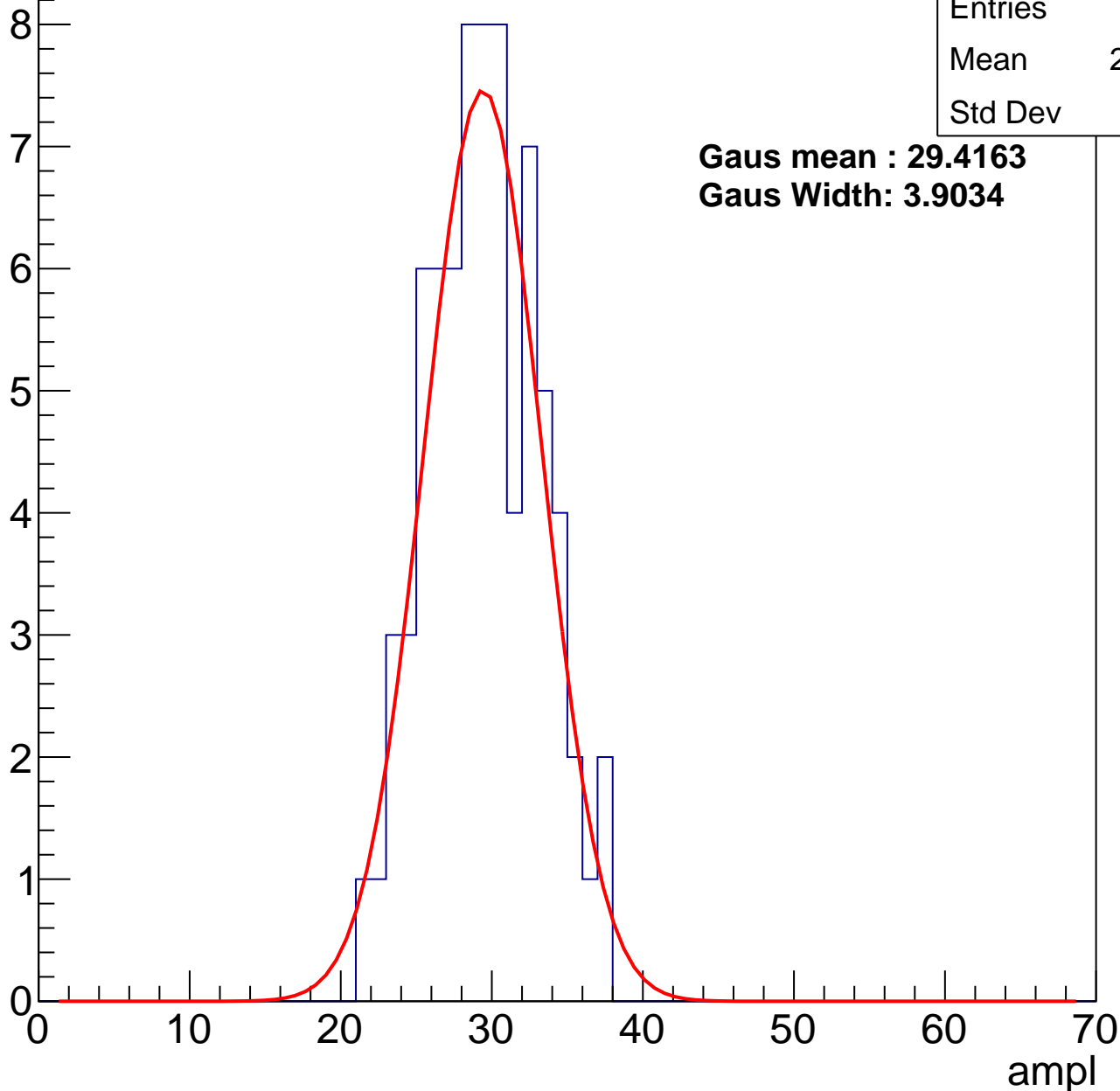
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	29.03
Std Dev	3.64

Gaus mean : 29.4163

Gaus Width: 3.9034



B1L102S, U18-ch101, adc1

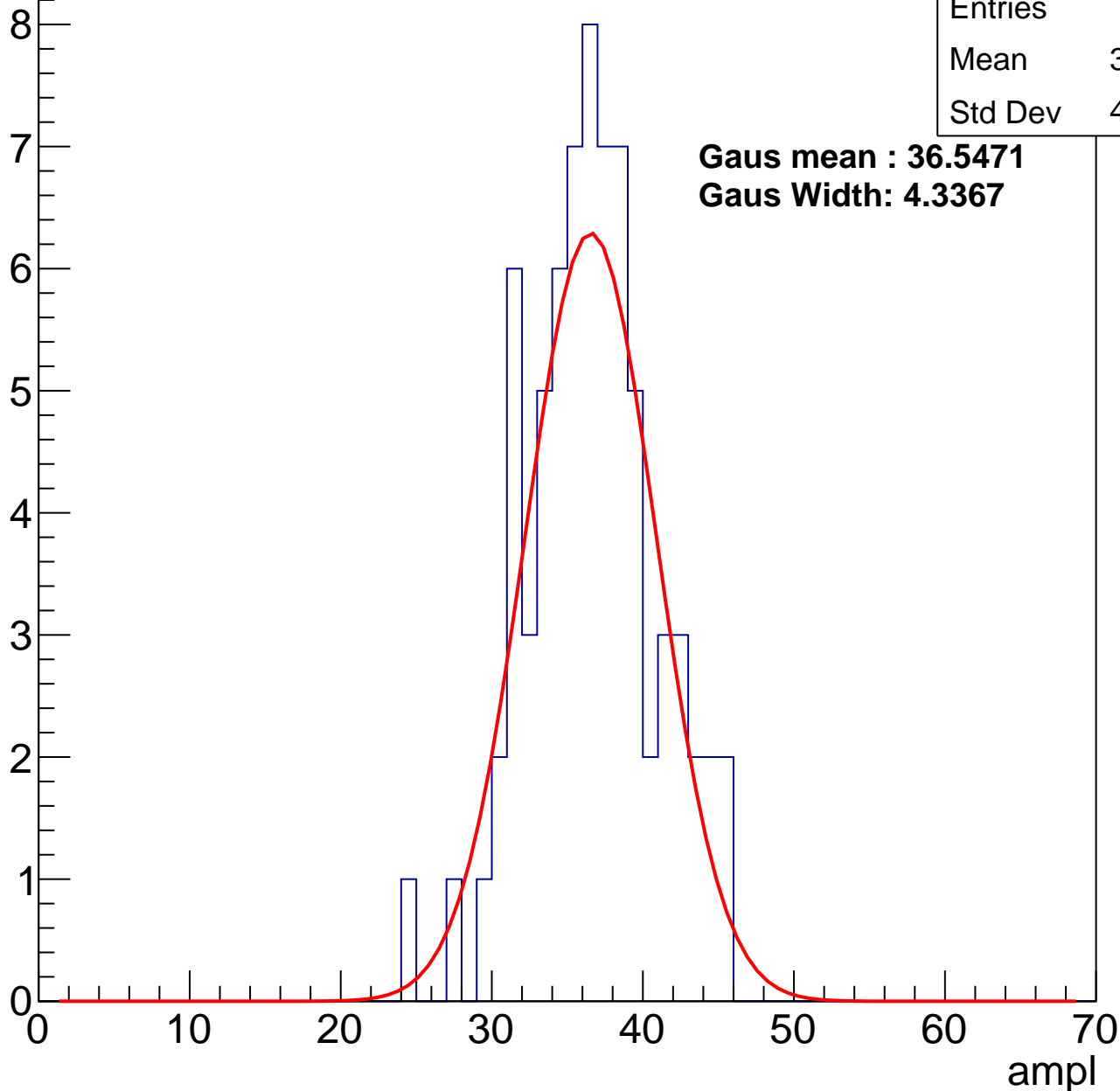
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	36.12
Std Dev	4.223

Gaus mean : 36.5471

Gaus Width: 4.3367



B1L102S, U18-ch101, adc2

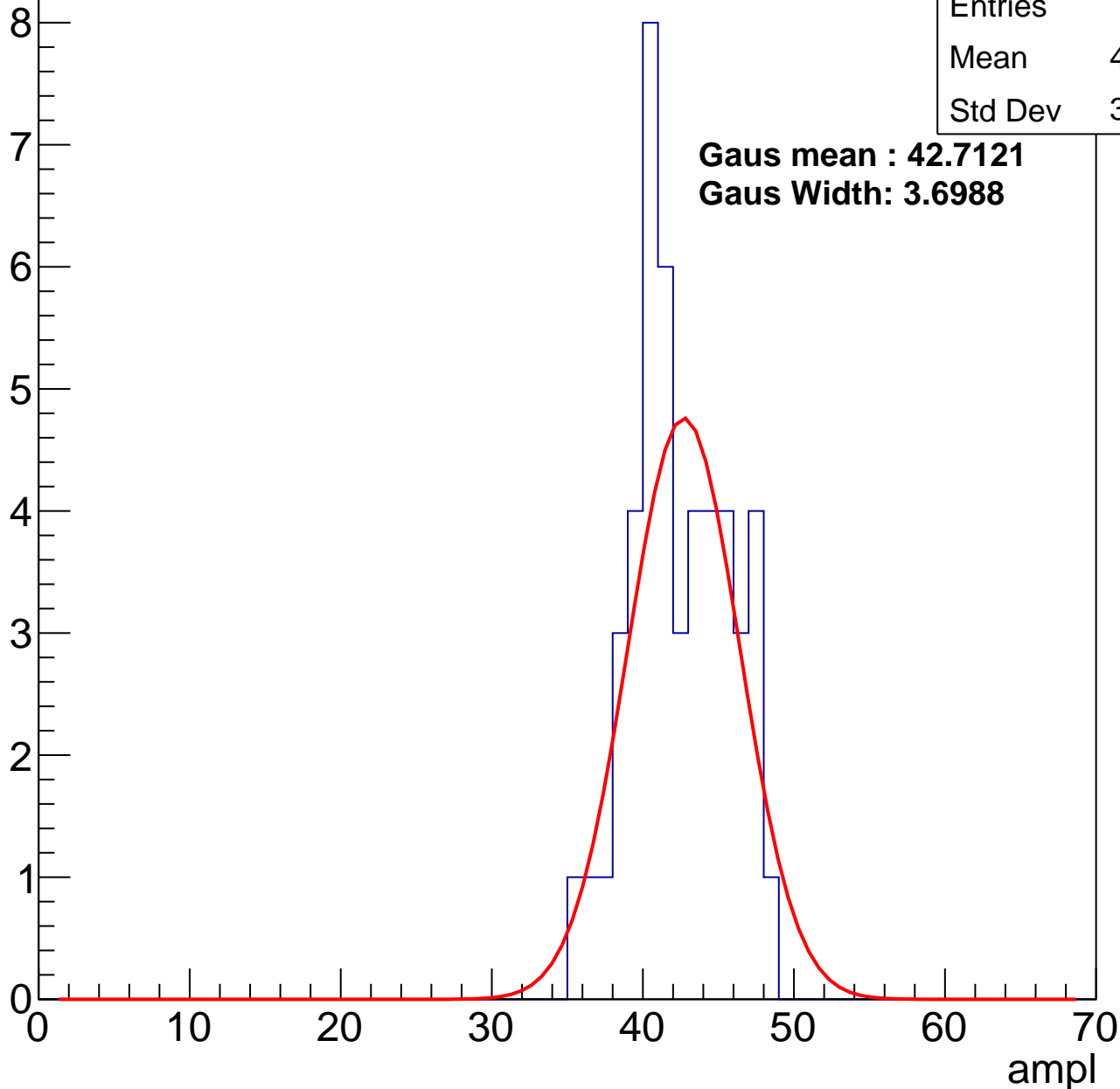
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	41.96
Std Dev	3.169

Gaus mean : 42.7121

Gaus Width: 3.6988

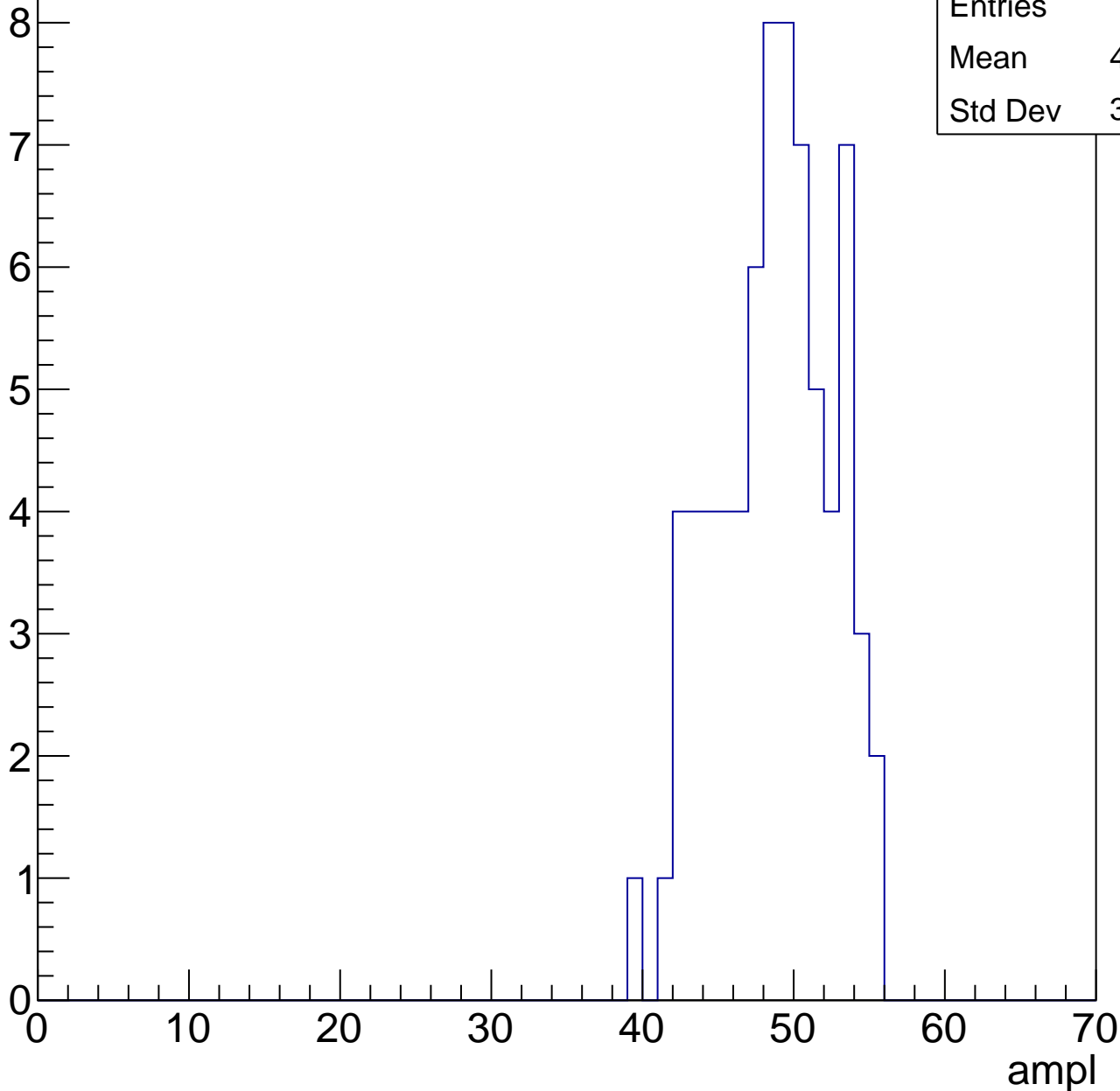


B1L102S, U18-ch101, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	48.25
Std Dev	3.759

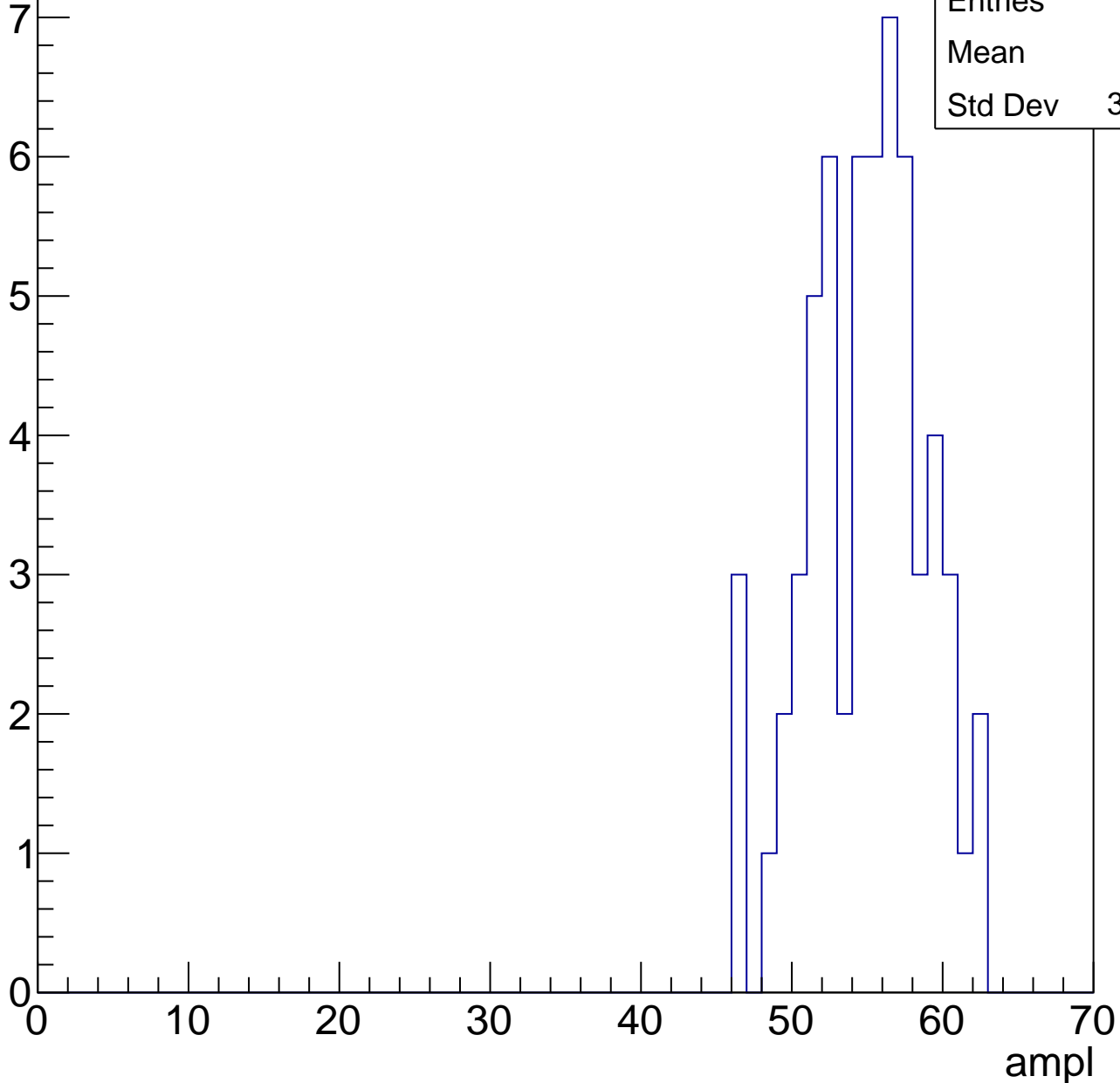


B1L102S, U18-ch101, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	54.5
Std Dev	3.879



B1L102S, U18-ch101, adc5

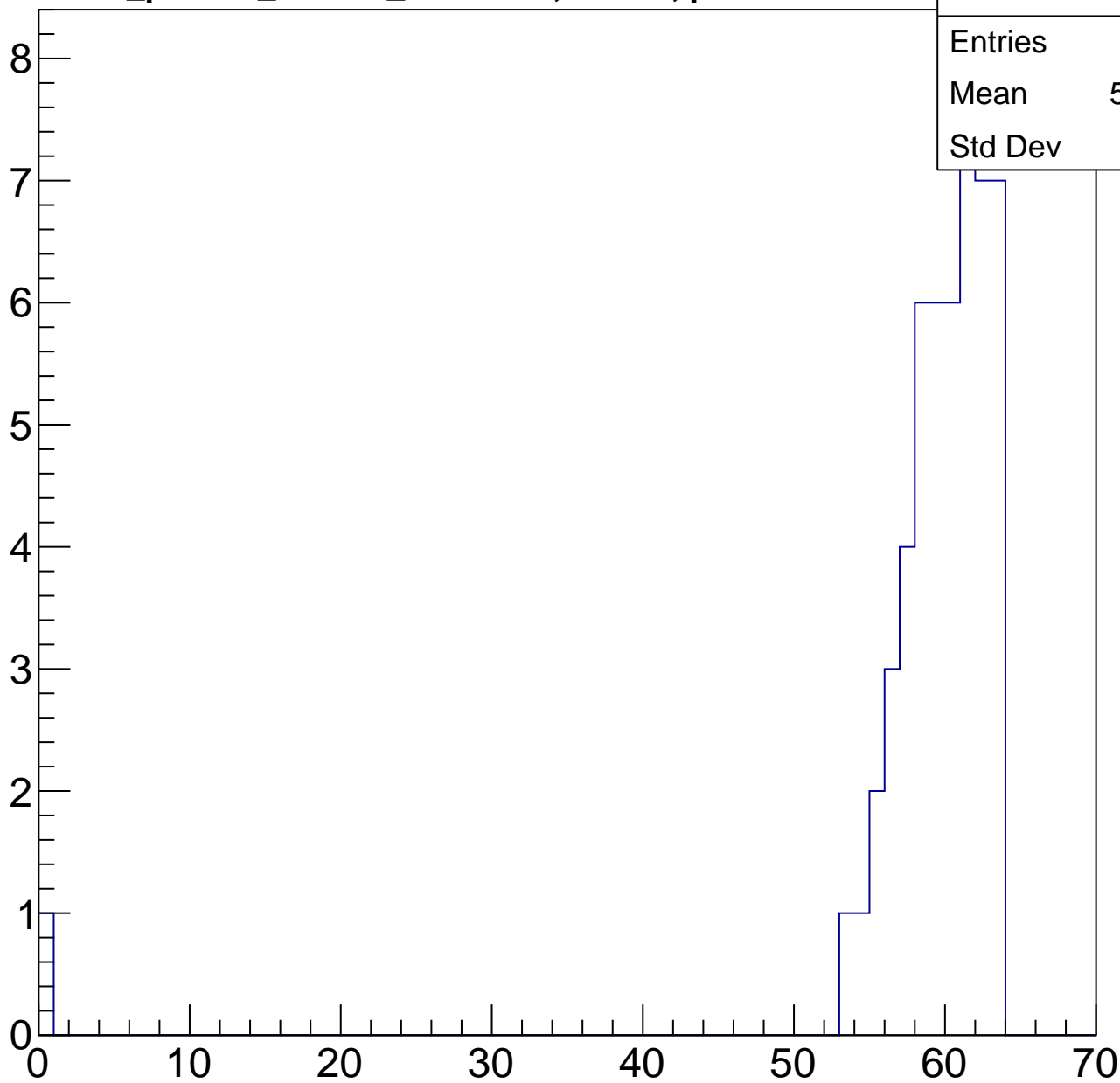
calib_packv5_042523_0143.root, FC#11, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.42
Std Dev	8.57

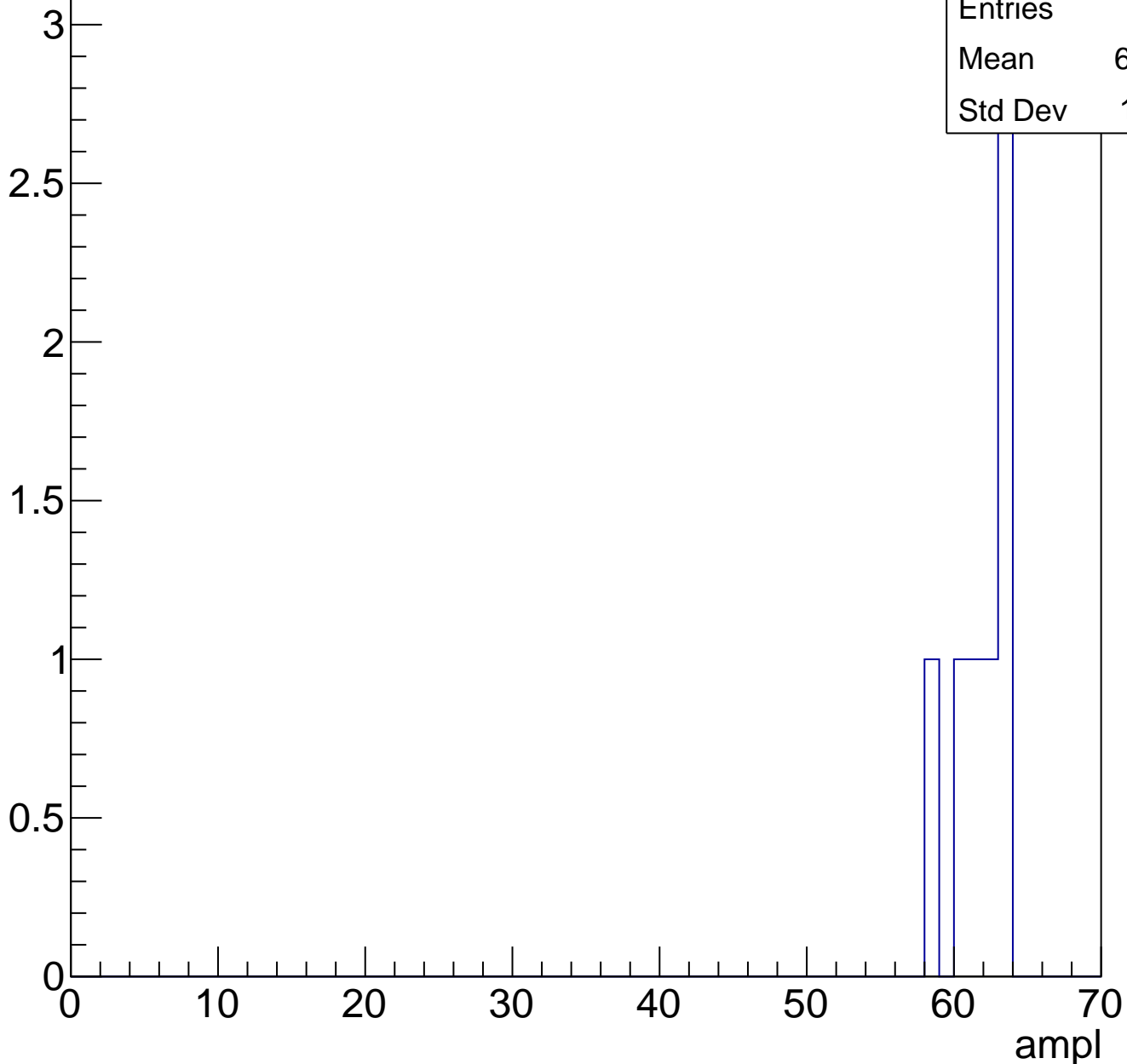
ampl



B1L102S, U18-ch101, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	7
Mean	61.43
Std Dev	1.761

B1L102S, U18-ch101, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	19
Std Dev	0

ampl

B1L102S, U18-ch102, adc0

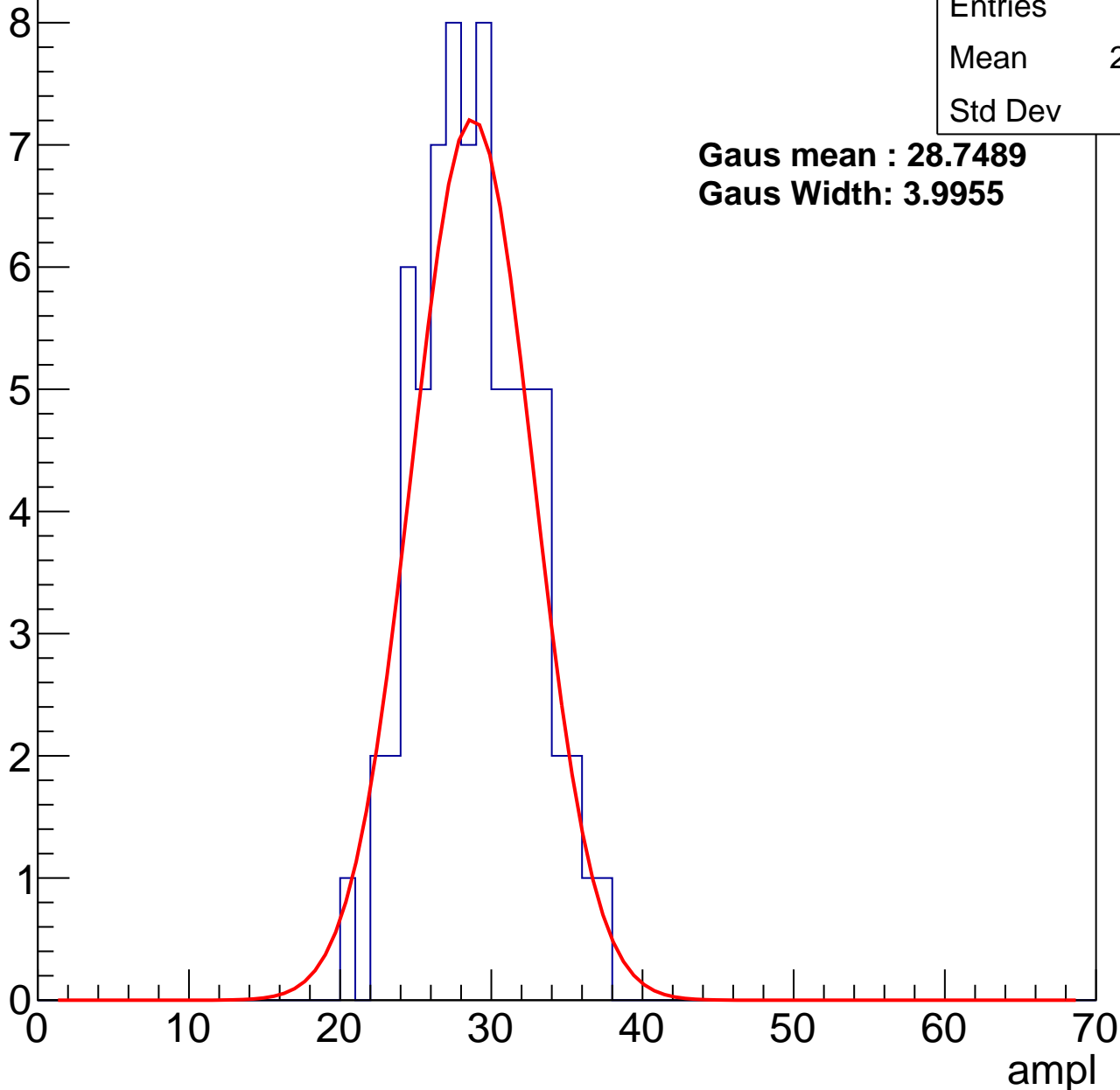
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.42
Std Dev	3.62

Gaus mean : 28.7489

Gaus Width: 3.9955



B1L102S, U18-ch102, adc1

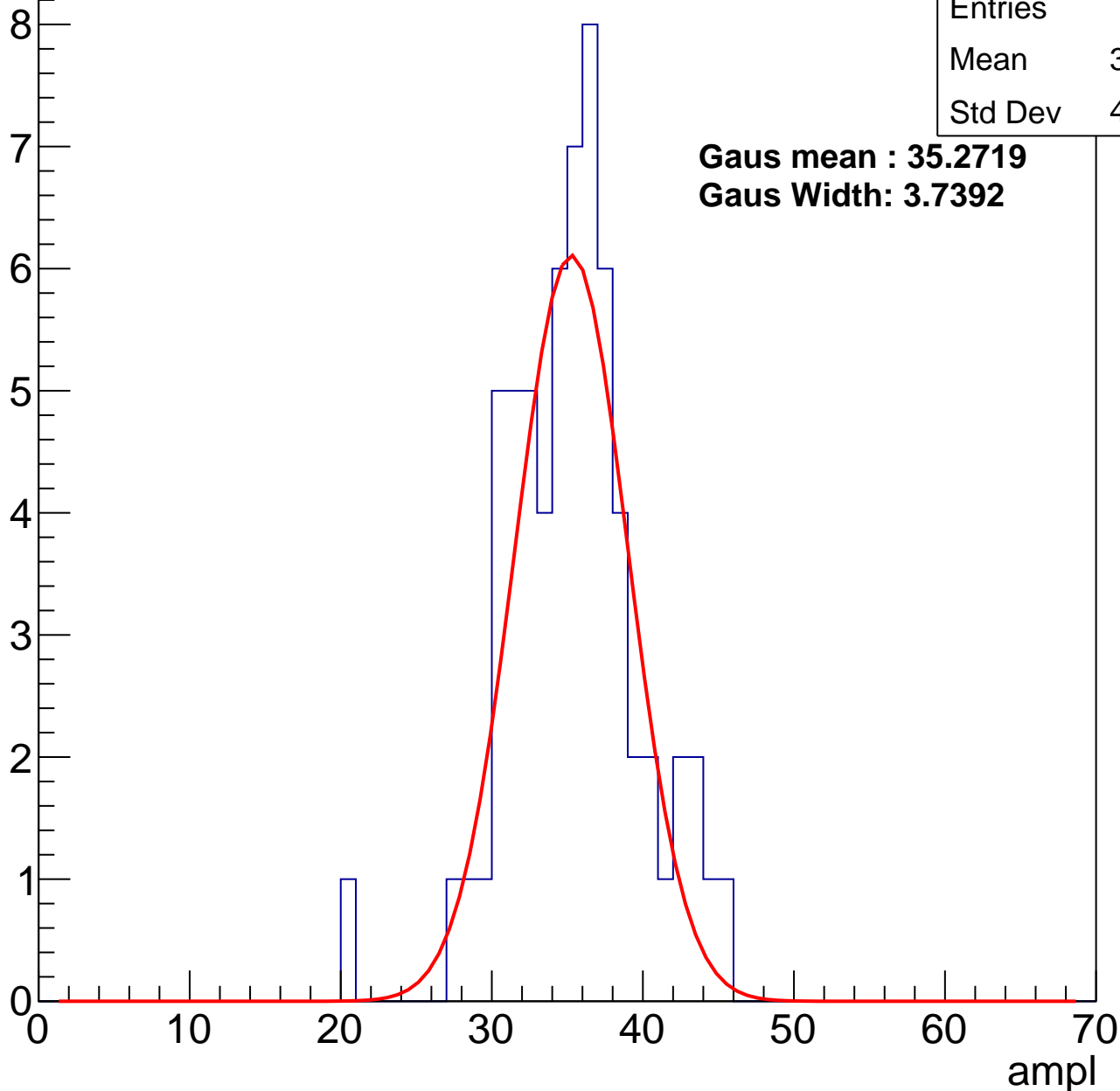
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	34.92
Std Dev	4.365

Gaus mean : 35.2719

Gaus Width: 3.7392



B1L102S, U18-ch102, adc2

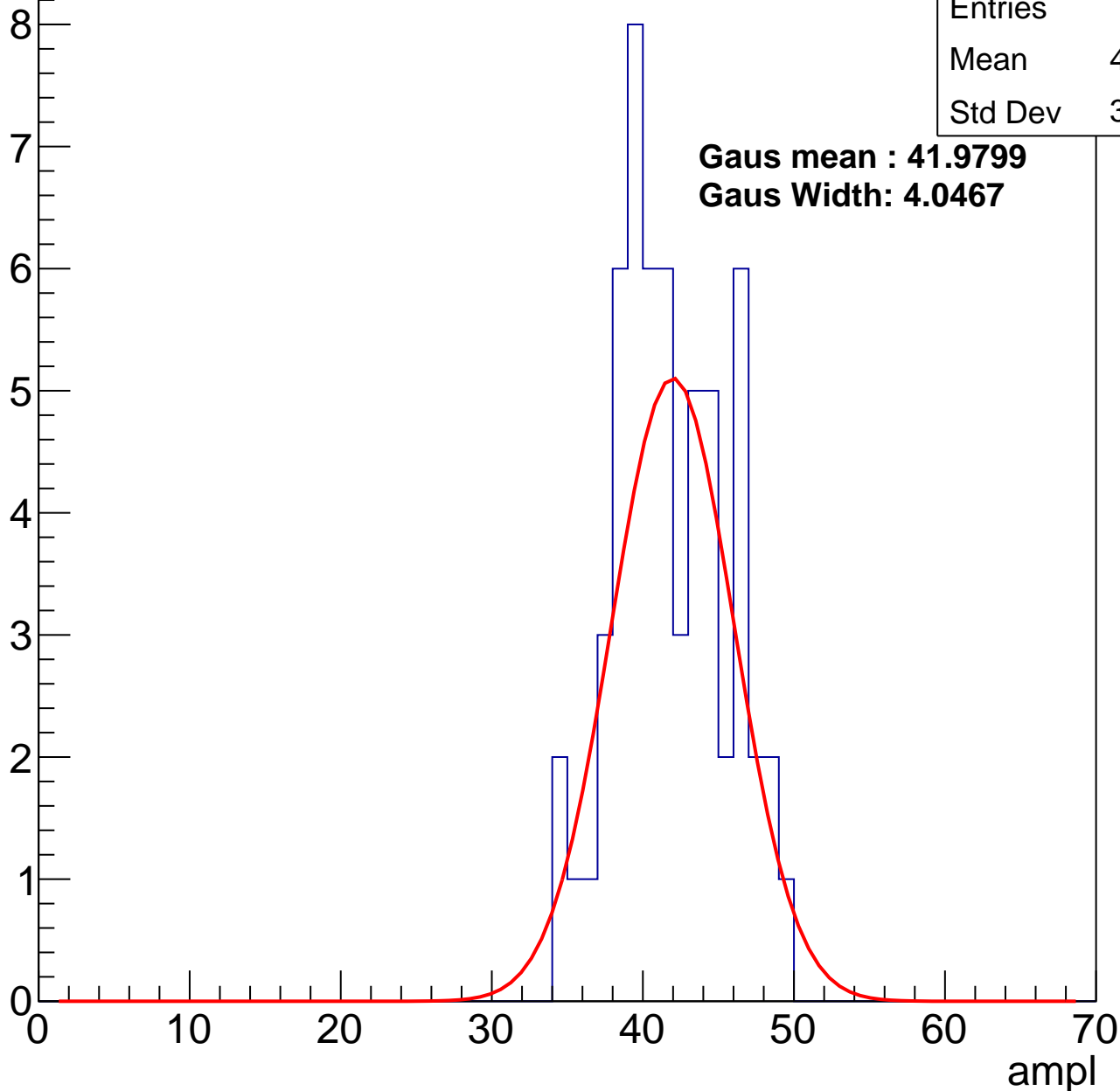
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	41.39
Std Dev	3.622

Gaus mean : 41.9799

Gaus Width: 4.0467

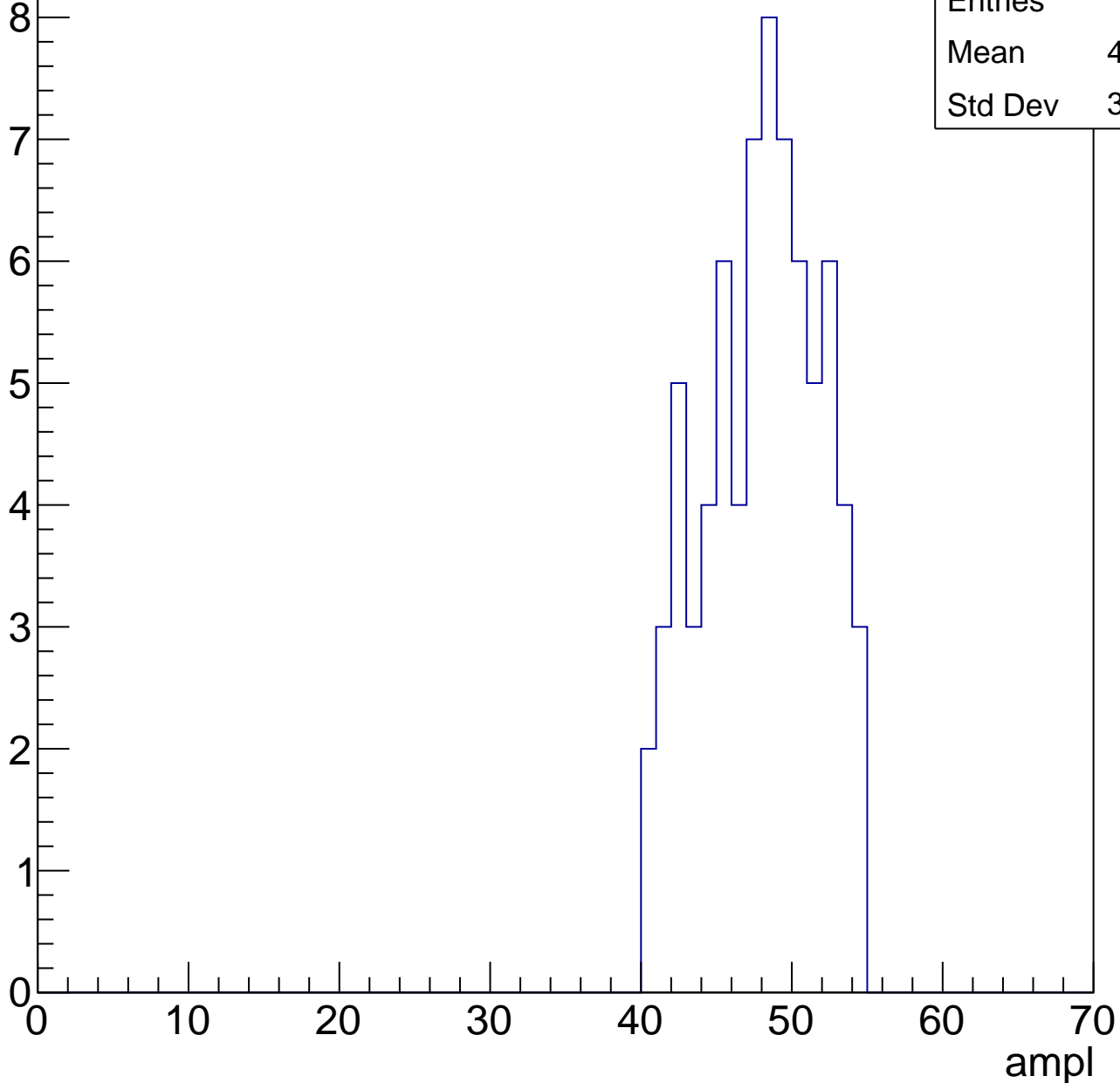


B1L102S, U18-ch102, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	47.52
Std Dev	3.764

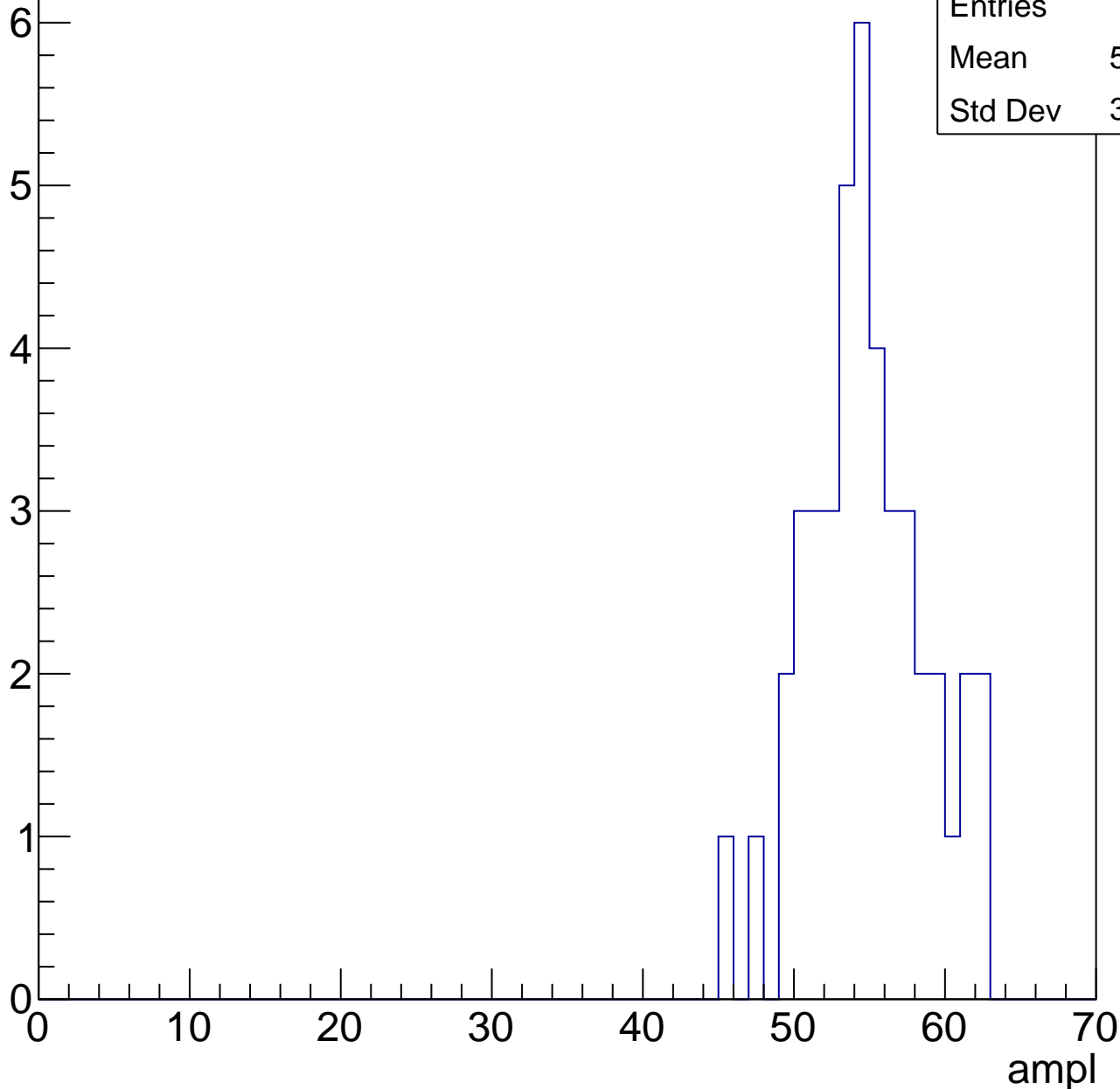


B1L102S, U18-ch102, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	54.35
Std Dev	3.899

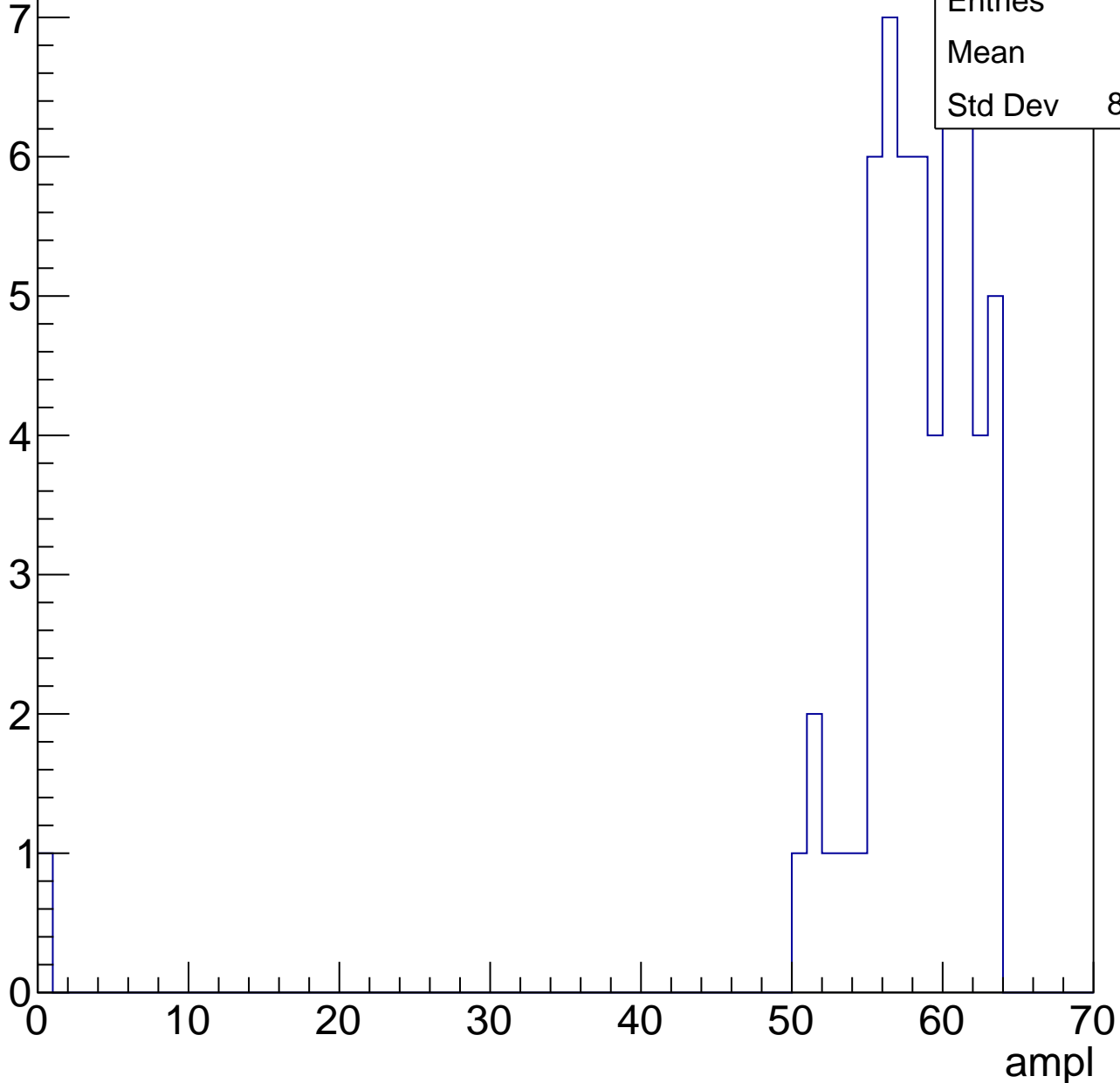


B1L102S, U18-ch102, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.1
Std Dev	8.159

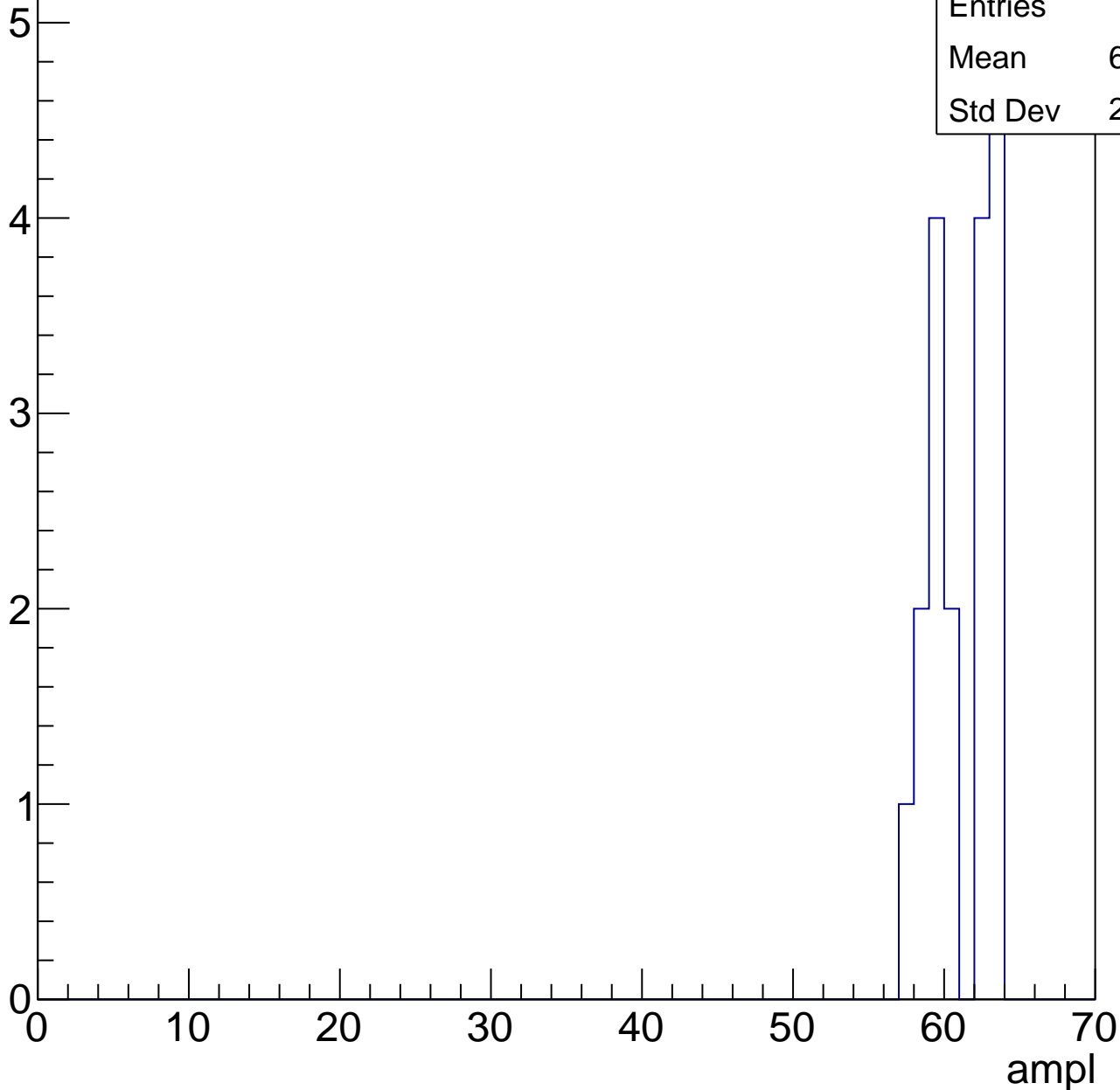


B1L102S, U18-ch102, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	60.67
Std Dev	2.028



B1L102S, U18-ch102, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch103, adc0

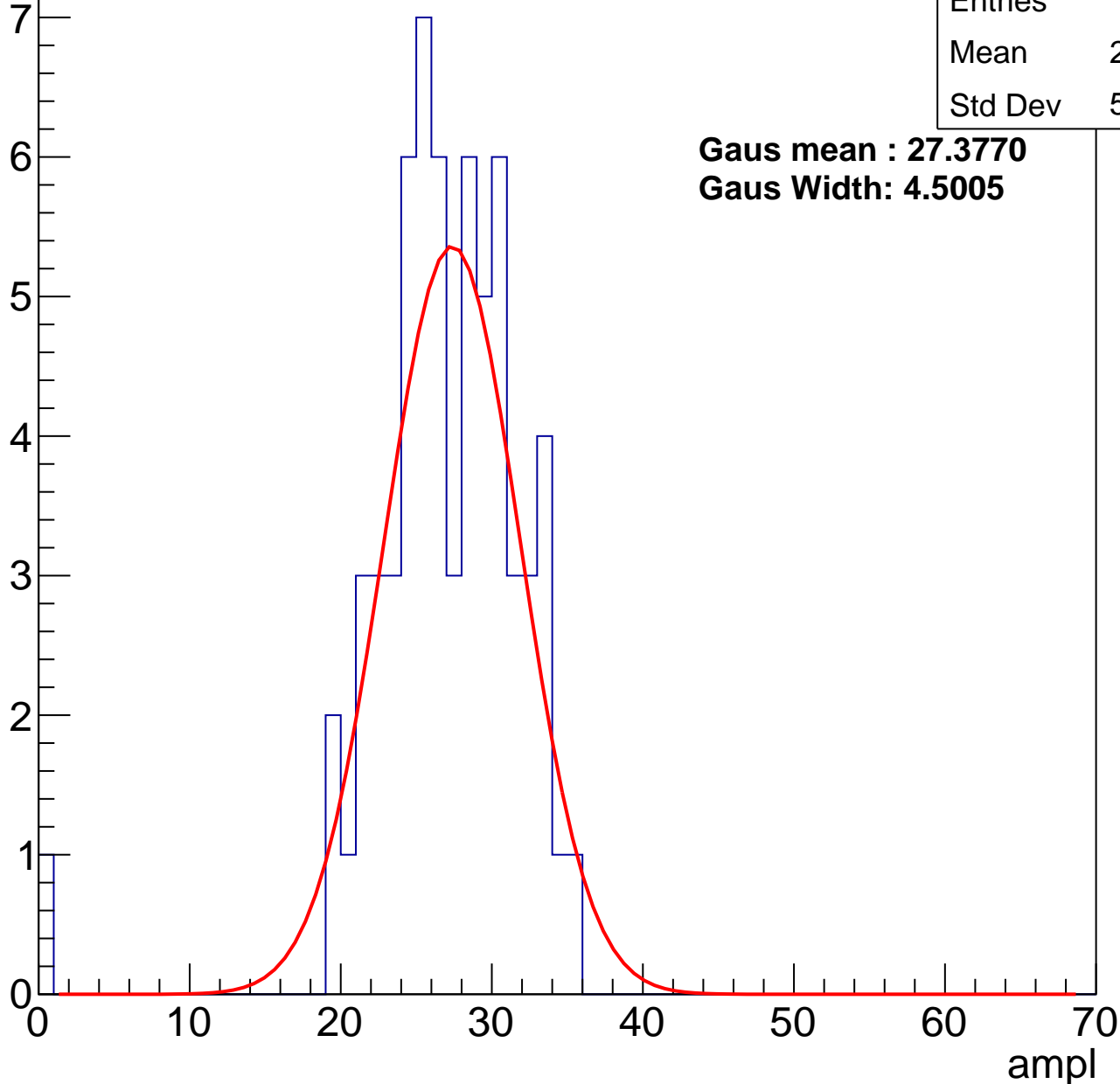
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	26.48
Std Dev	5.105

Gaus mean : 27.3770

Gaus Width: 4.5005



B1L102S, U18-ch103, adc1

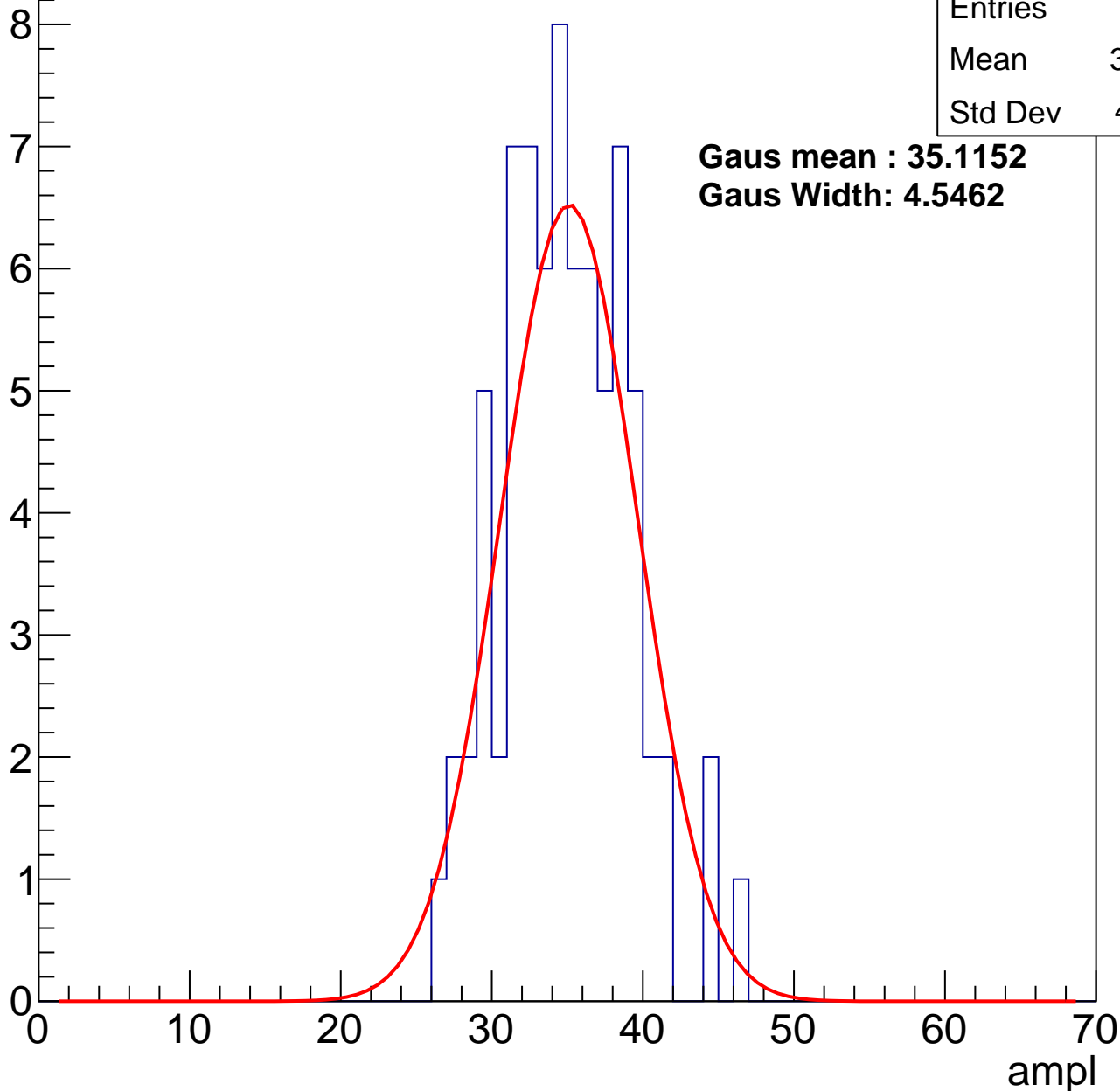
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	34.47
Std Dev	4.131

Gaus mean : 35.1152

Gaus Width: 4.5462



B1L102S, U18-ch103, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	41.55
Std Dev	3.922

Gaus mean : 41.6233

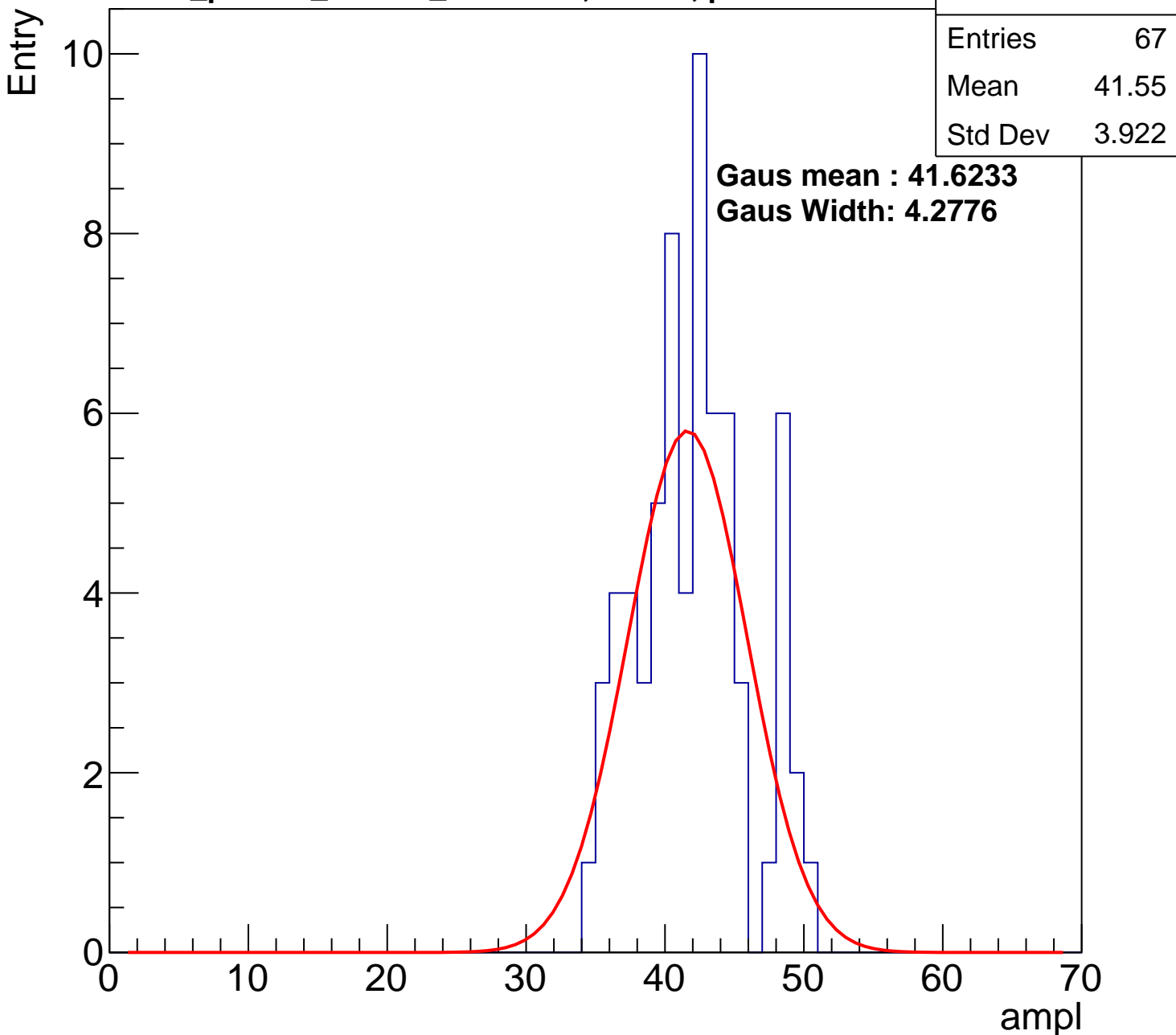
Gaus Width: 4.2776

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

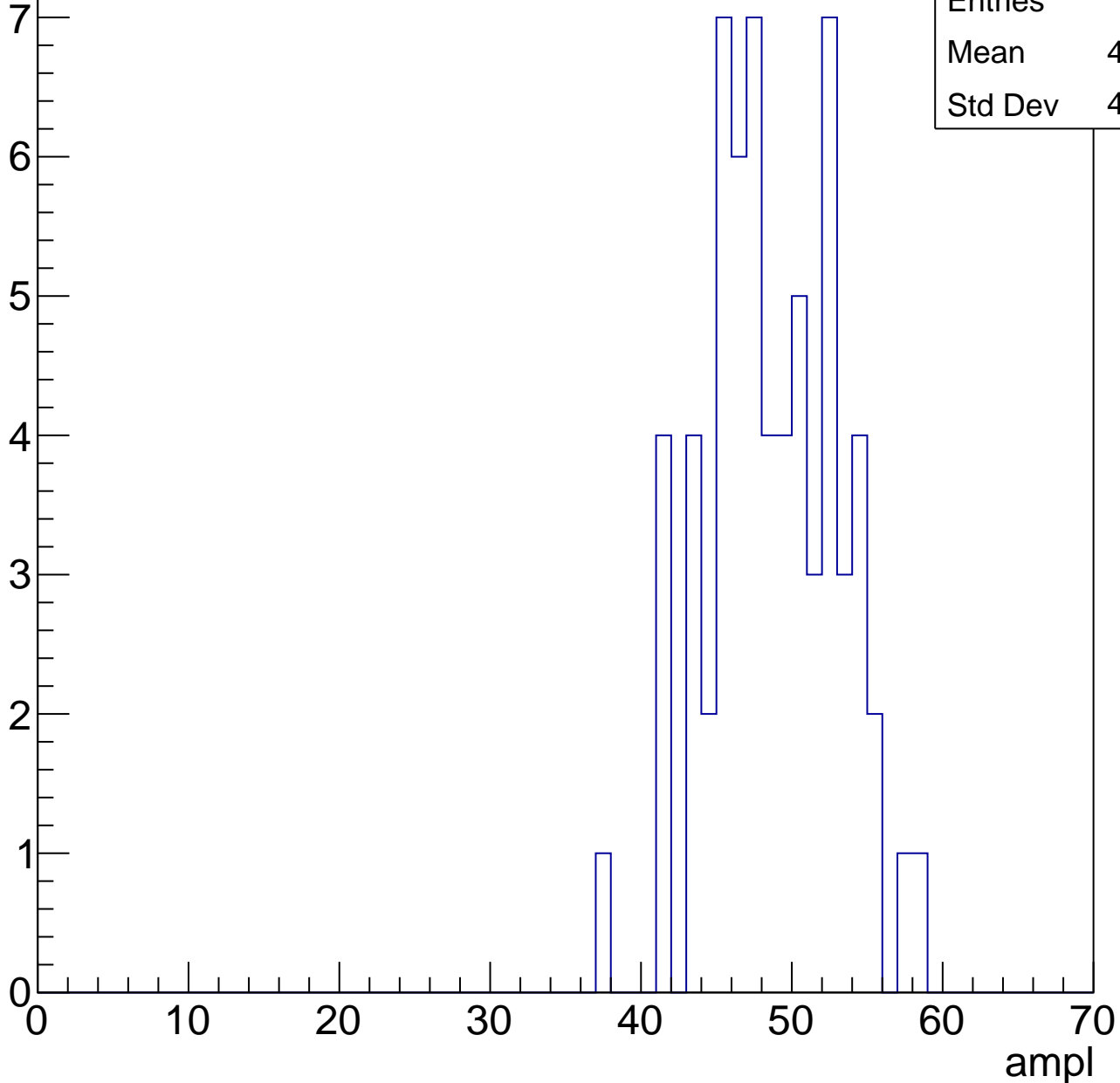


B1L102S, U18-ch103, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	48.25
Std Dev	4.297

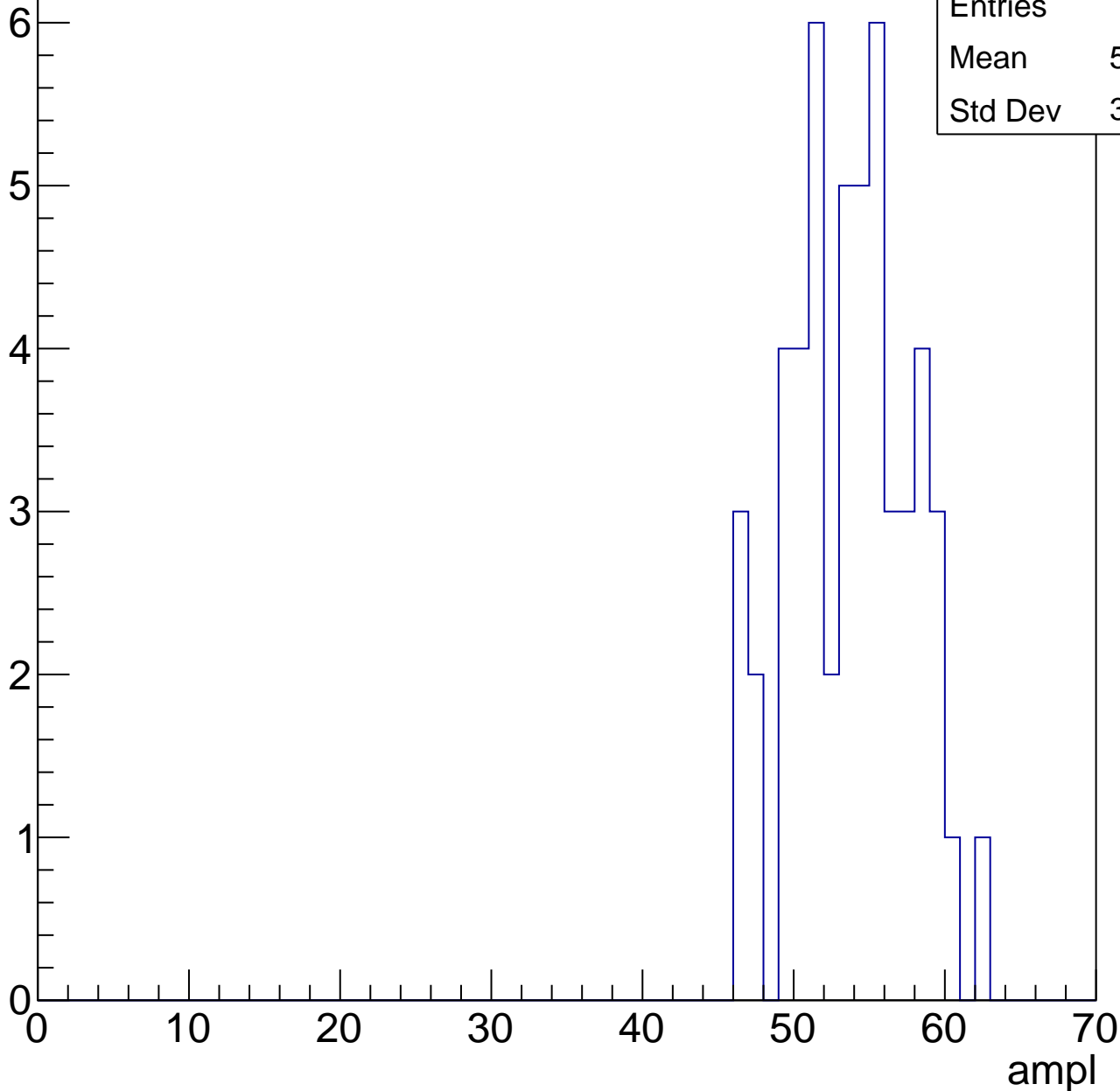


B1L102S, U18-ch103, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.33
Std Dev	3.877

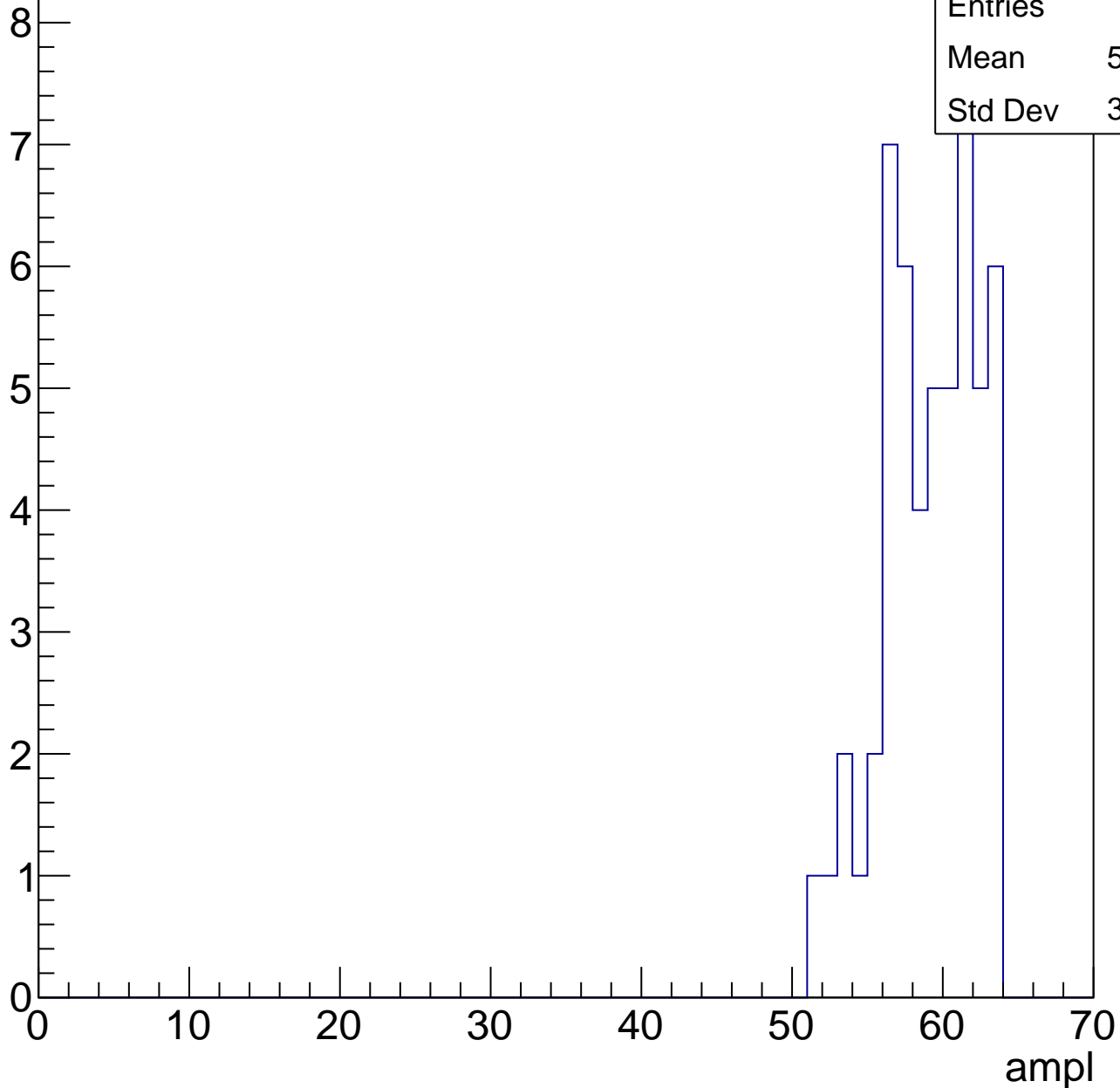


B1L102S, U18-ch103, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.68
Std Dev	3.088



B1L102S, U18-ch103, adc6

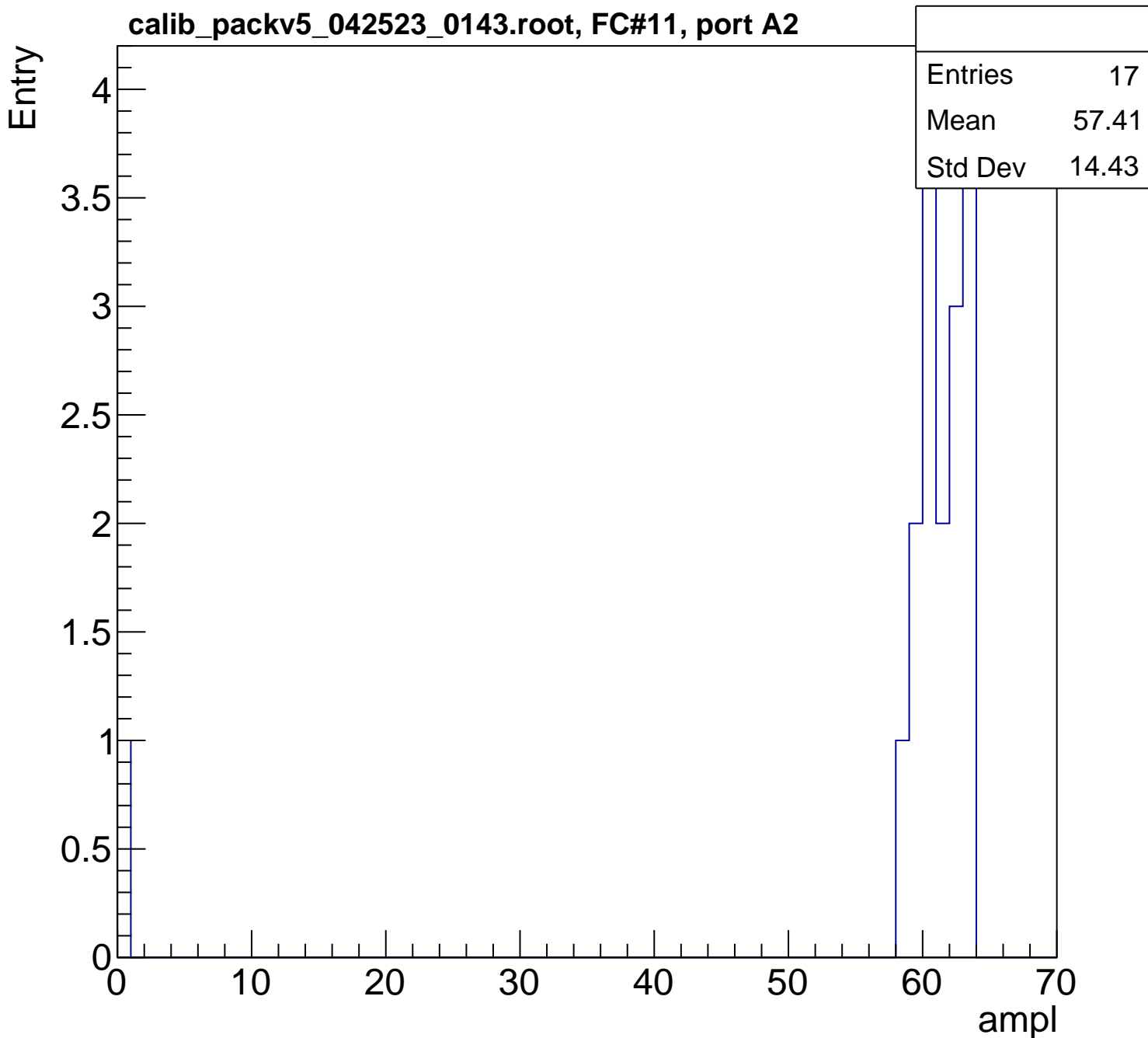
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	17
Mean	57.41
Std Dev	14.43

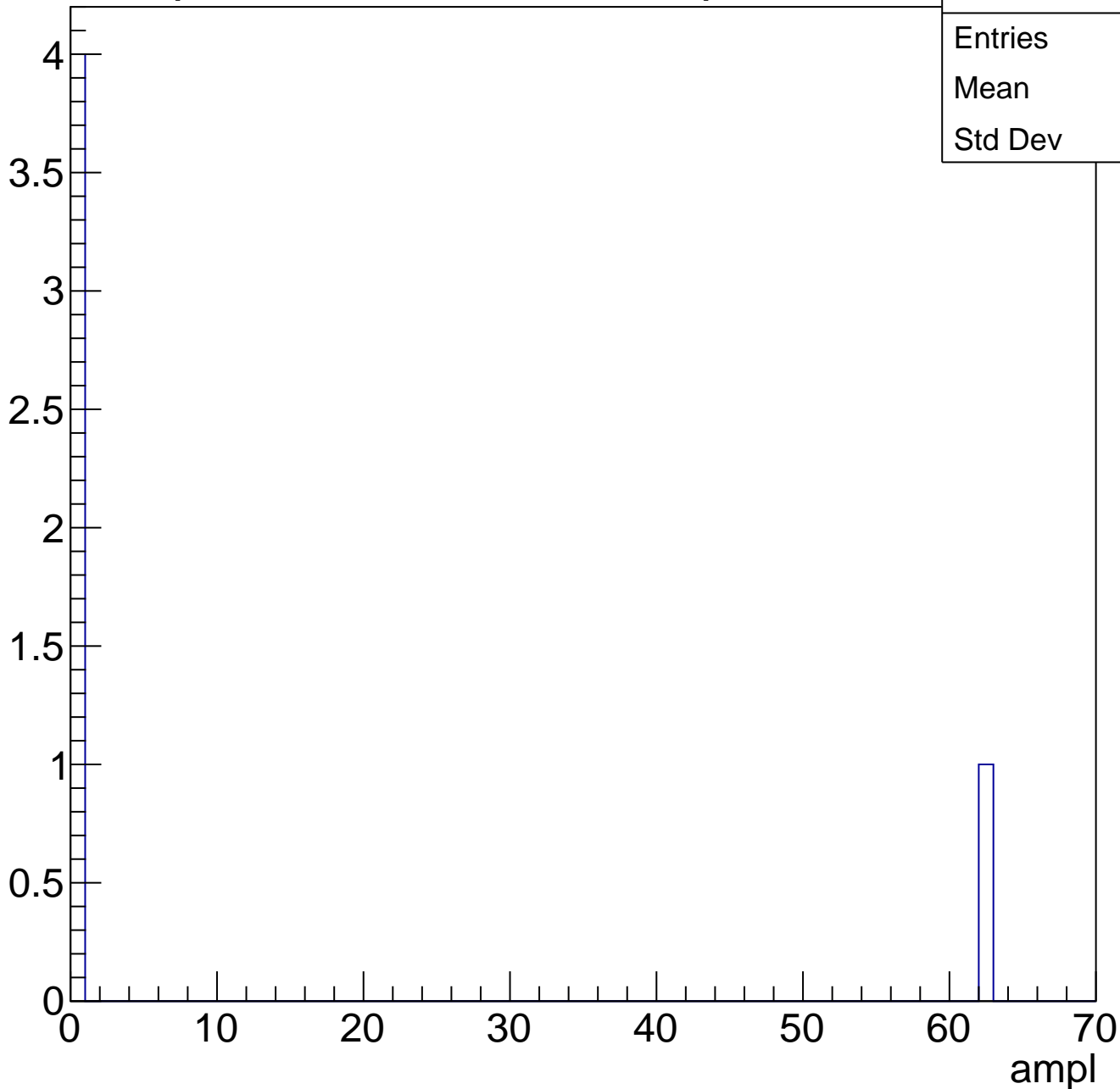
ampl



B1L102S, U18-ch103, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch104, adc0

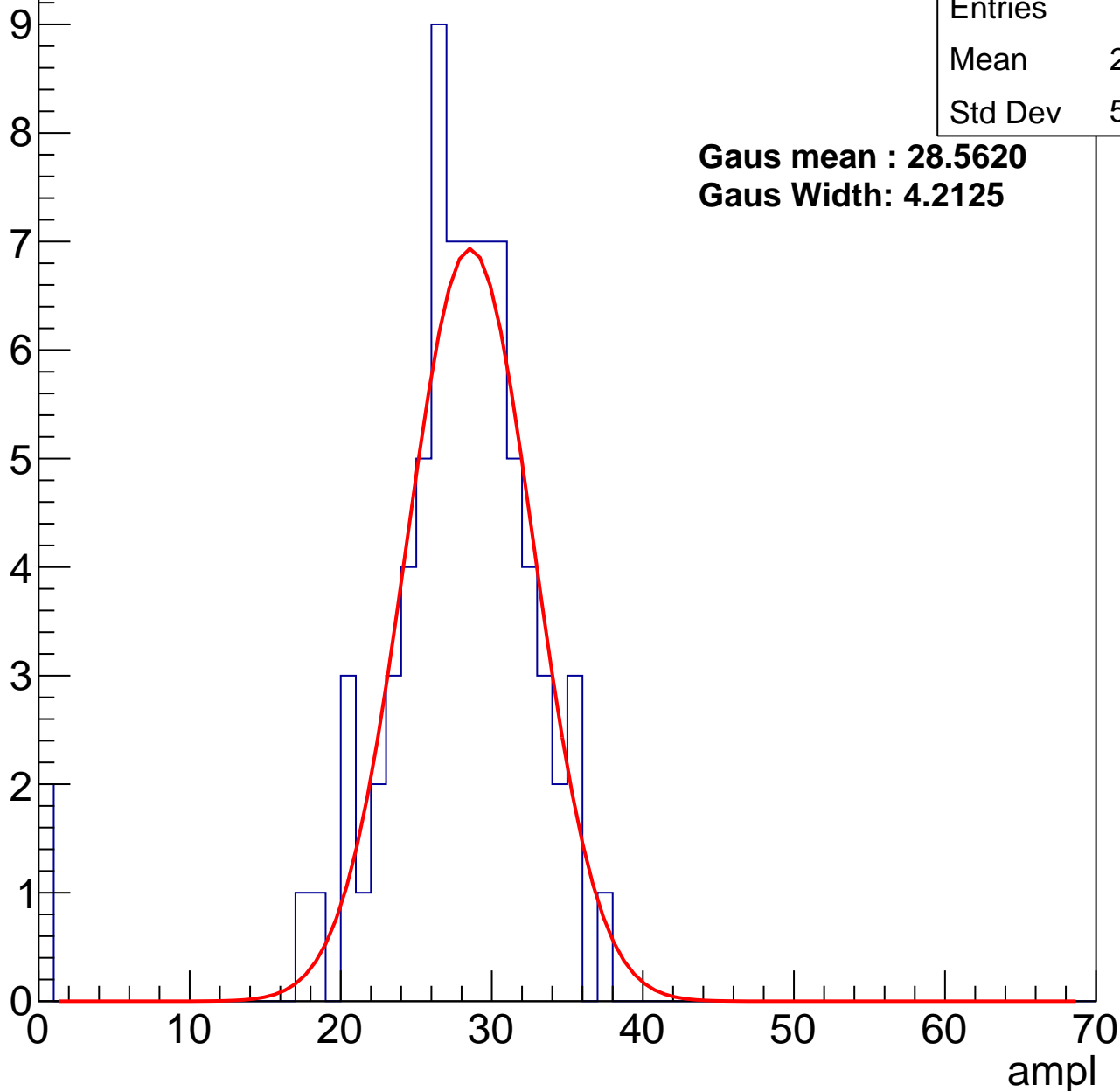
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	77
Mean	26.94
Std Dev	5.988

Gaus mean : 28.5620

Gaus Width: 4.2125



B1L102S, U18-ch104, adc1

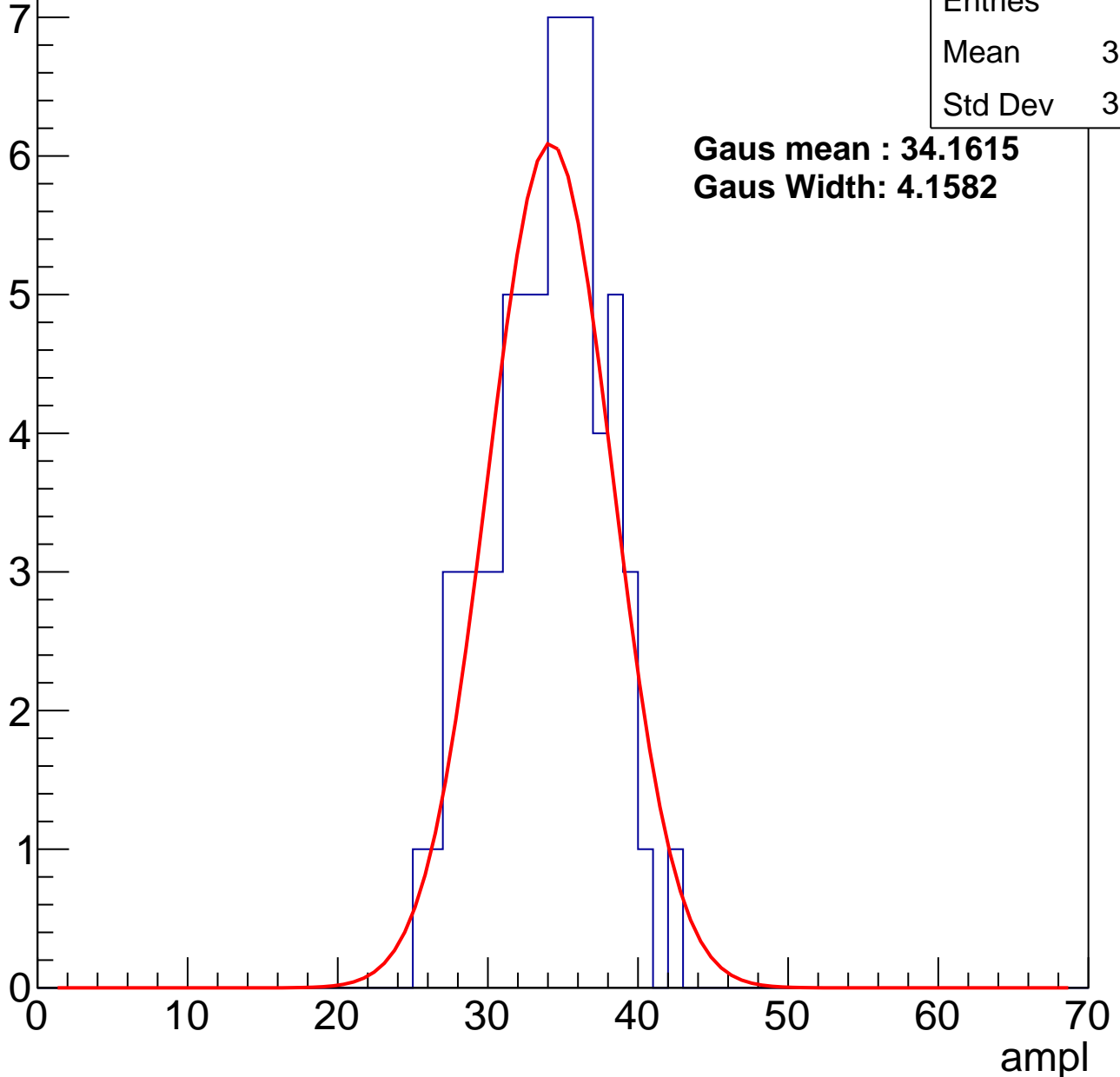
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	33.52
Std Dev	3.758

Gaus mean : 34.1615

Gaus Width: 4.1582



B1L102S, U18-ch104, adc2

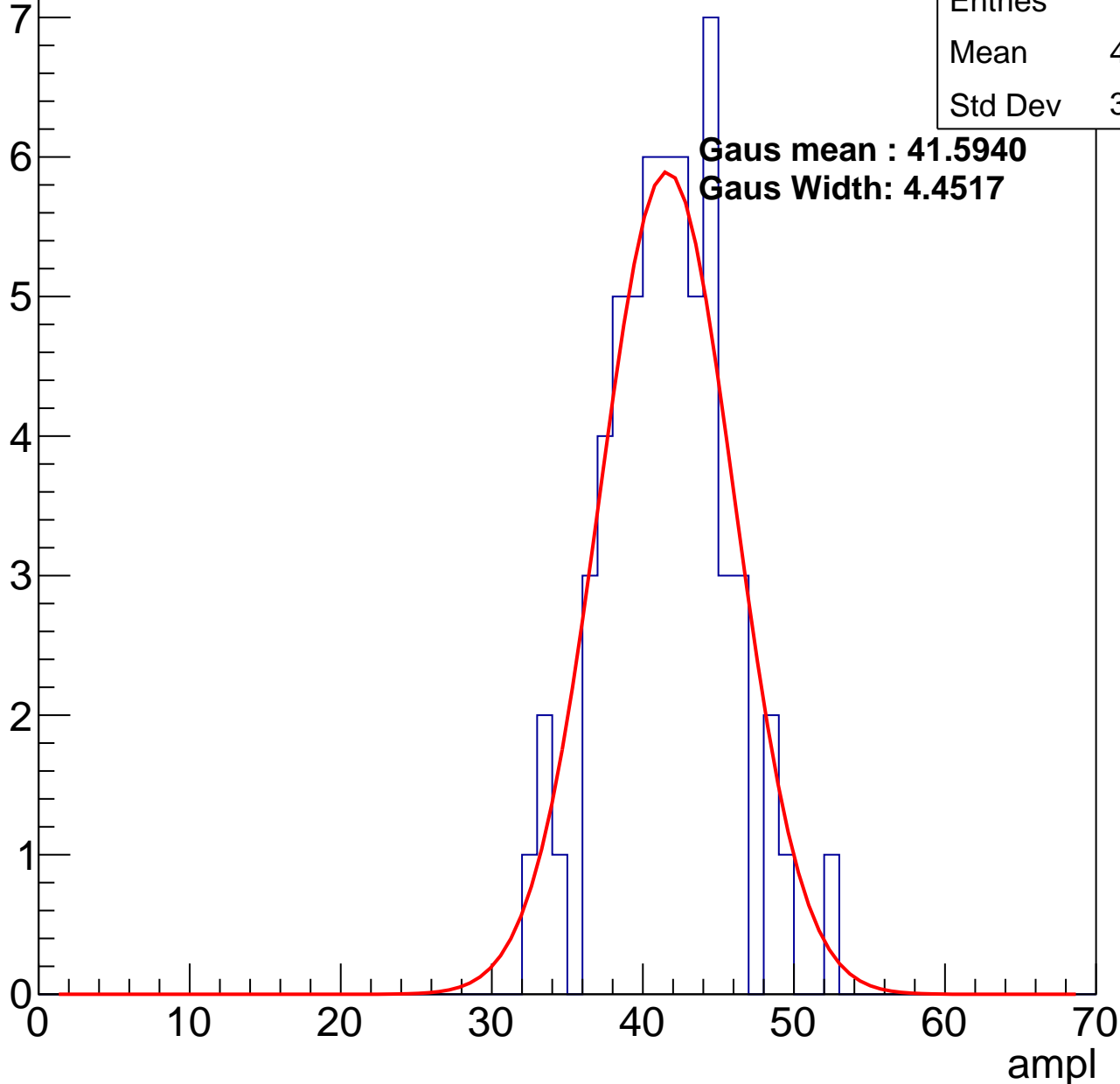
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	41.05
Std Dev	3.973

Gaus mean : 41.5940

Gaus Width: 4.4517

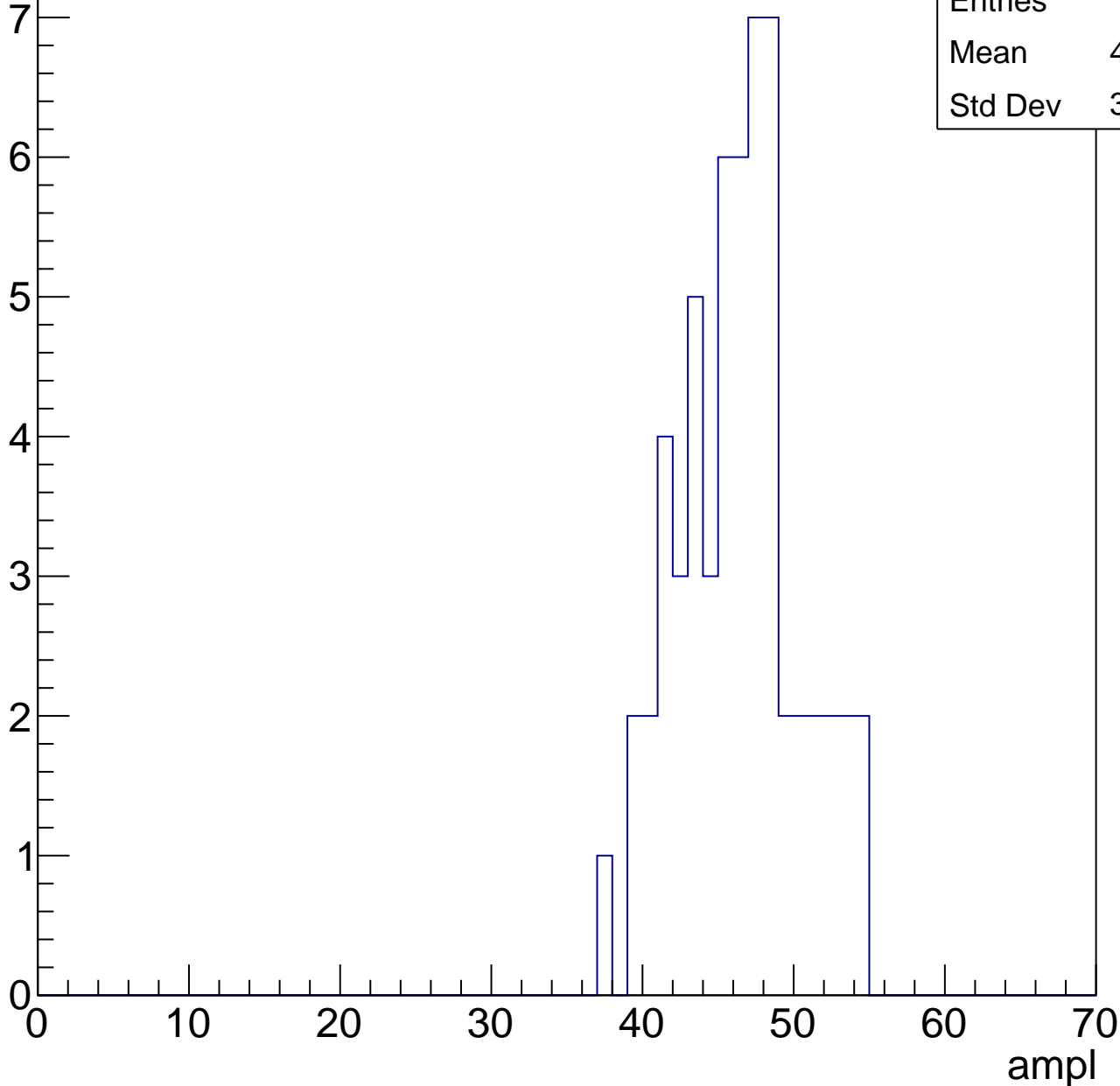


B1L102S, U18-ch104, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	45.88
Std Dev	3.926

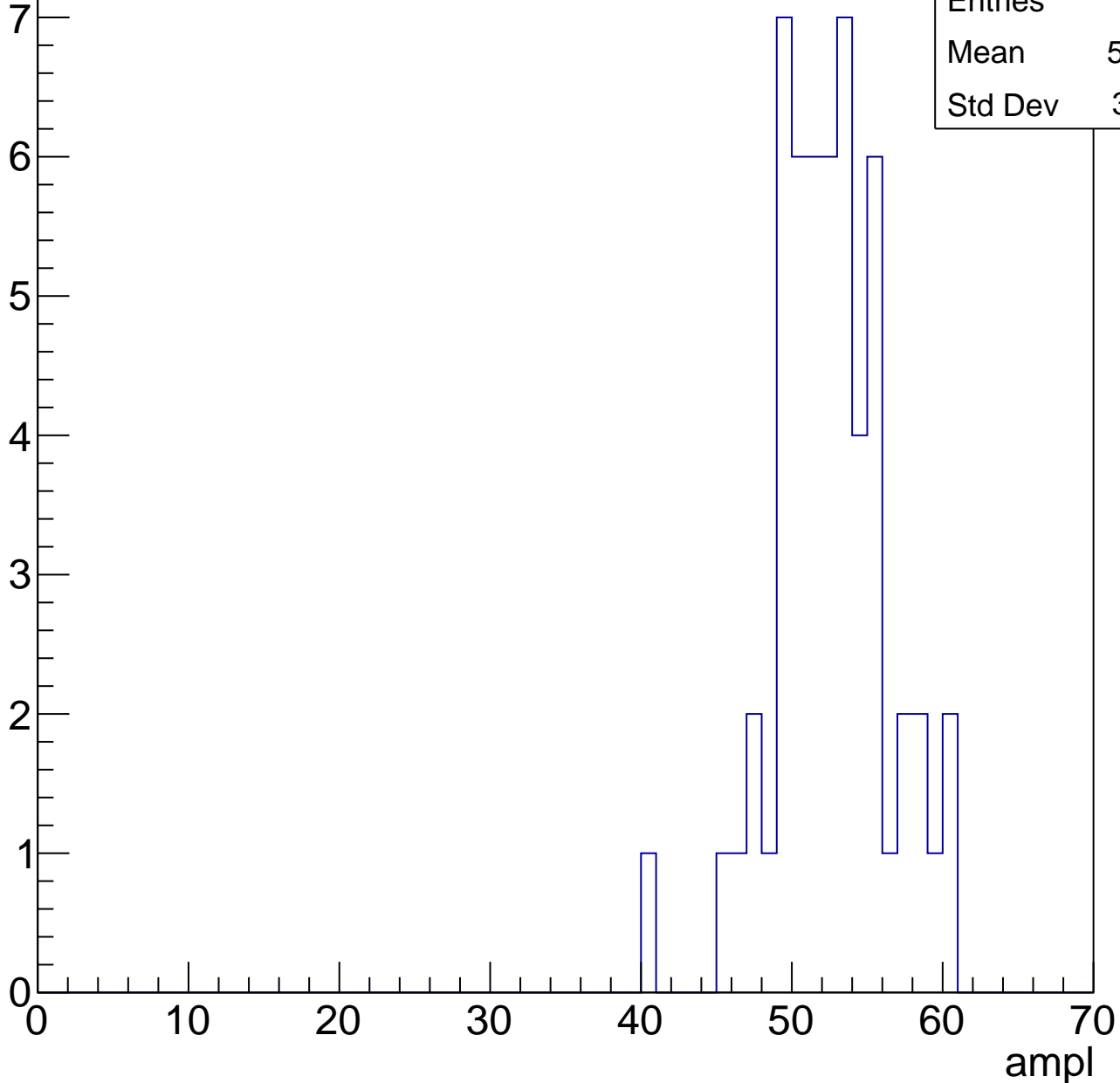


B1L102S, U18-ch104, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

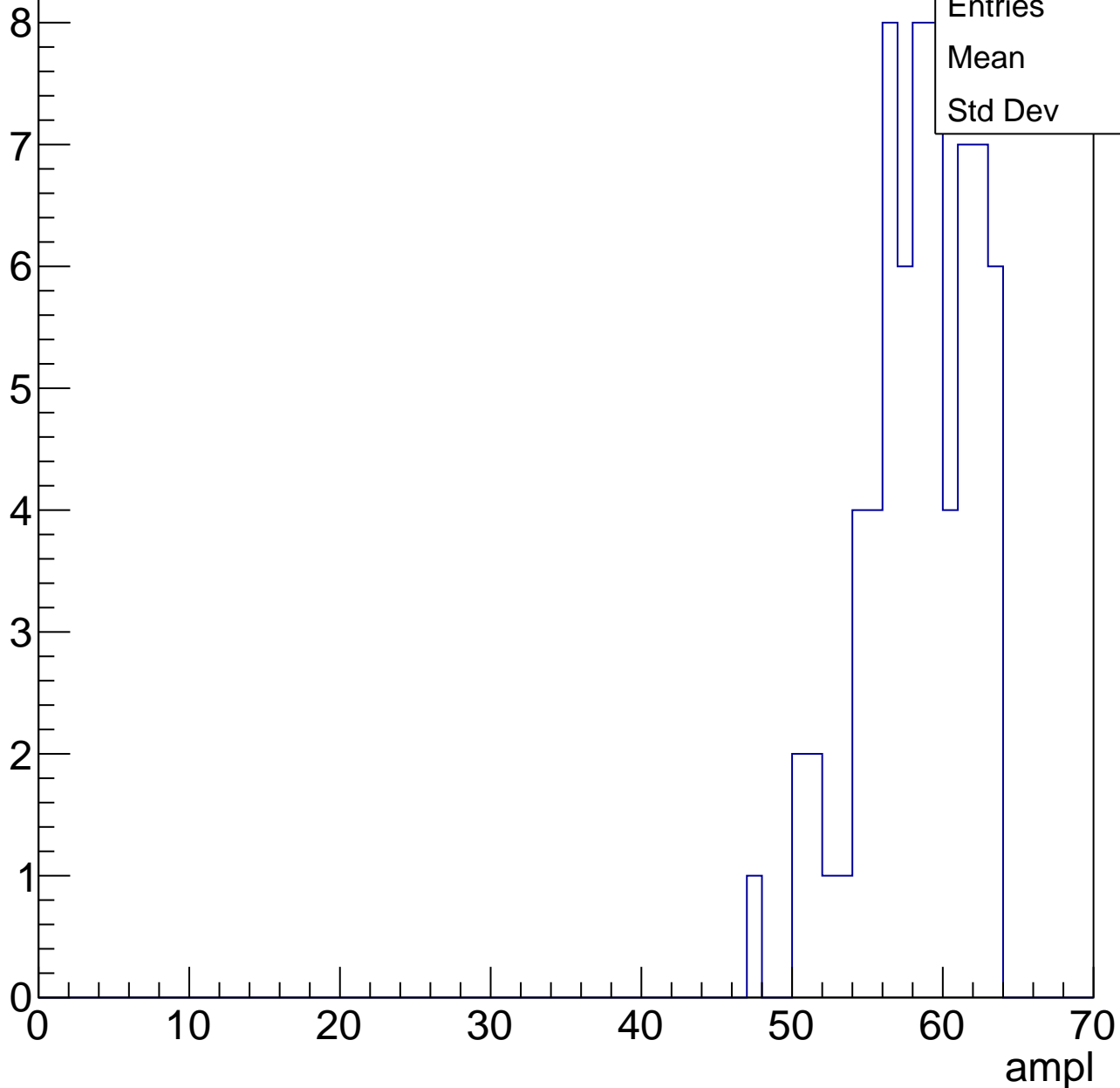
Entries	56
Mean	52.07
Std Dev	3.731



B1L102S, U18-ch104, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch104, adc6

calib_packv5_042523_0143.root, FC#11, port A2

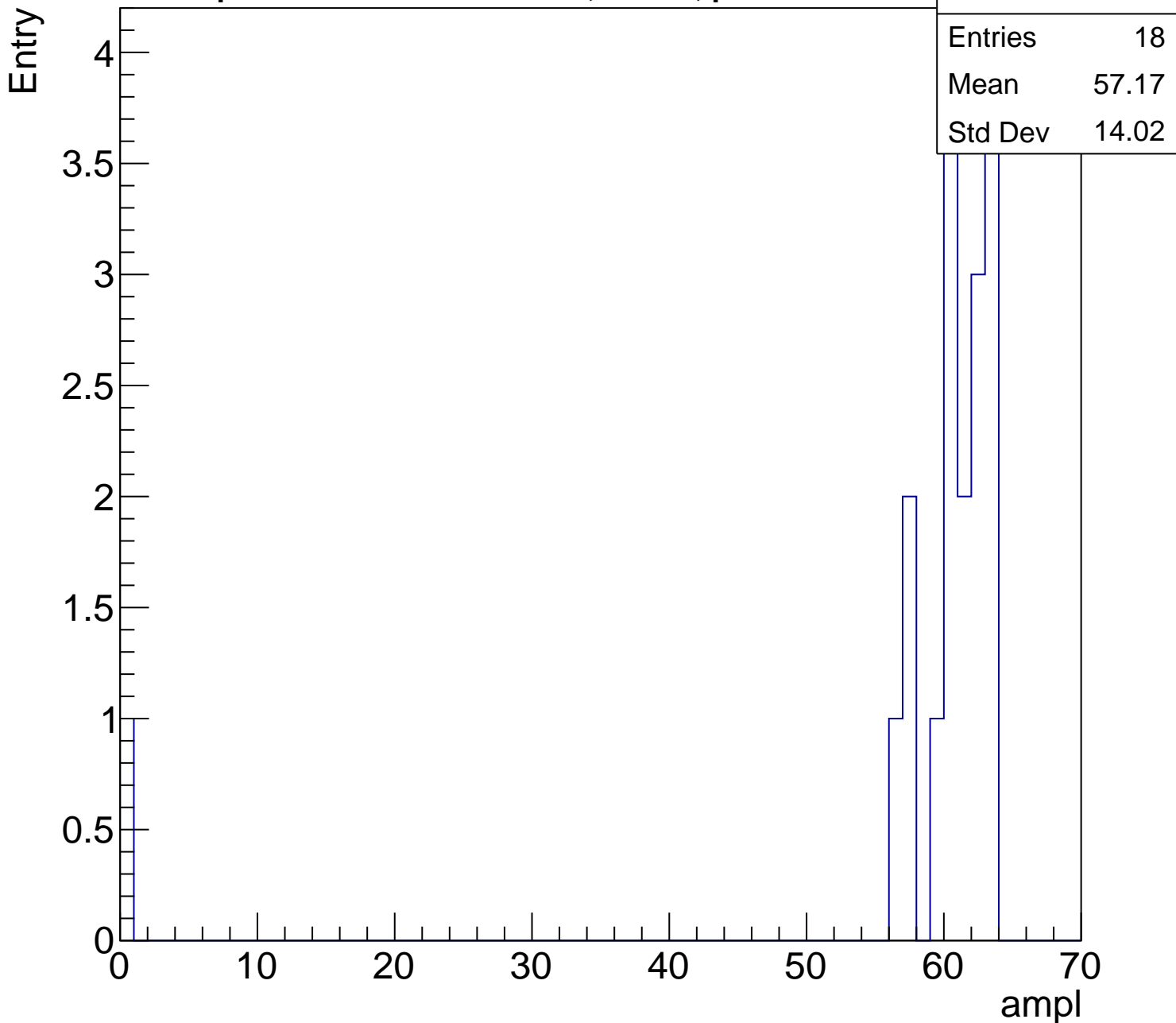
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	18
Mean	57.17
Std Dev	14.02

ampl

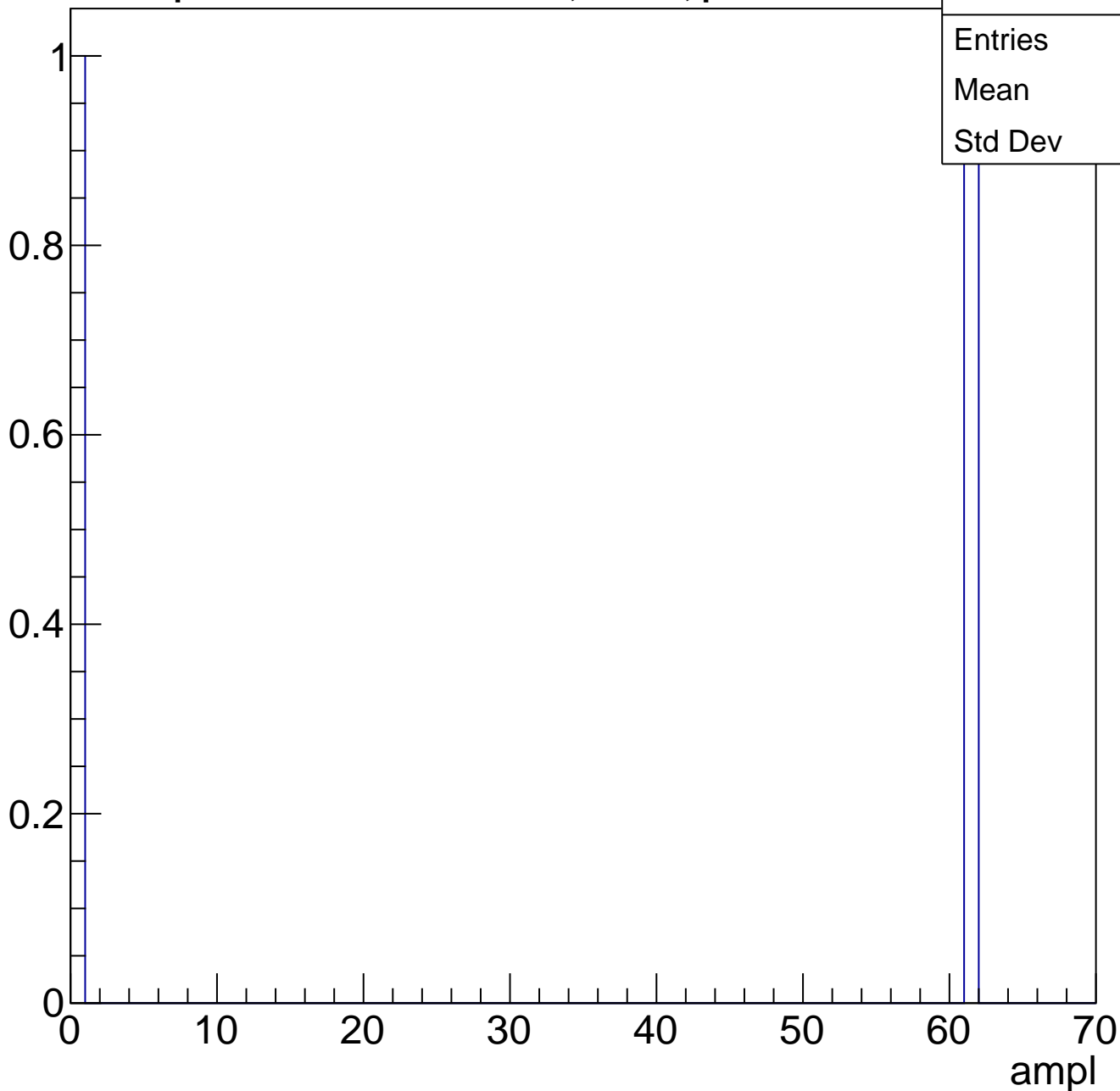
0 10 20 30 40 50 60 70



B1L102S, U18-ch104, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch105, adc0

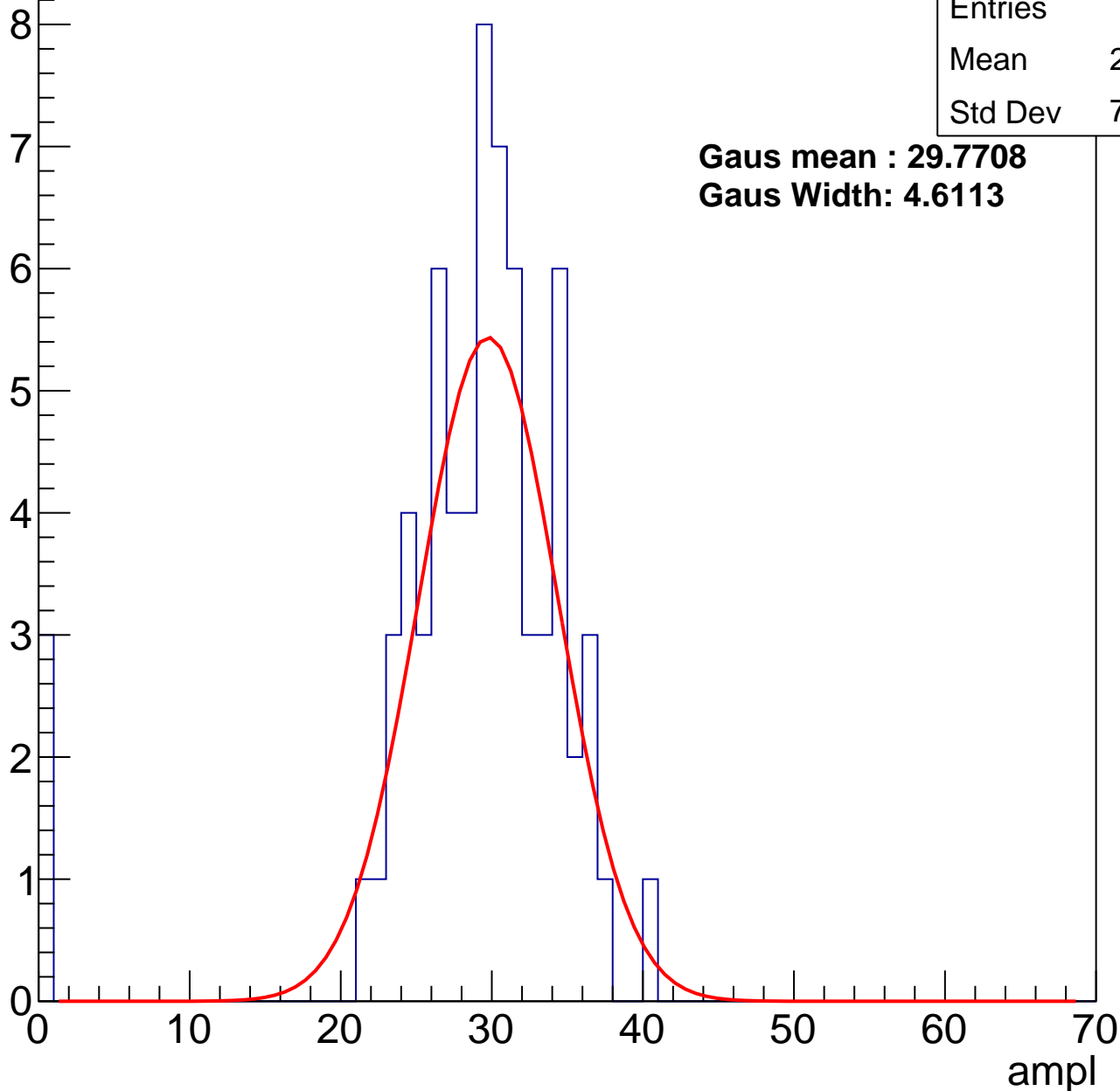
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	28.13
Std Dev	7.197

Gaus mean : 29.7708

Gaus Width: 4.6113



B1L102S, U18-ch105, adc1

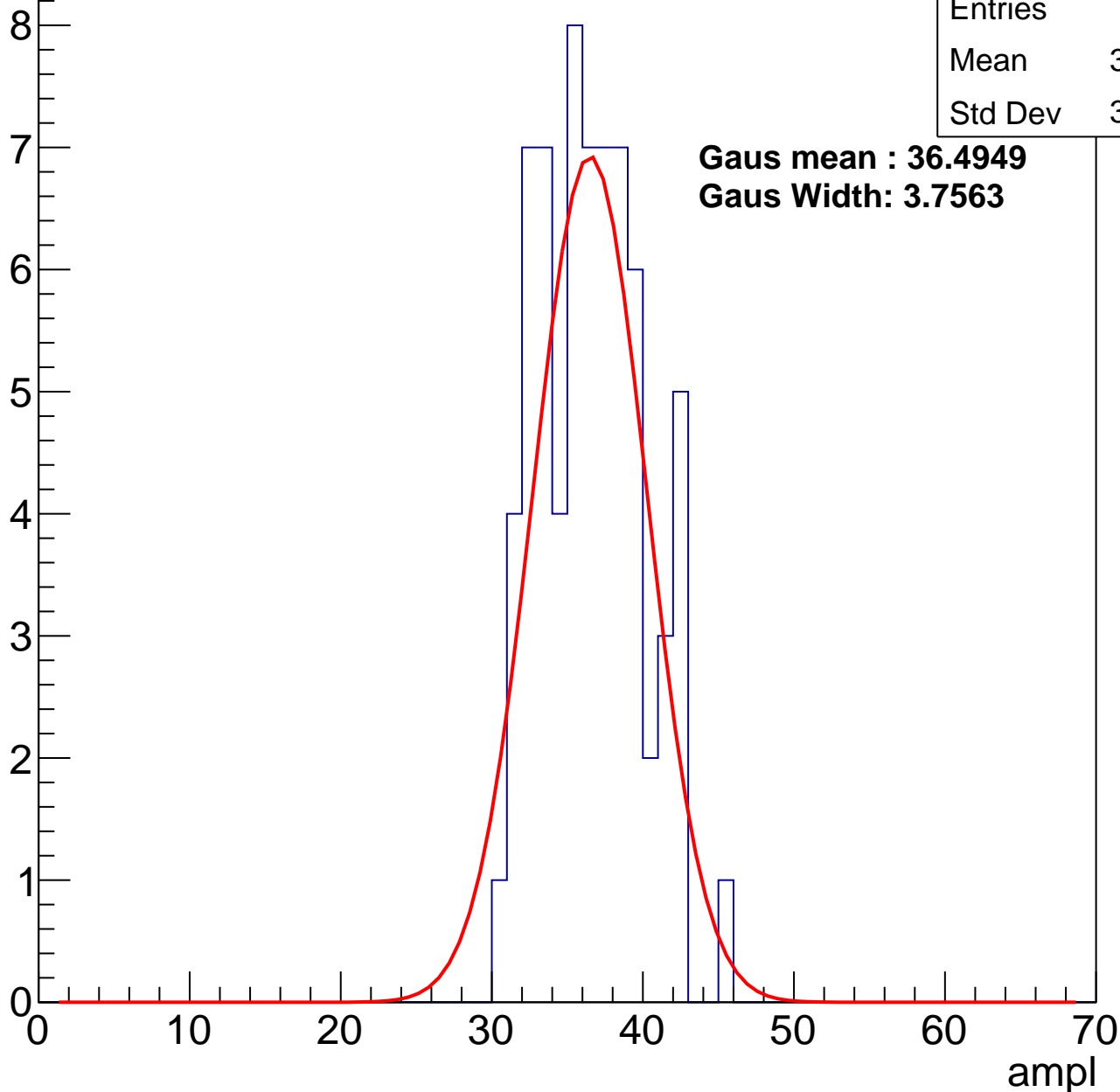
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	36.14
Std Dev	3.385

Gaus mean : 36.4949

Gaus Width: 3.7563



B1L102S, U18-ch105, adc2

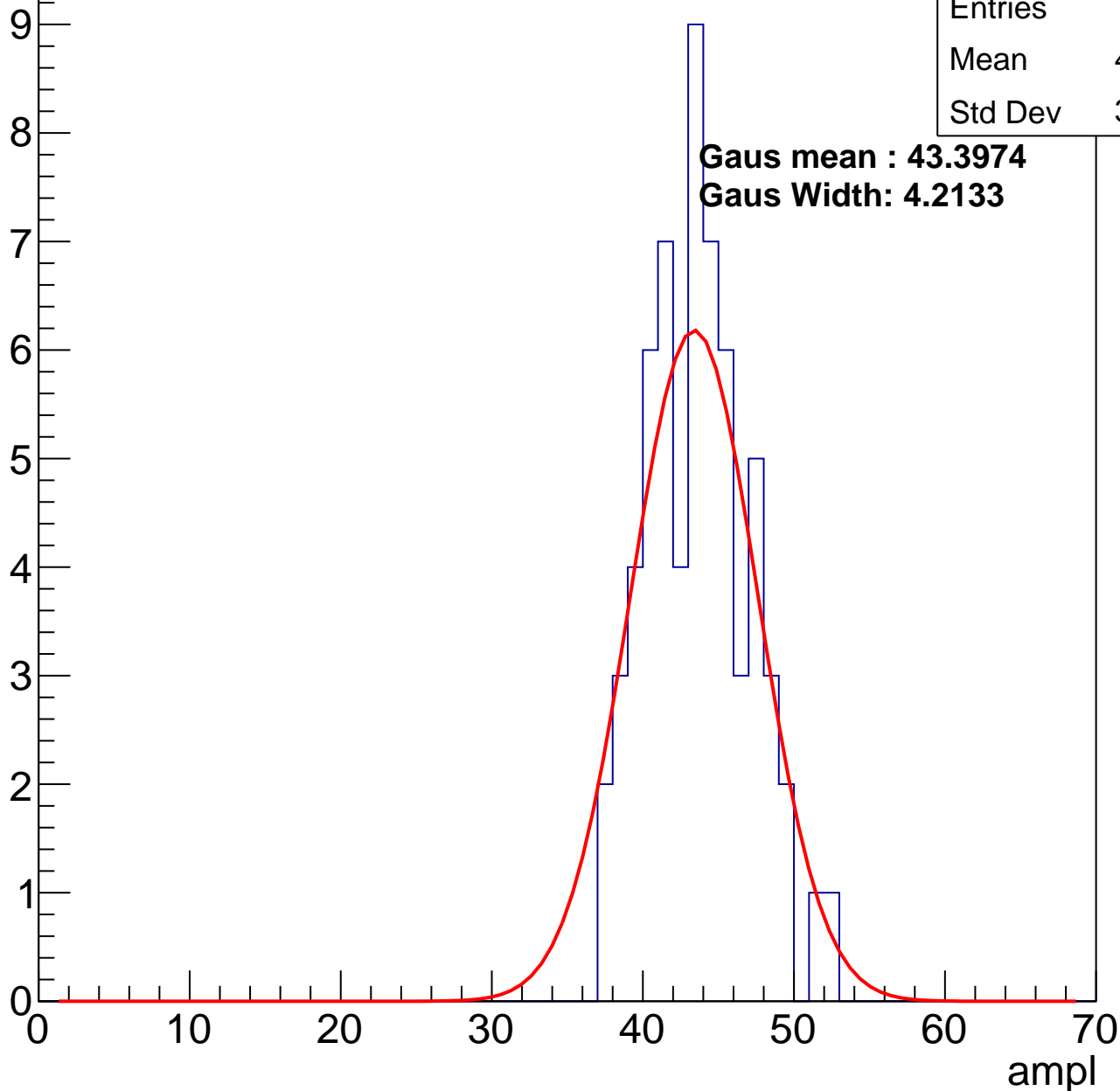
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	43.21
Std Dev	3.391

Gaus mean : 43.3974

Gaus Width: 4.2133

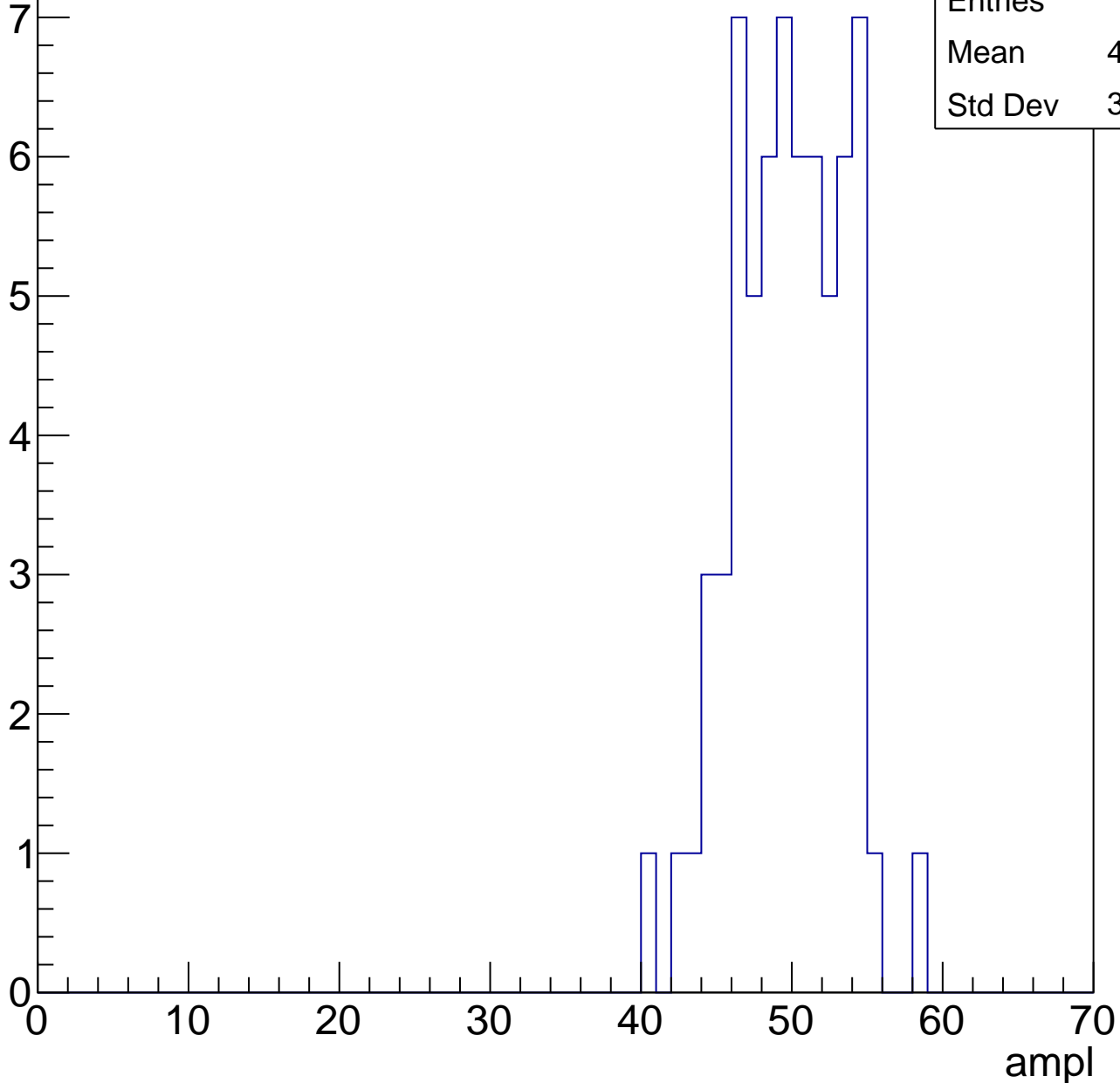


B1L102S, U18-ch105, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	49.32
Std Dev	3.555

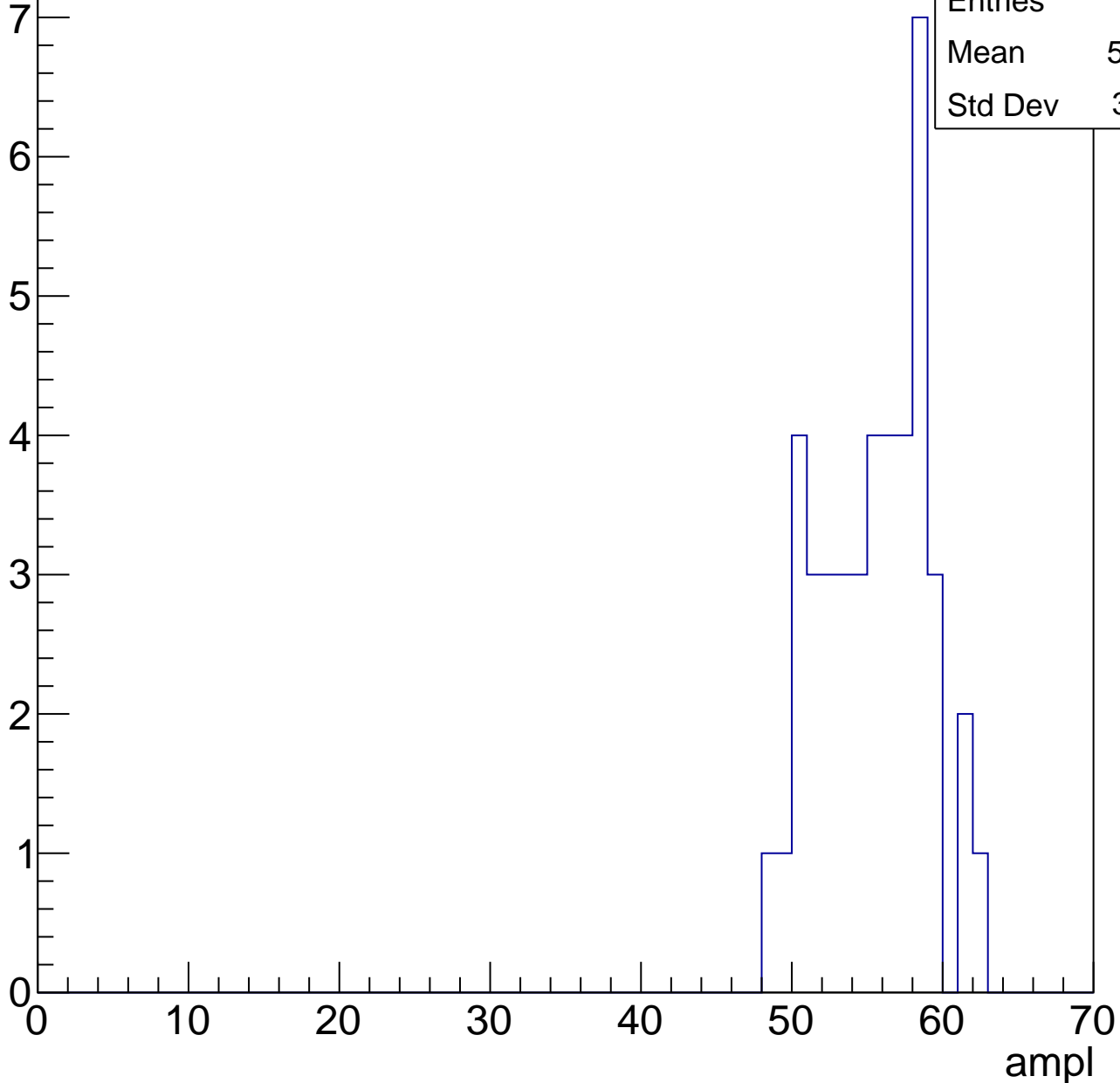


B1L102S, U18-ch105, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	55.02
Std Dev	3.501

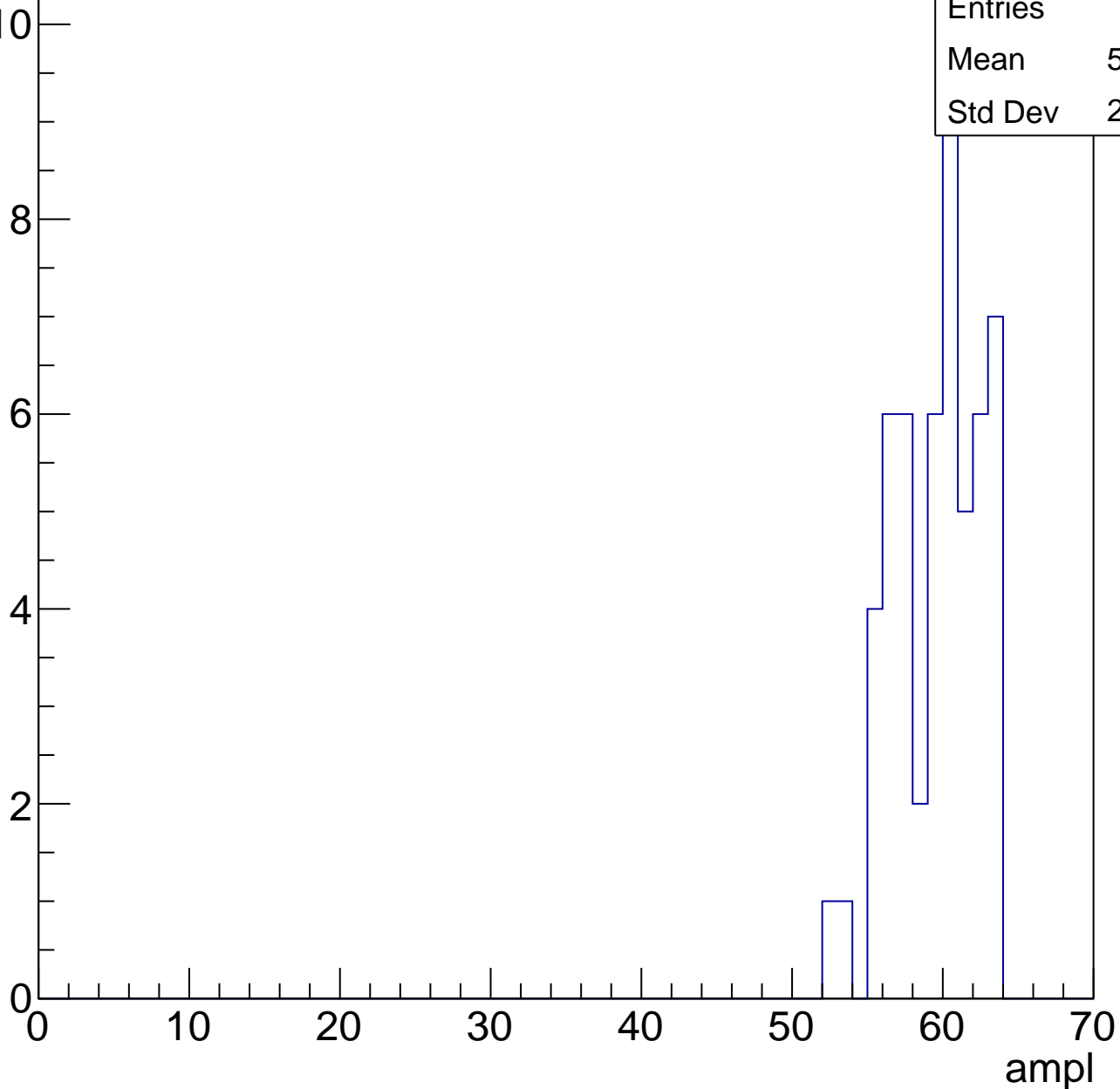


B1L102S, U18-ch105, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

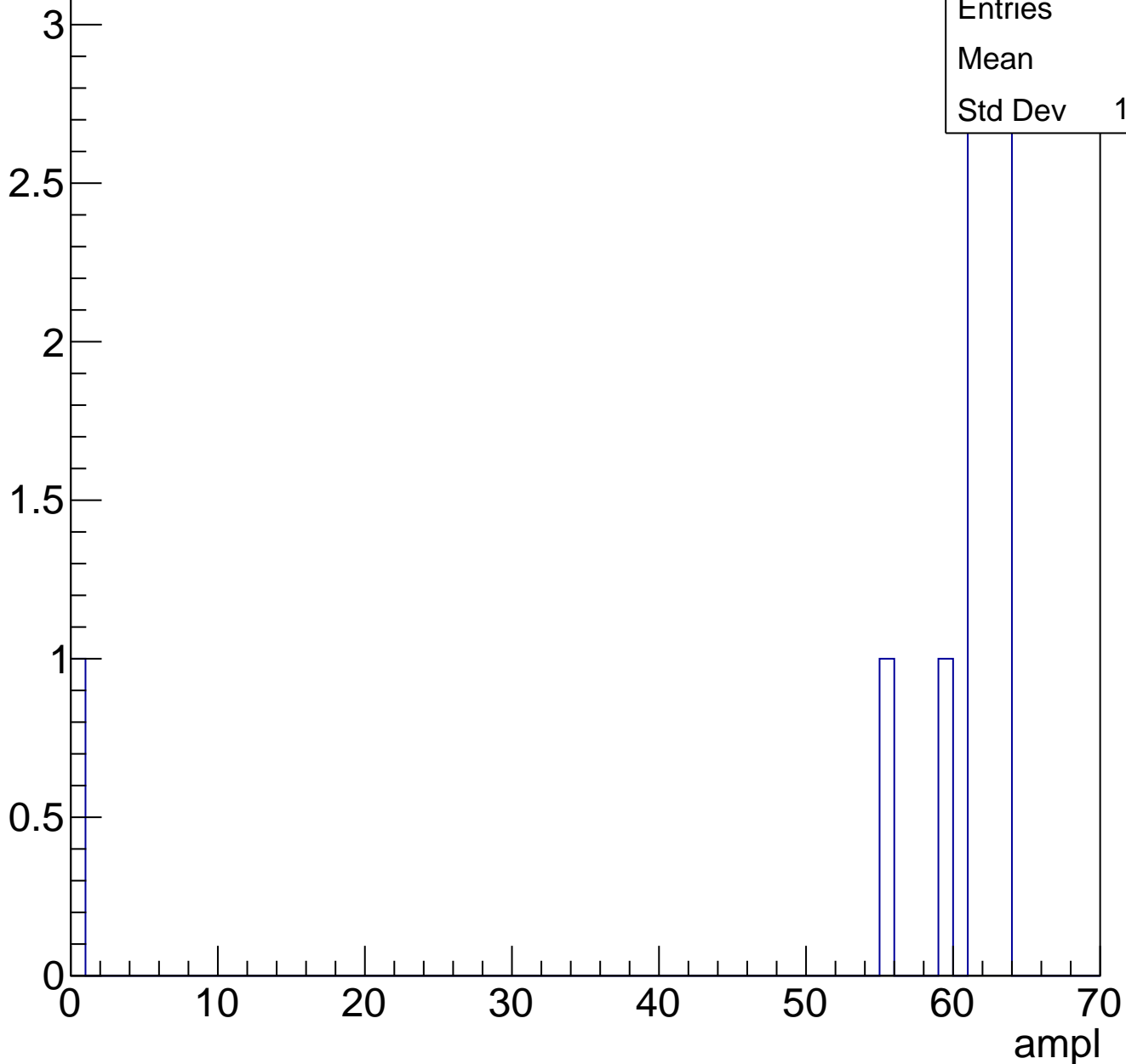
Entries	54
Mean	59.09
Std Dev	2.804



B1L102S, U18-ch105, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch105, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch106, adc0

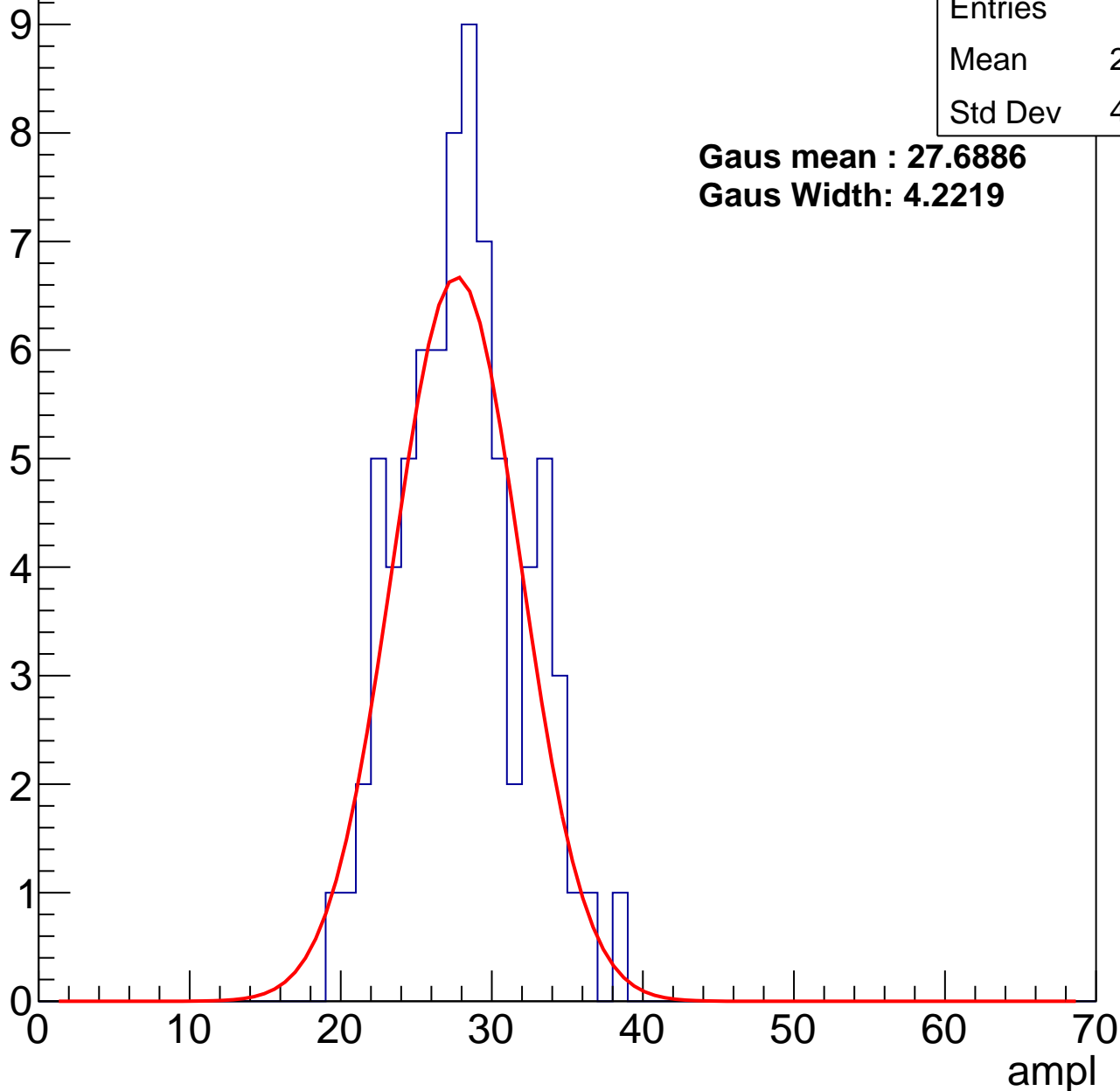
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	27.58
Std Dev	4.024

Gaus mean : 27.6886

Gaus Width: 4.2219



B1L102S, U18-ch106, adc1

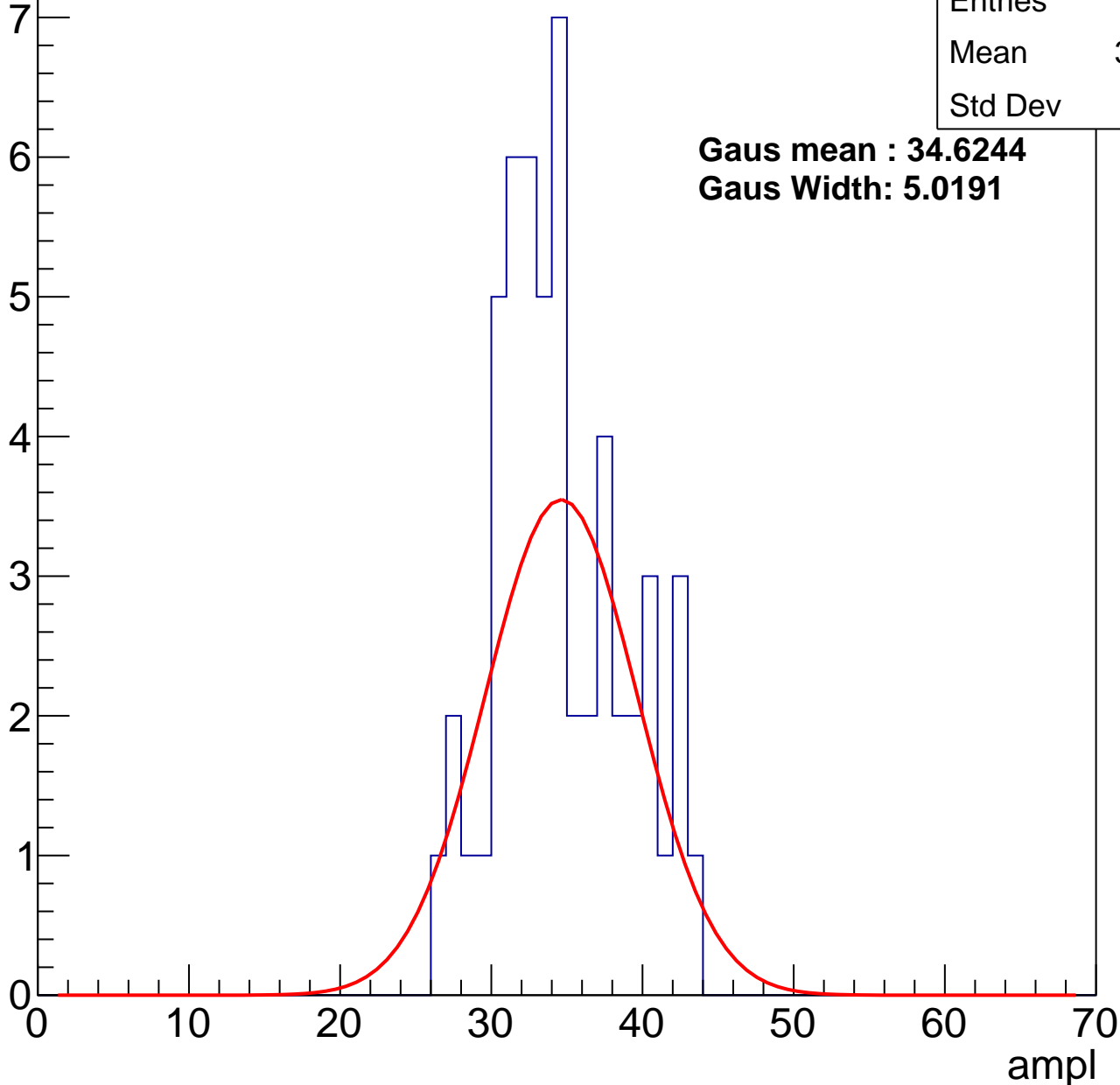
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	34.11
Std Dev	4.18

Gaus mean : 34.6244

Gaus Width: 5.0191



B1L102S, U18-ch106, adc2

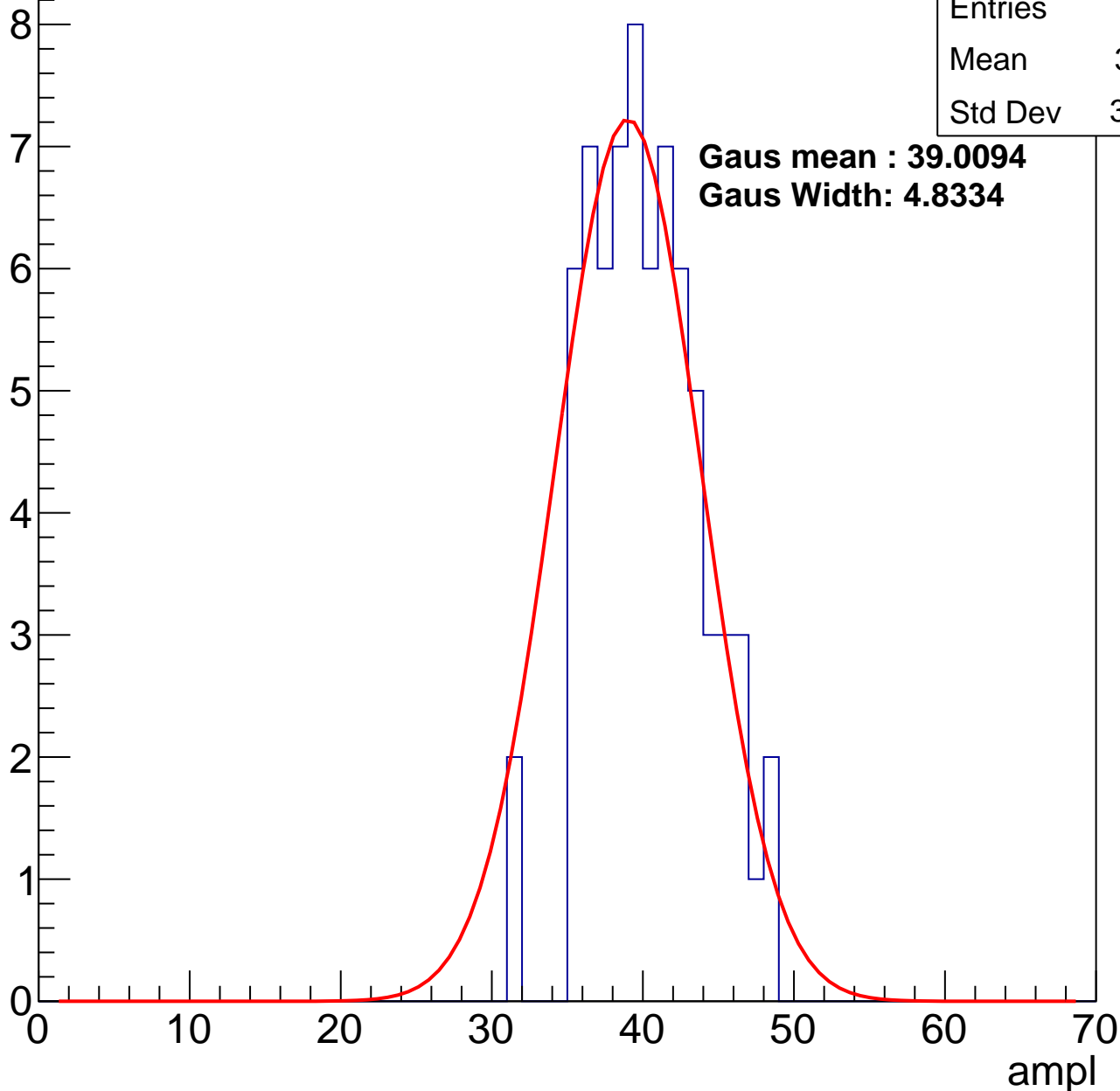
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	39.81
Std Dev	3.718

Gaus mean : 39.0094

Gaus Width: 4.8334

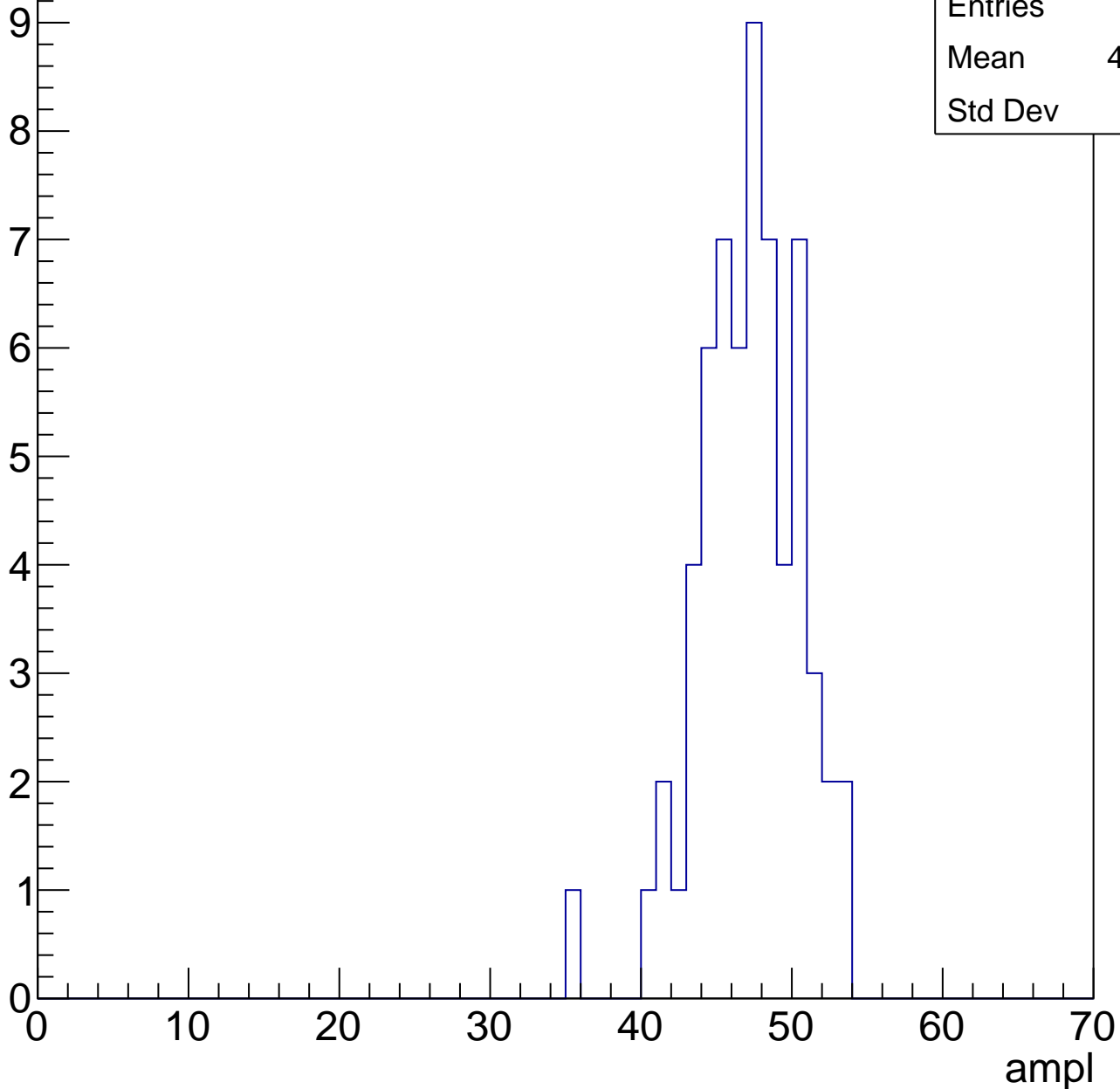


B1L102S, U18-ch106, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	46.68
Std Dev	3.34

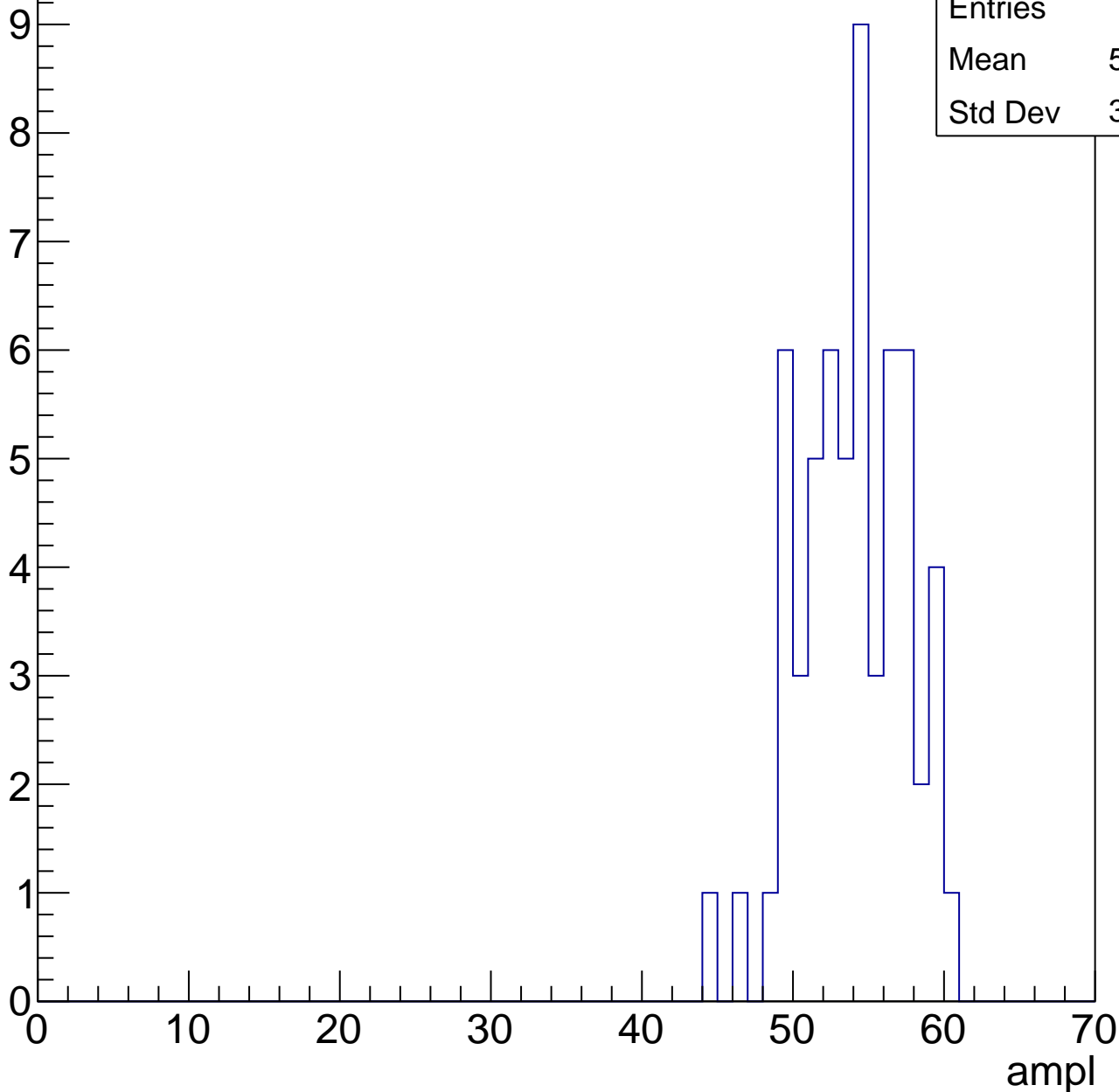


B1L102S, U18-ch106, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	53.47
Std Dev	3.456

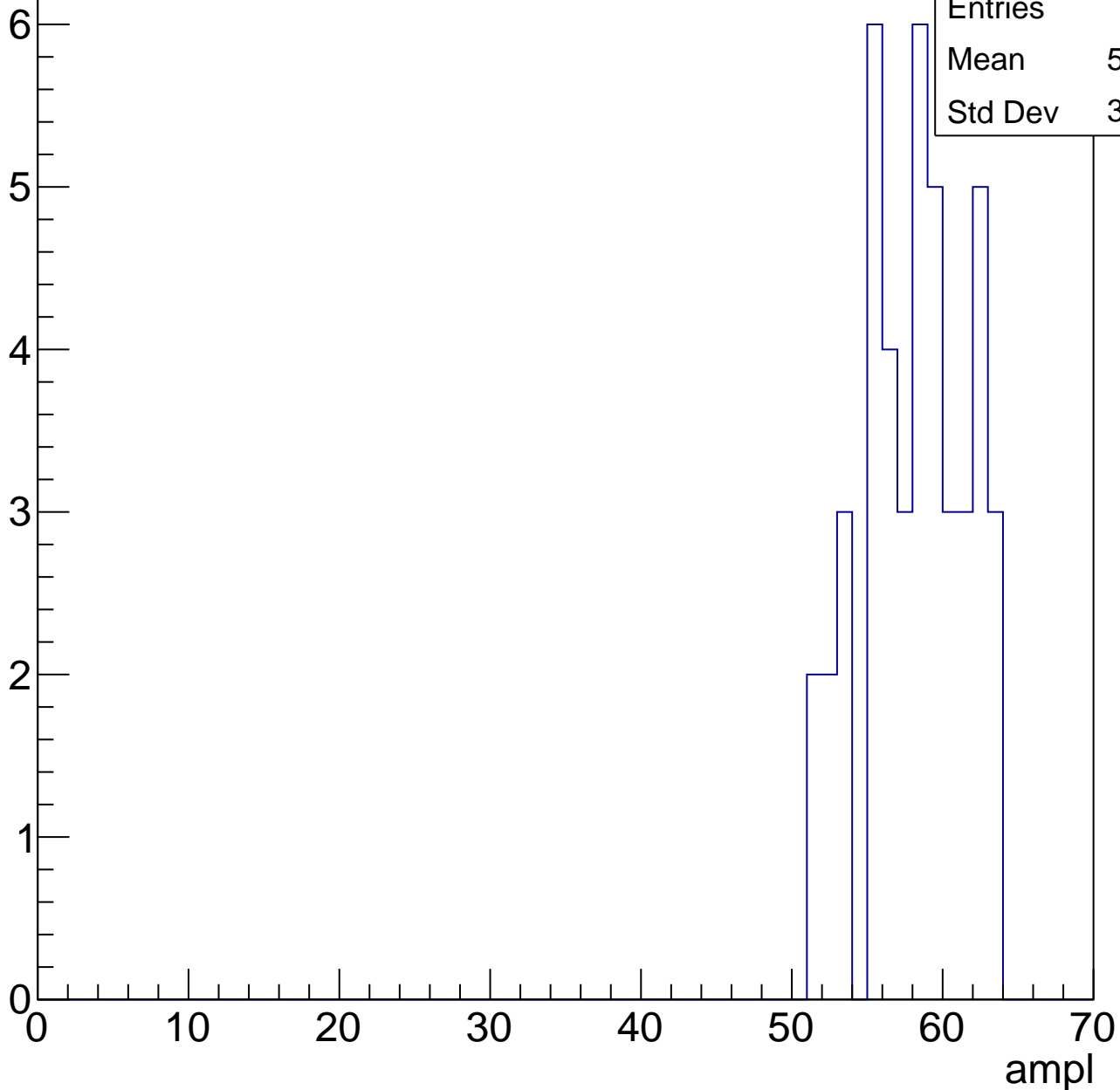


B1L102S, U18-ch106, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	57.67
Std Dev	3.373

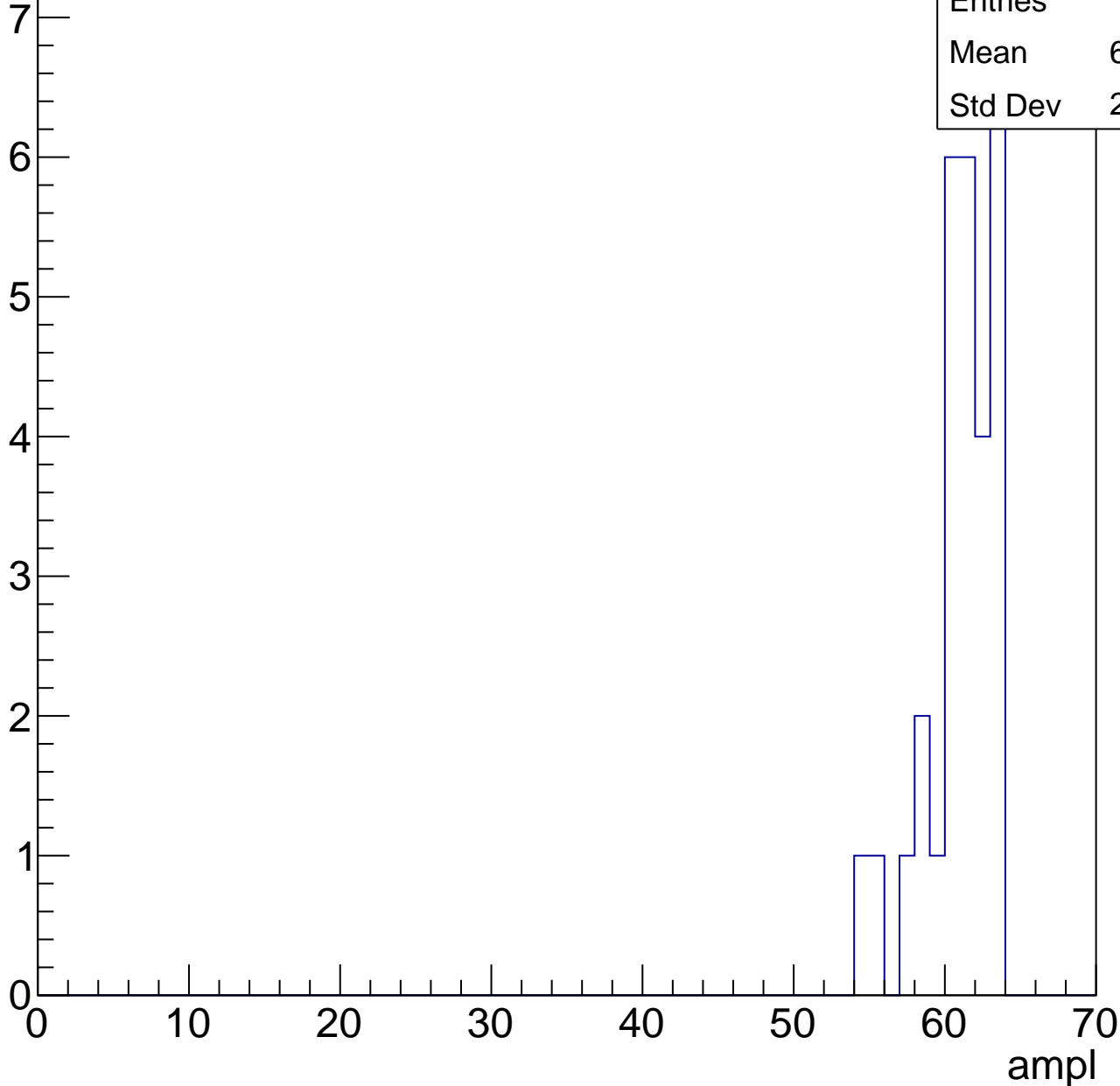


B1L102S, U18-ch106, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

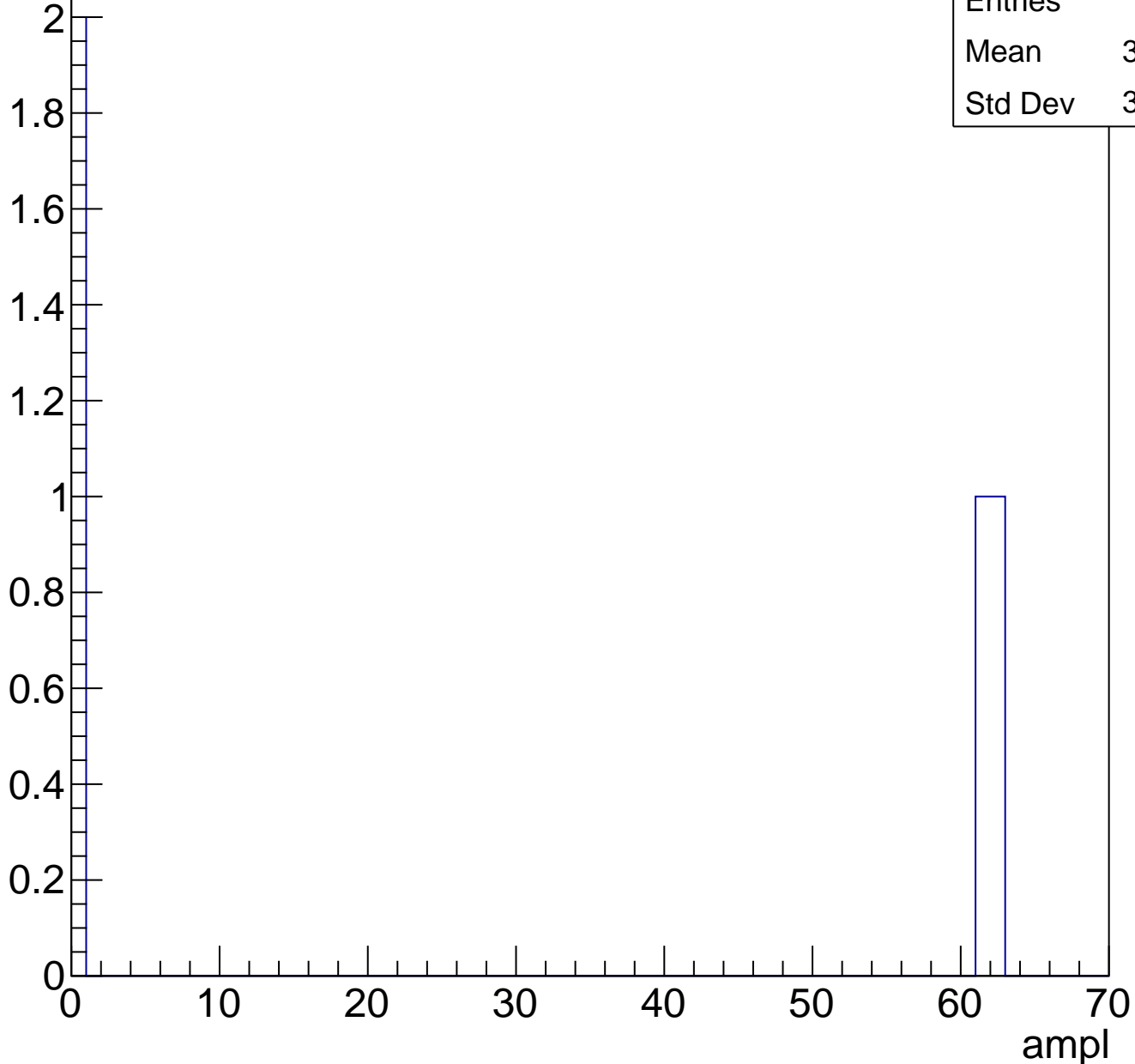
Entries	29
Mean	60.55
Std Dev	2.313



B1L102S, U18-ch106, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	30.75
Std Dev	30.75

B1L102S, U18-ch107, adc0

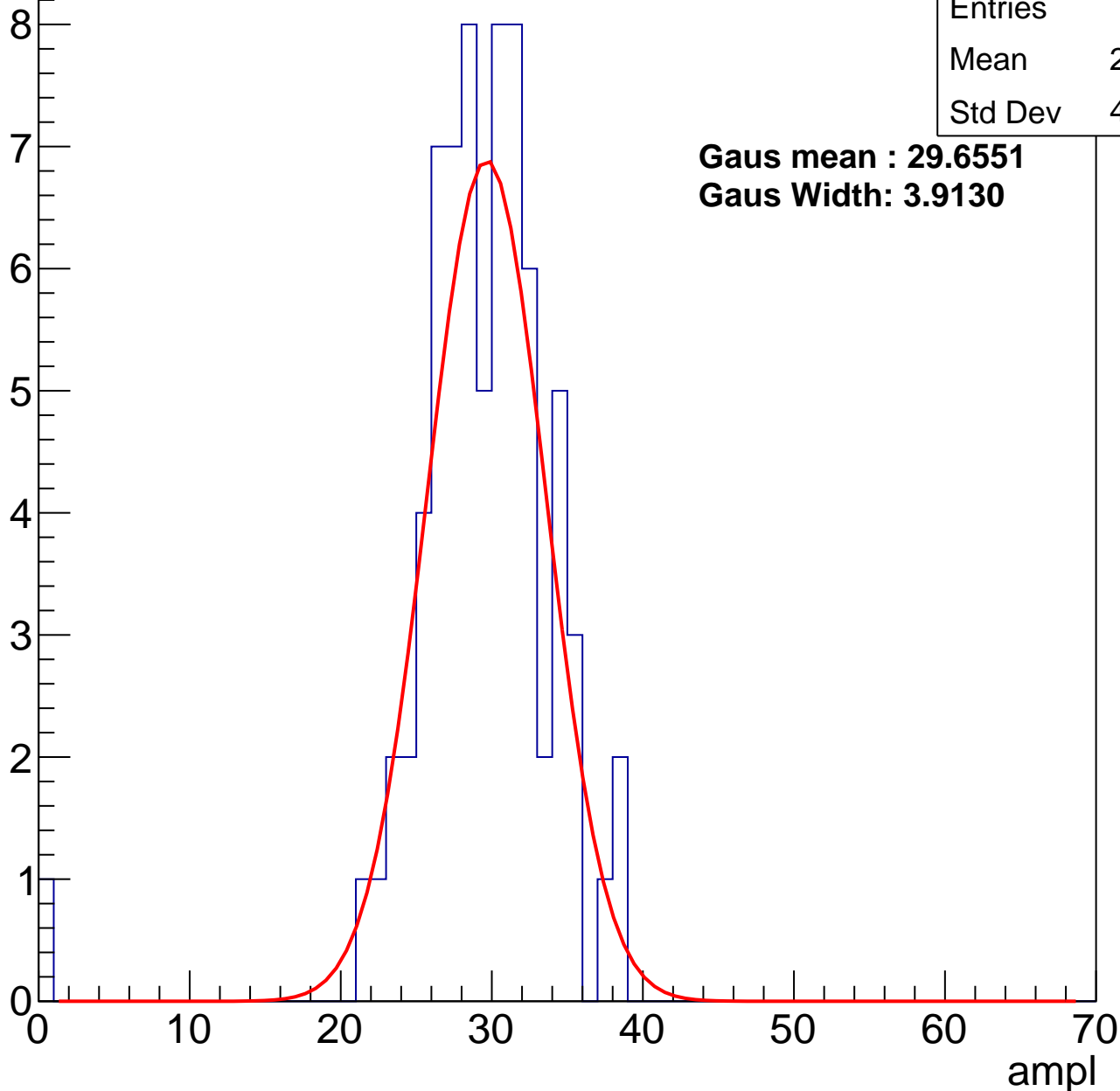
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	28.92
Std Dev	4.998

Gaus mean : 29.6551

Gaus Width: 3.9130



B1L102S, U18-ch107, adc1

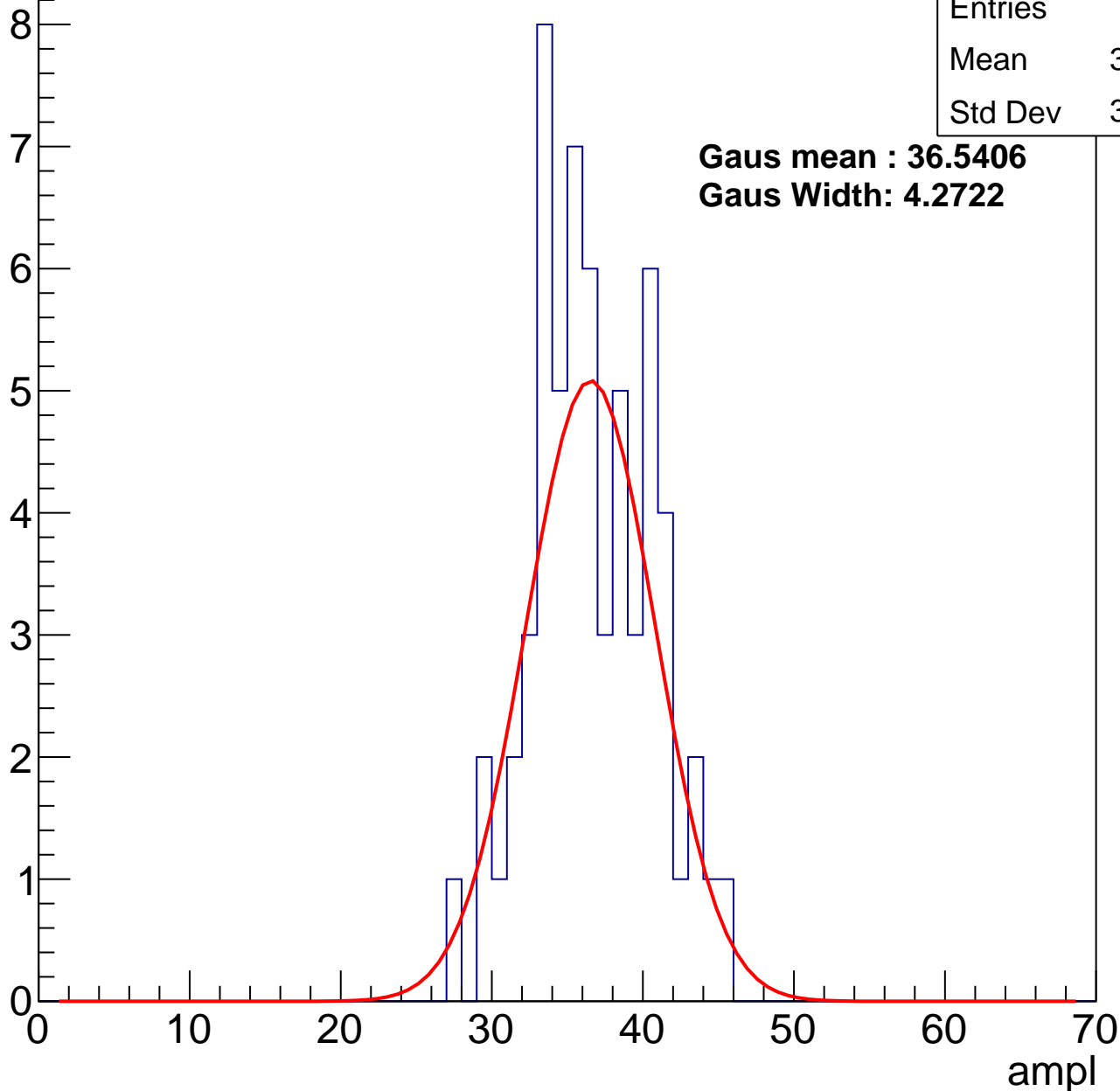
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	36.18
Std Dev	3.915

Gaus mean : 36.5406

Gaus Width: 4.2722



B1L102S, U18-ch107, adc2

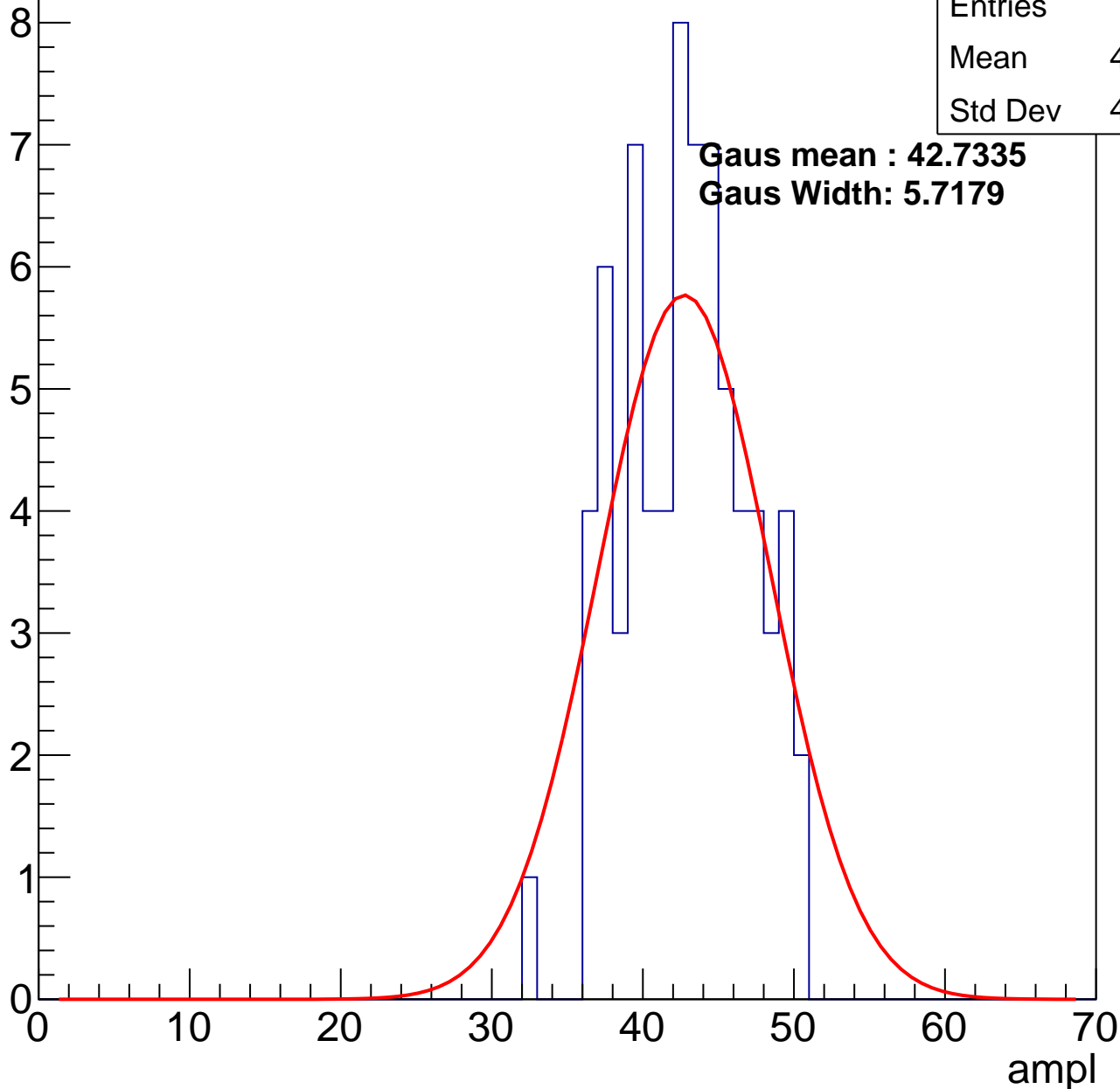
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	42.34
Std Dev	4.042

Gaus mean : 42.7335

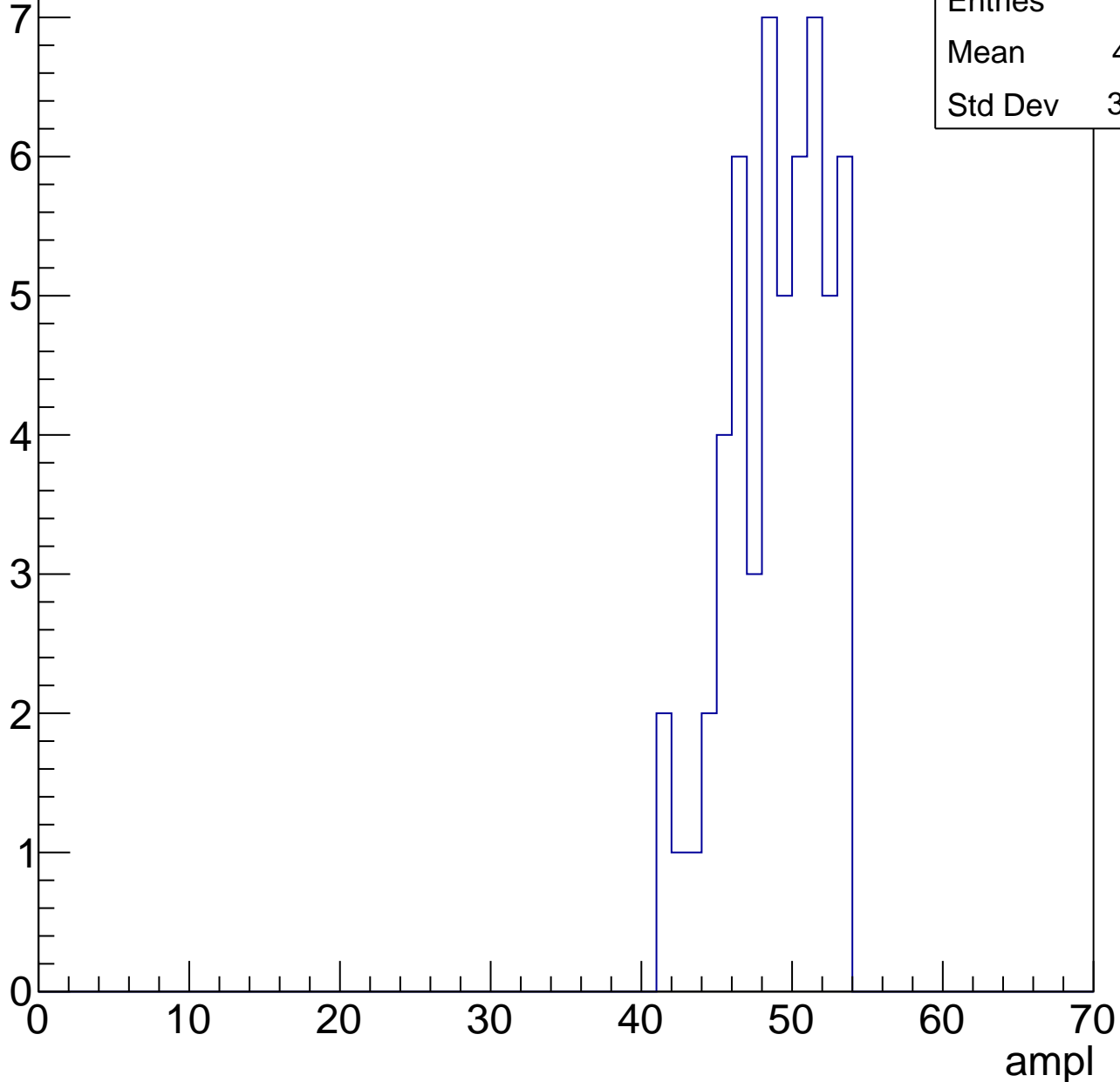
Gaus Width: 5.7179



B1L102S, U18-ch107, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	55
Mean	48.51
Std Dev	3.196

B1L102S, U18-ch107, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entries	65
Mean	55.15
Std Dev	3.429

Entry

10

8

6

4

2

0

0

10

20

30

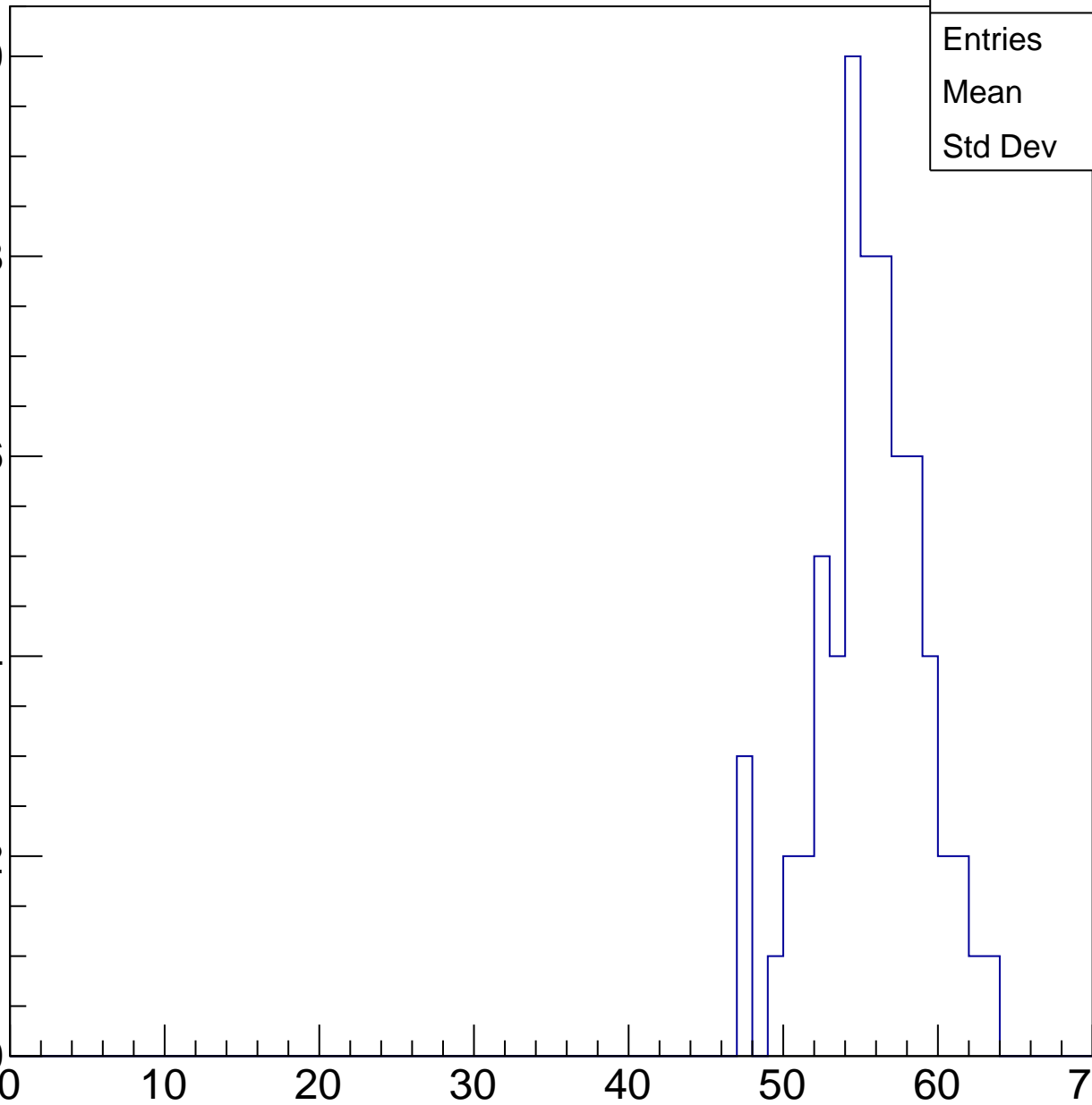
40

50

60

70

ampl

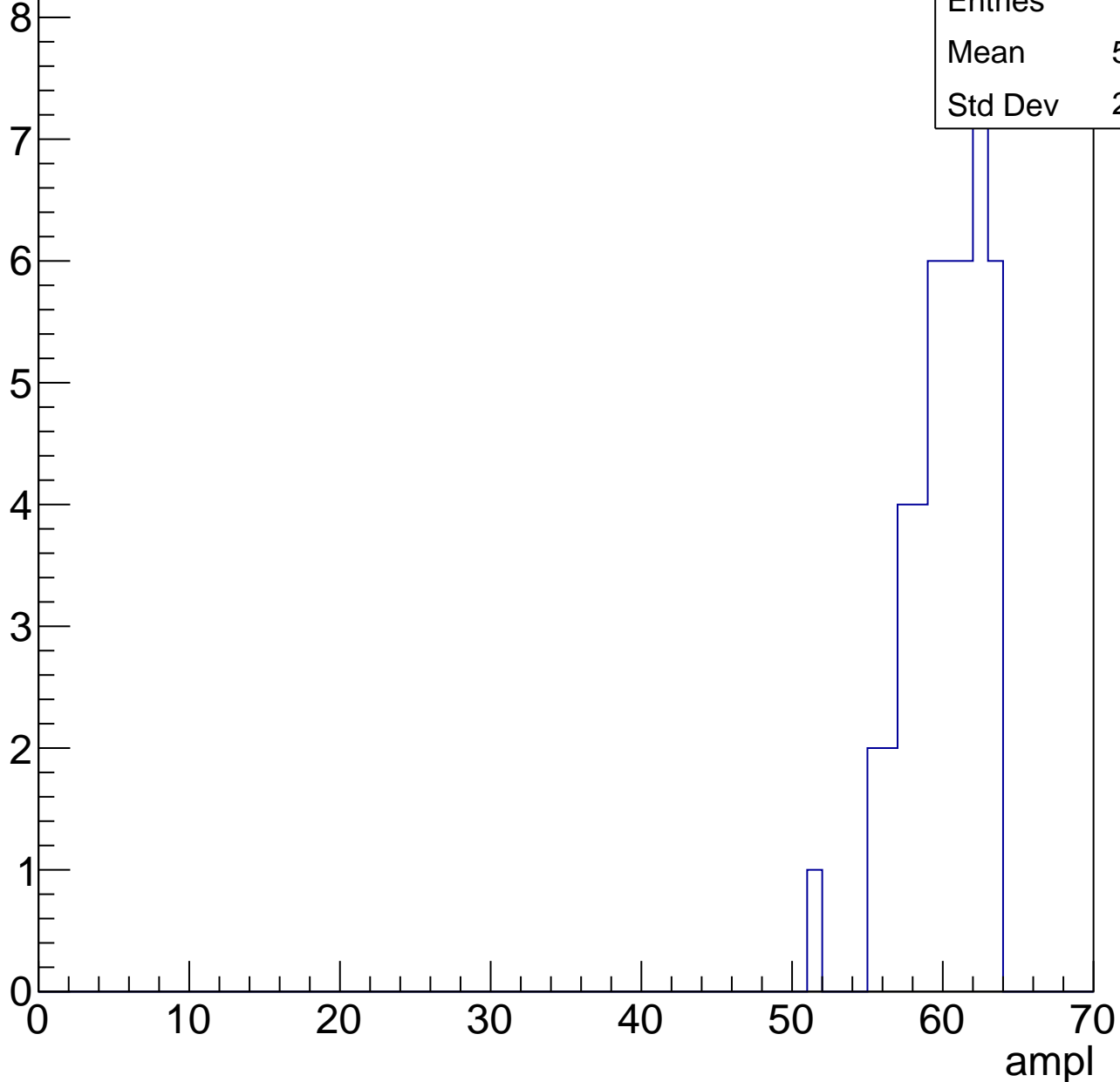


B1L102S, U18-ch107, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

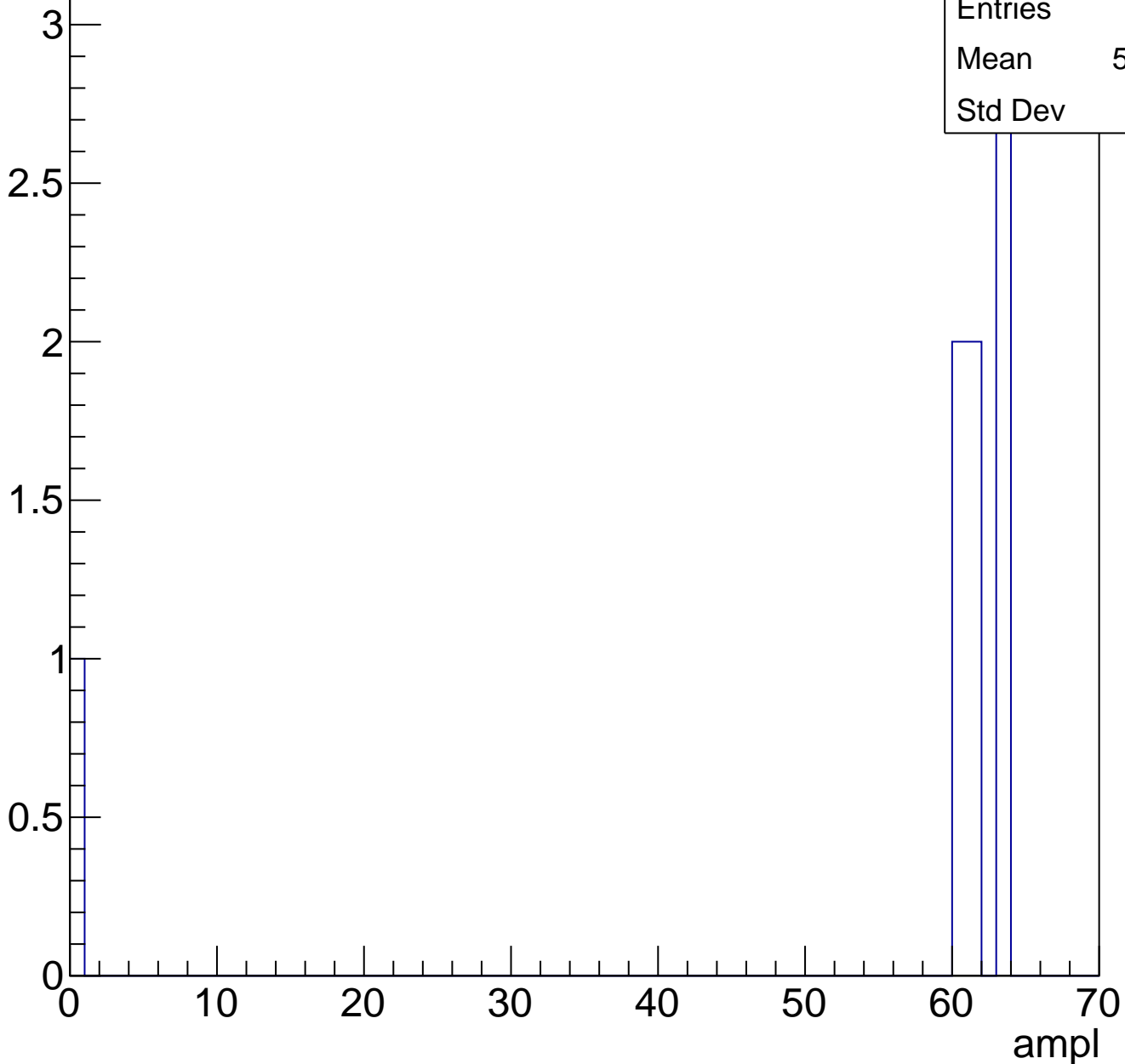
Entries	45
Mean	59.71
Std Dev	2.621



B1L102S, U18-ch107, adc6

calib_packv5_042523_0143.root, FC#11, port A2

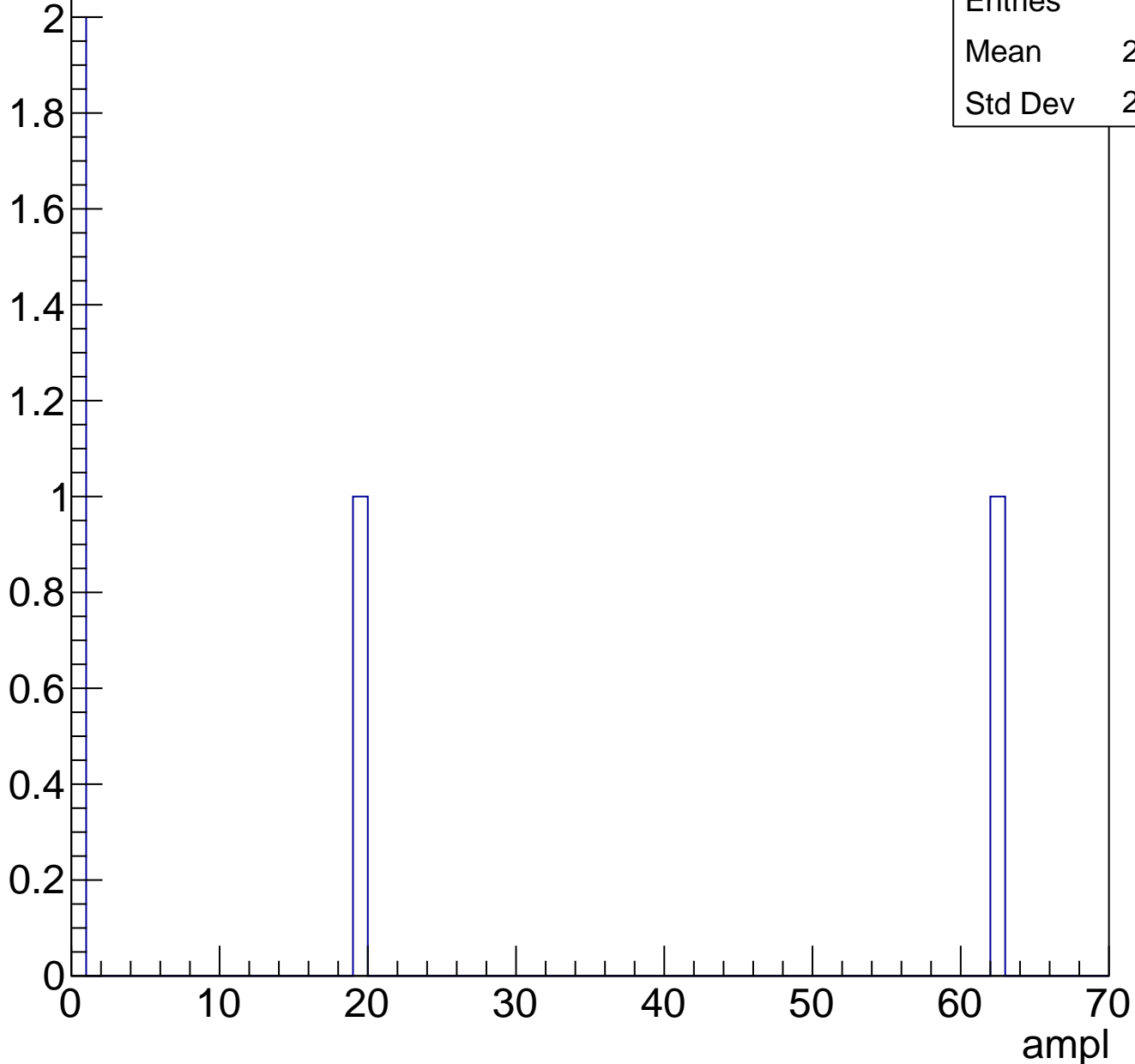
Entry



B1L102S, U18-ch107, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	4
Mean	20.25
Std Dev	25.32

B1L102S, U18-ch108, adc0

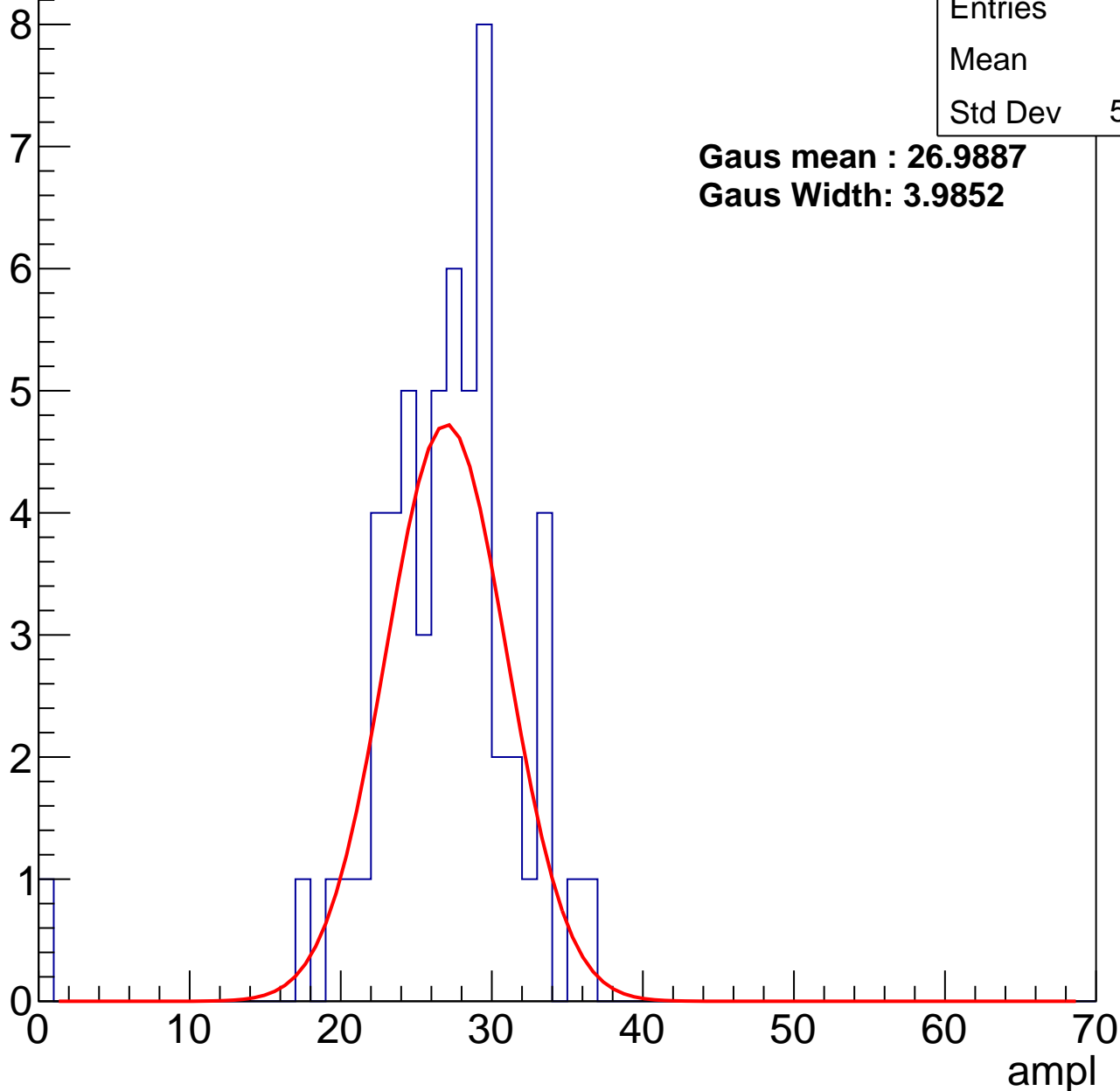
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	26.3
Std Dev	5.308

Gaus mean : 26.9887

Gaus Width: 3.9852



B1L102S, U18-ch108, adc1

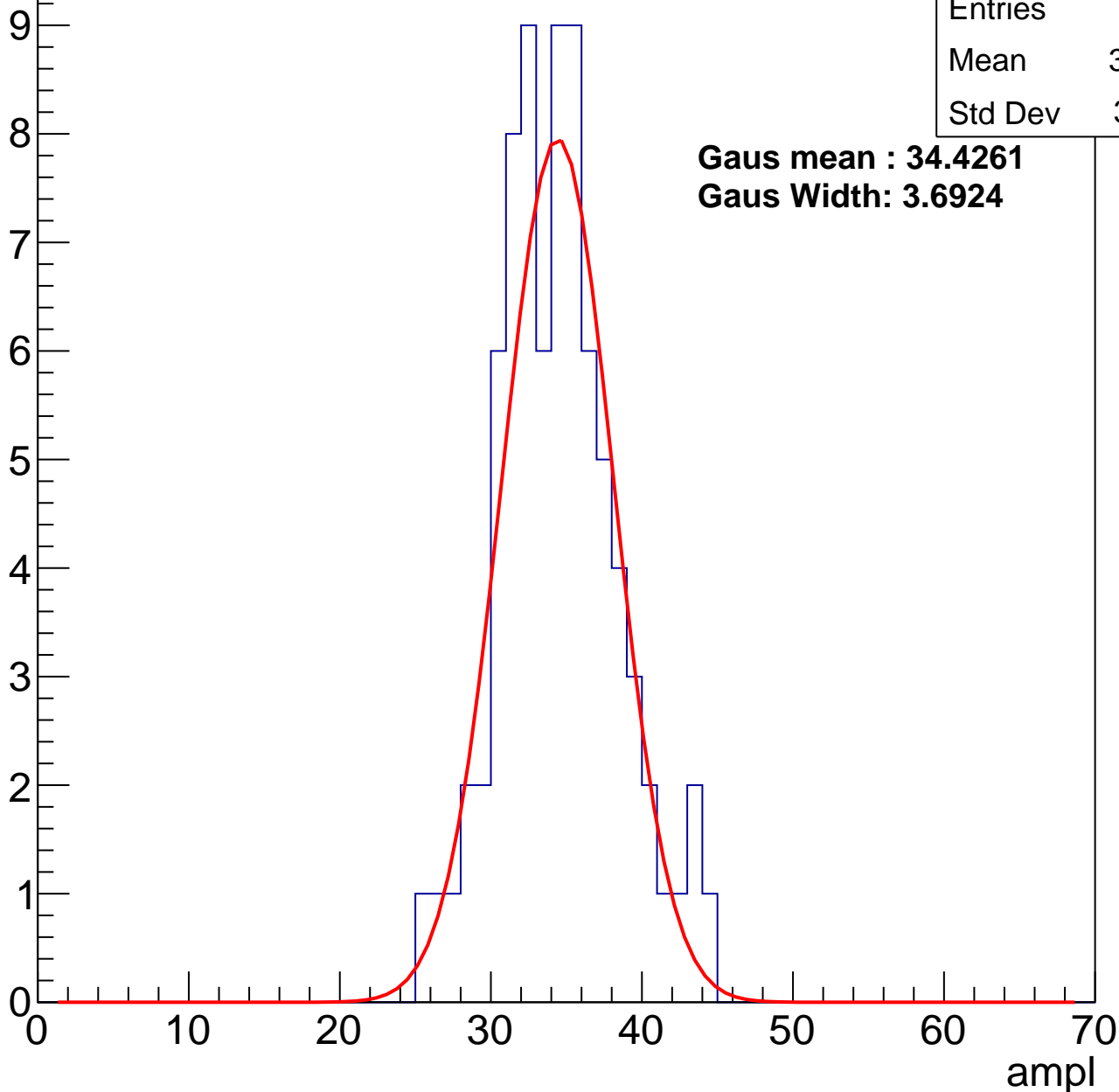
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	34.05
Std Dev	3.881

Gaus mean : 34.4261

Gaus Width: 3.6924



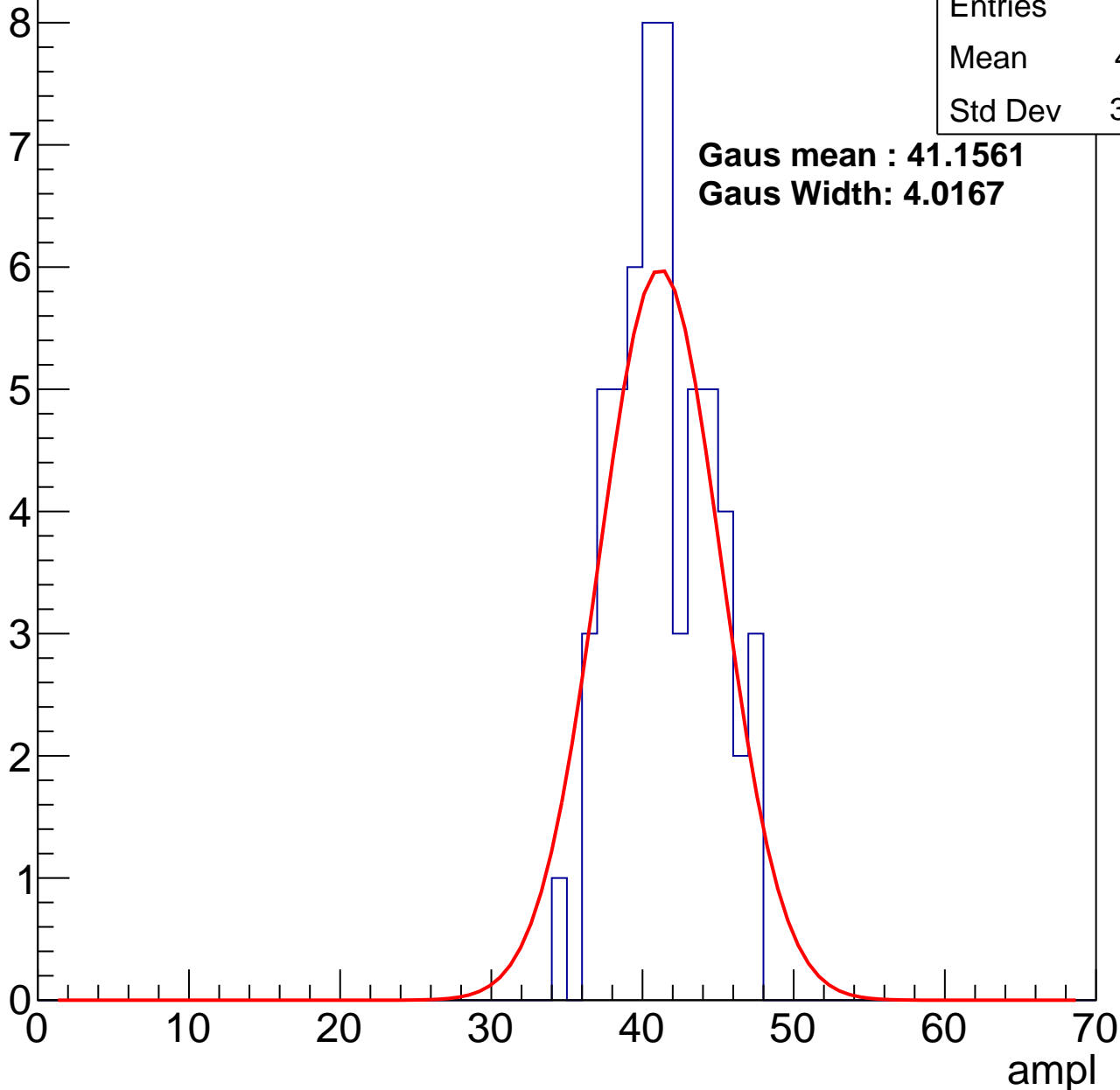
B1L102S, U18-ch108, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	40.91
Std Dev	3.136

Gaus mean : 41.1561
Gaus Width: 4.0167

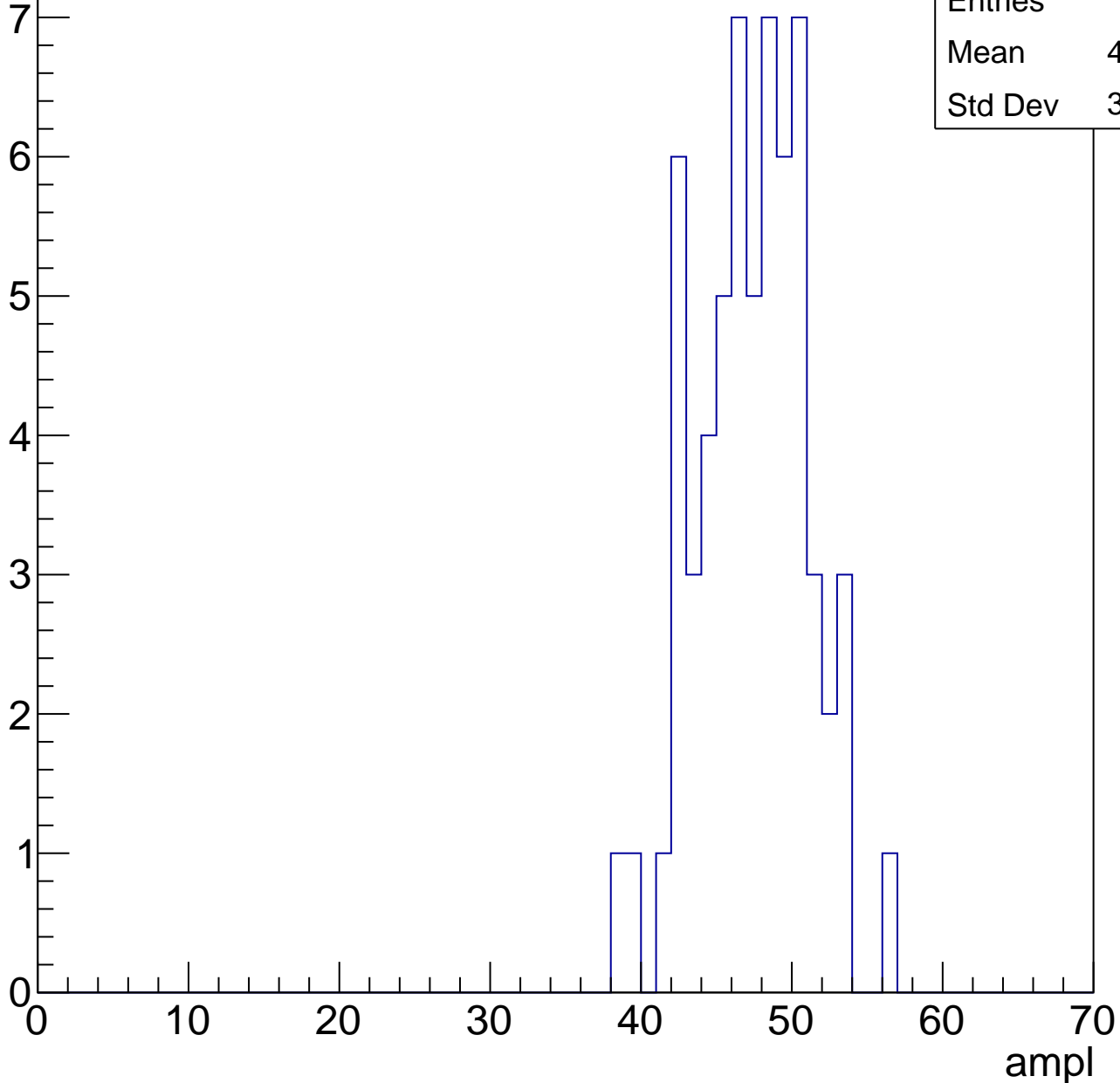


B1L102S, U18-ch108, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	46.92
Std Dev	3.642

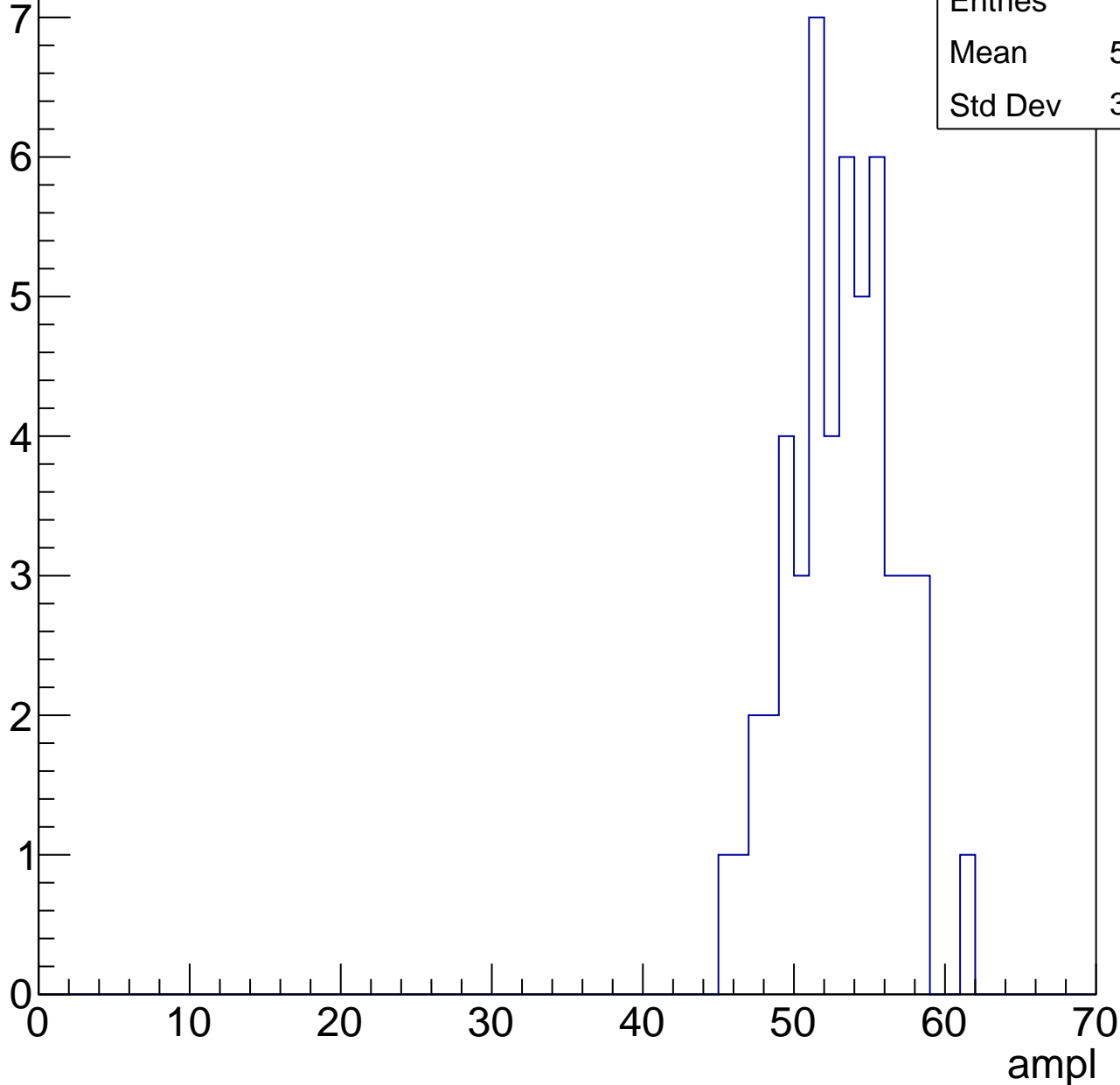


B1L102S, U18-ch108, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	51
Mean	52.63
Std Dev	3.413

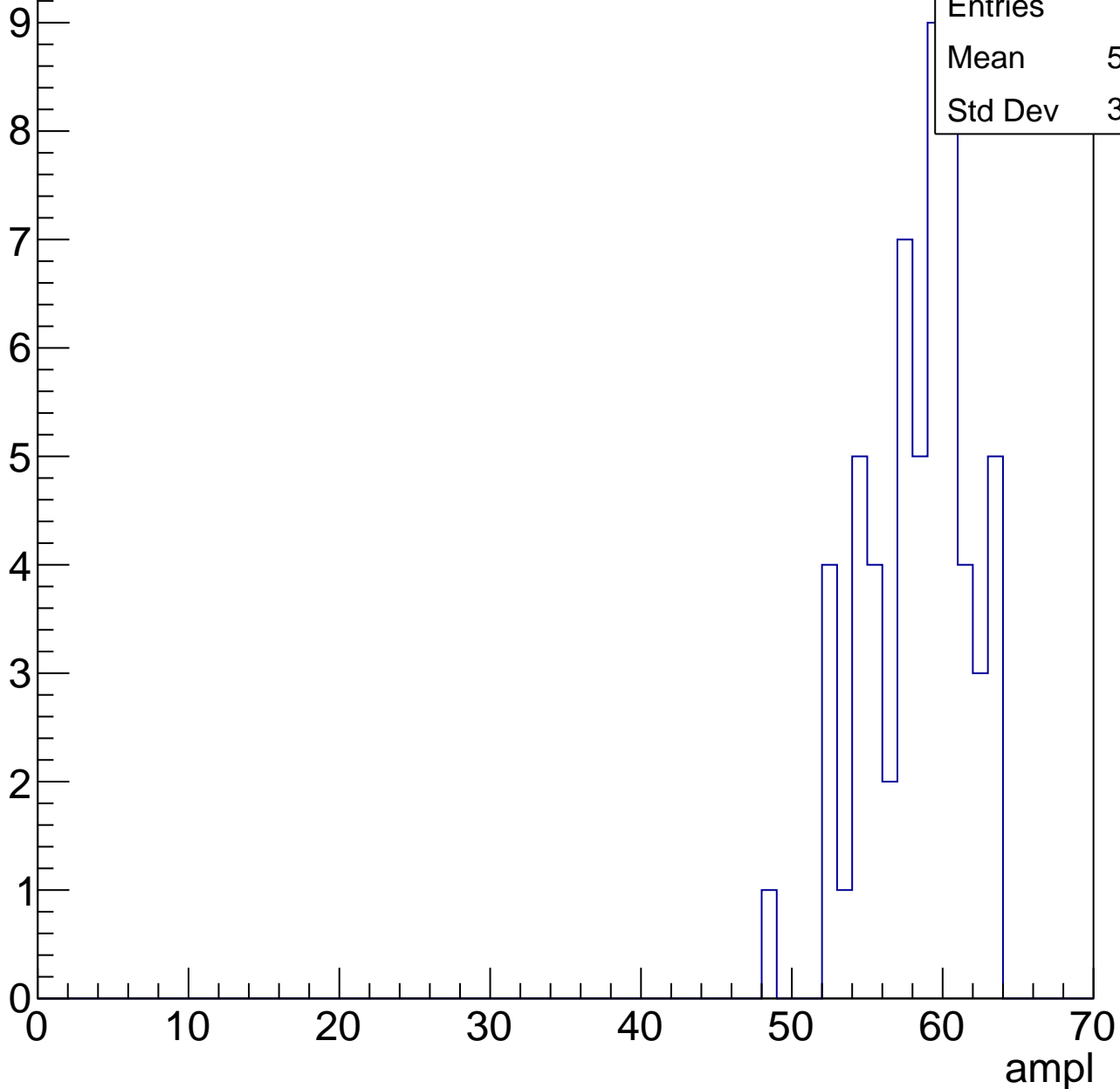


B1L102S, U18-ch108, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	57.86
Std Dev	3.345

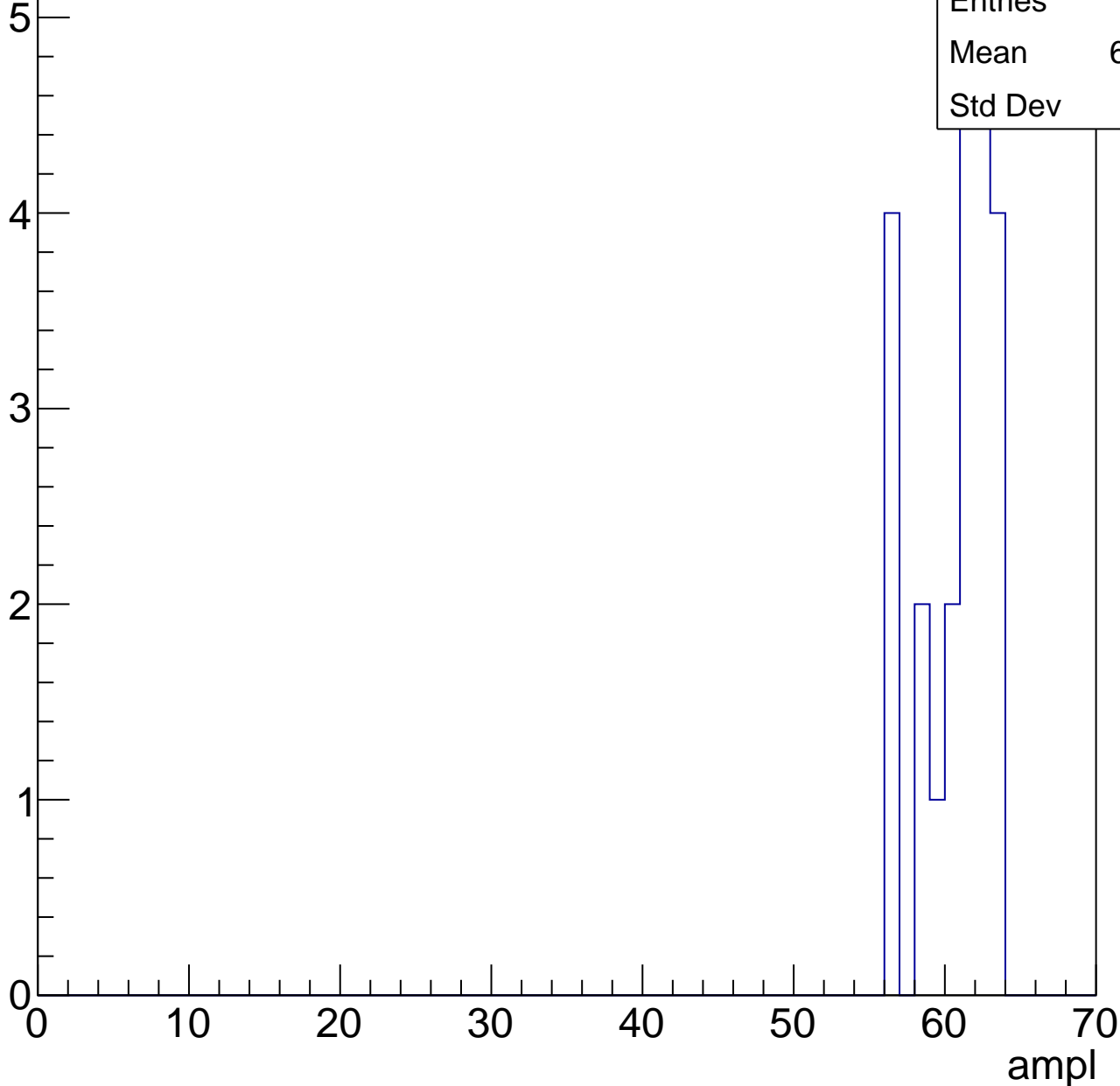


B1L102S, U18-ch108, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

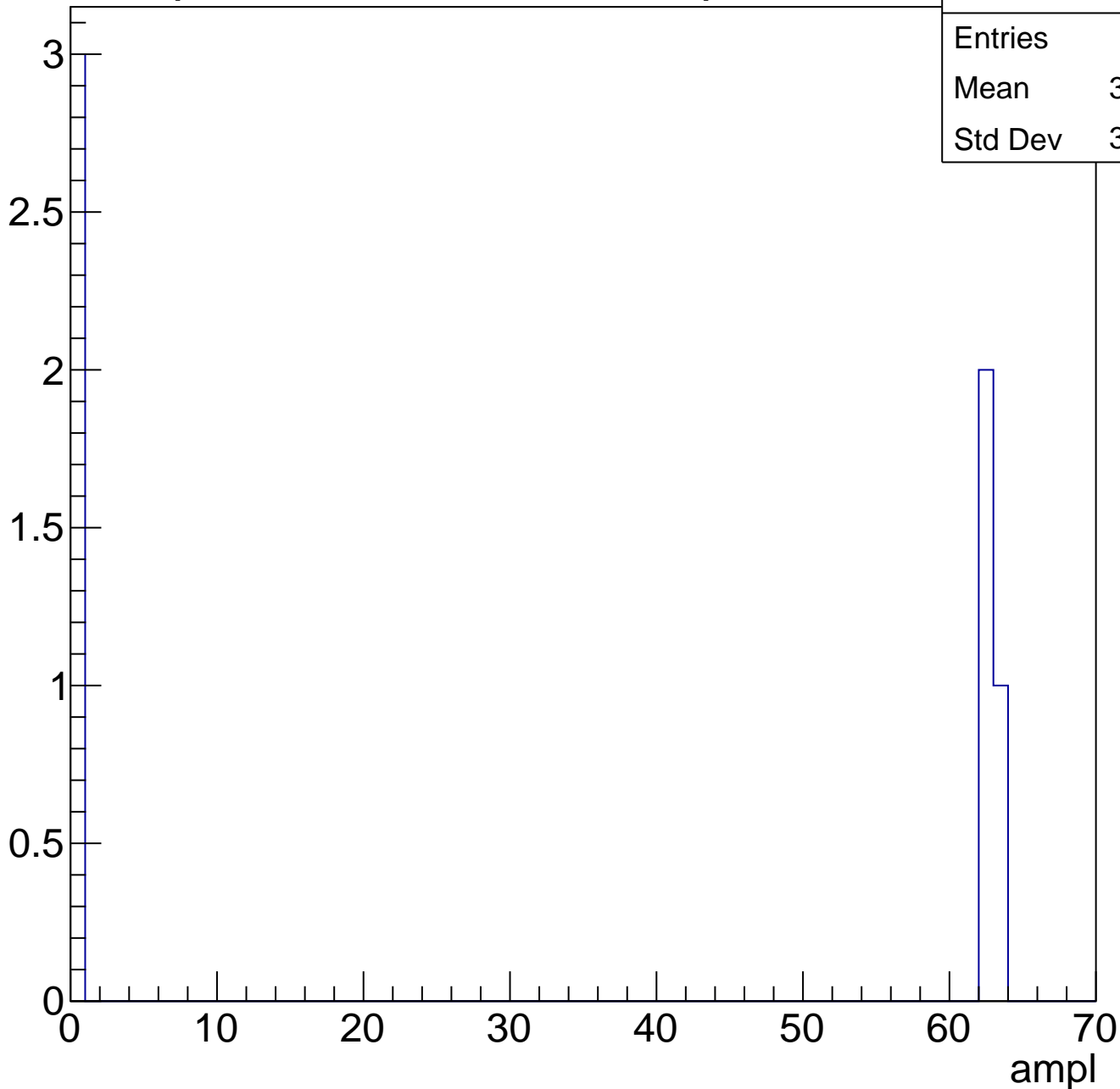
Entries	23
Mean	60.26
Std Dev	2.4



B1L102S, U18-ch108, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	6
Mean	31.17
Std Dev	31.17

B1L102S, U18-ch109, adc0

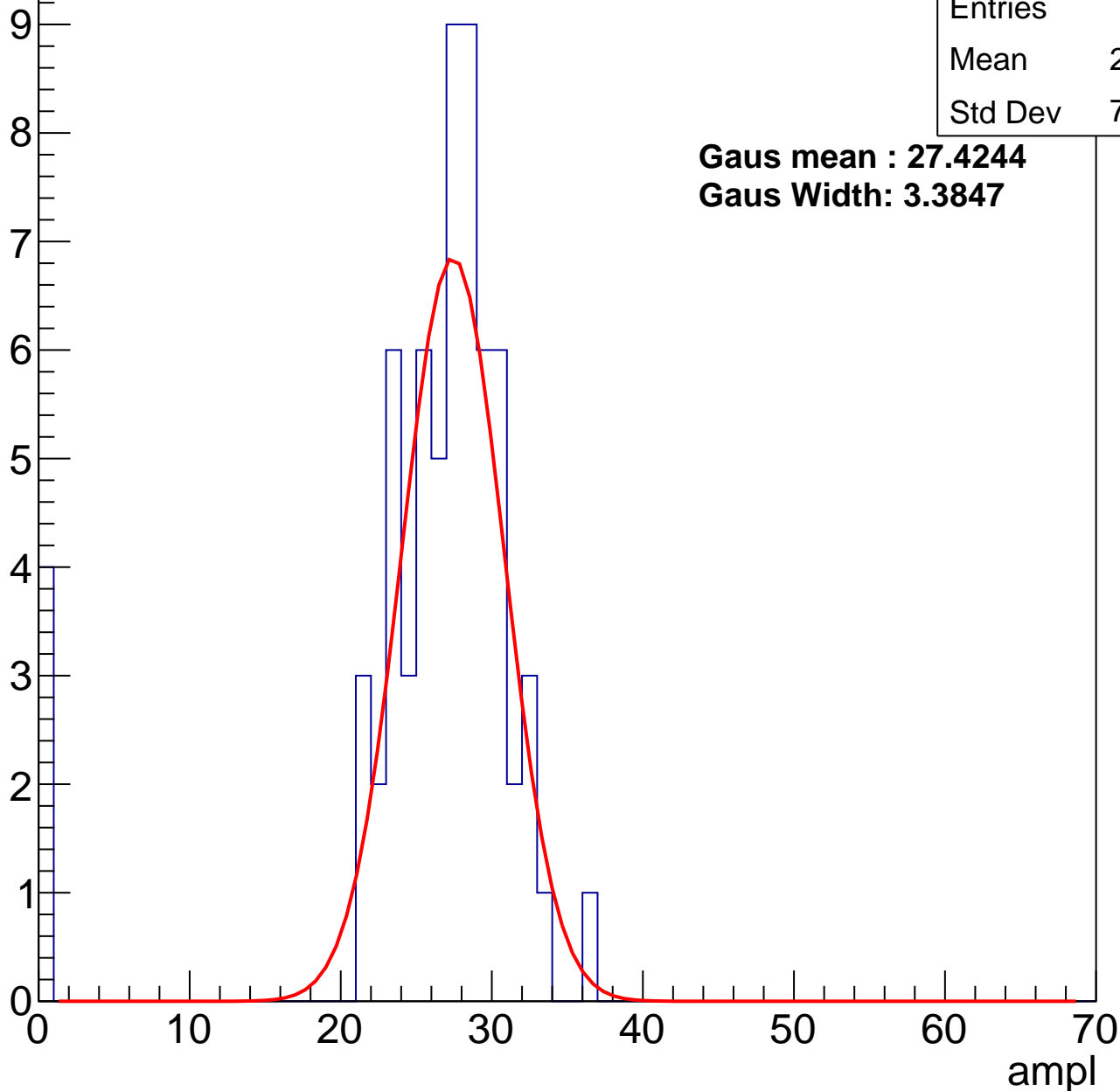
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	25.35
Std Dev	7.132

Gaus mean : 27.4244

Gaus Width: 3.3847



B1L102S, U18-ch109, adc1

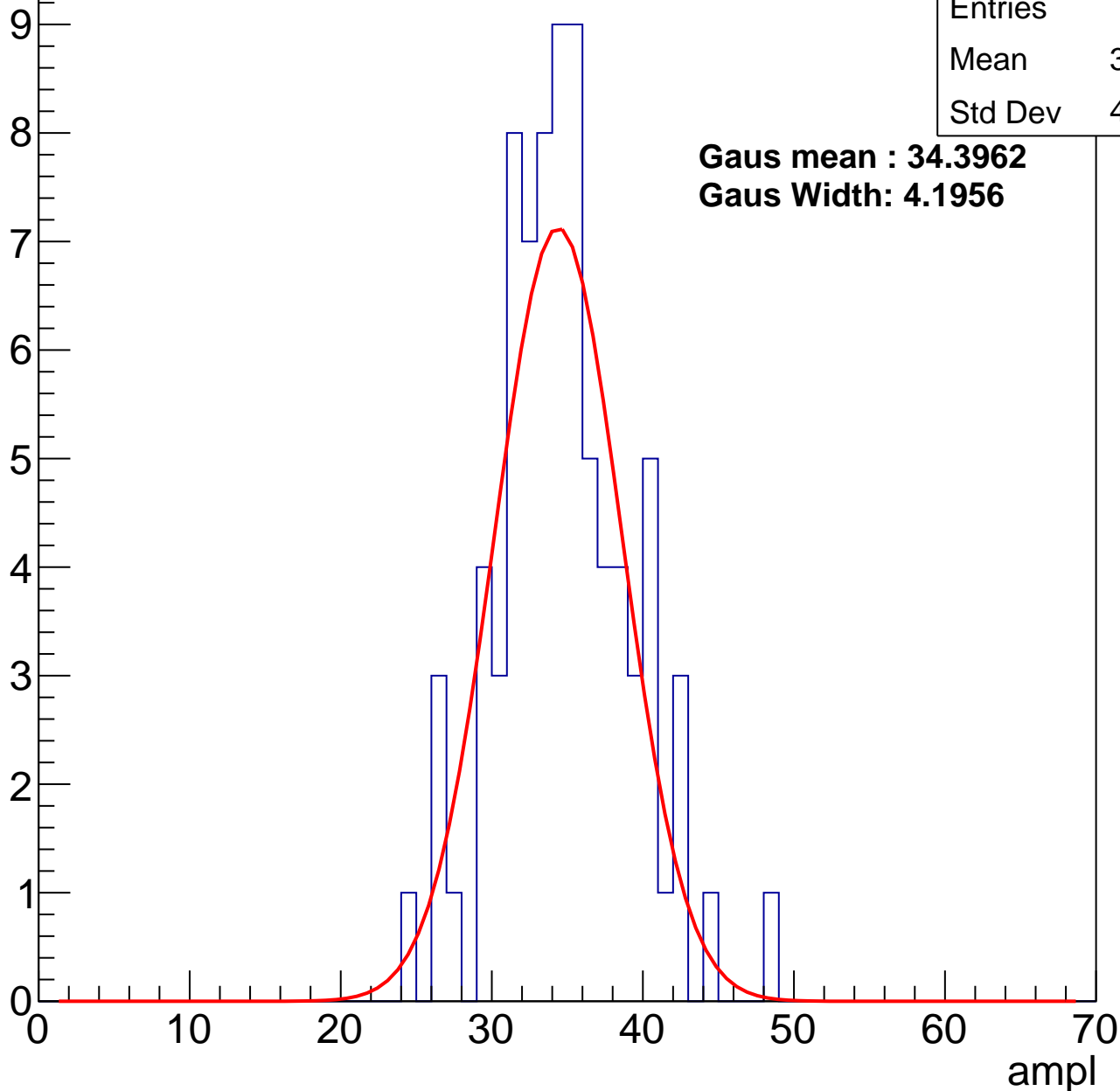
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	34.35
Std Dev	4.342

Gaus mean : 34.3962

Gaus Width: 4.1956



B1L102S, U18-ch109, adc2

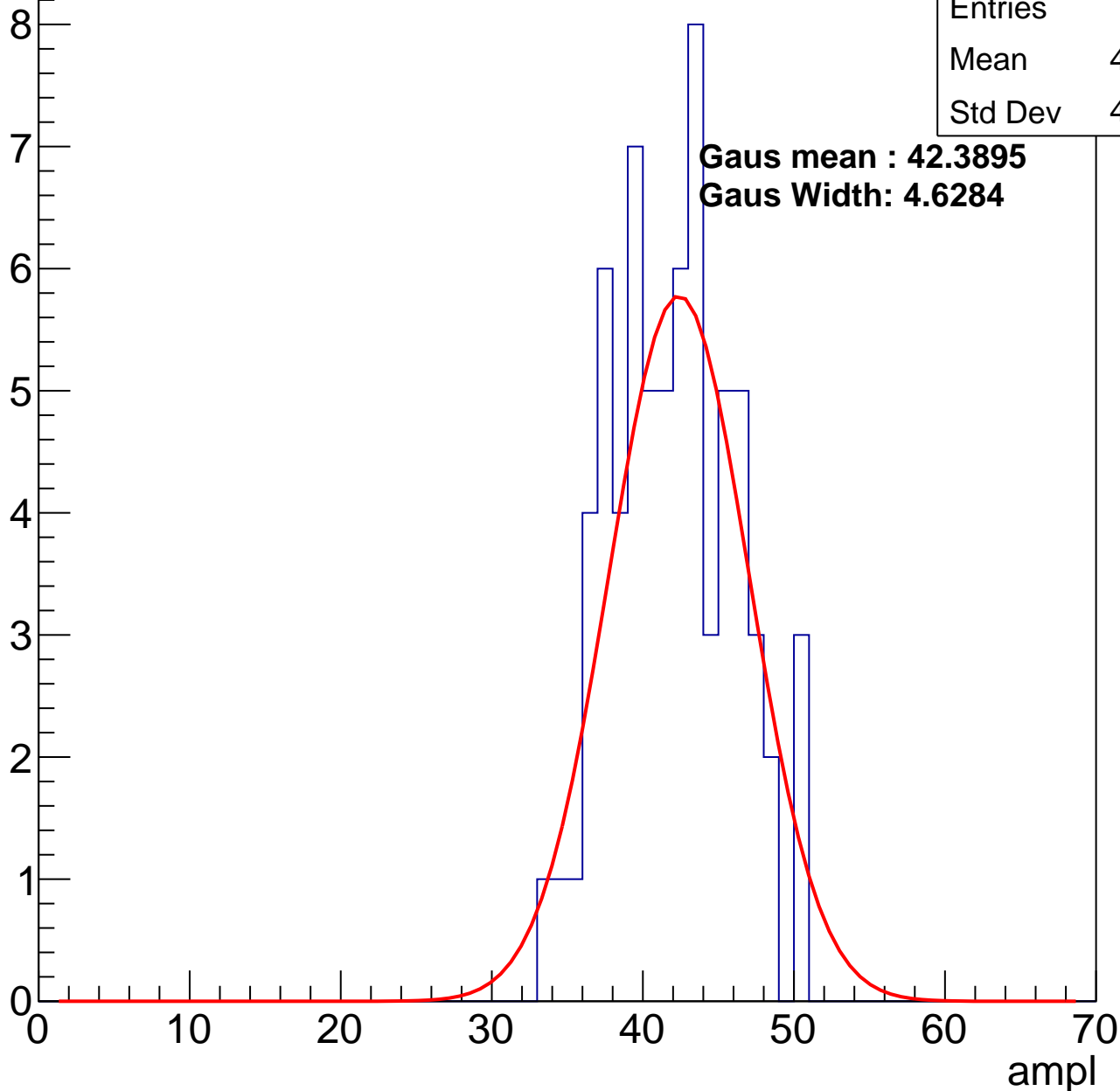
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.57
Std Dev	4.013

Gaus mean : 42.3895

Gaus Width: 4.6284

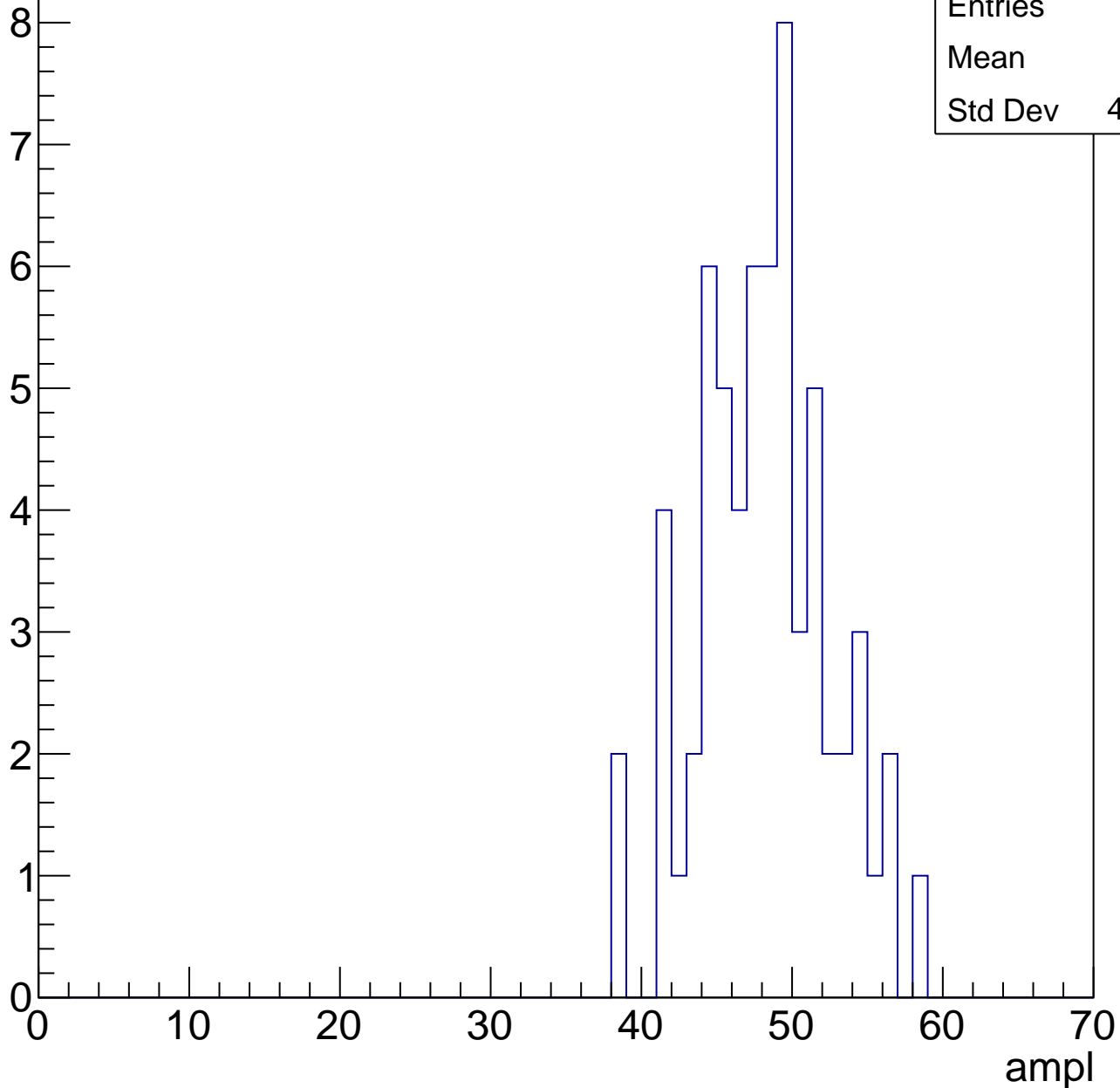


B1L102S, U18-ch109, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	47.7
Std Dev	4.312

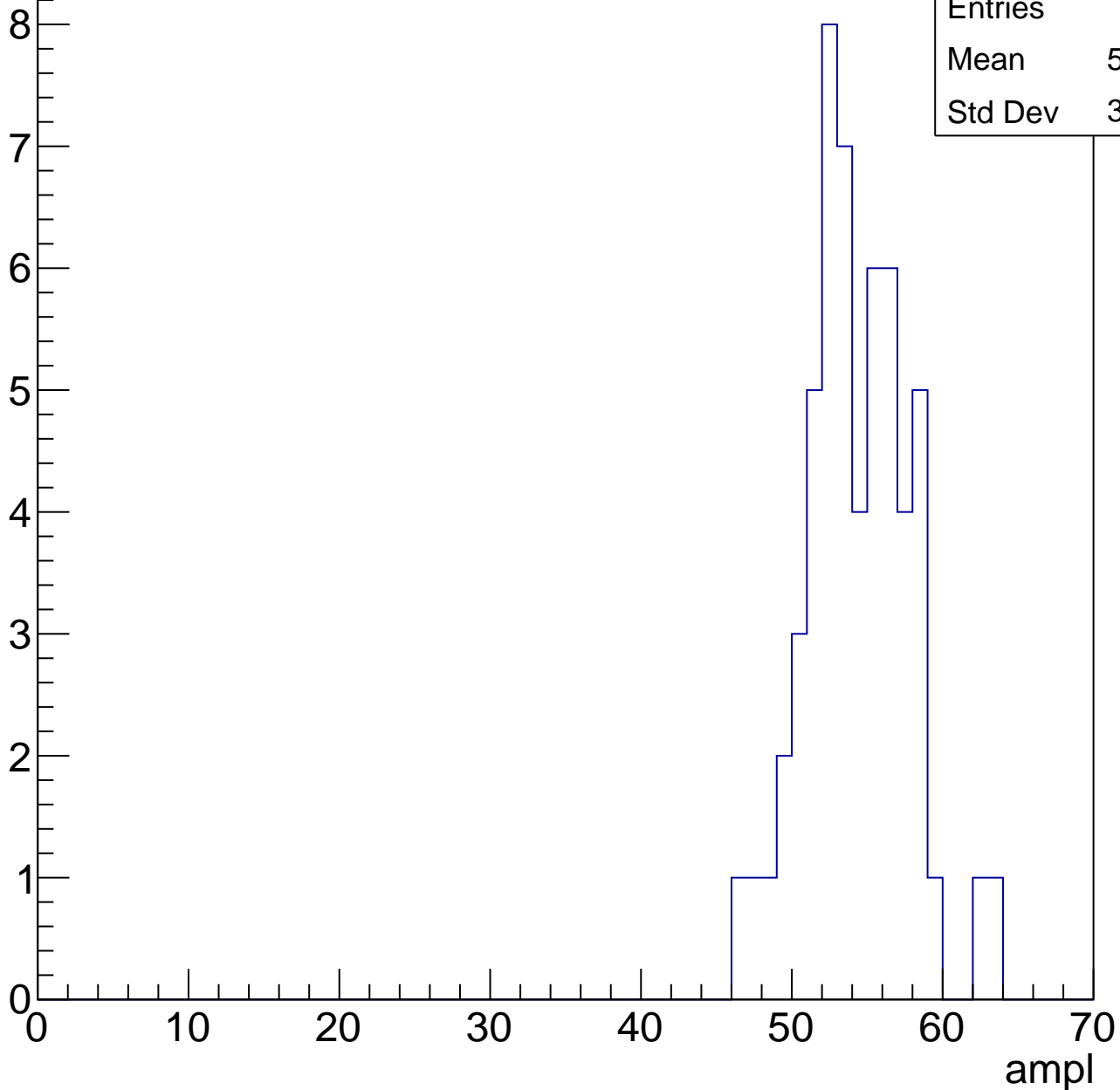


B1L102S, U18-ch109, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	53.84
Std Dev	3.395

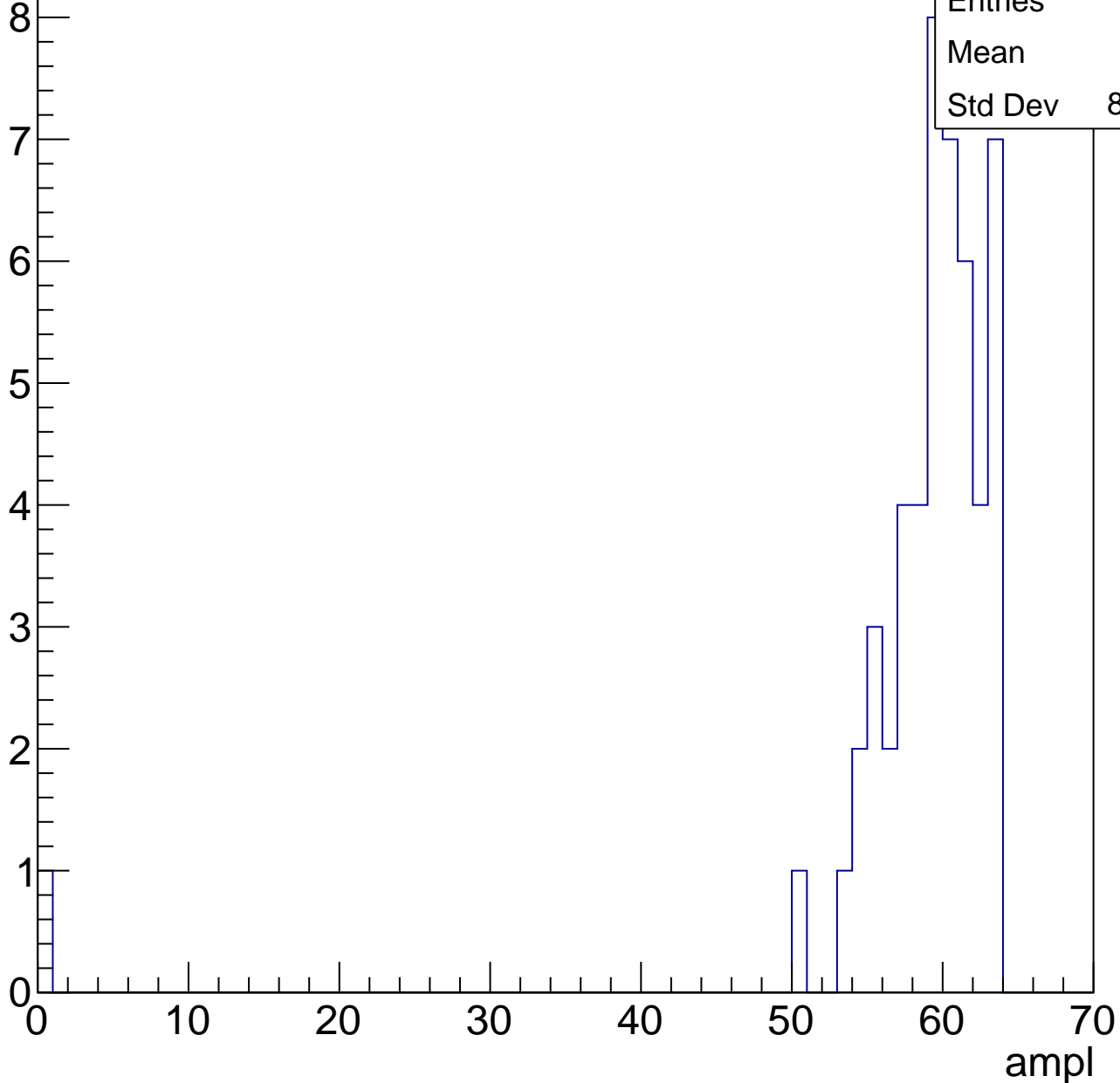


B1L102S, U18-ch109, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.9
Std Dev	8.778



B1L102S, U18-ch109, adc6

calib_packv5_042523_0143.root, FC#11, port A2

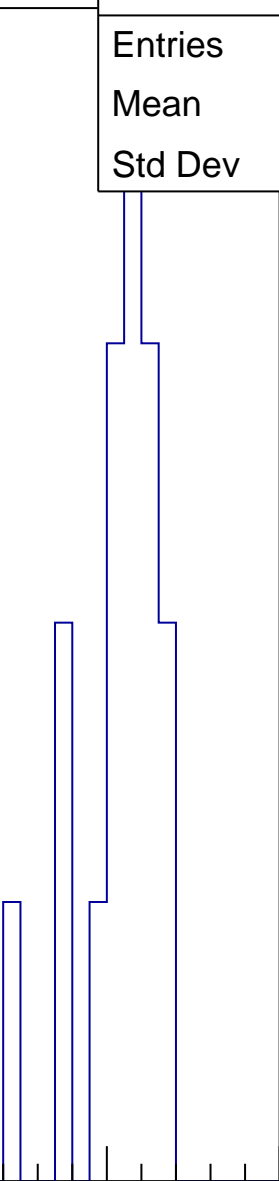
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	16
Mean	60.19
Std Dev	2.351

ampl

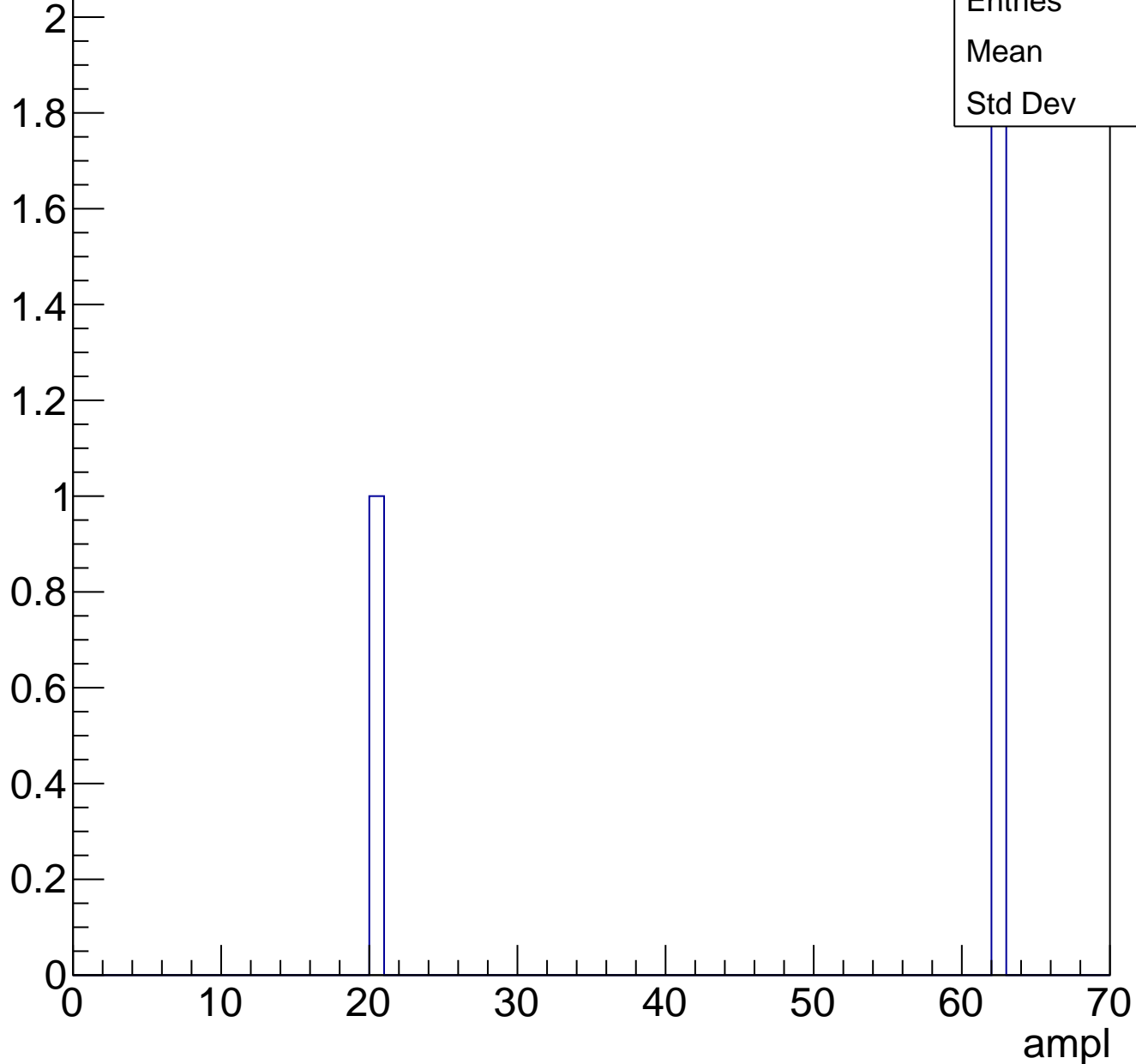
0 10 20 30 40 50 60 70



B1L102S, U18-ch109, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch110, adc0

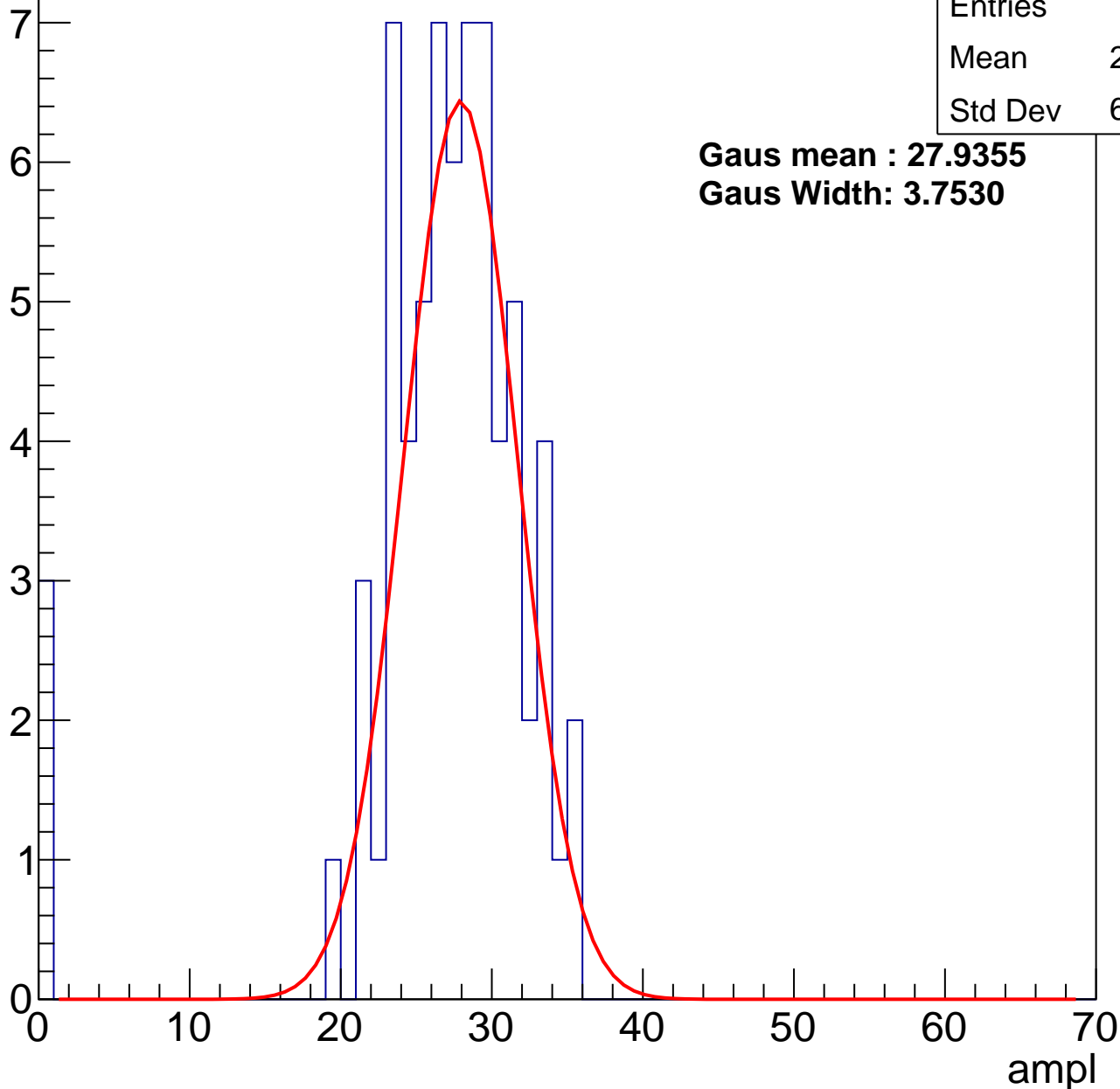
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	26.14
Std Dev	6.627

Gaus mean : 27.9355

Gaus Width: 3.7530



B1L102S, U18-ch110, adc1

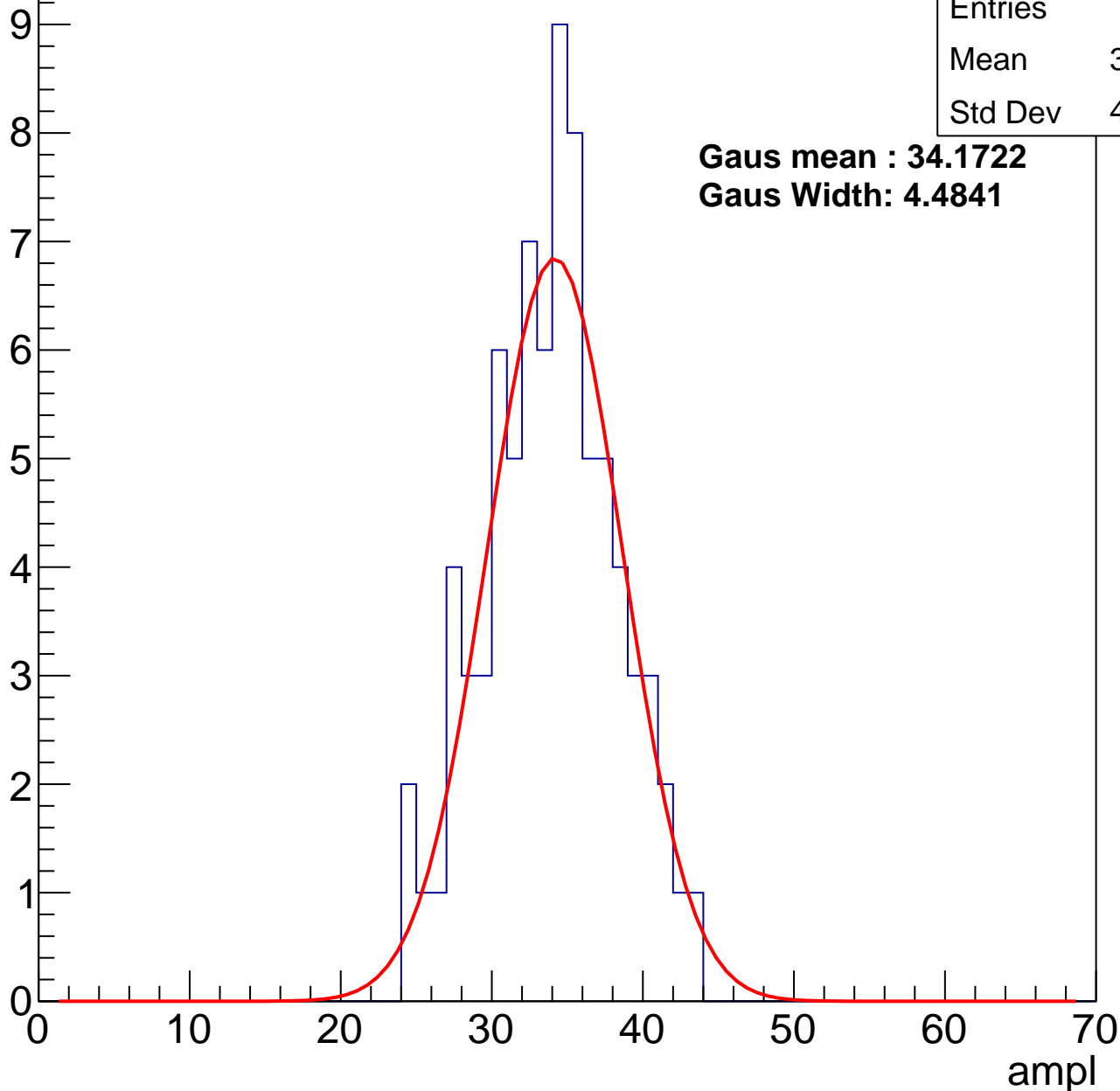
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	33.44
Std Dev	4.266

Gaus mean : 34.1722

Gaus Width: 4.4841



B1L102S, U18-ch110, adc2

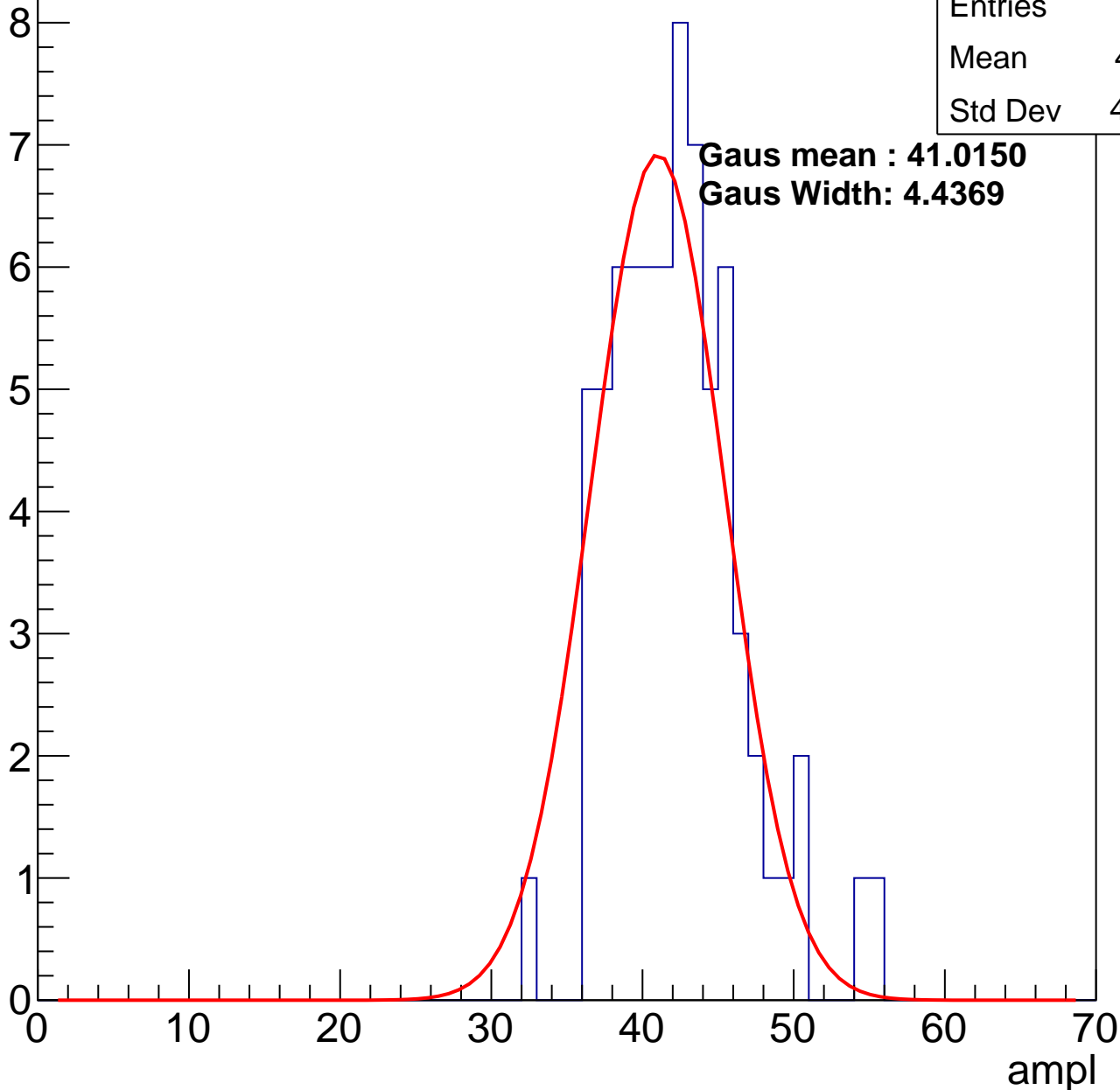
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	41.81
Std Dev	4.228

Gaus mean : 41.0150

Gaus Width: 4.4369

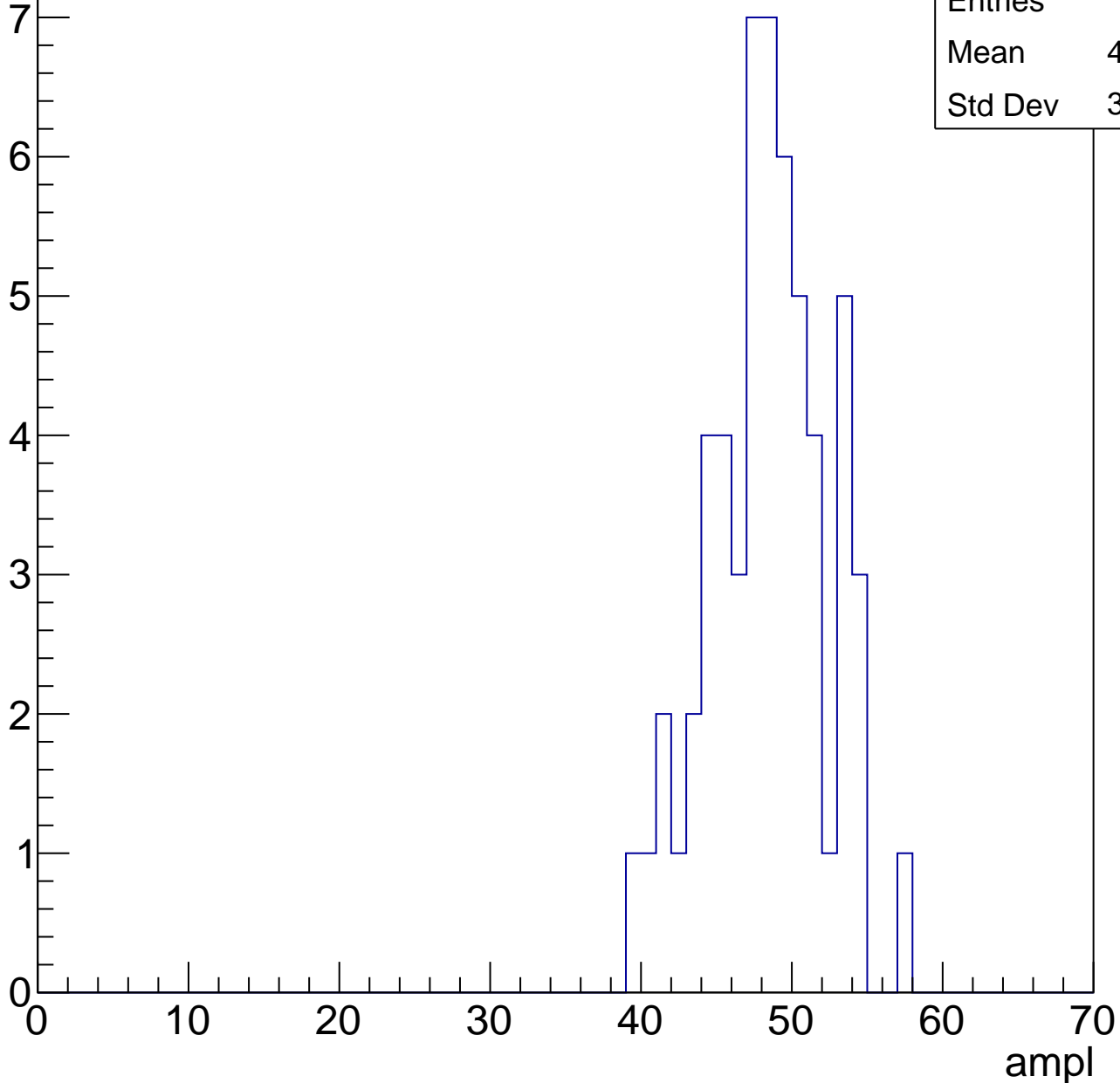


B1L102S, U18-ch110, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	47.93
Std Dev	3.838

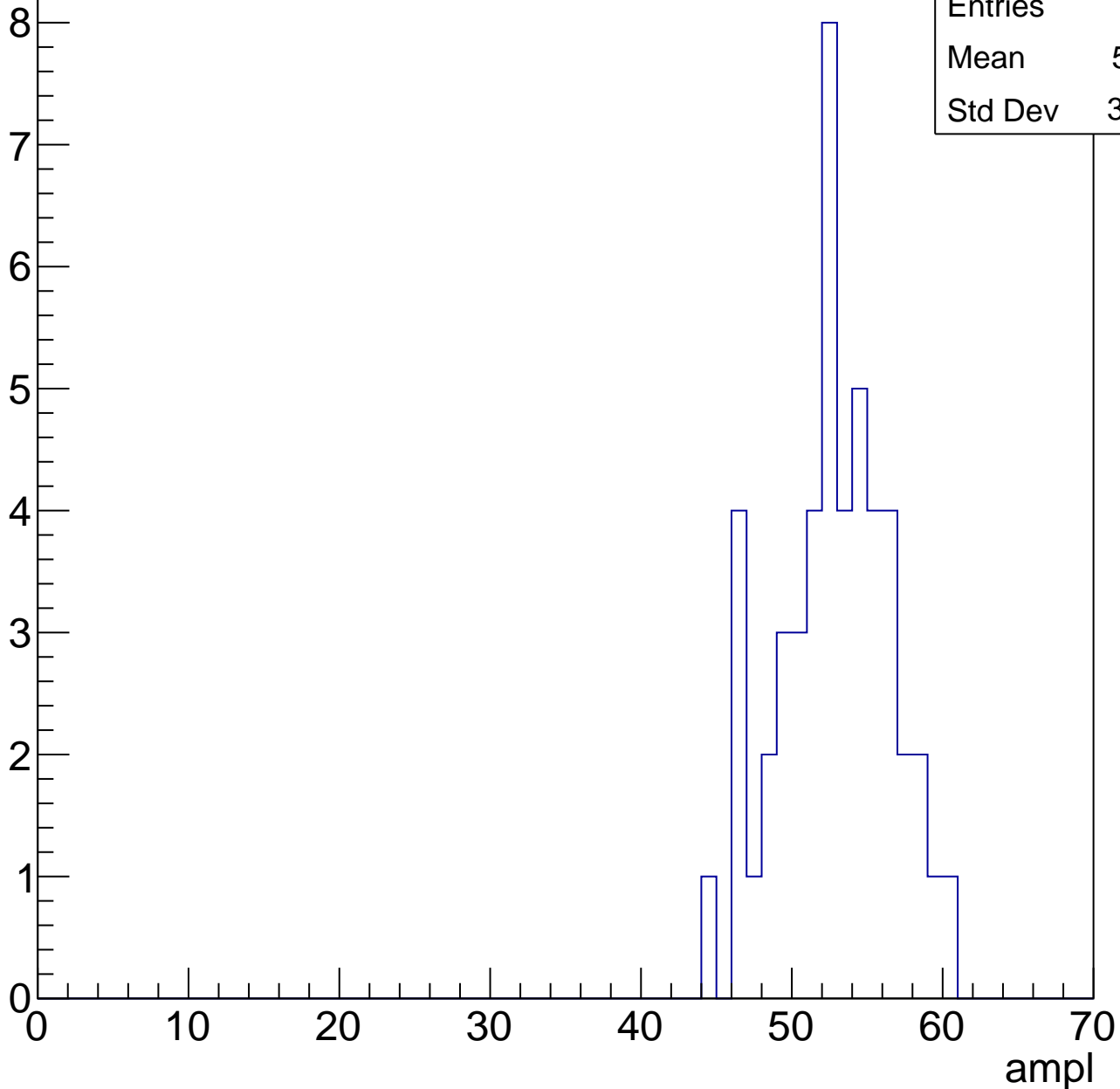


B1L102S, U18-ch110, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

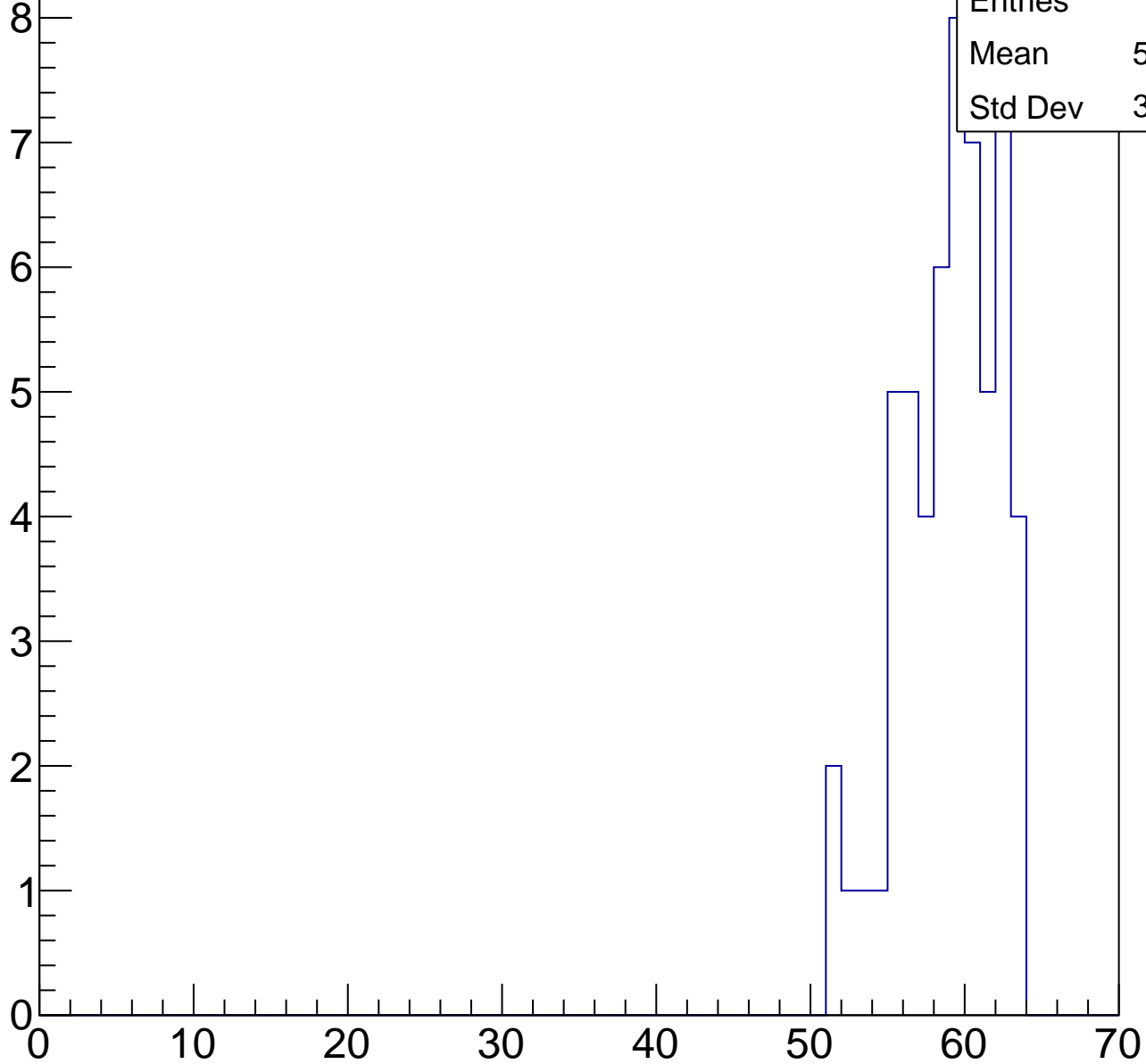
Entries	49
Mean	52.31
Std Dev	3.677



B1L102S, U18-ch110, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	57
Mean	58.54
Std Dev	3.067

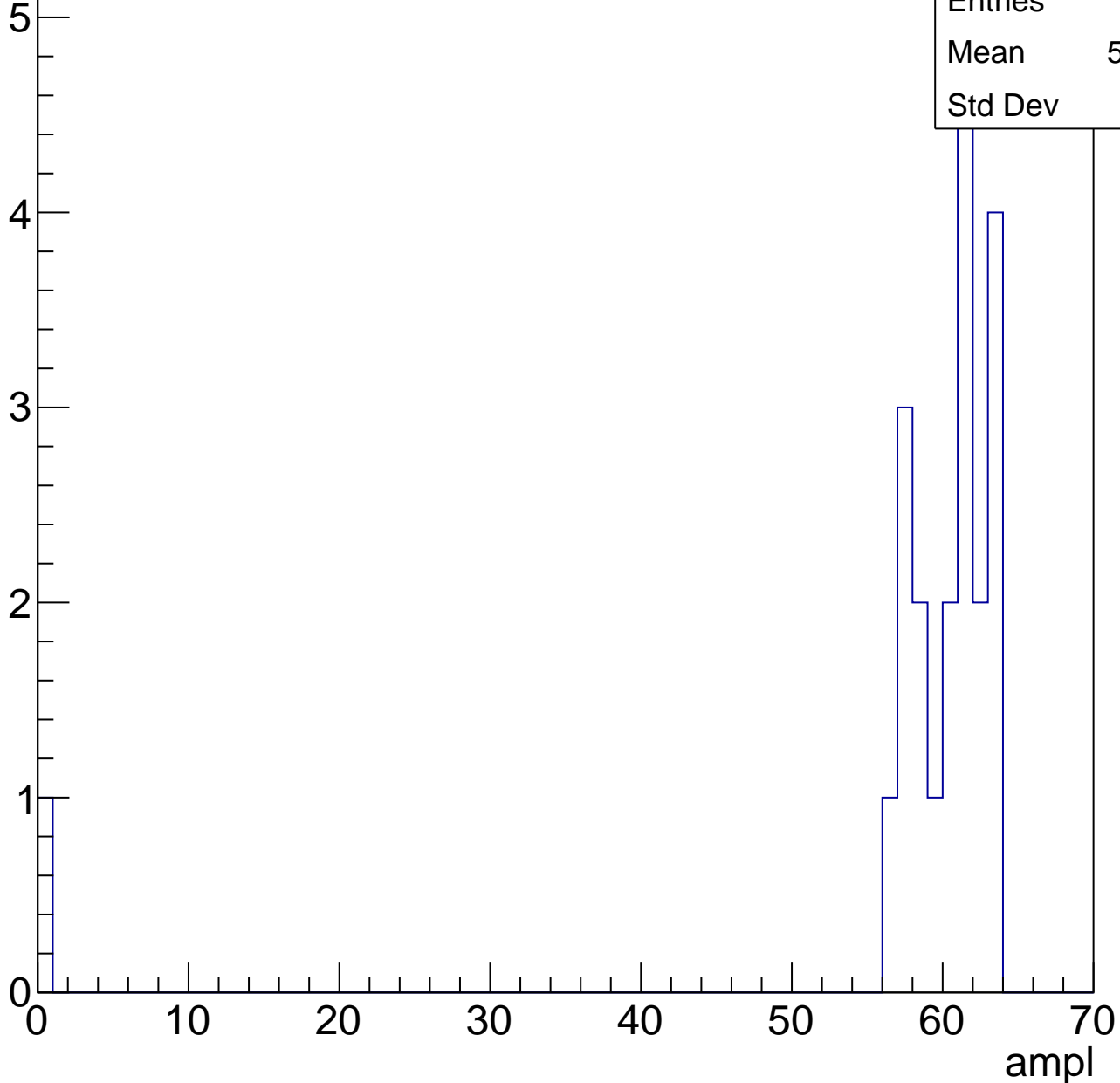
ampl

B1L102S, U18-ch110, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	21
Mean	57.29
Std Dev	13



B1L102S, U18-ch110, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch111, adc0

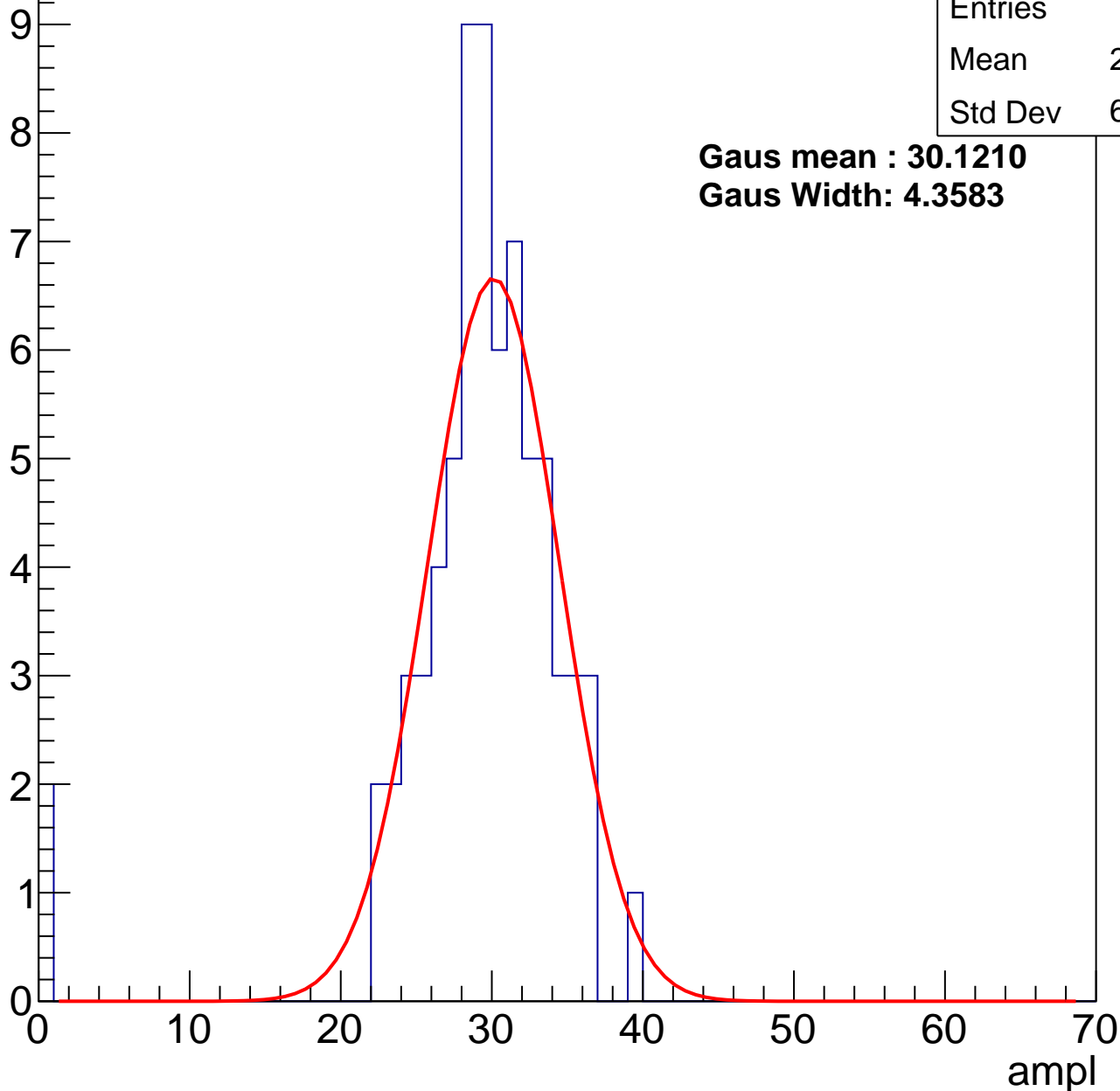
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	28.68
Std Dev	6.034

Gaus mean : 30.1210

Gaus Width: 4.3583



B1L102S, U18-ch111, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	67
Mean	35.66
Std Dev	3.471

Gaus mean : 35.9697

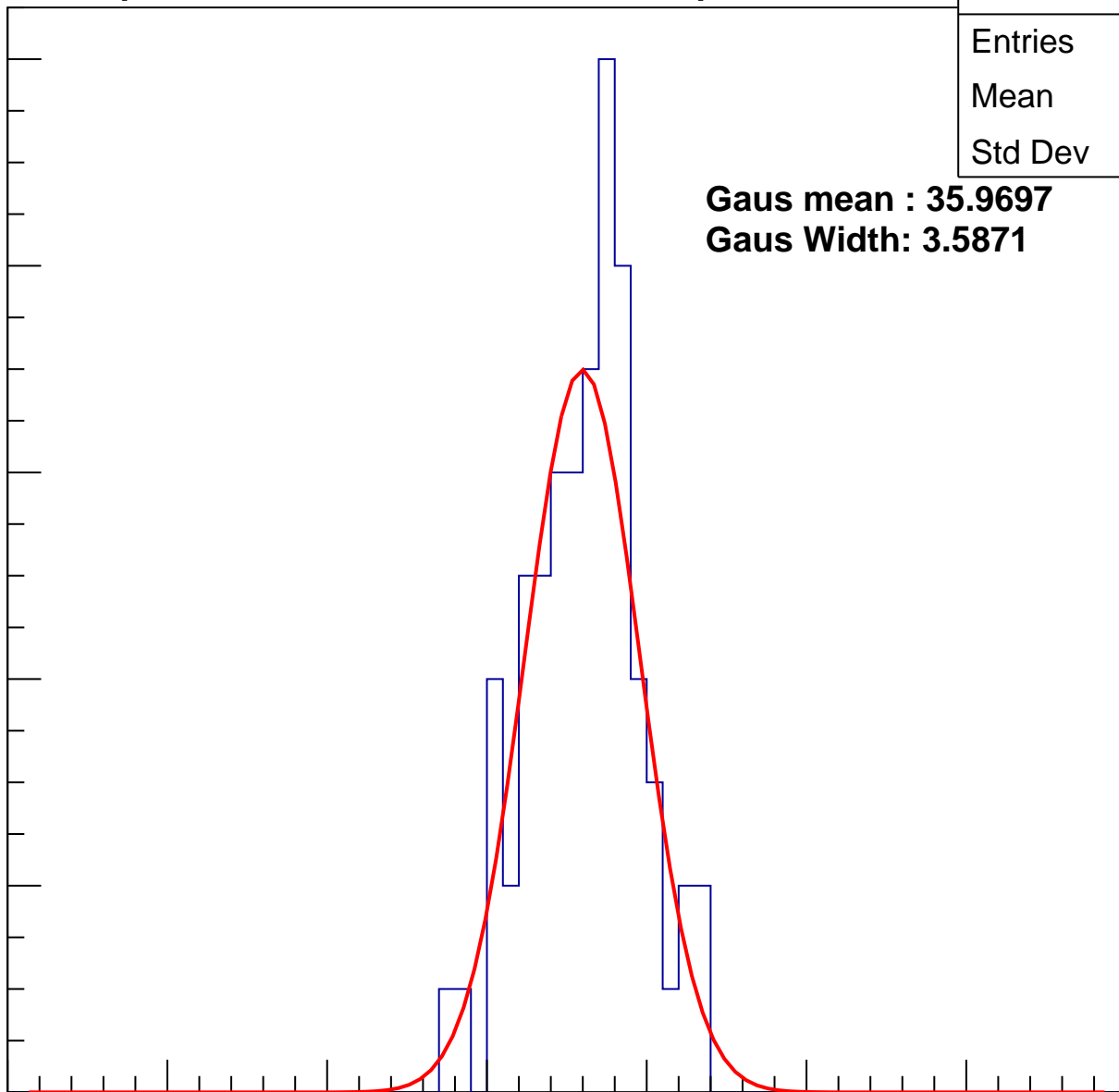
Gaus Width: 3.5871

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L102S, U18-ch111, adc2

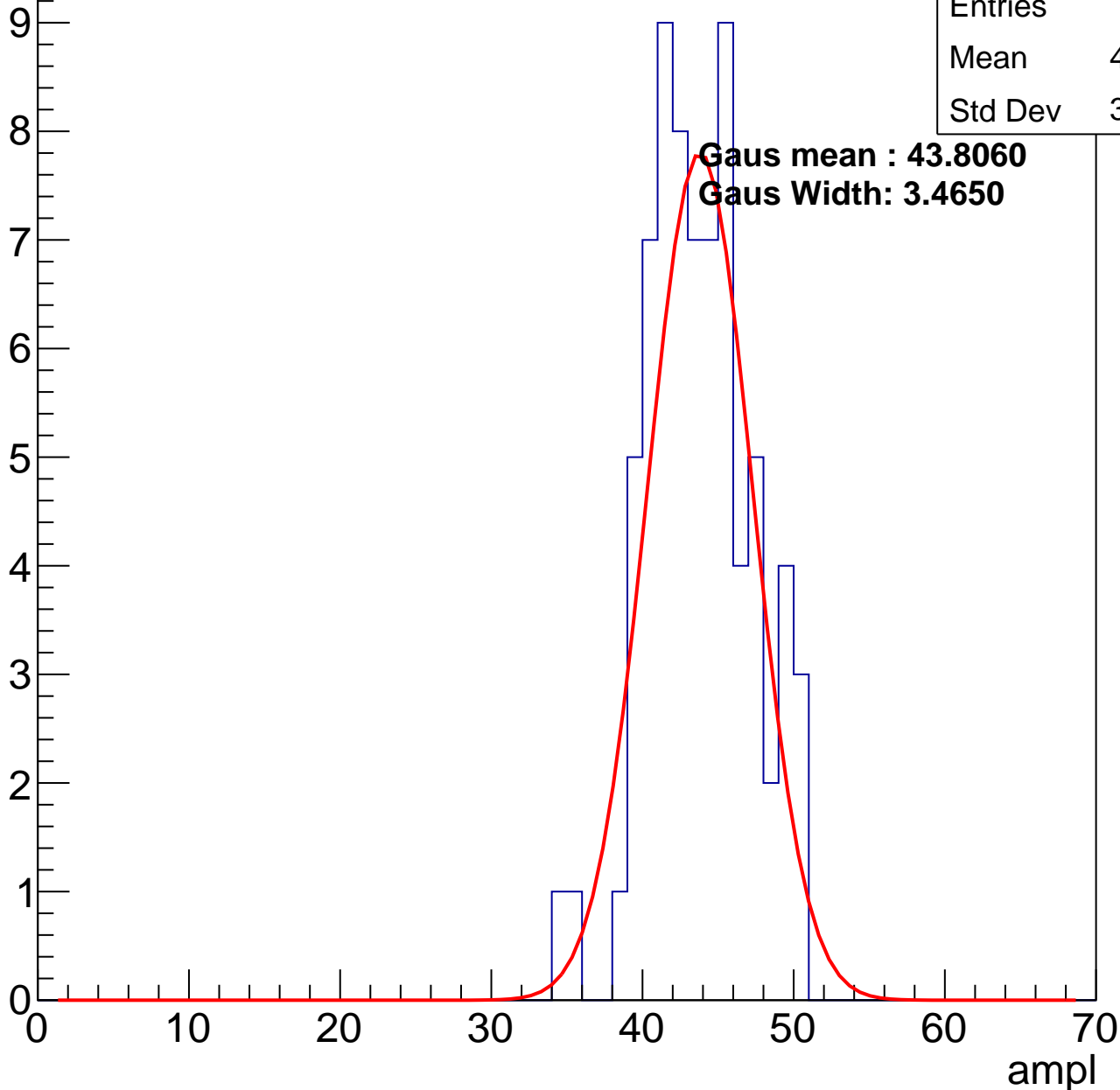
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	73
Mean	43.32
Std Dev	3.412

Gaus mean : 43.8060

Gaus Width: 3.4650

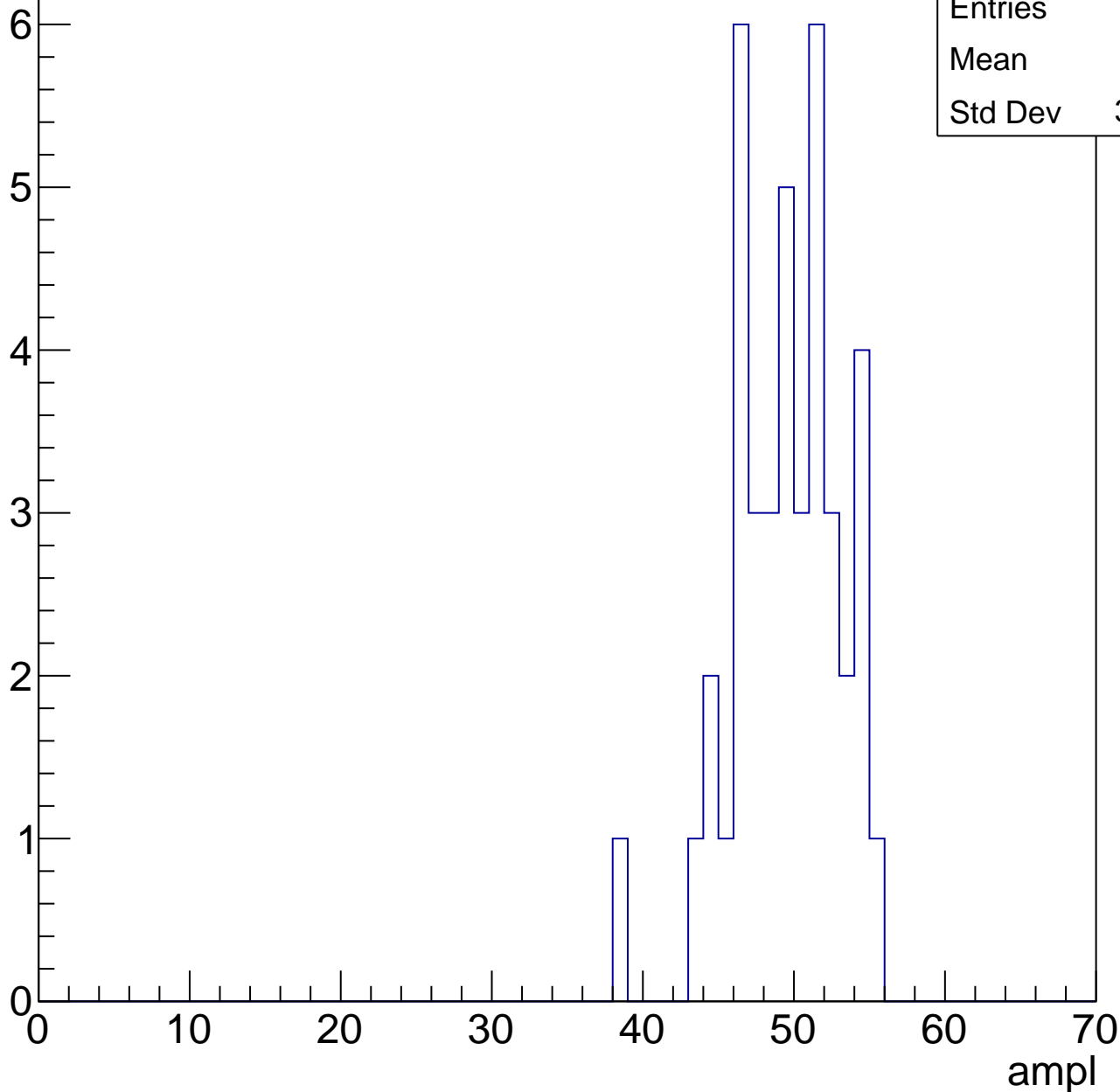


B1L102S, U18-ch111, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	41
Mean	49
Std Dev	3.541

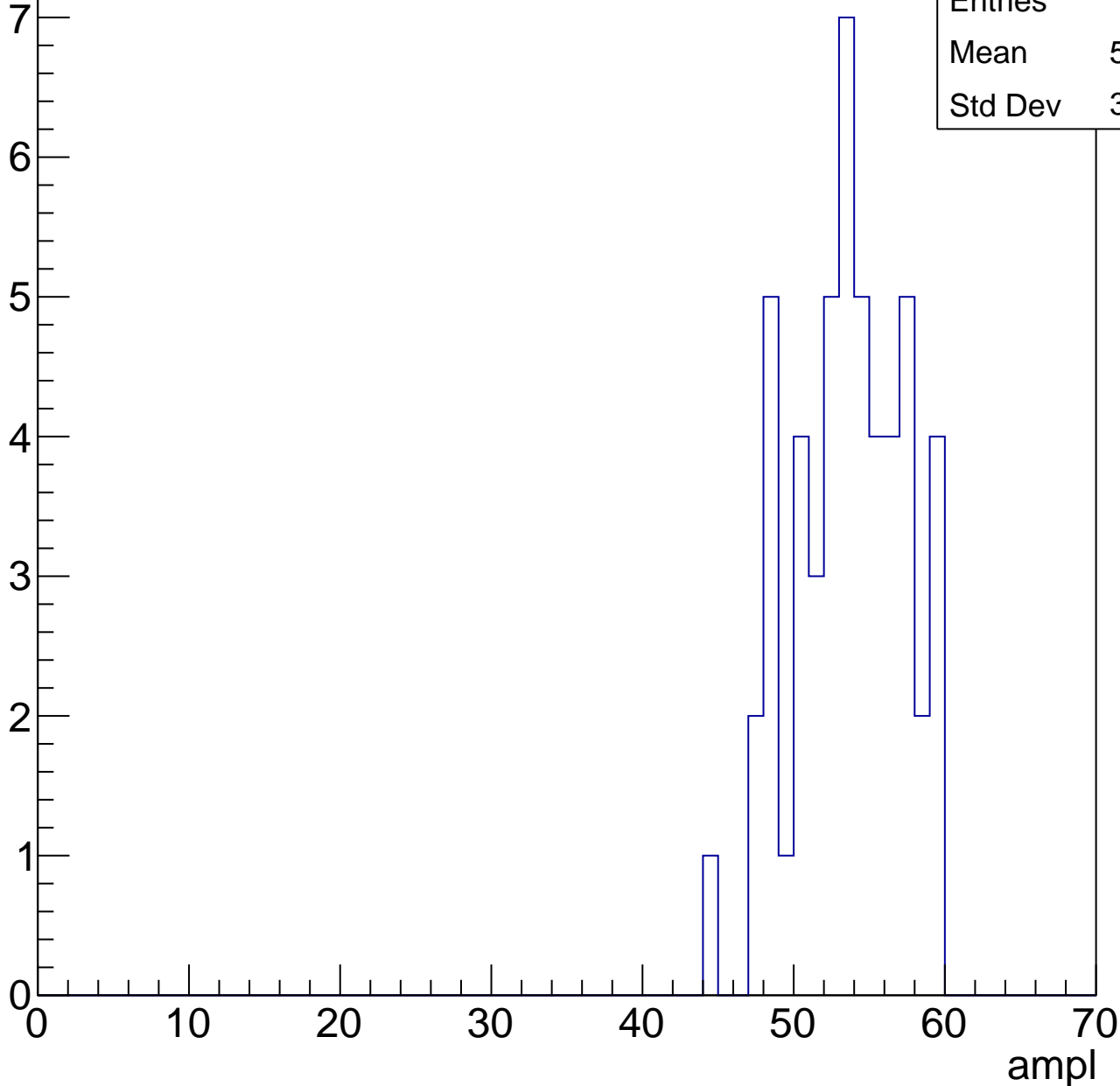


B1L102S, U18-ch111, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	52
Mean	53.12
Std Dev	3.609

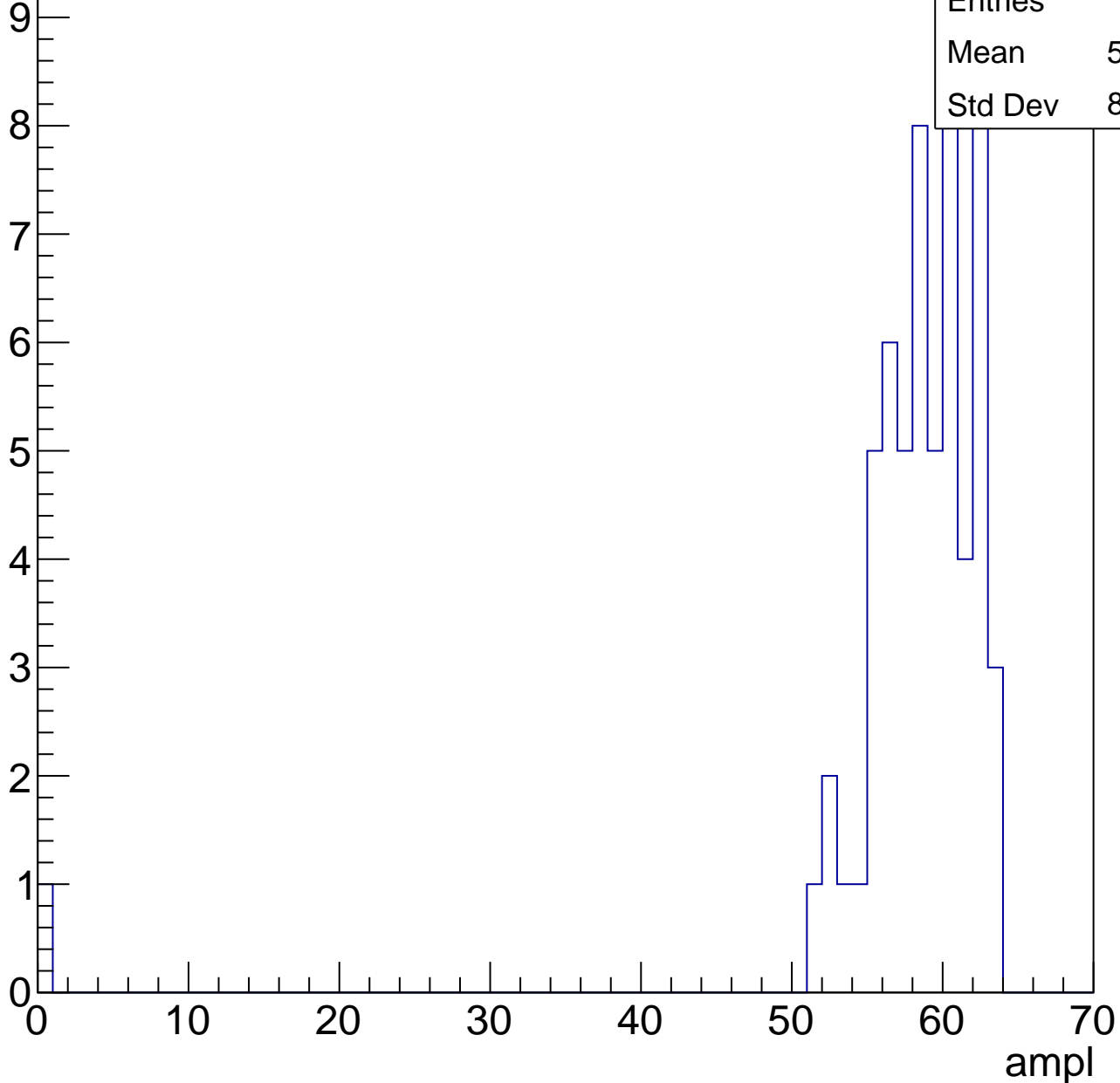


B1L102S, U18-ch111, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	57.39
Std Dev	8.086

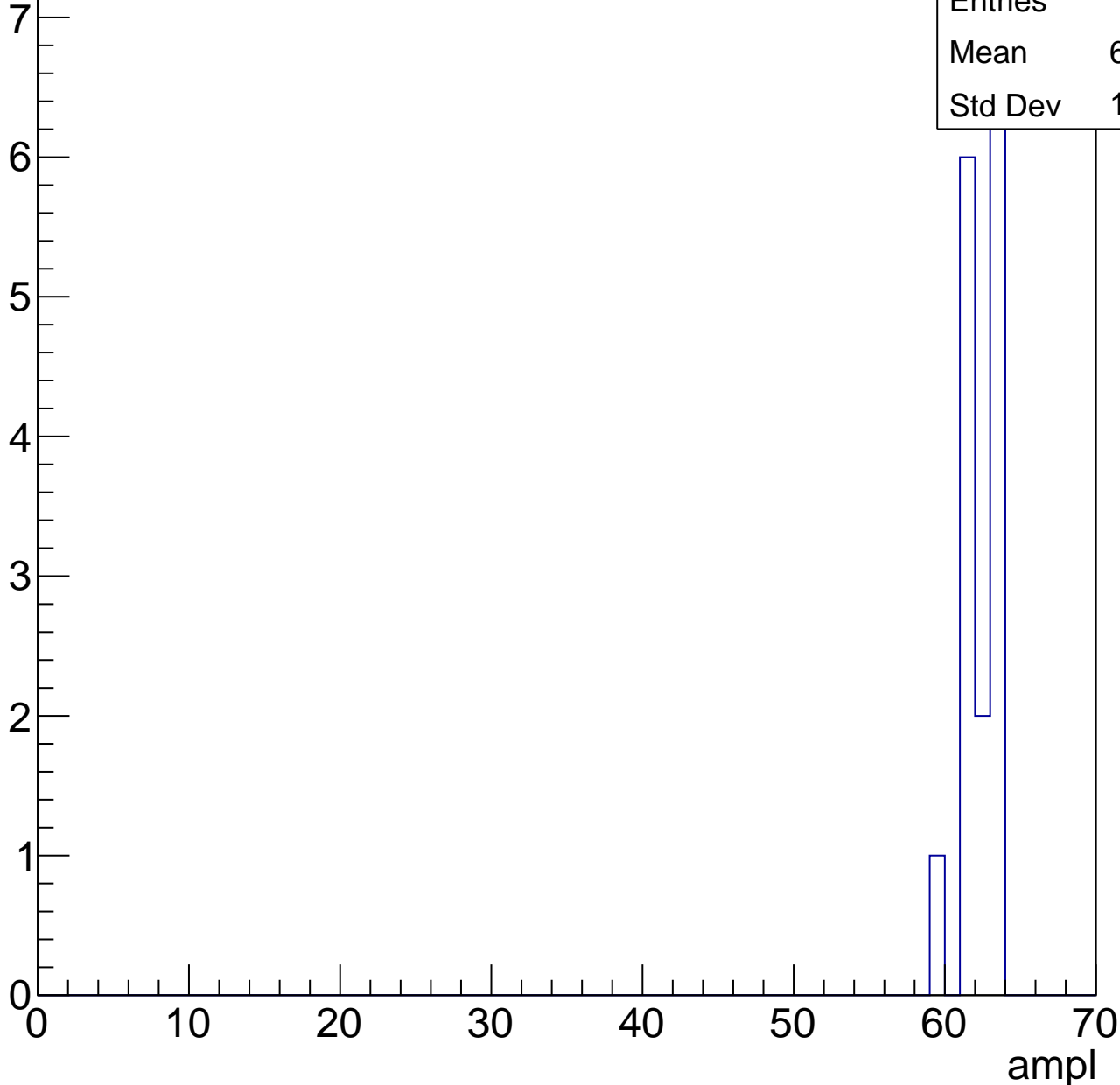


B1L102S, U18-ch111, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

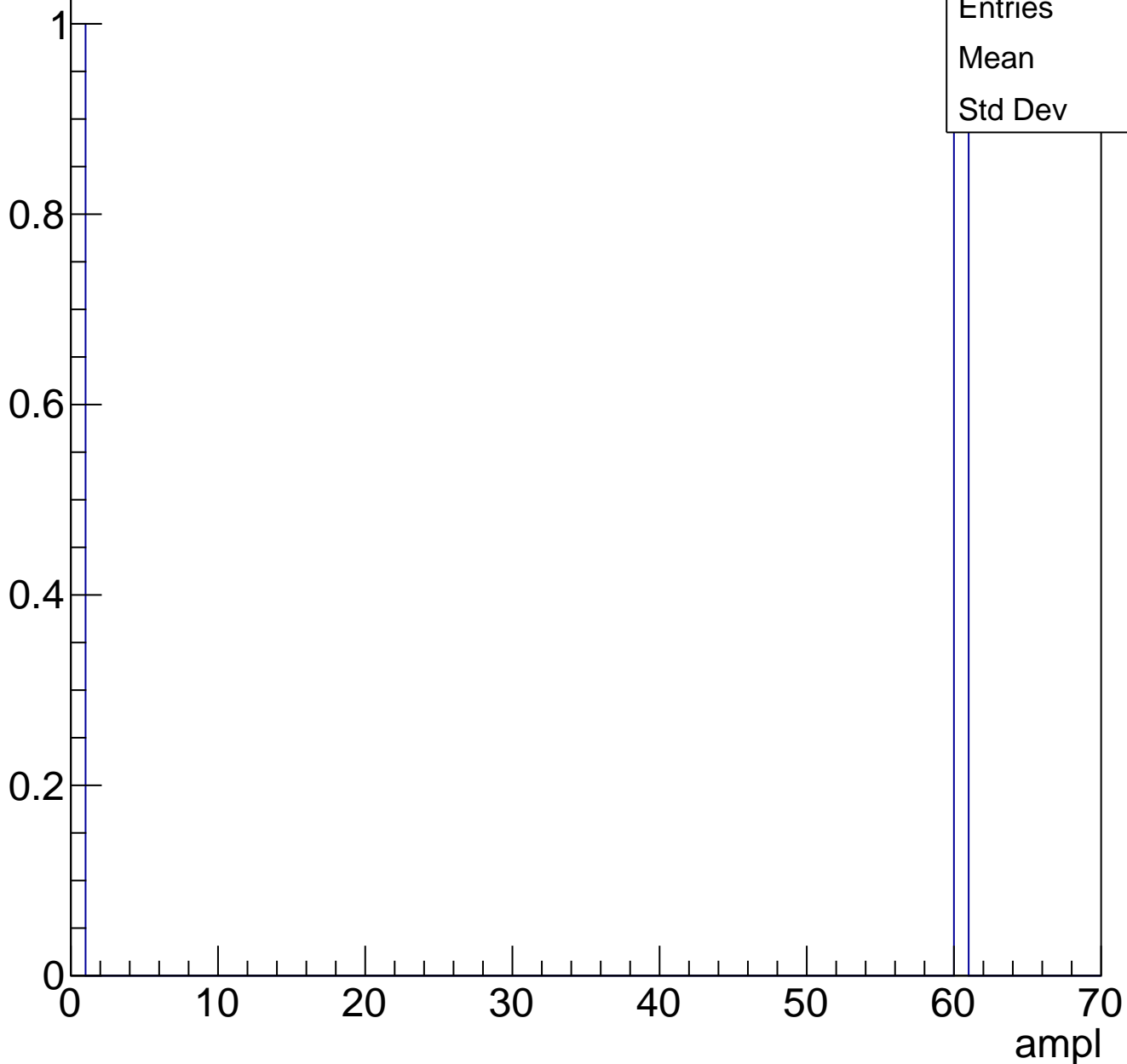
Entries	16
Mean	61.88
Std Dev	1.166



B1L102S, U18-ch111, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch112, adc0

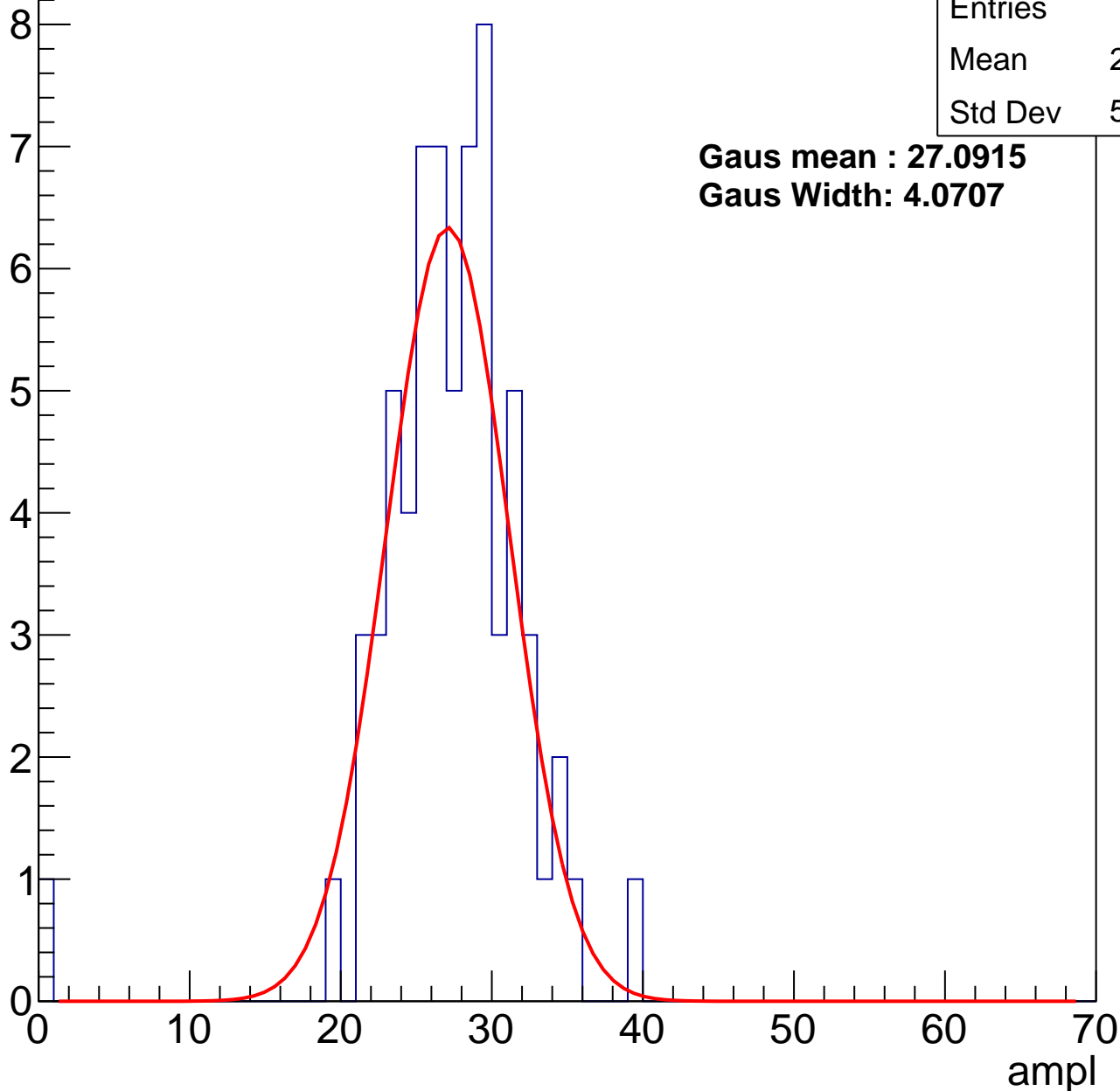
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	26.79
Std Dev	5.018

Gaus mean : 27.0915

Gaus Width: 4.0707



B1L102S, U18-ch112, adc1

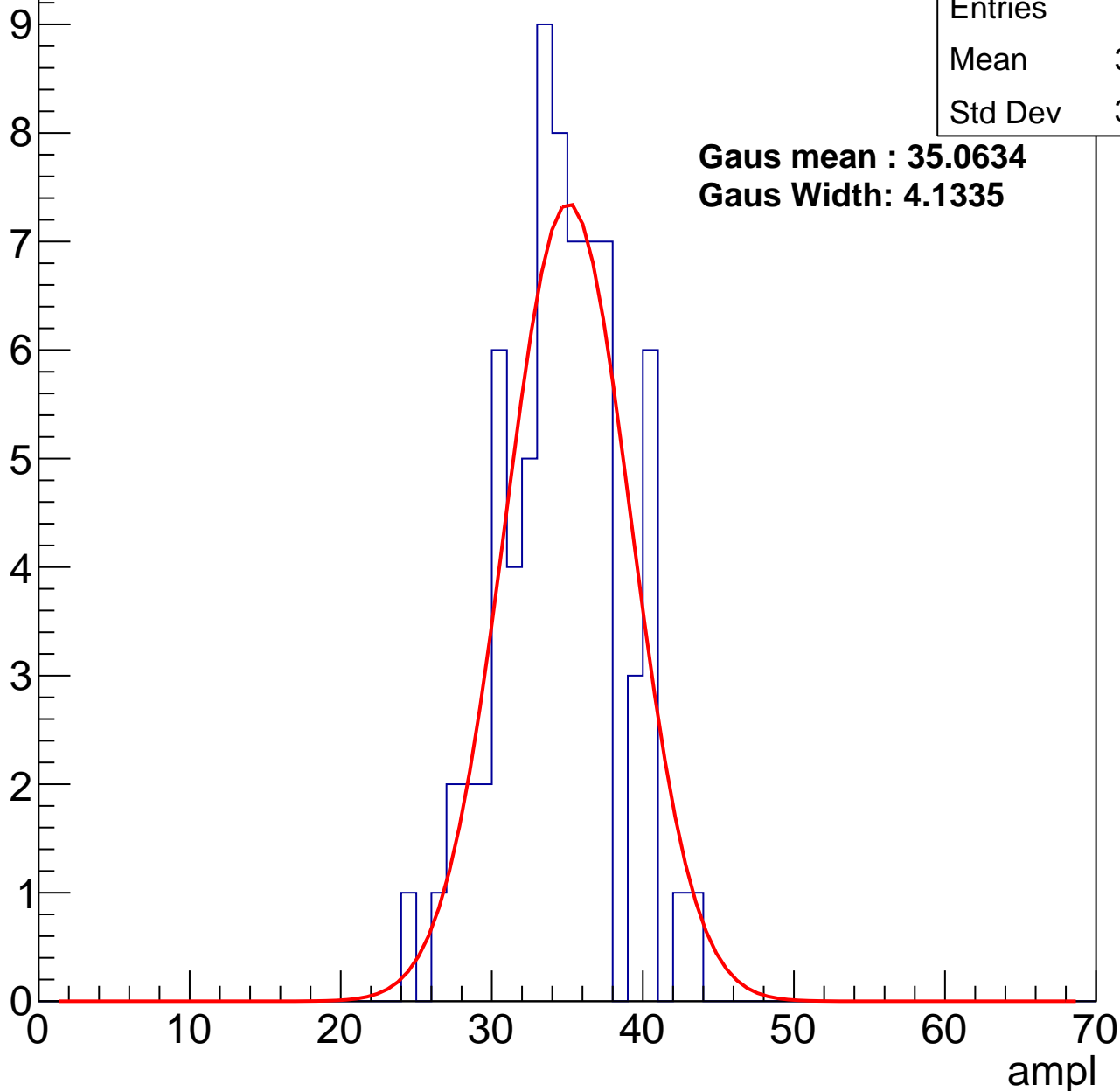
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	72
Mean	34.01
Std Dev	3.871

Gaus mean : 35.0634

Gaus Width: 4.1335



B1L102S, U18-ch112, adc2

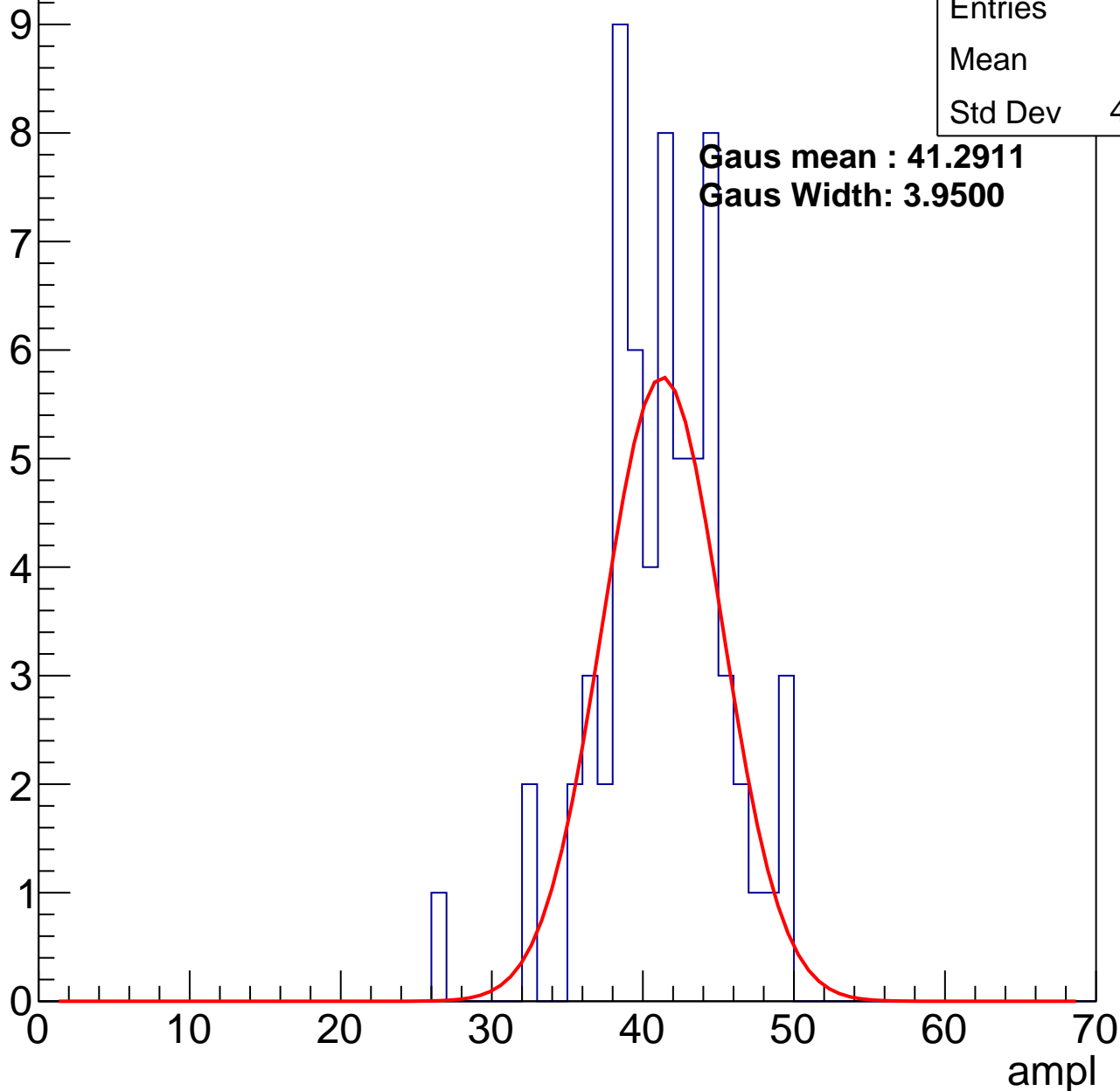
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	40.8
Std Dev	4.196

Gaus mean : 41.2911

Gaus Width: 3.9500

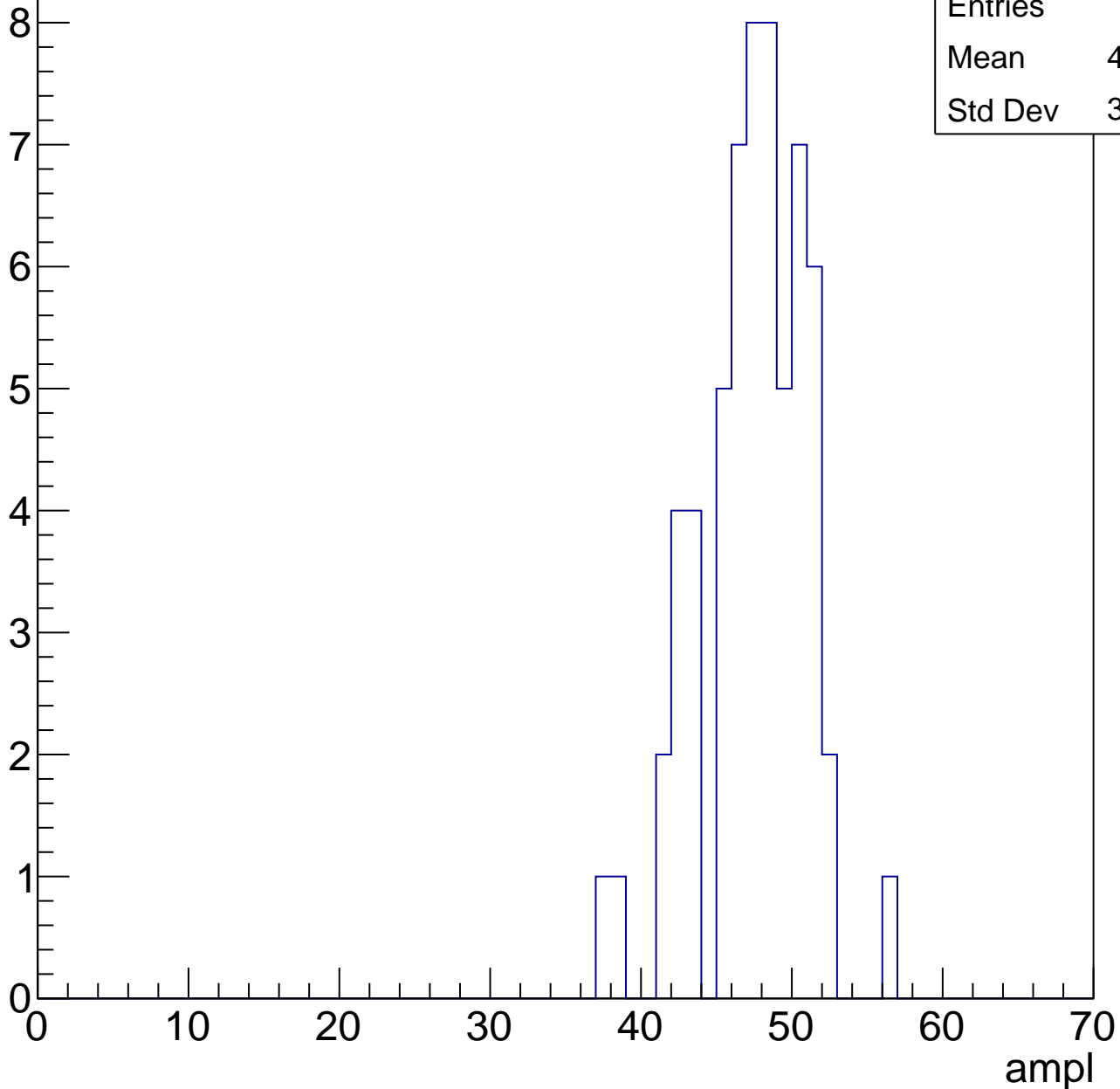


B1L102S, U18-ch112, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	46.97
Std Dev	3.534

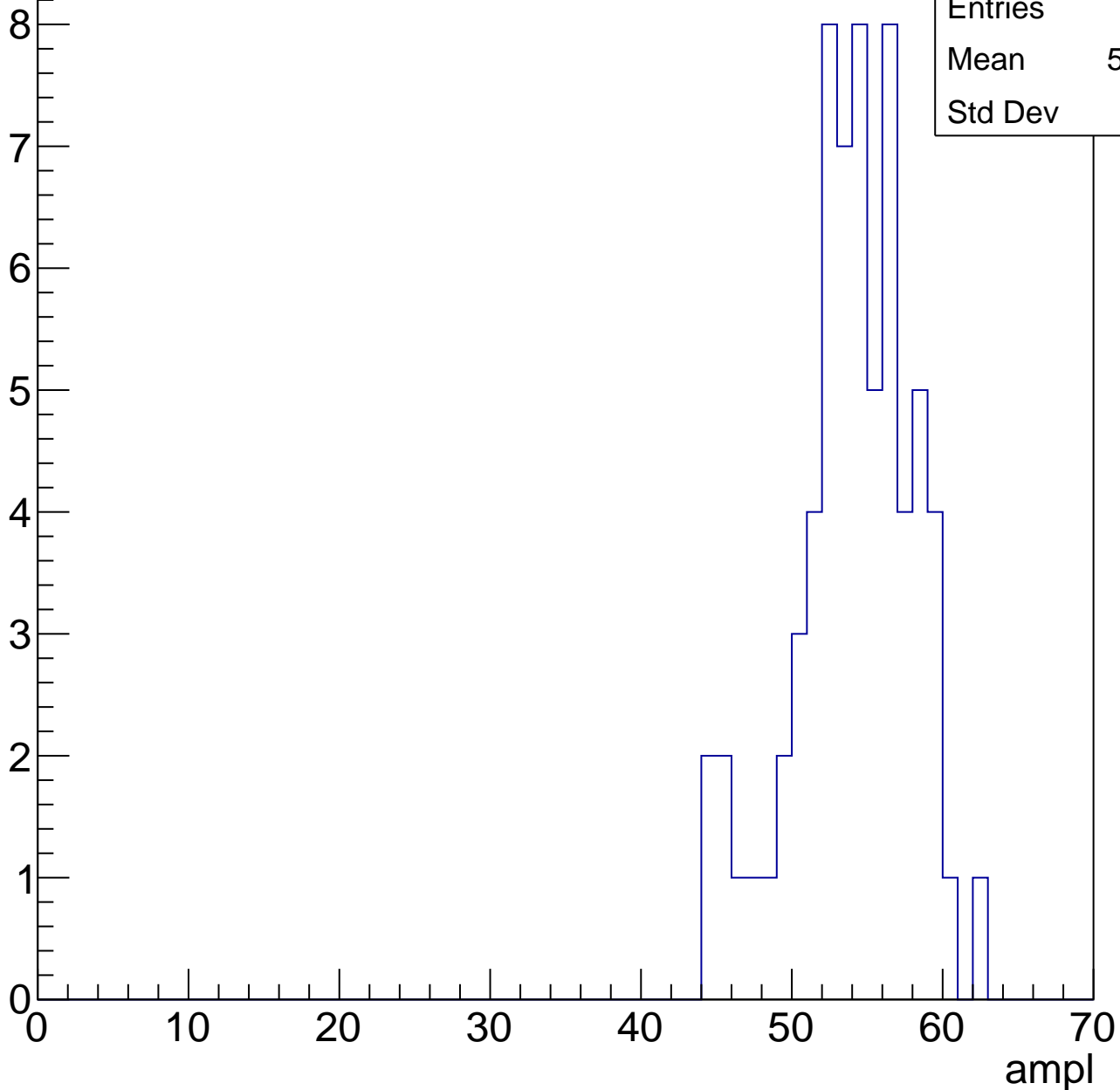


B1L102S, U18-ch112, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

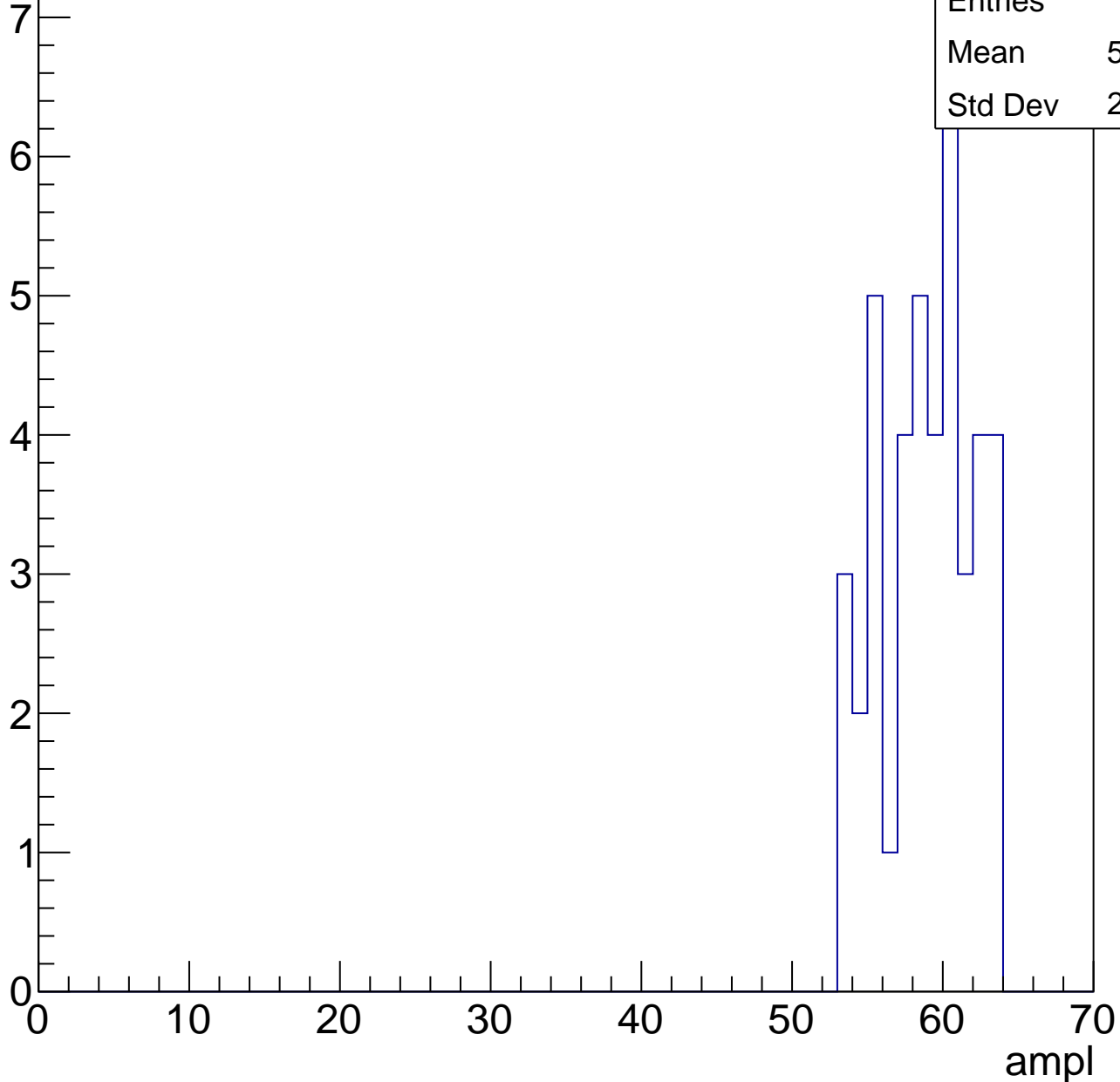
Entries	67
Mean	53.57
Std Dev	3.91



B1L102S, U18-ch112, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



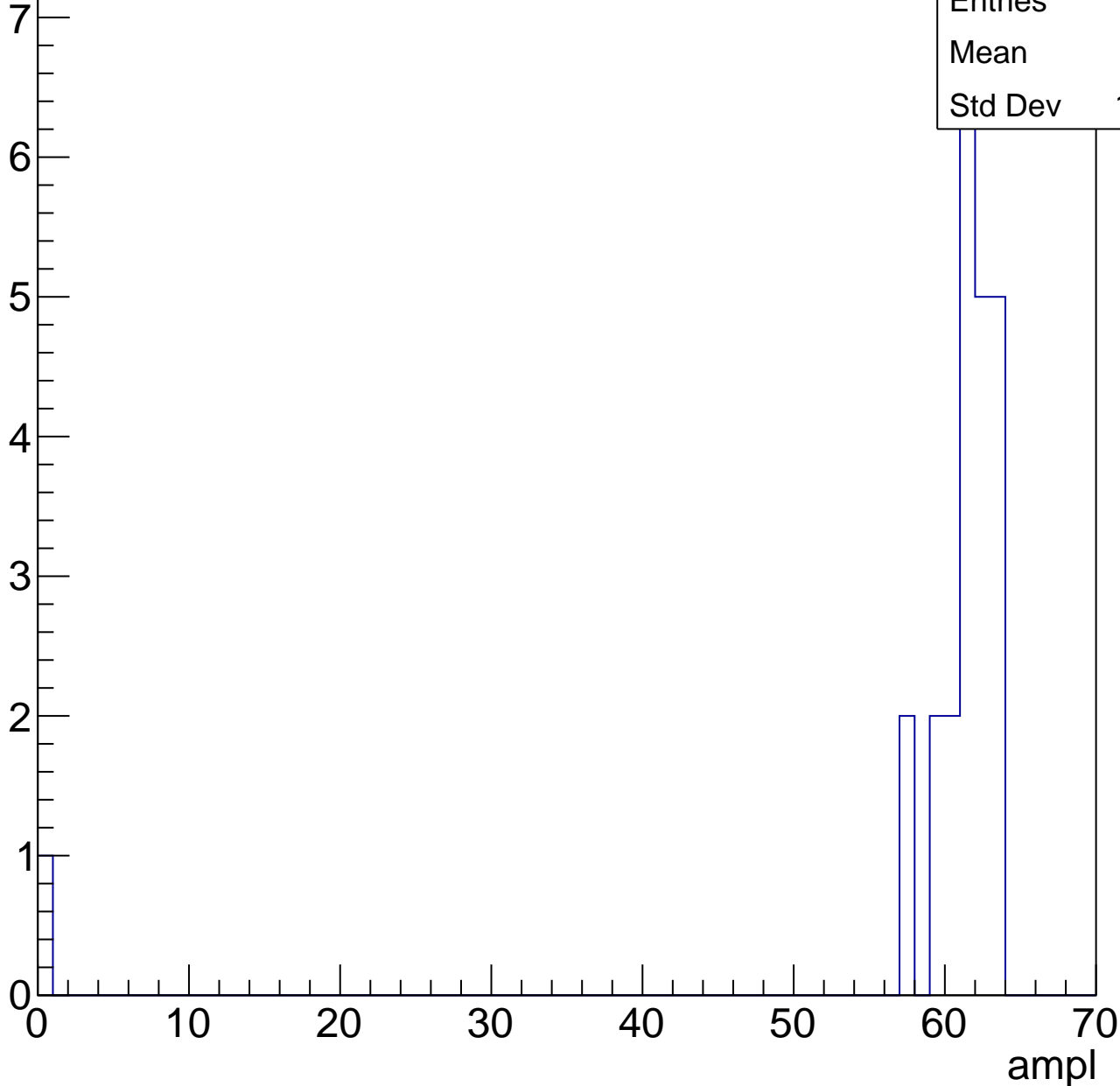
Entries	42
Mean	58.45
Std Dev	2.986

B1L102S, U18-ch112, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

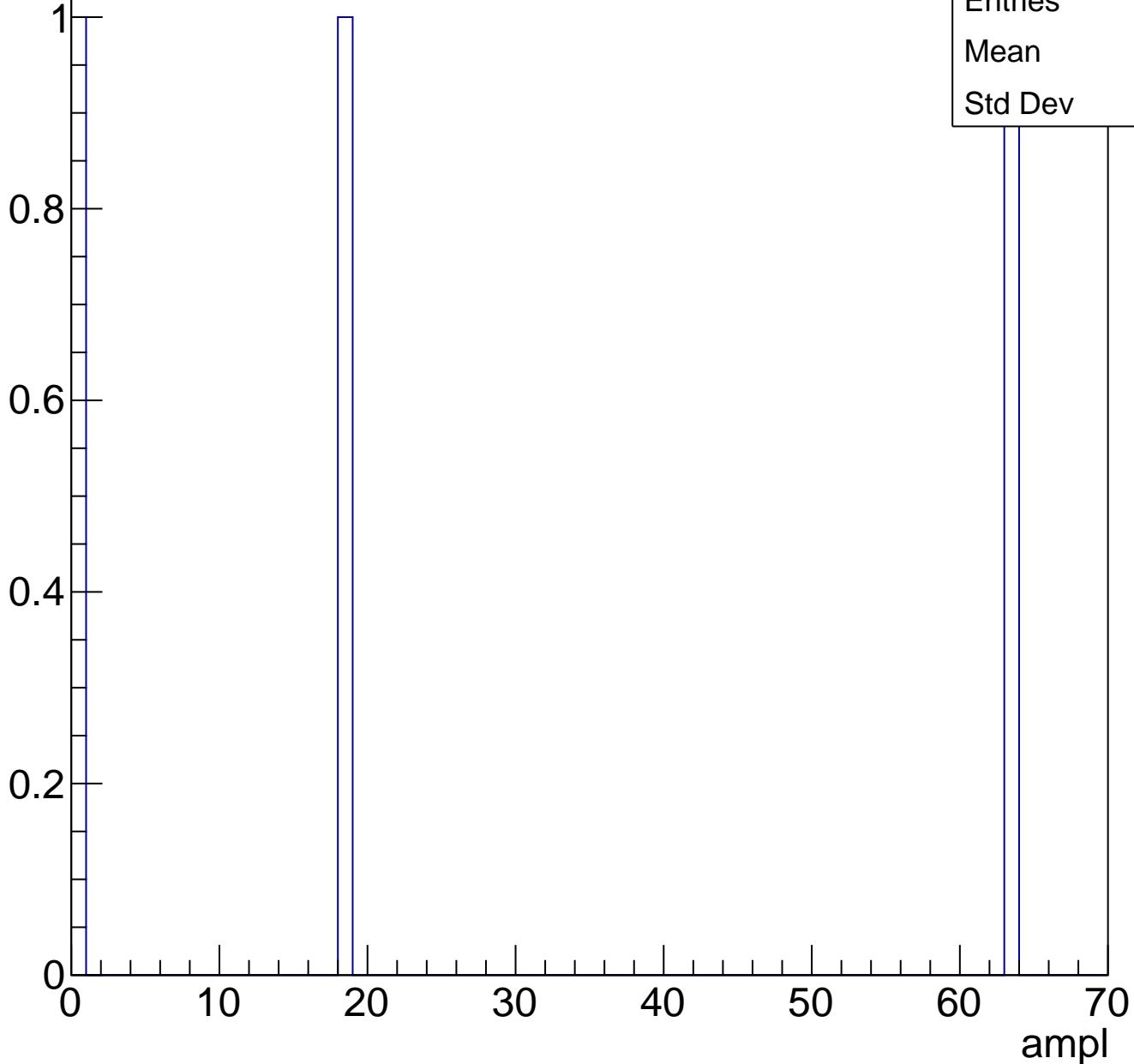
Entries	24
Mean	58.5
Std Dev	12.31



B1L102S, U18-ch112, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	3
Mean	27
Std Dev	26.5

B1L102S, U18-ch113, adc0

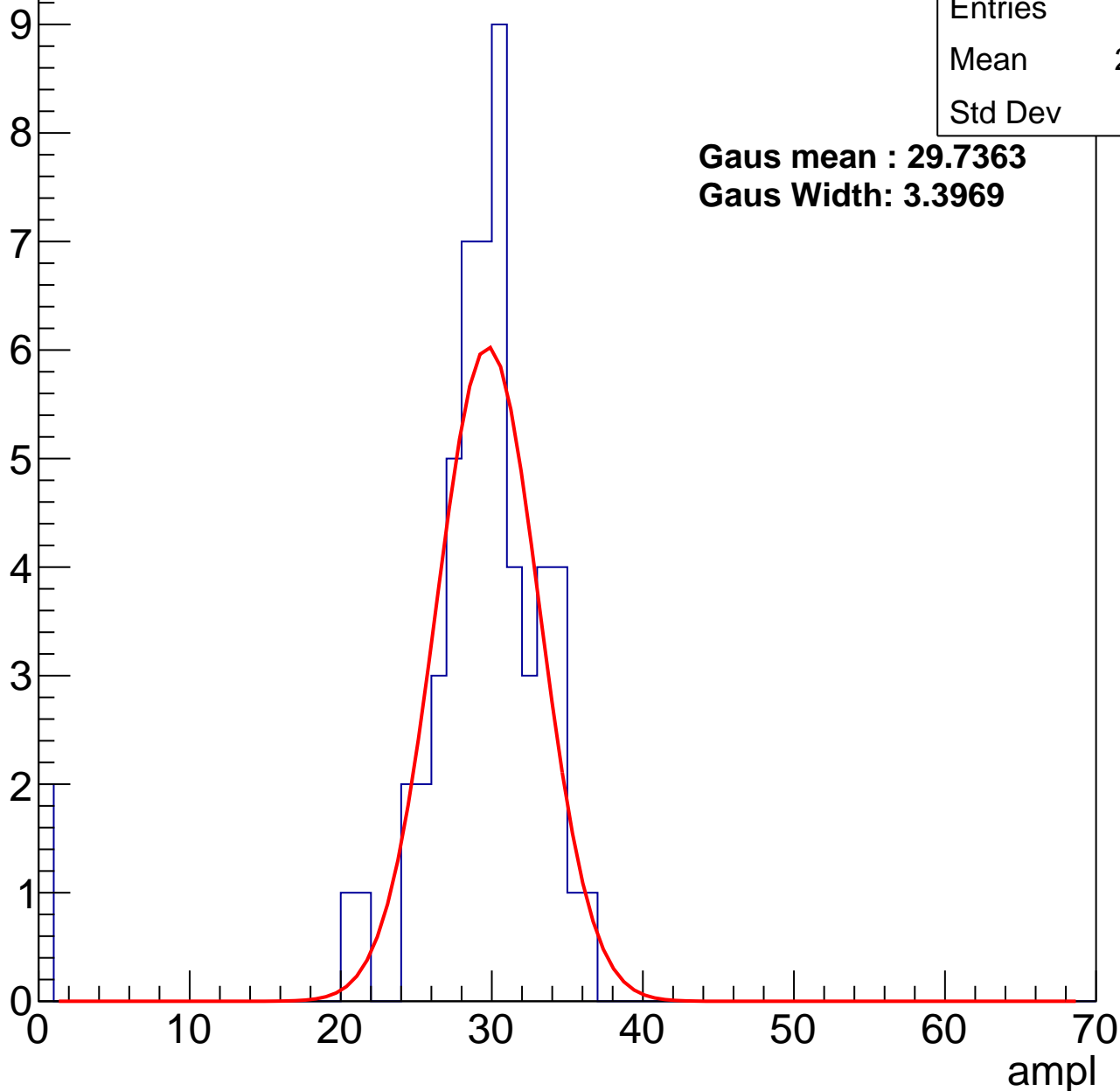
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	28.21
Std Dev	6.31

Gaus mean : 29.7363

Gaus Width: 3.3969



B1L102S, U18-ch113, adc1

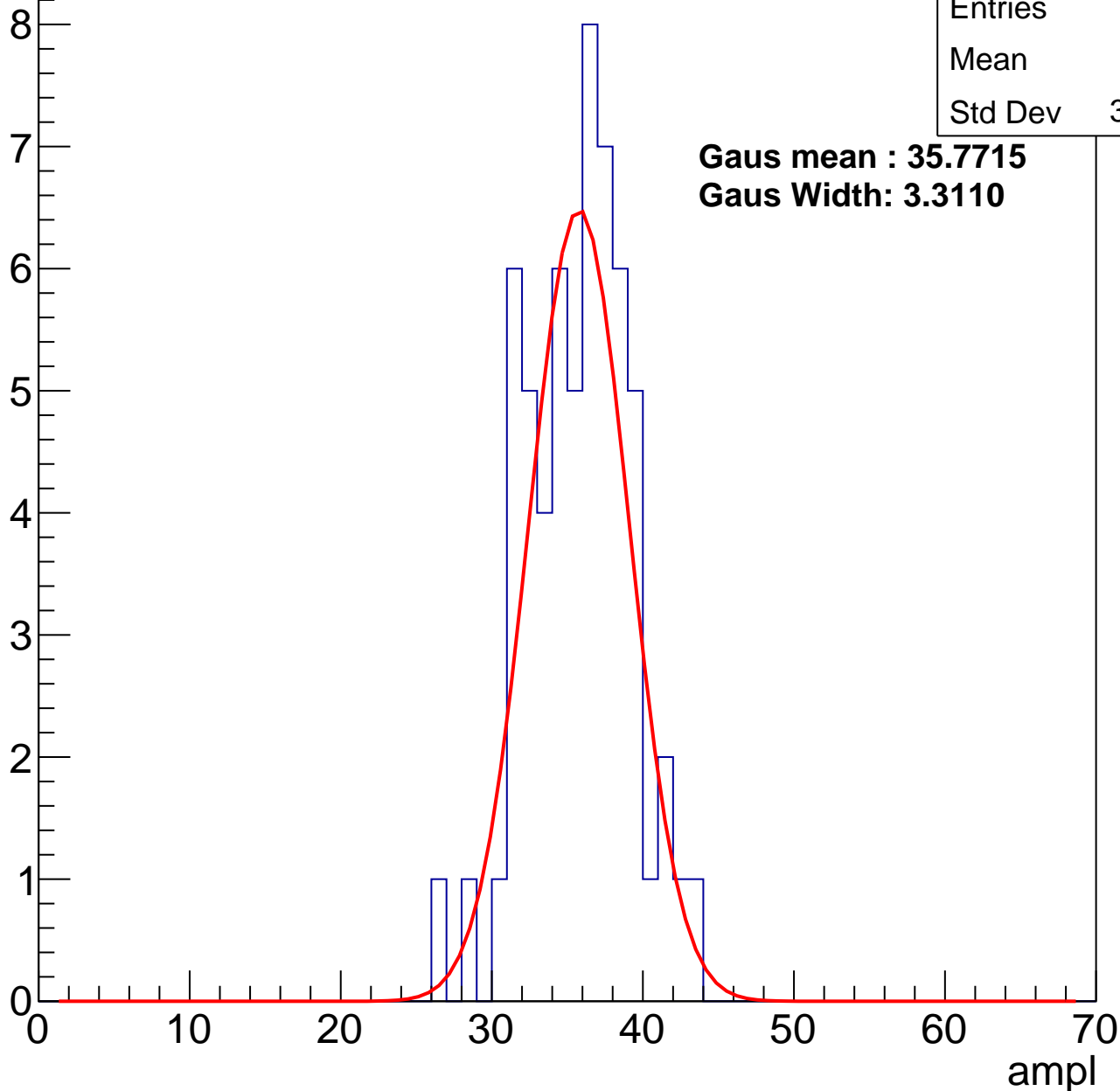
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	35.3
Std Dev	3.398

Gaus mean : 35.7715

Gaus Width: 3.3110



B1L102S, U18-ch113, adc2

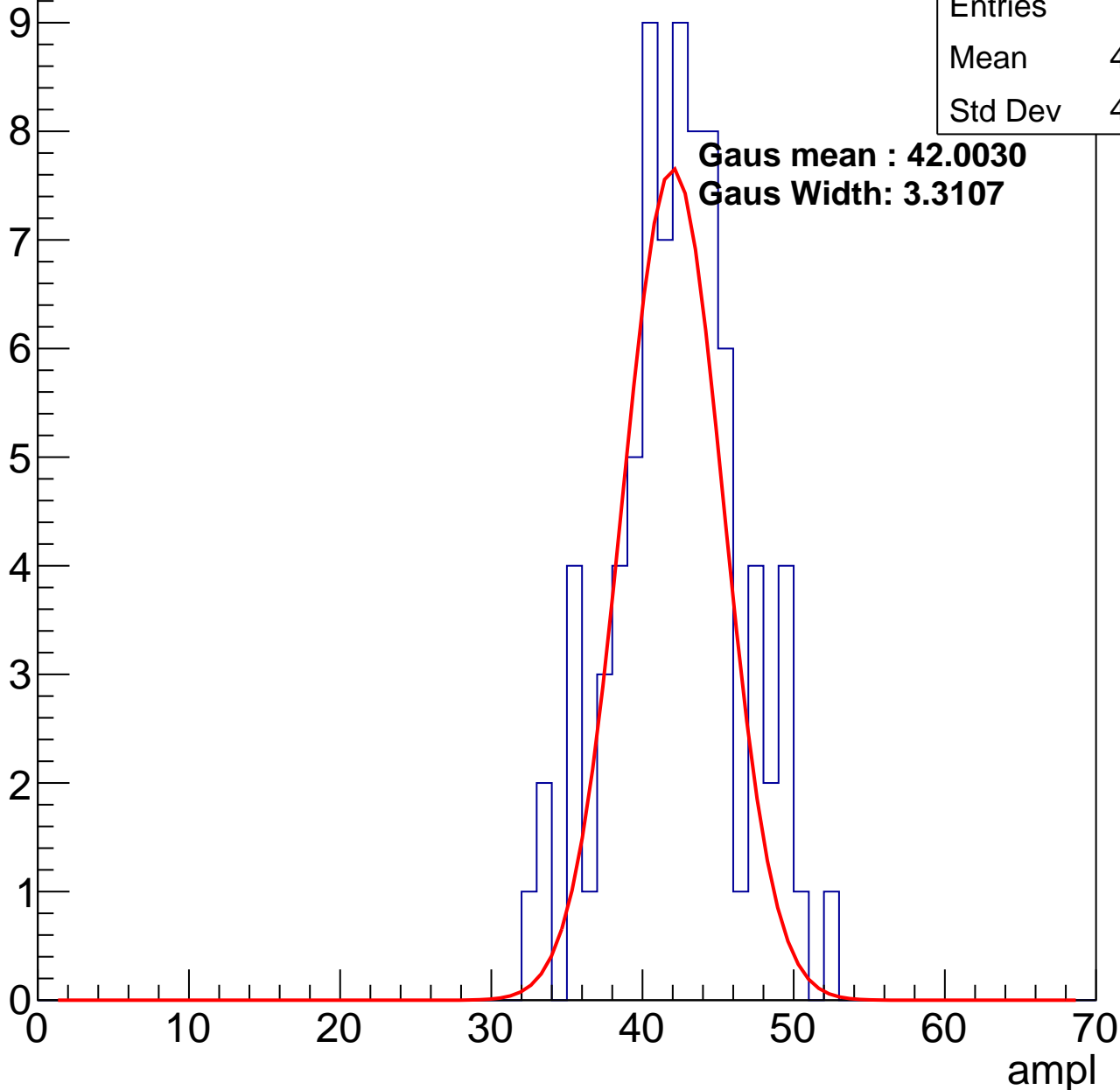
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	80
Mean	41.89
Std Dev	4.126

Gaus mean : 42.0030

Gaus Width: 3.3107

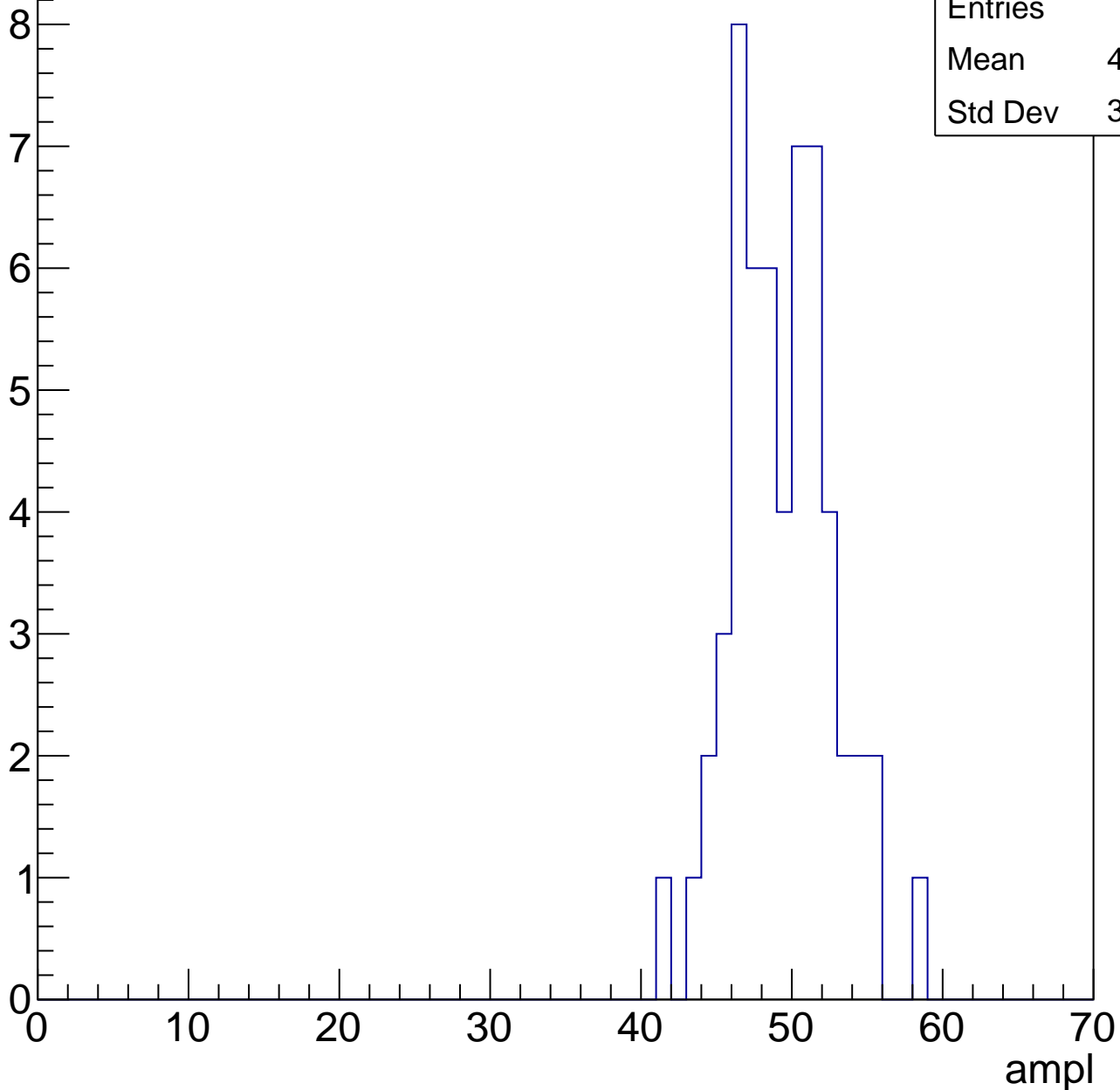


B1L102S, U18-ch113, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	48.89
Std Dev	3.288

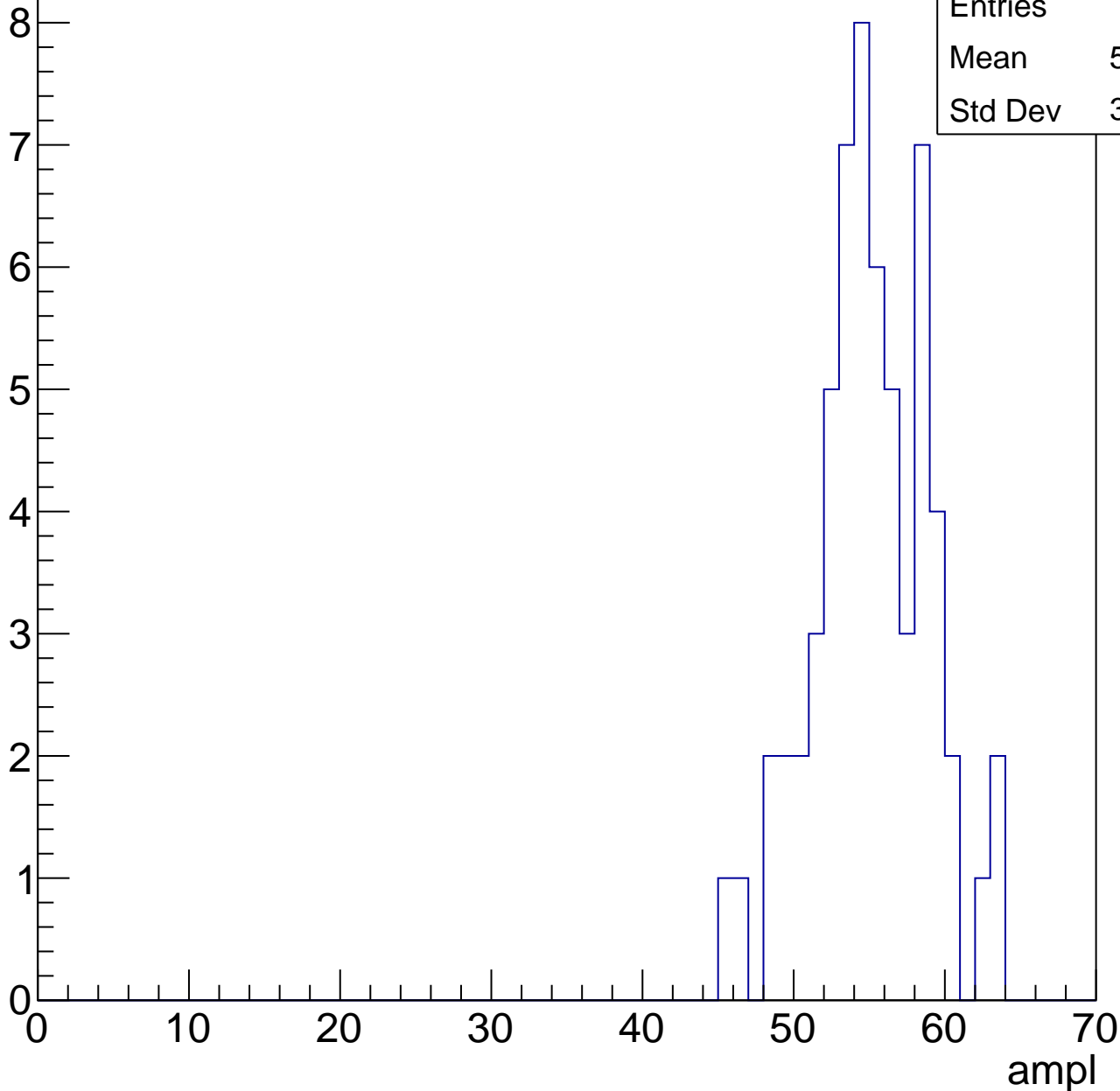


B1L102S, U18-ch113, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	54.62
Std Dev	3.825

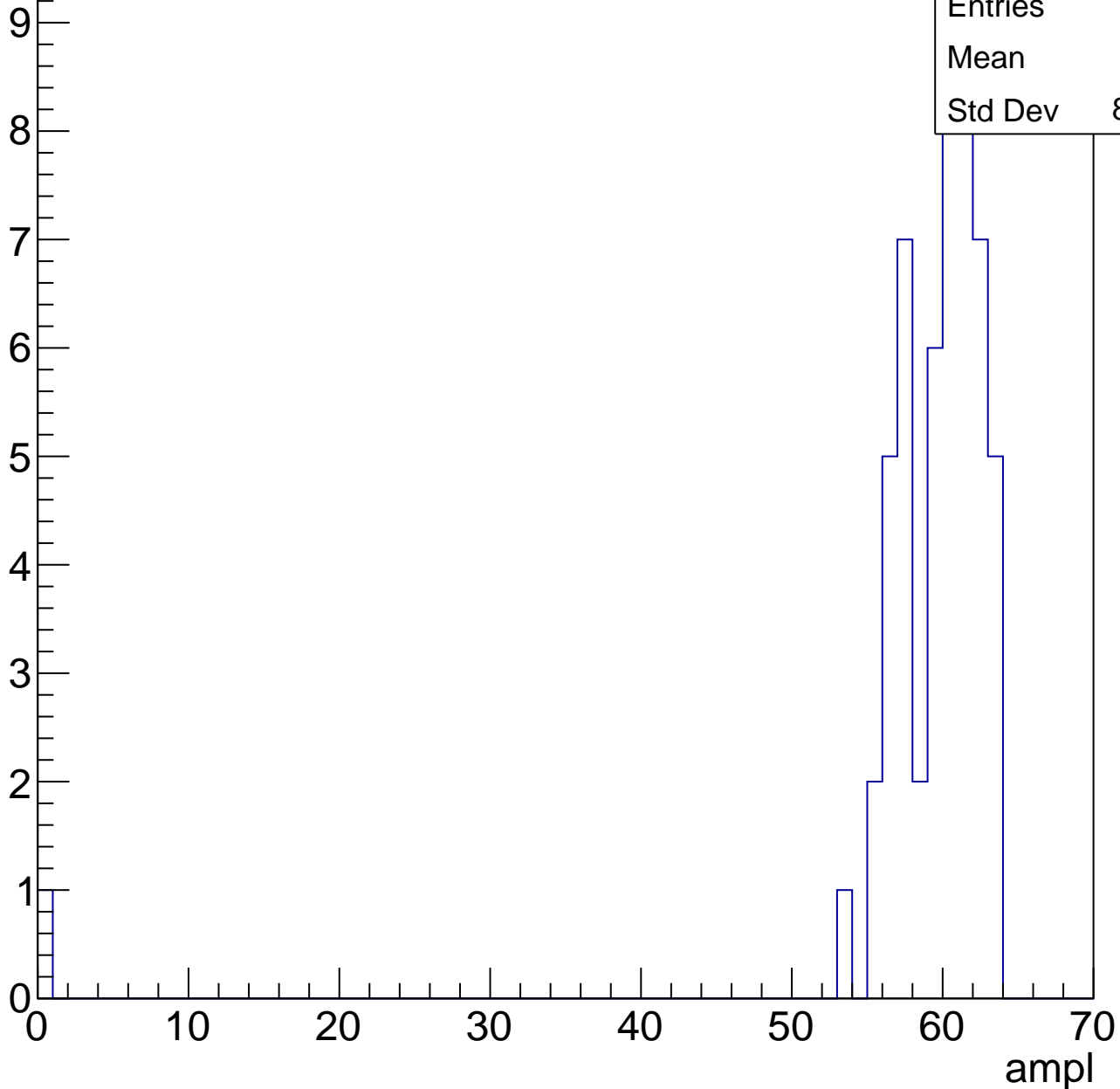


B1L102S, U18-ch113, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	58.3
Std Dev	8.451



B1L102S, U18-ch113, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch113, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch114, adc0

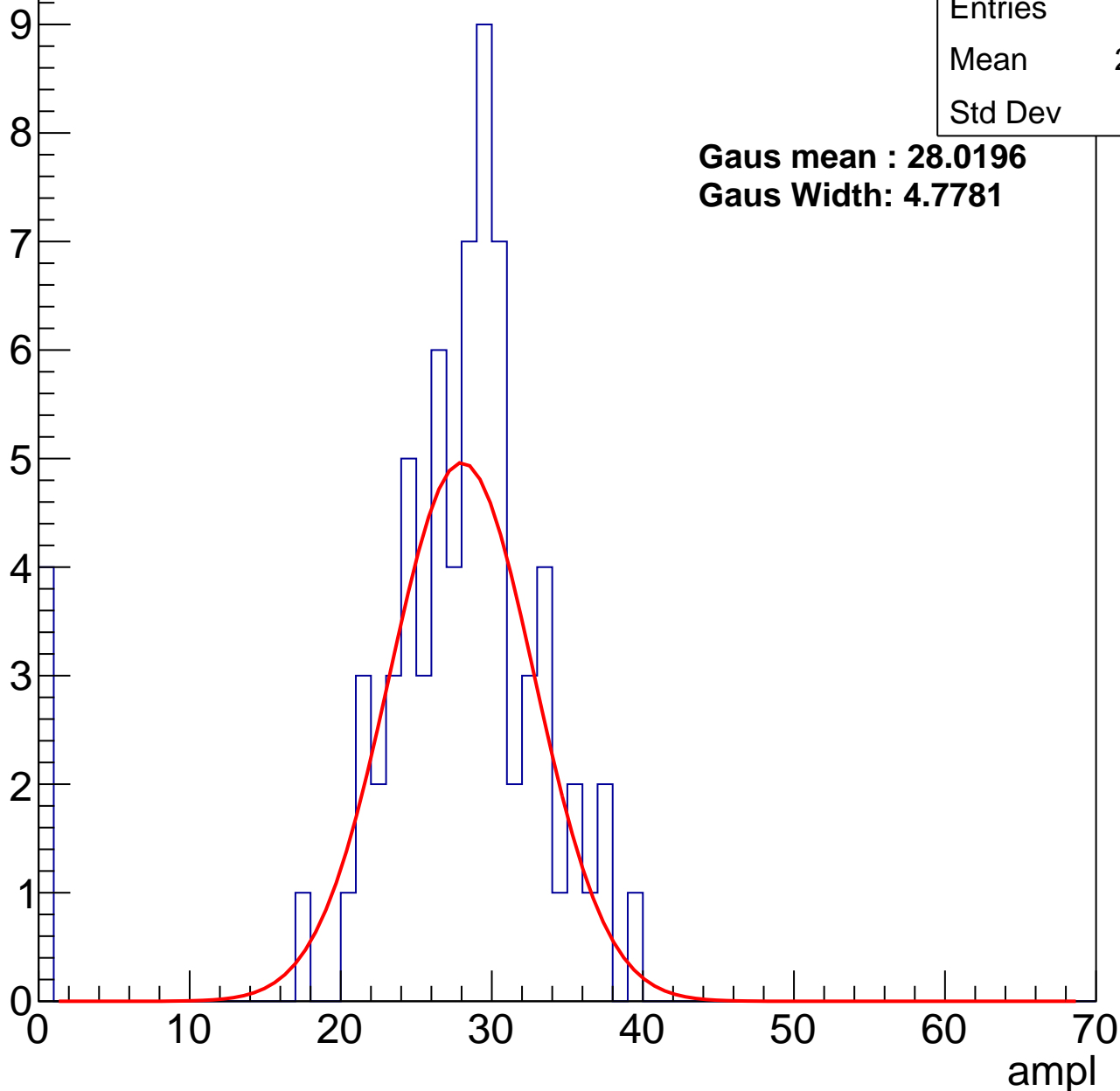
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	26.51
Std Dev	7.76

Gaus mean : 28.0196

Gaus Width: 4.7781



B1L102S, U18-ch114, adc1

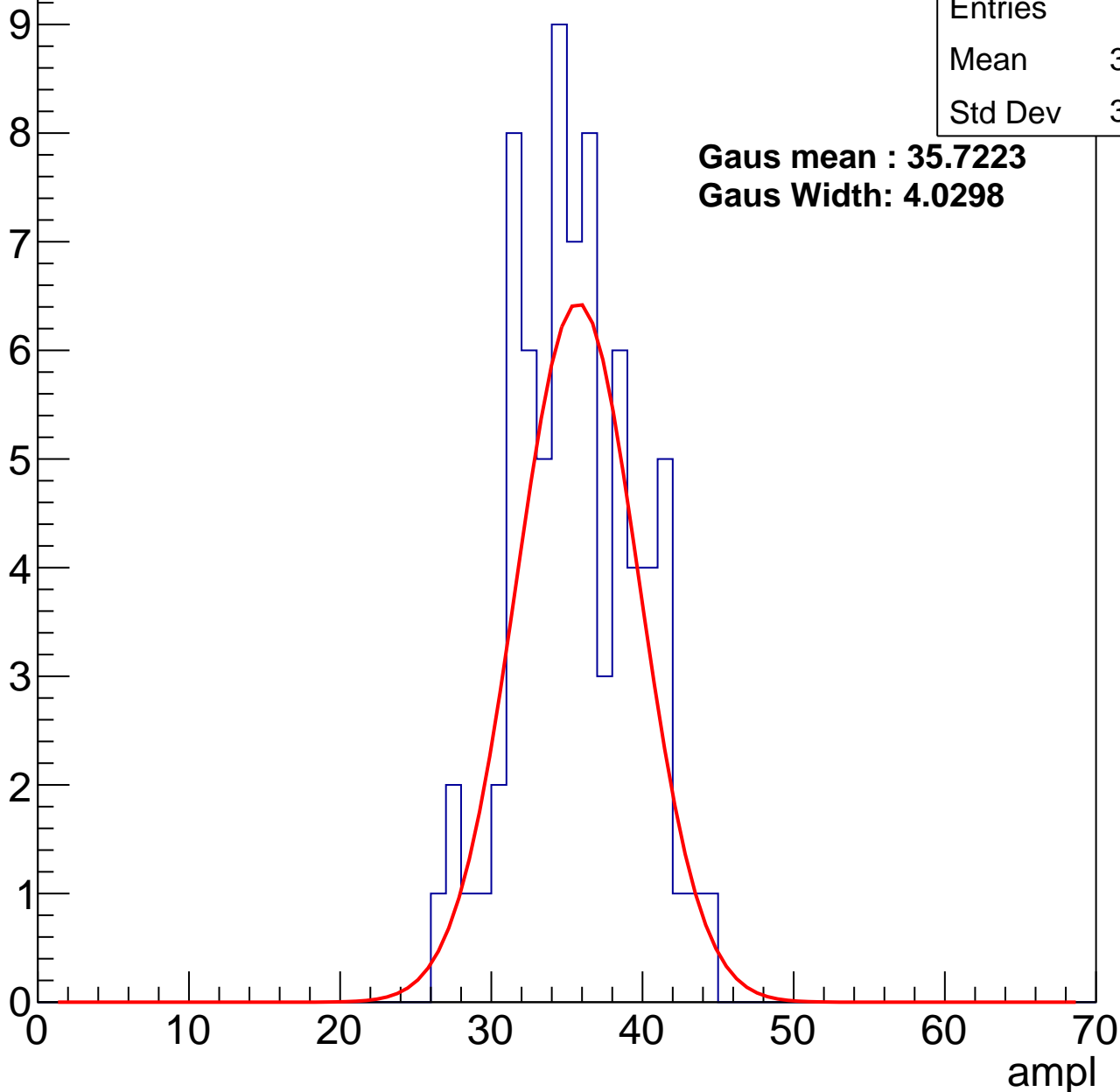
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.07
Std Dev	3.944

Gaus mean : 35.7223

Gaus Width: 4.0298



B1L102S, U18-ch114, adc2

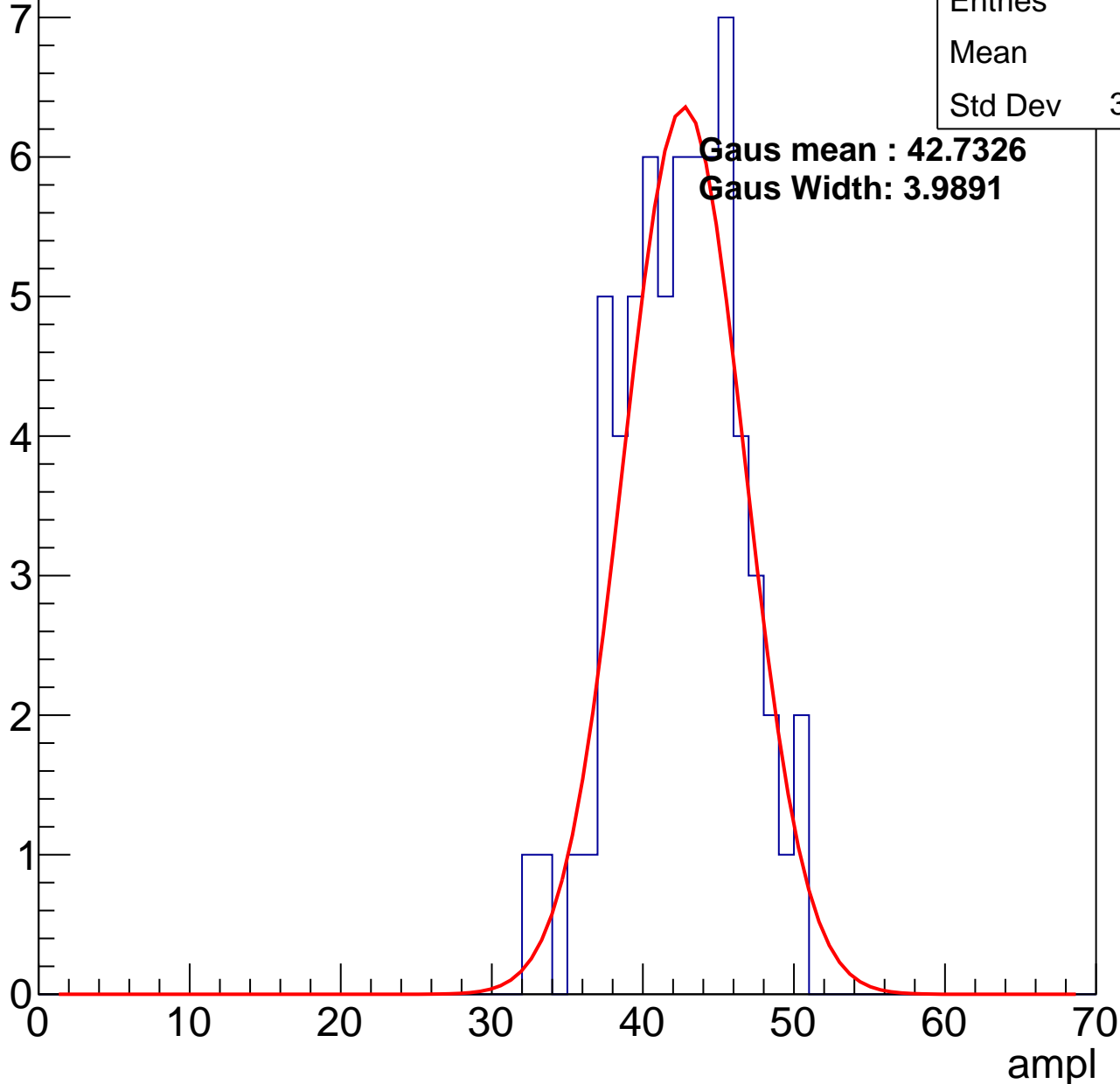
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42
Std Dev	3.912

Gaus mean : 42.7326

Gaus Width: 3.9891

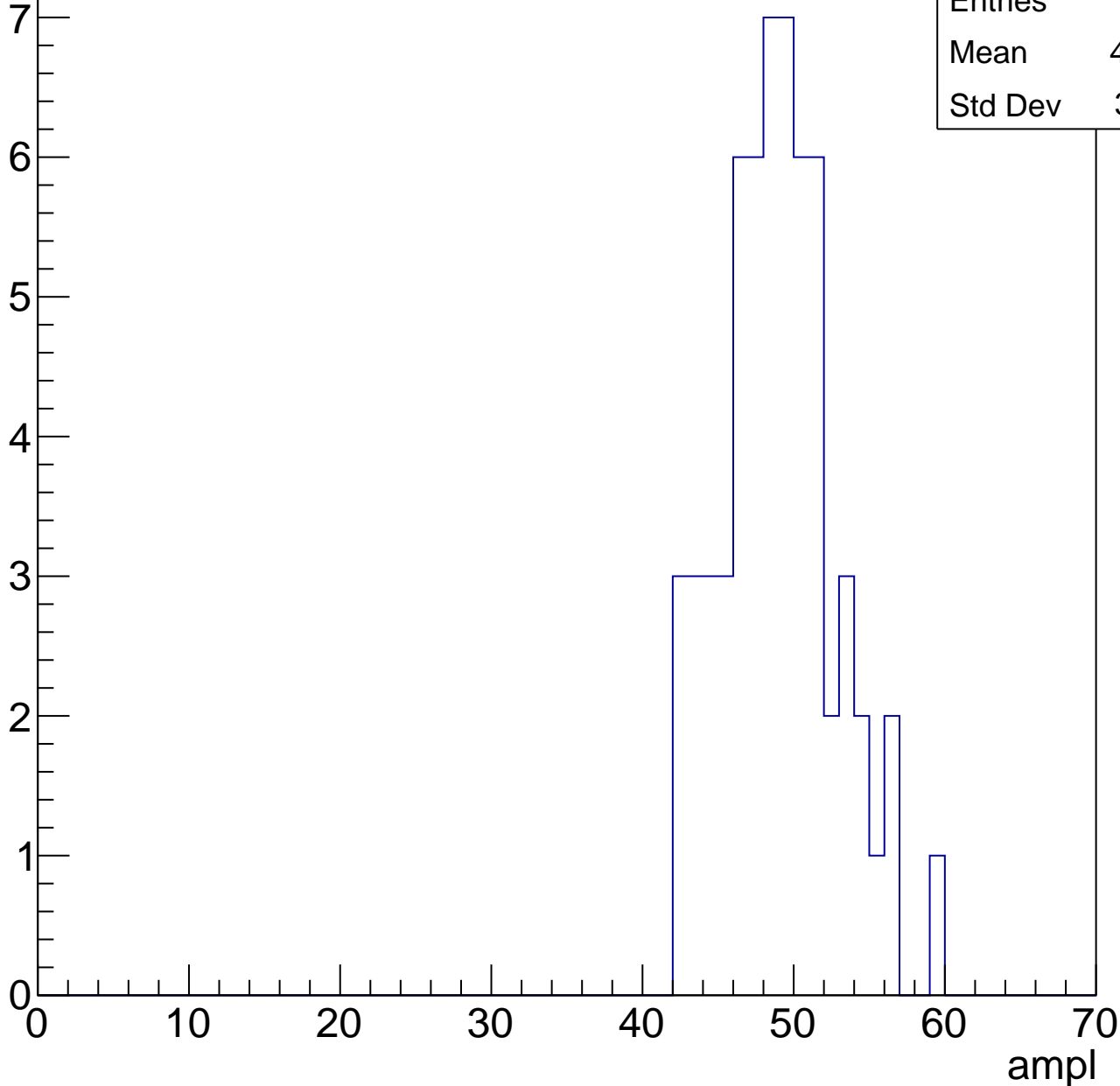


B1L102S, U18-ch114, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	48.56
Std Dev	3.691

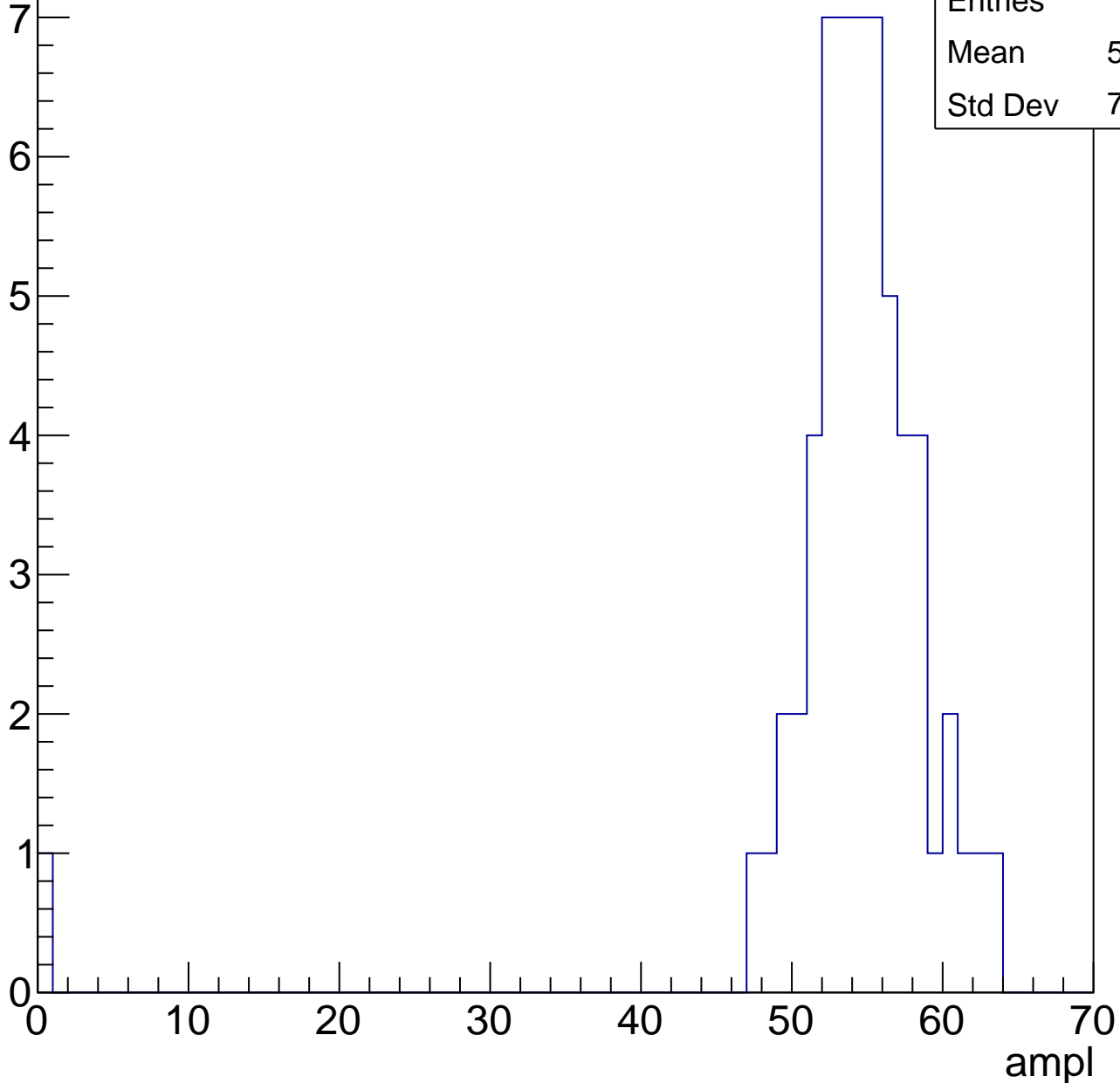


B1L102S, U18-ch114, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	53.45
Std Dev	7.828

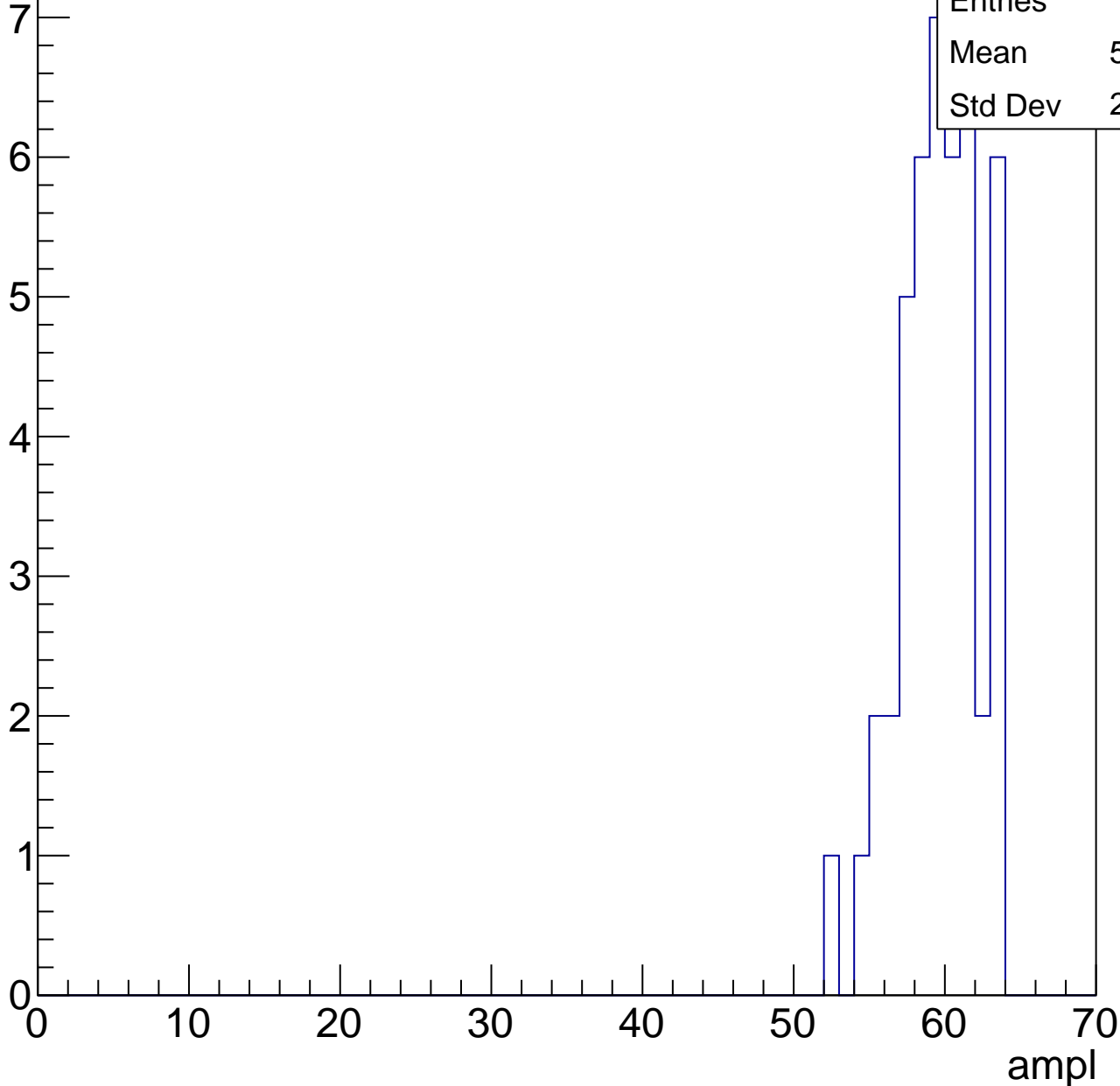


B1L102S, U18-ch114, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	45
Mean	59.18
Std Dev	2.567

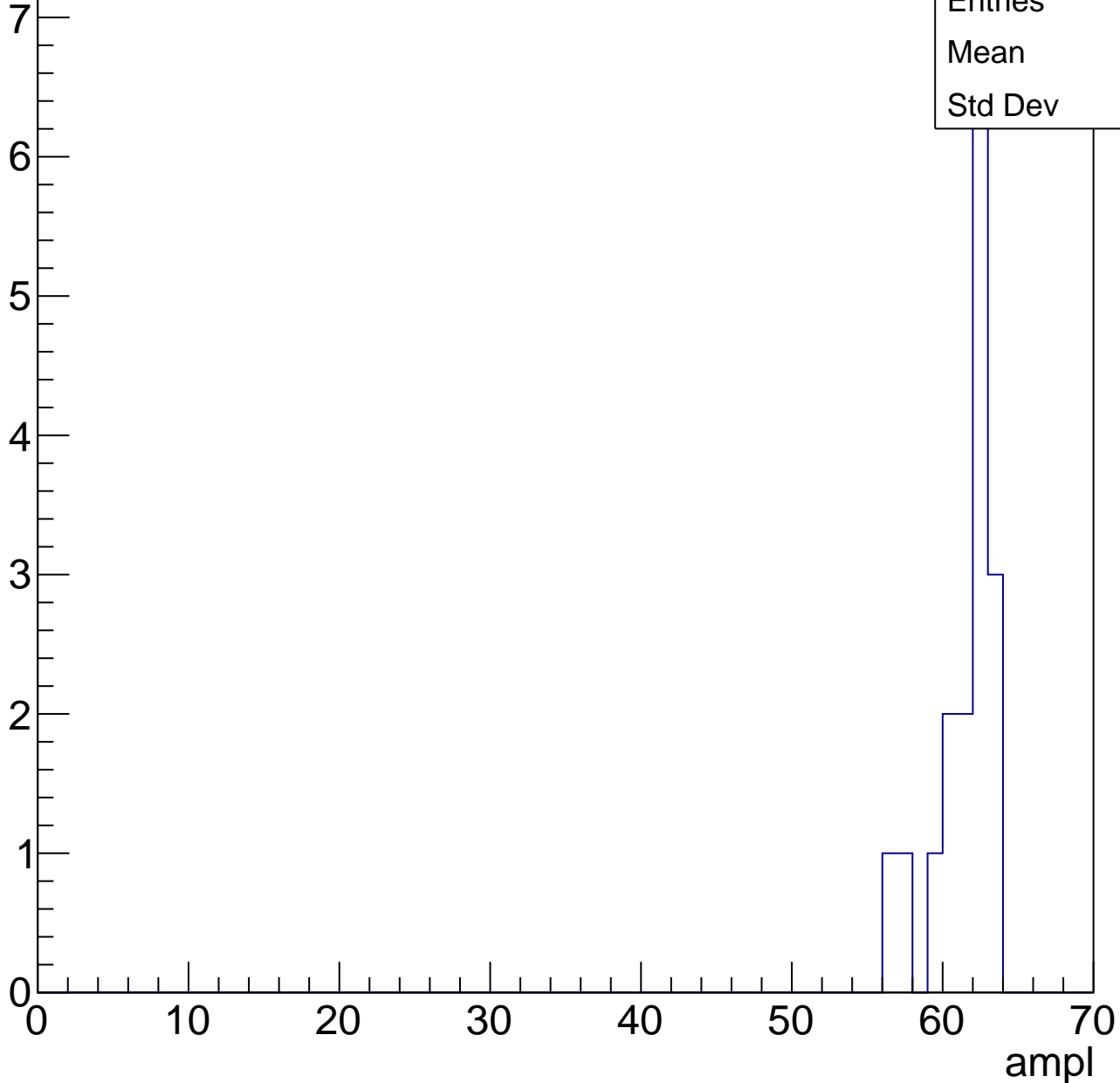


B1L102S, U18-ch114, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	17
Mean	61
Std Dev	1.97



B1L102S, U18-ch114, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch115, adc0

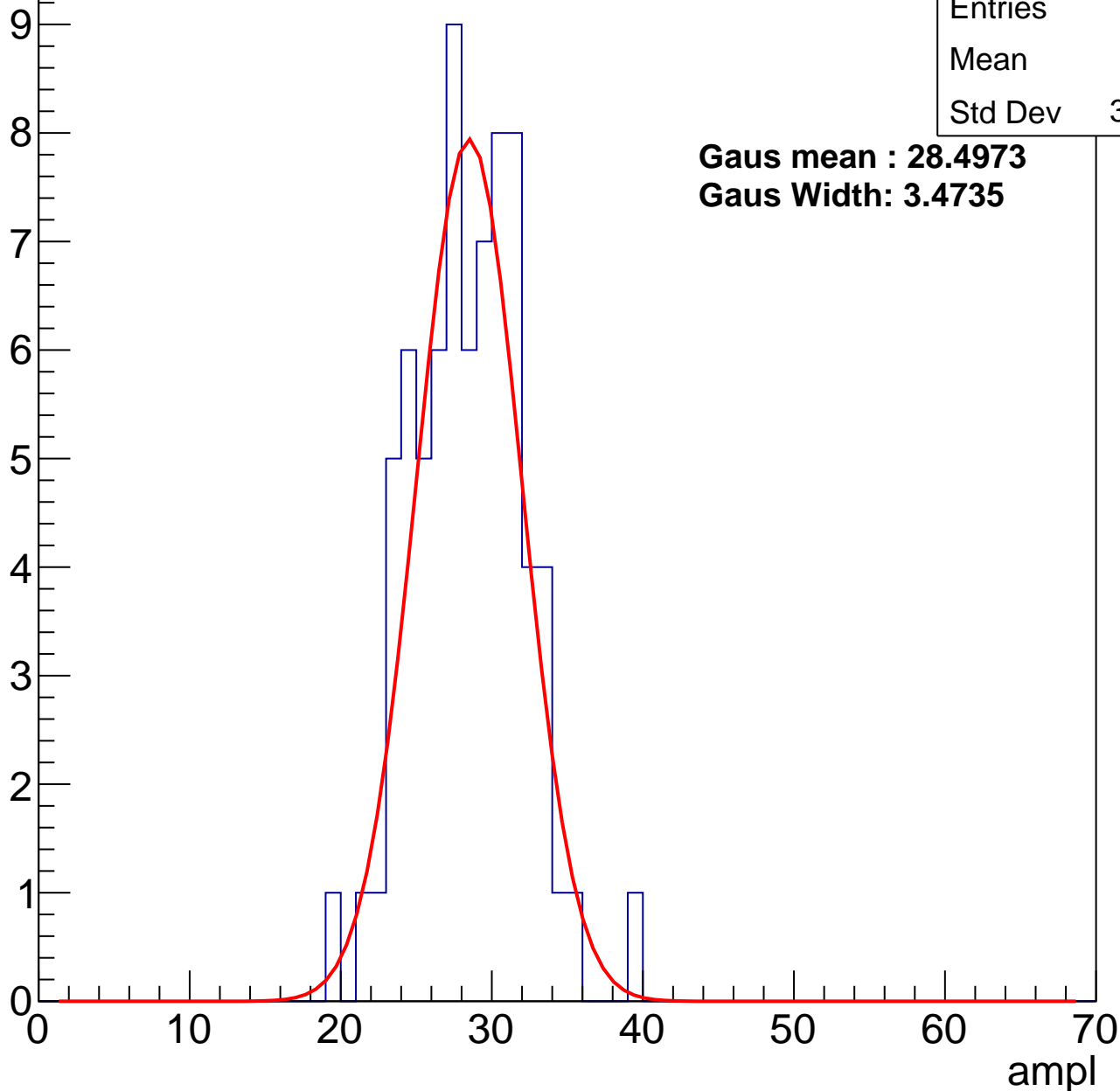
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	74
Mean	28
Std Dev	3.575

Gaus mean : 28.4973

Gaus Width: 3.4735



B1L102S, U18-ch115, adc1

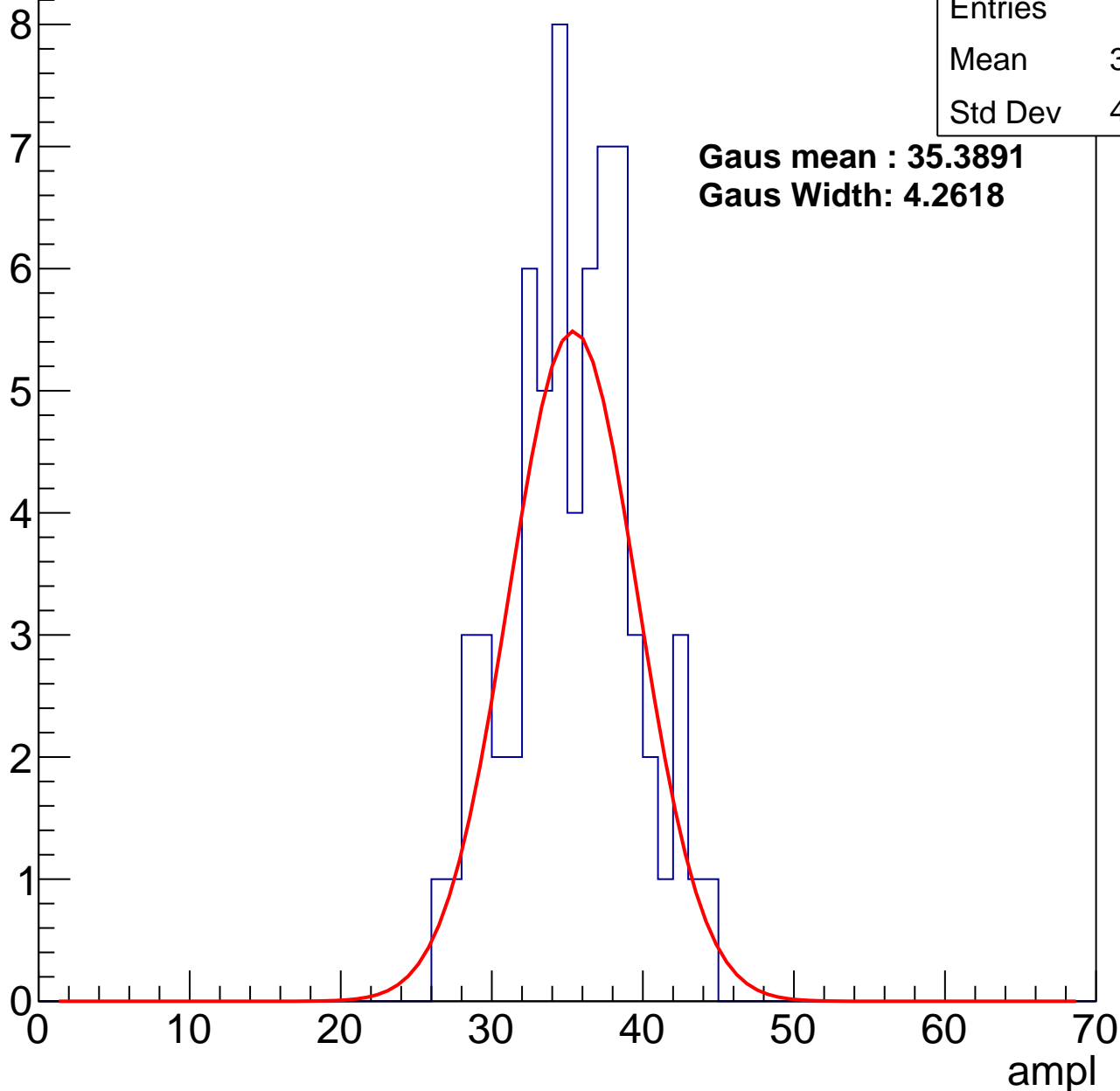
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	34.95
Std Dev	4.058

Gaus mean : 35.3891

Gaus Width: 4.2618



B1L102S, U18-ch115, adc2

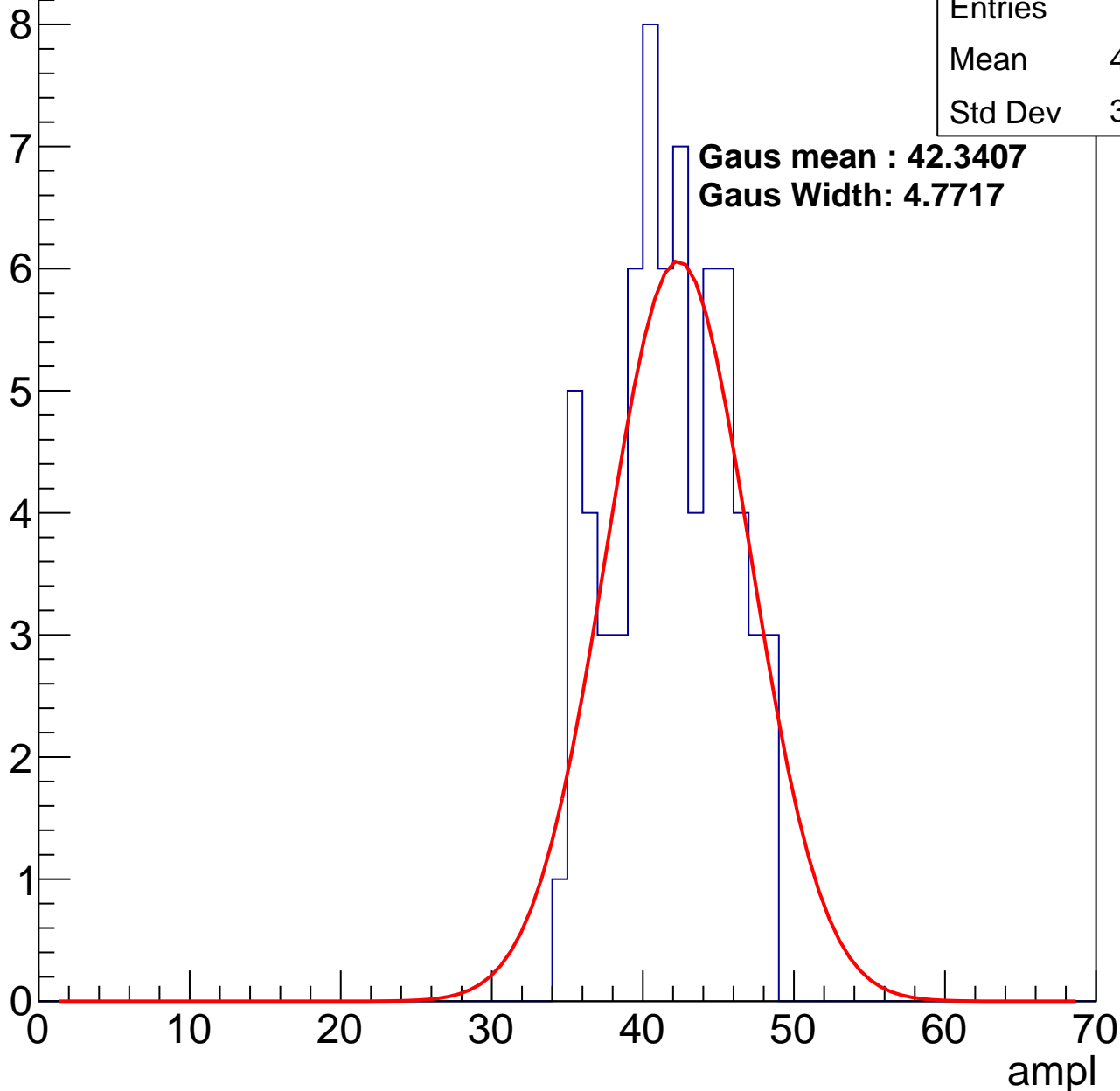
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	69
Mean	41.26
Std Dev	3.729

Gaus mean : 42.3407

Gaus Width: 4.7717

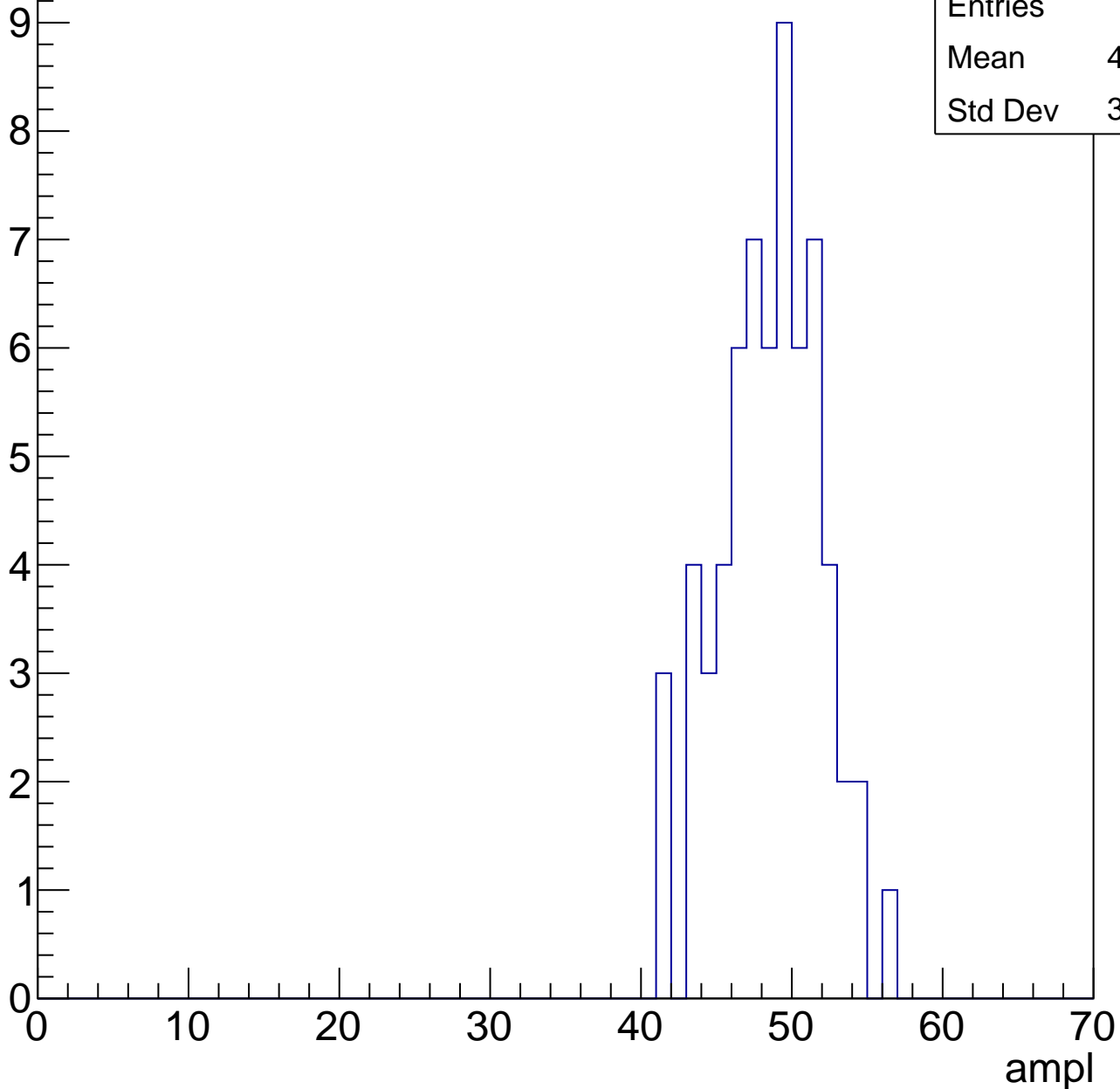


B1L102S, U18-ch115, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	48.06
Std Dev	3.325

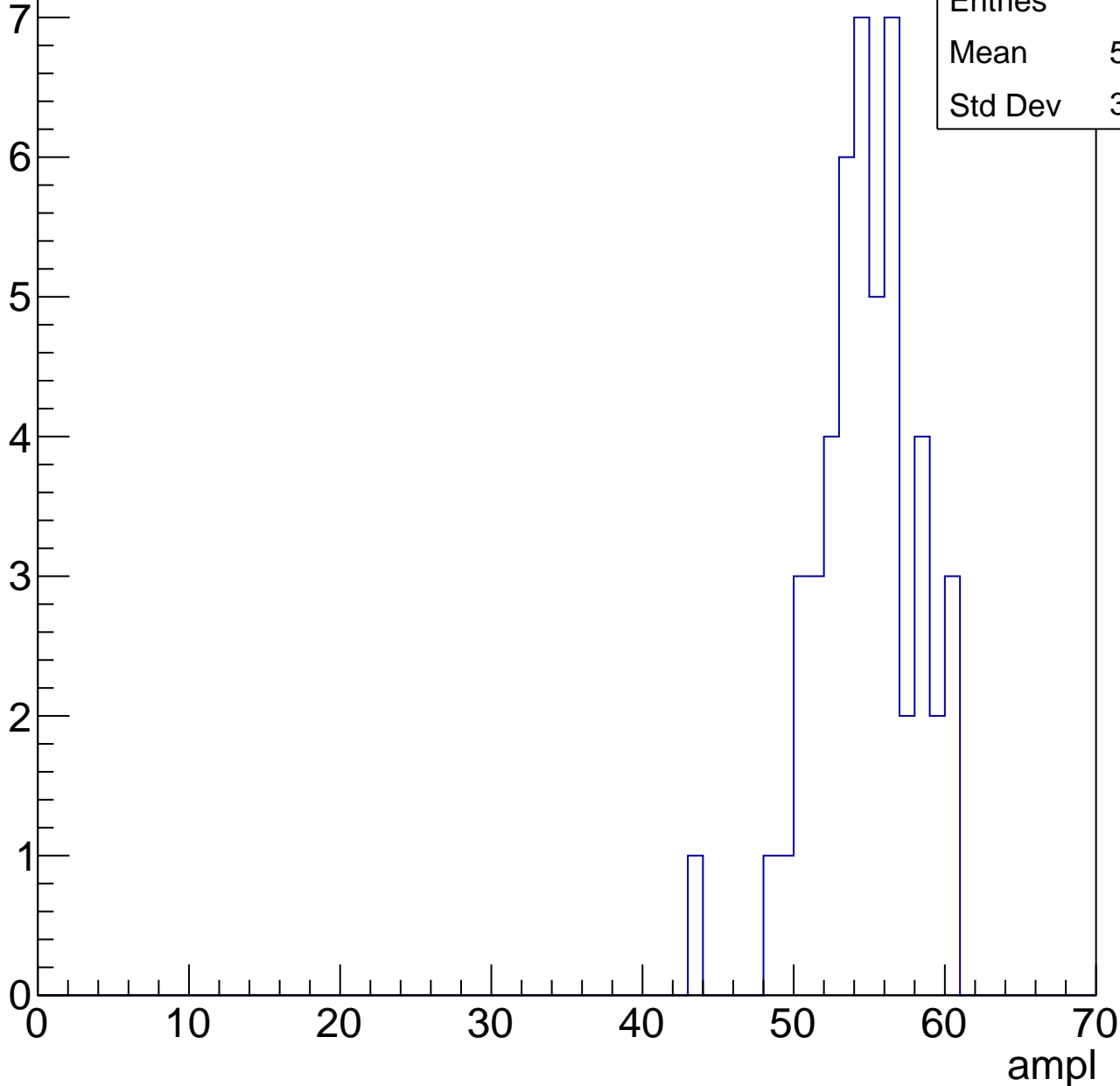


B1L102S, U18-ch115, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	54.24
Std Dev	3.347

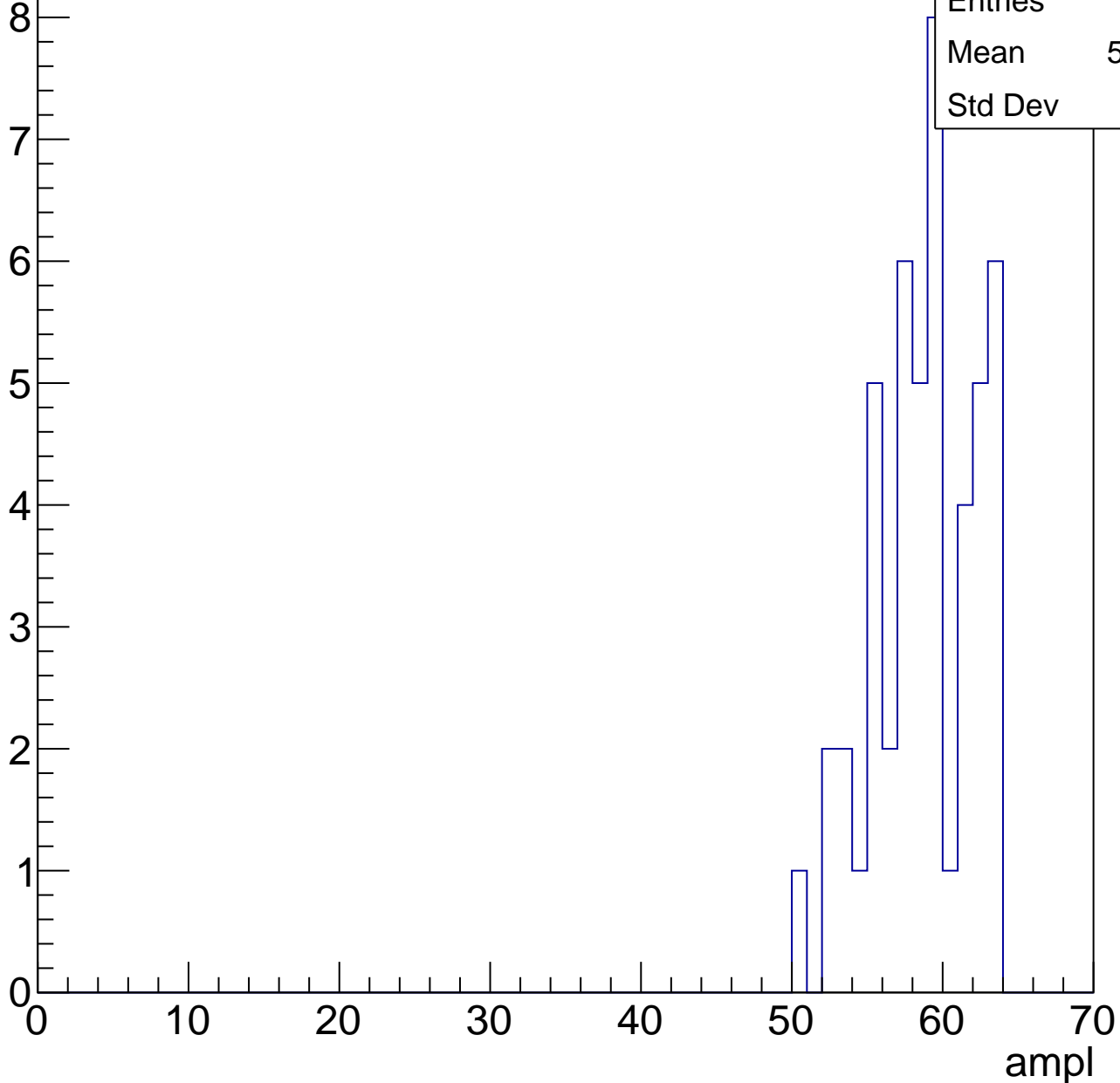


B1L102S, U18-ch115, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	58.27
Std Dev	3.34

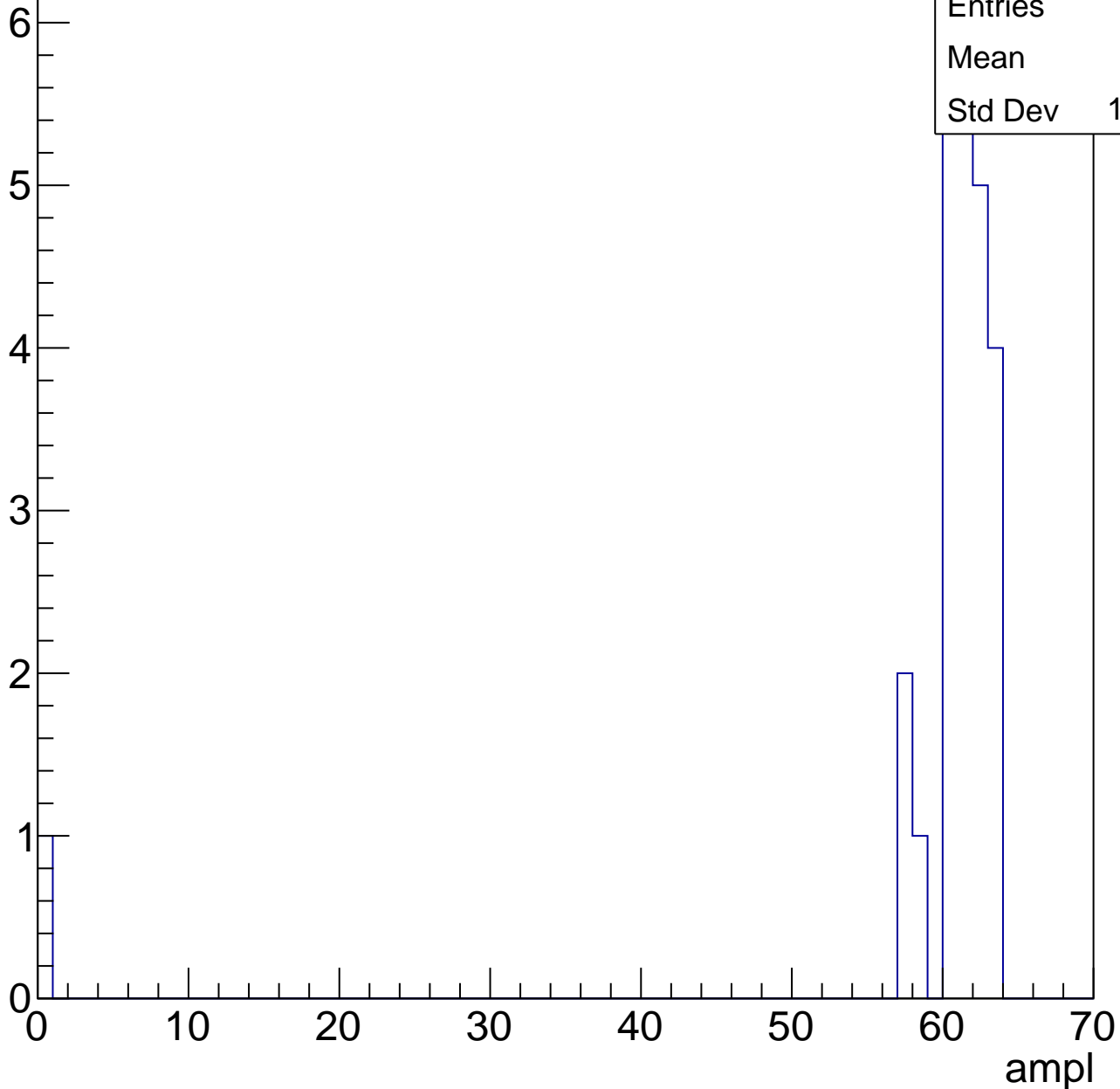


B1L102S, U18-ch115, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	25
Mean	58.4
Std Dev	12.03



B1L102S, U18-ch115, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch116, adc0

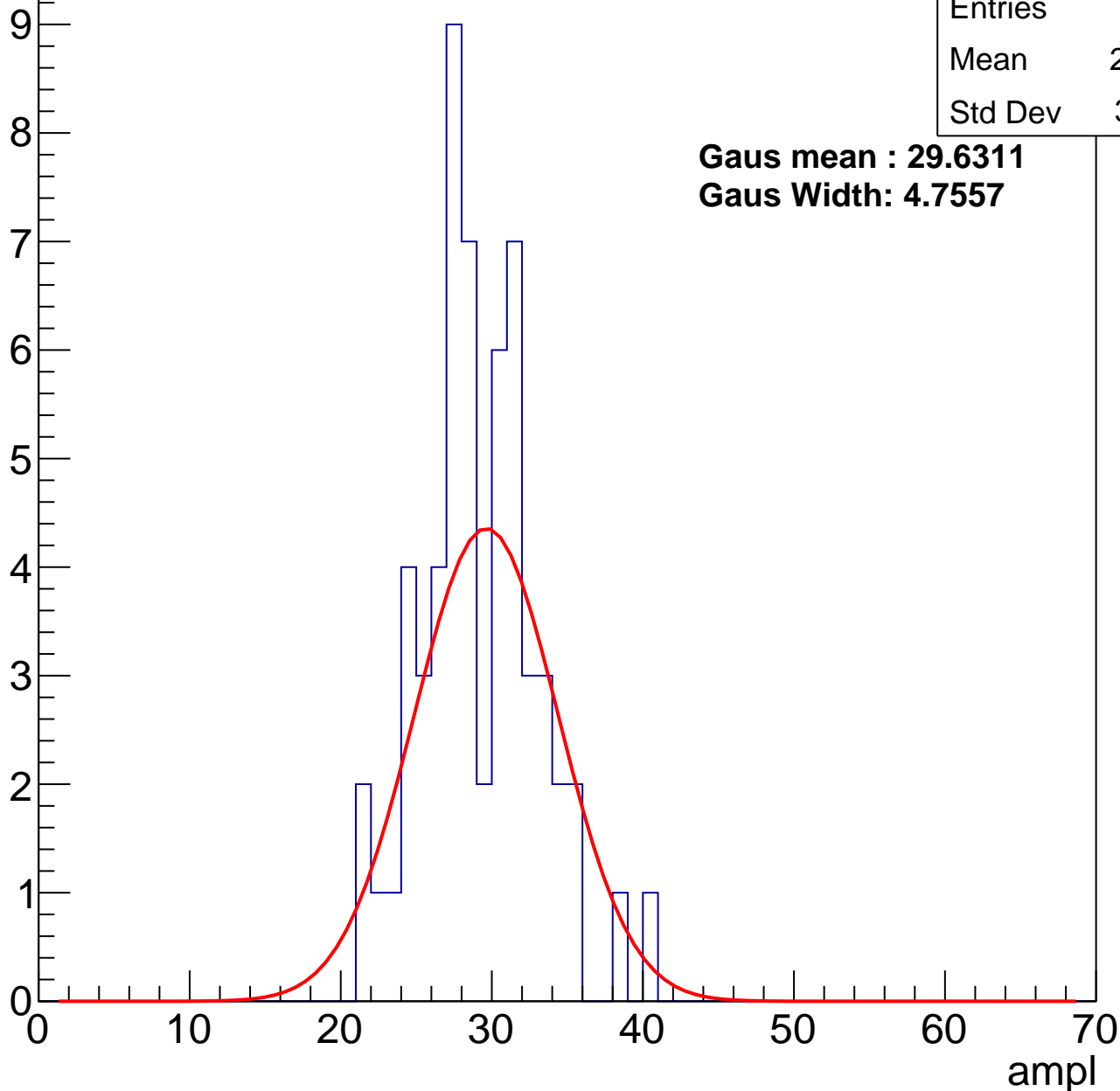
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	28.74
Std Dev	3.871

Gaus mean : 29.6311

Gaus Width: 4.7557



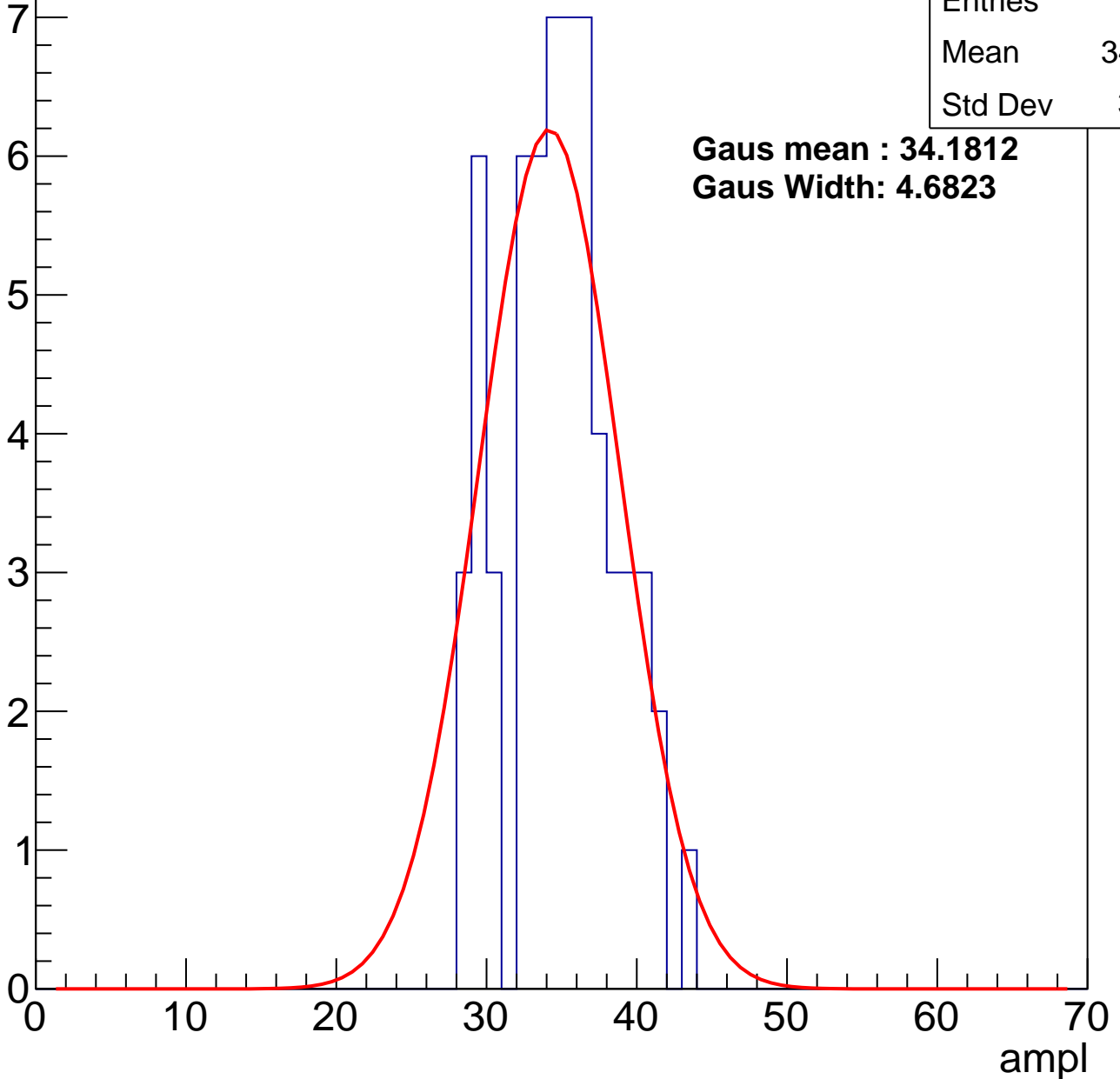
B1L102S, U18-ch116, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	61
Mean	34.38
Std Dev	3.64

Gaus mean : 34.1812
Gaus Width: 4.6823



B1L102S, U18-ch116, adc2

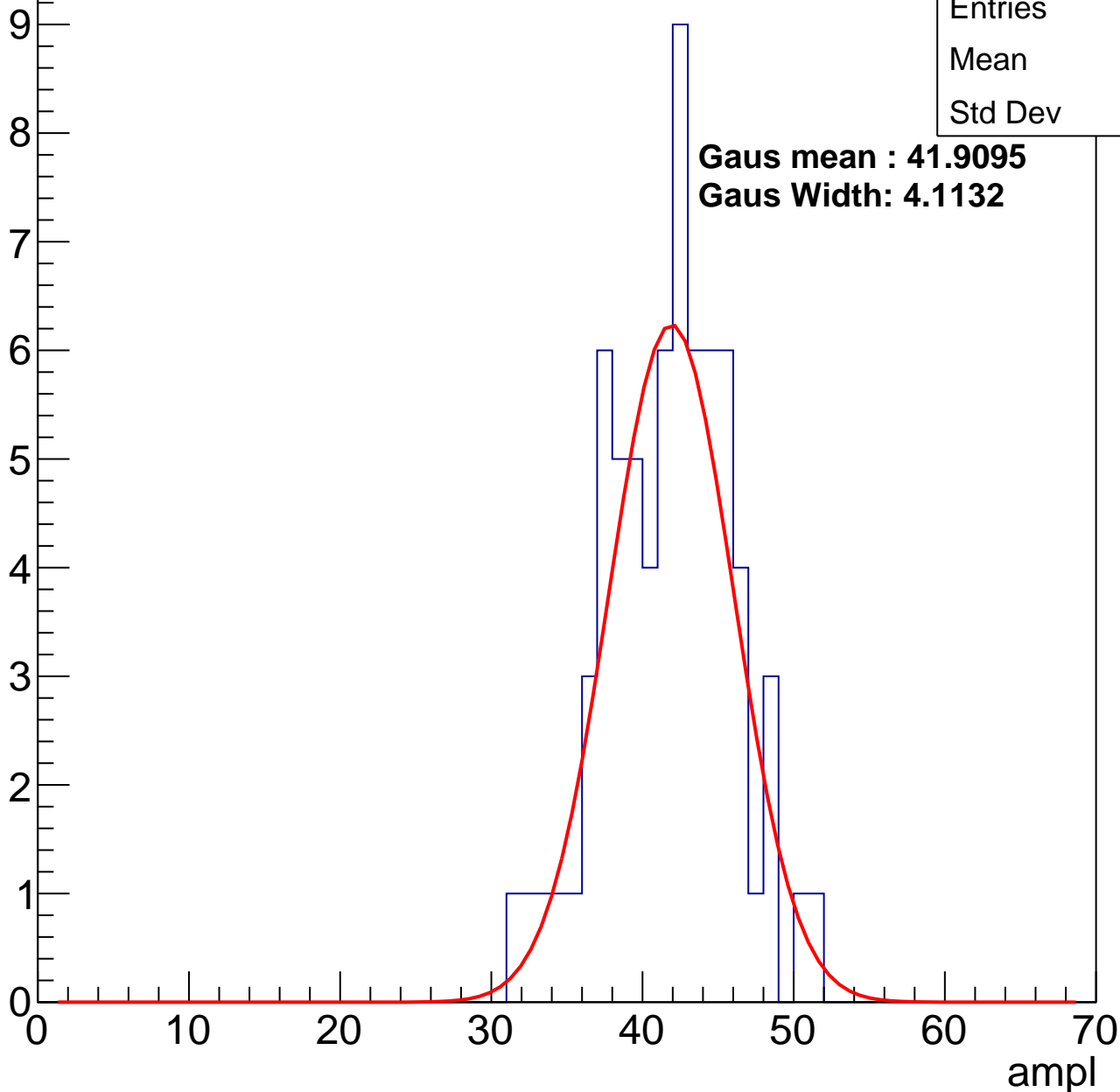
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	71
Mean	41.3
Std Dev	4.13

Gaus mean : 41.9095

Gaus Width: 4.1132

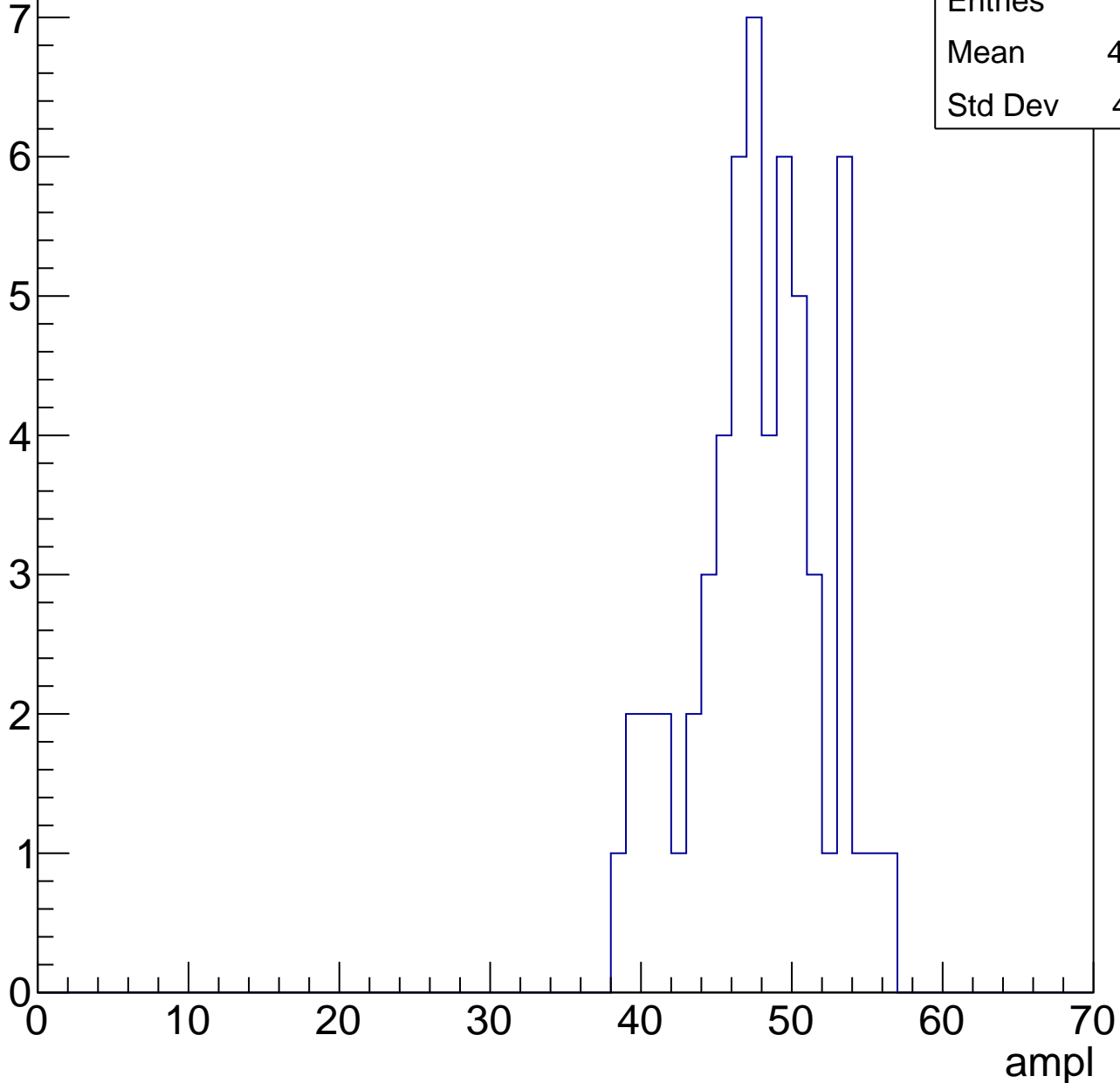


B1L102S, U18-ch116, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	47.36
Std Dev	4.221

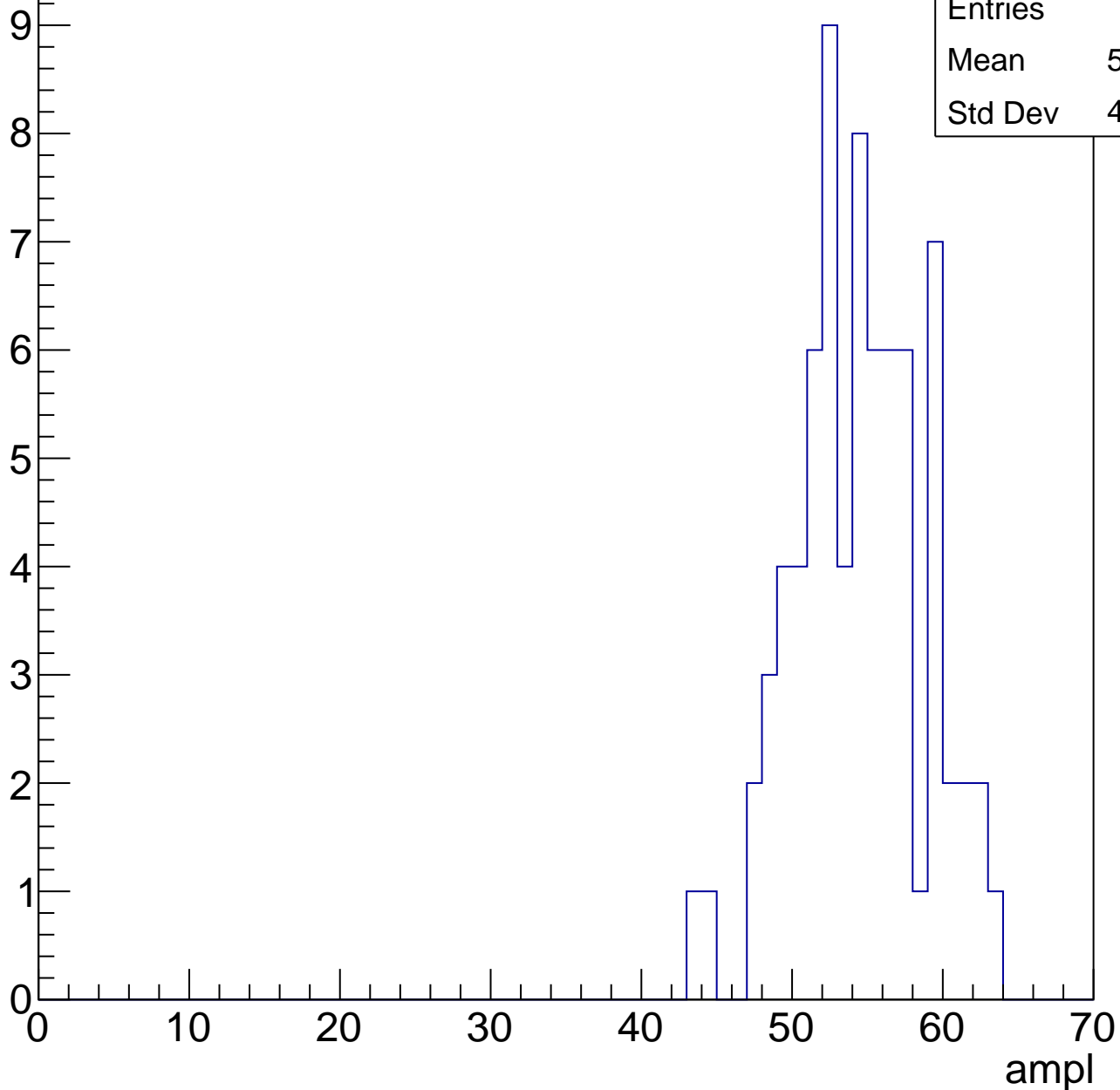


B1L102S, U18-ch116, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	53.96
Std Dev	4.225

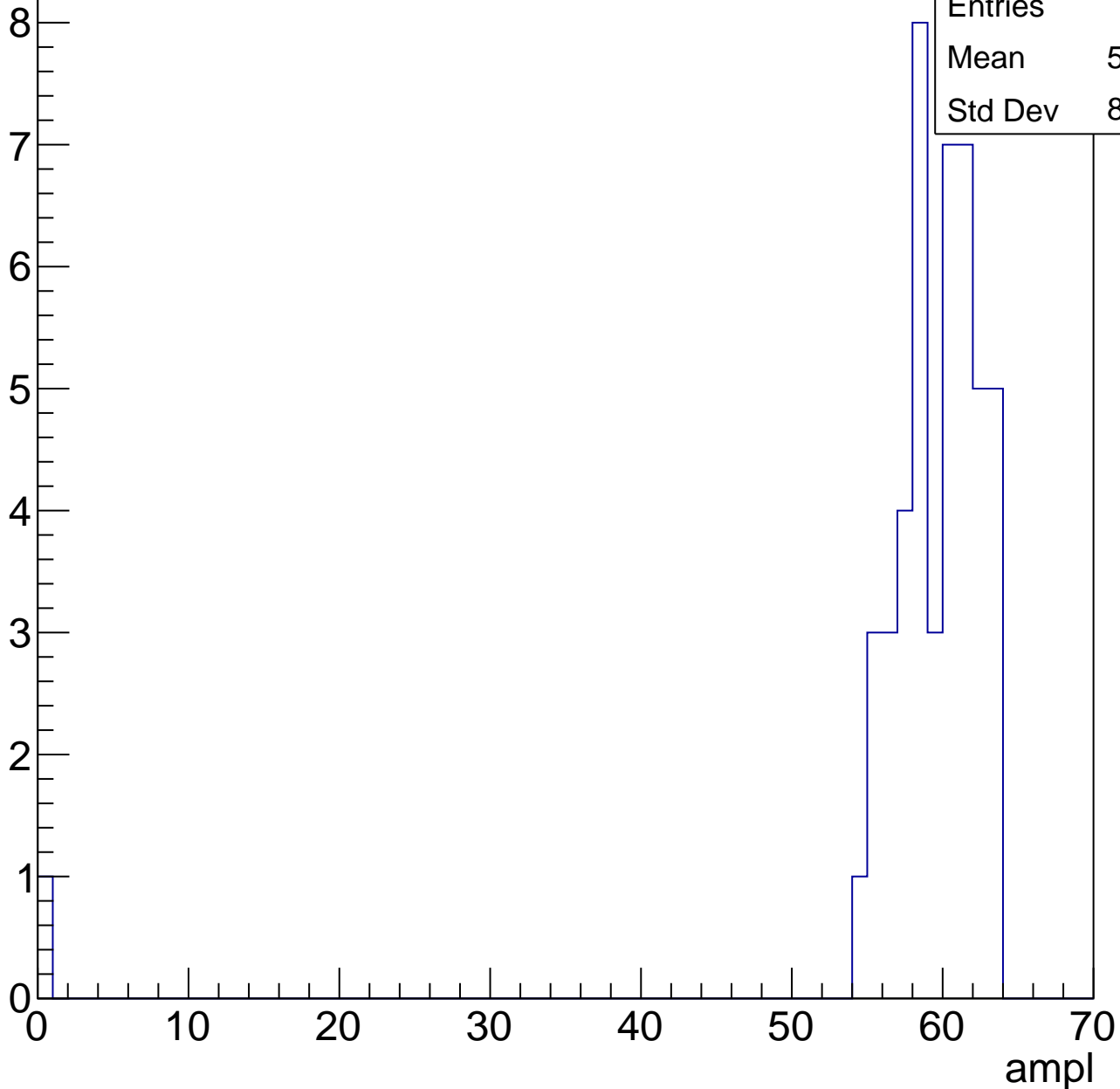


B1L102S, U18-ch116, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	58.04
Std Dev	8.899



B1L102S, U18-ch116, adc6

calib_packv5_042523_0143.root, FC#11, port A2

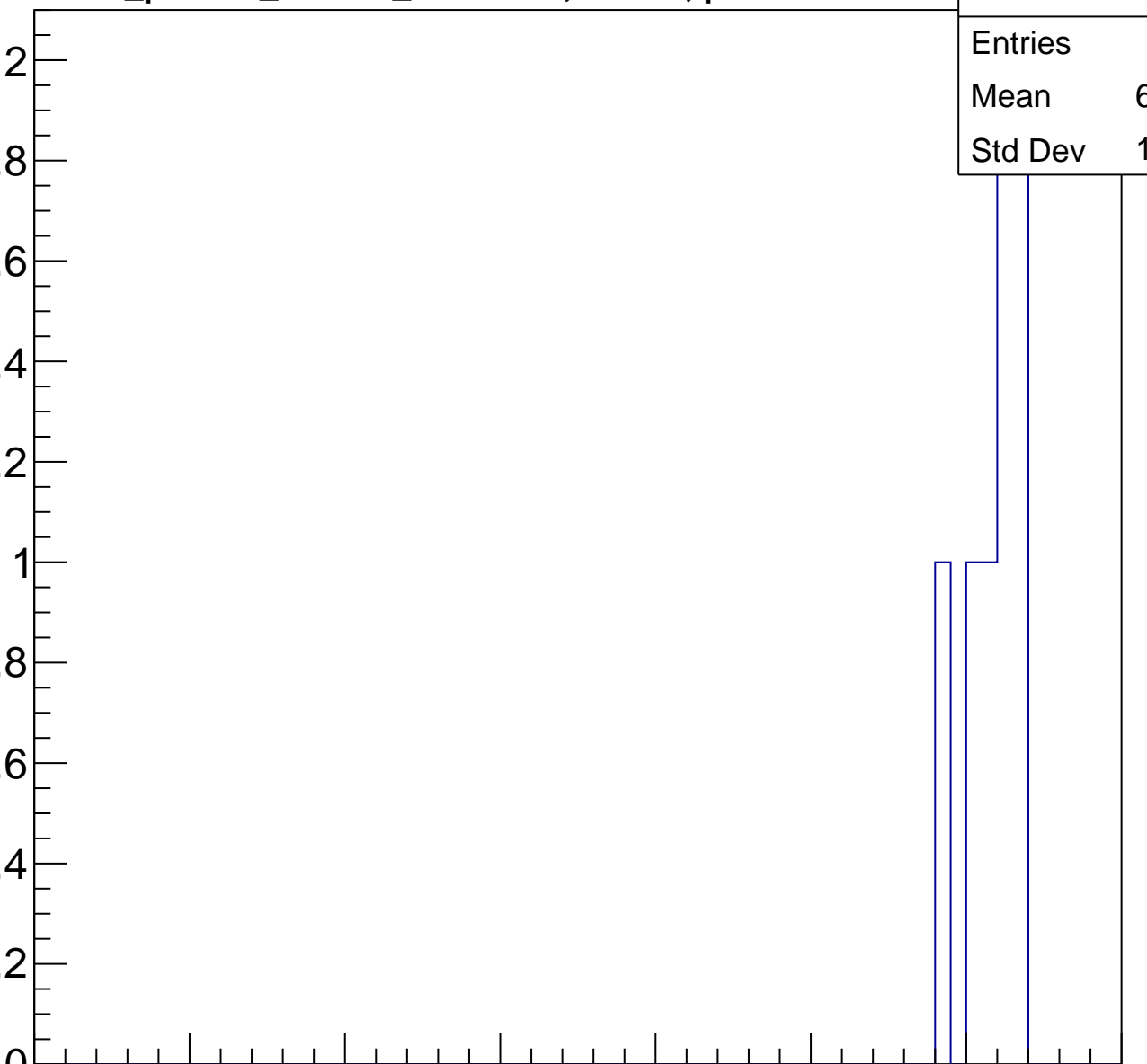
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.666

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch116, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch117, adc0

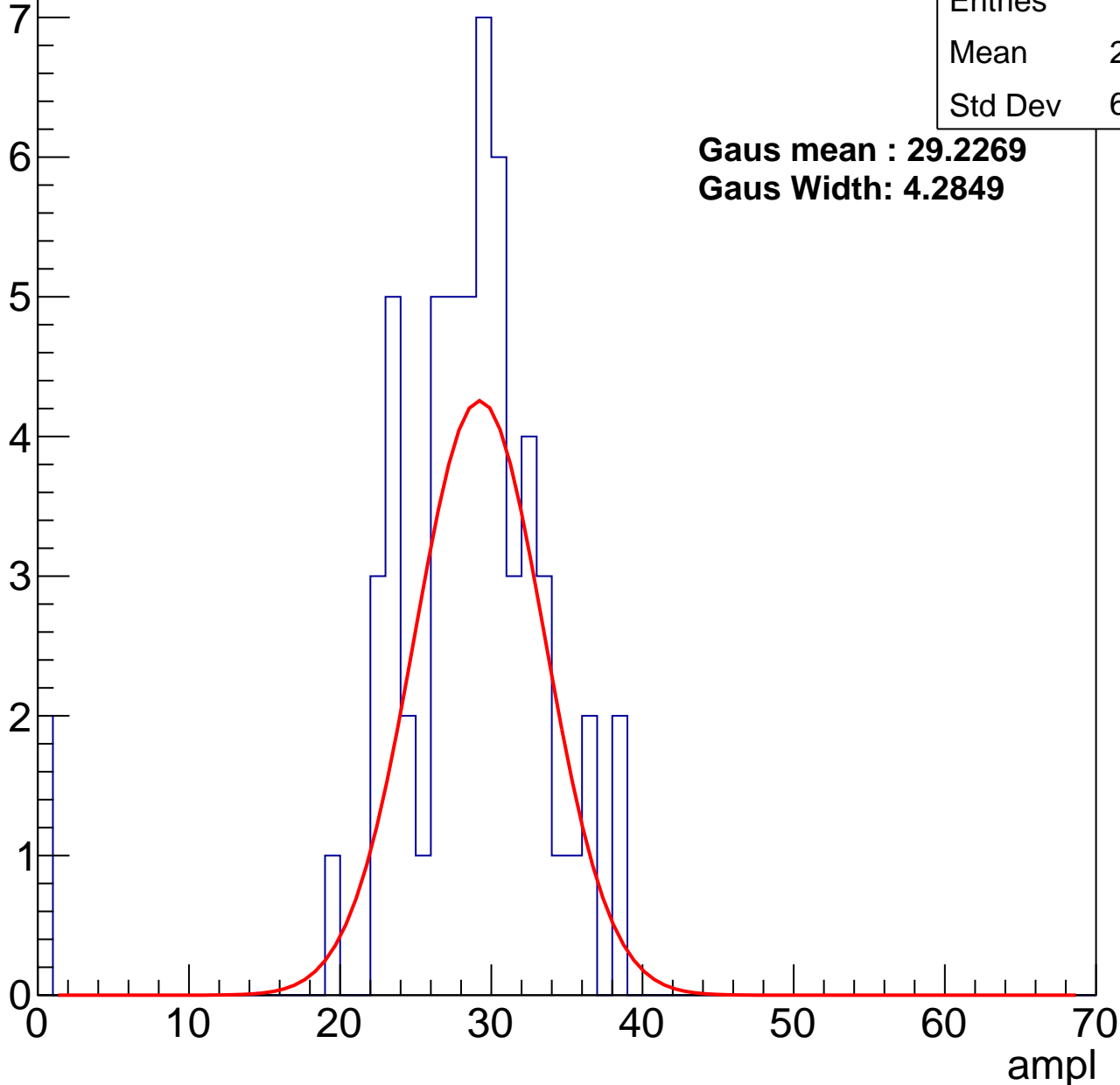
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	58
Mean	27.55
Std Dev	6.626

Gaus mean : 29.2269

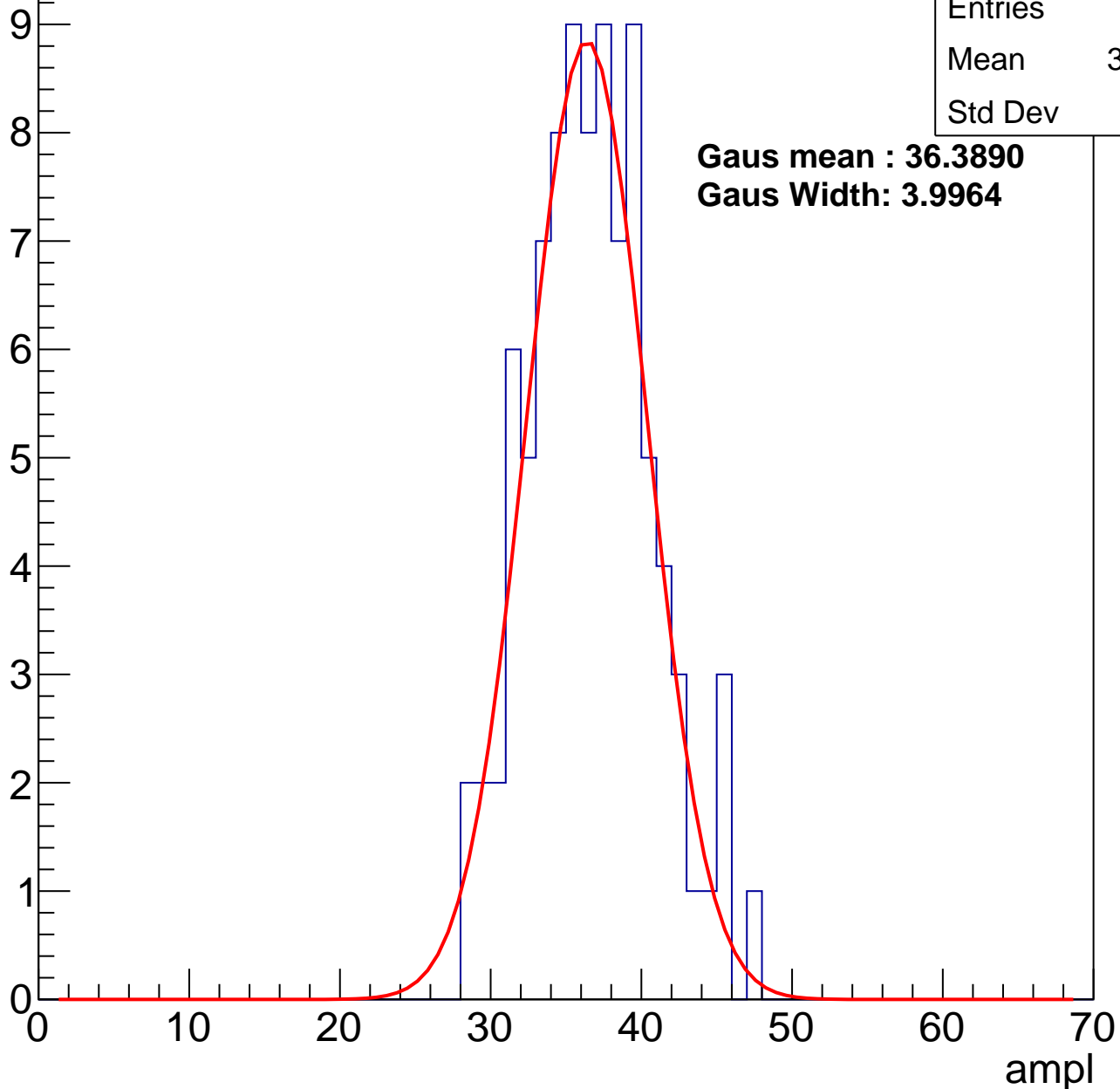
Gaus Width: 4.2849



B1L102S, U18-ch117, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch117, adc2

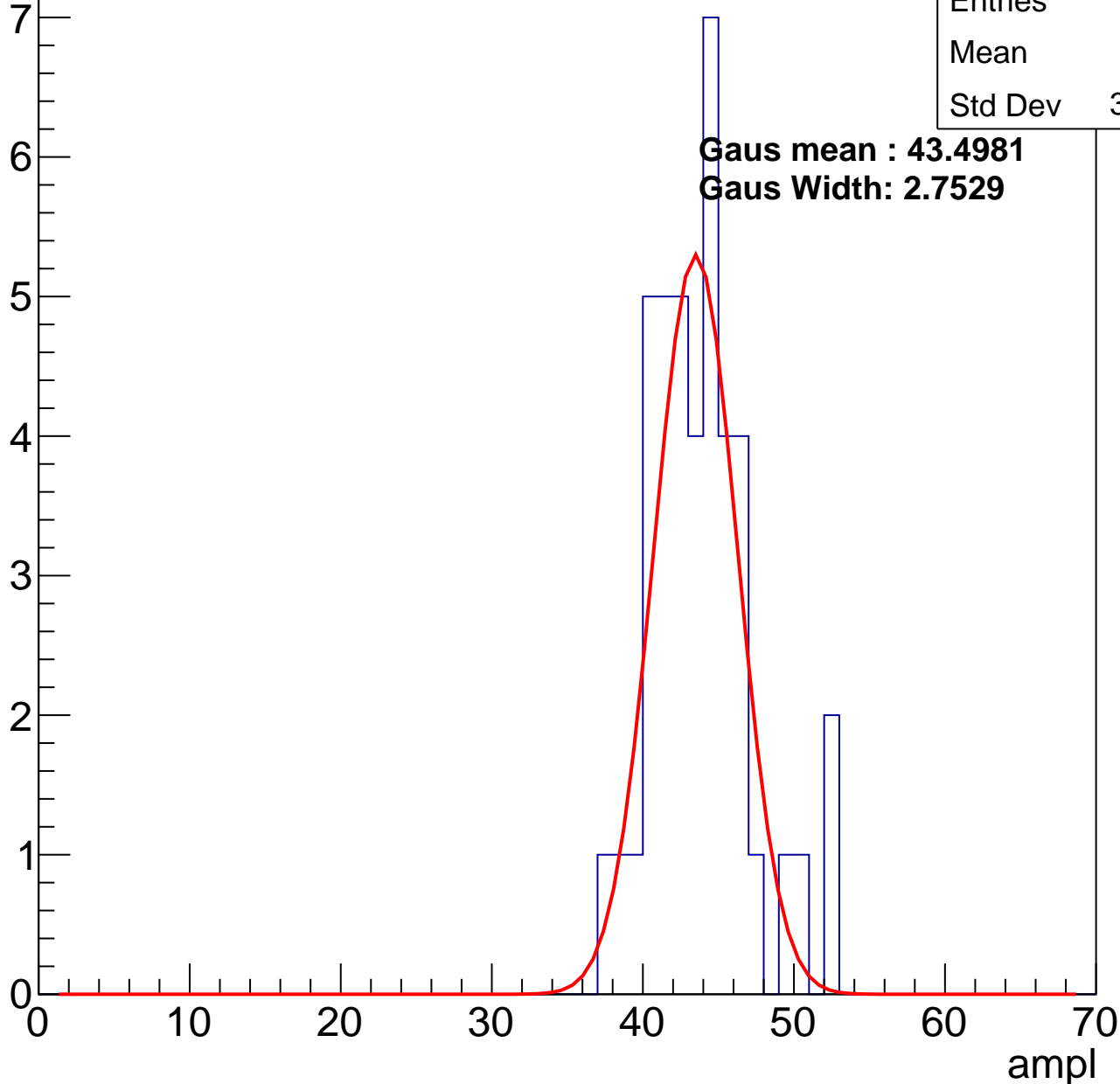
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	42
Mean	43.4
Std Dev	3.317

Gaus mean : 43.4981

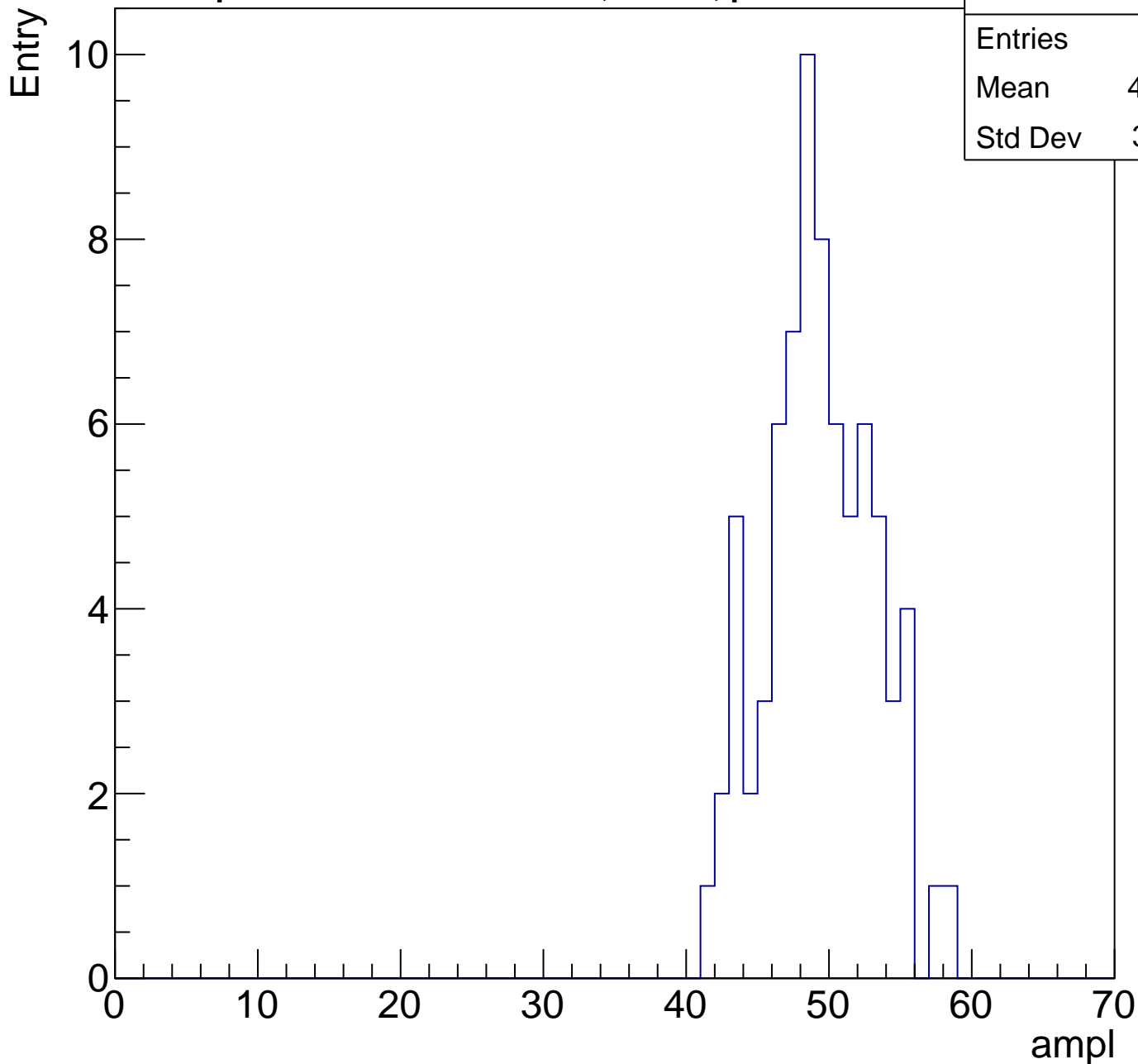
Gaus Width: 2.7529



B1L102S, U18-ch117, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entries	75
Mean	48.92
Std Dev	3.751

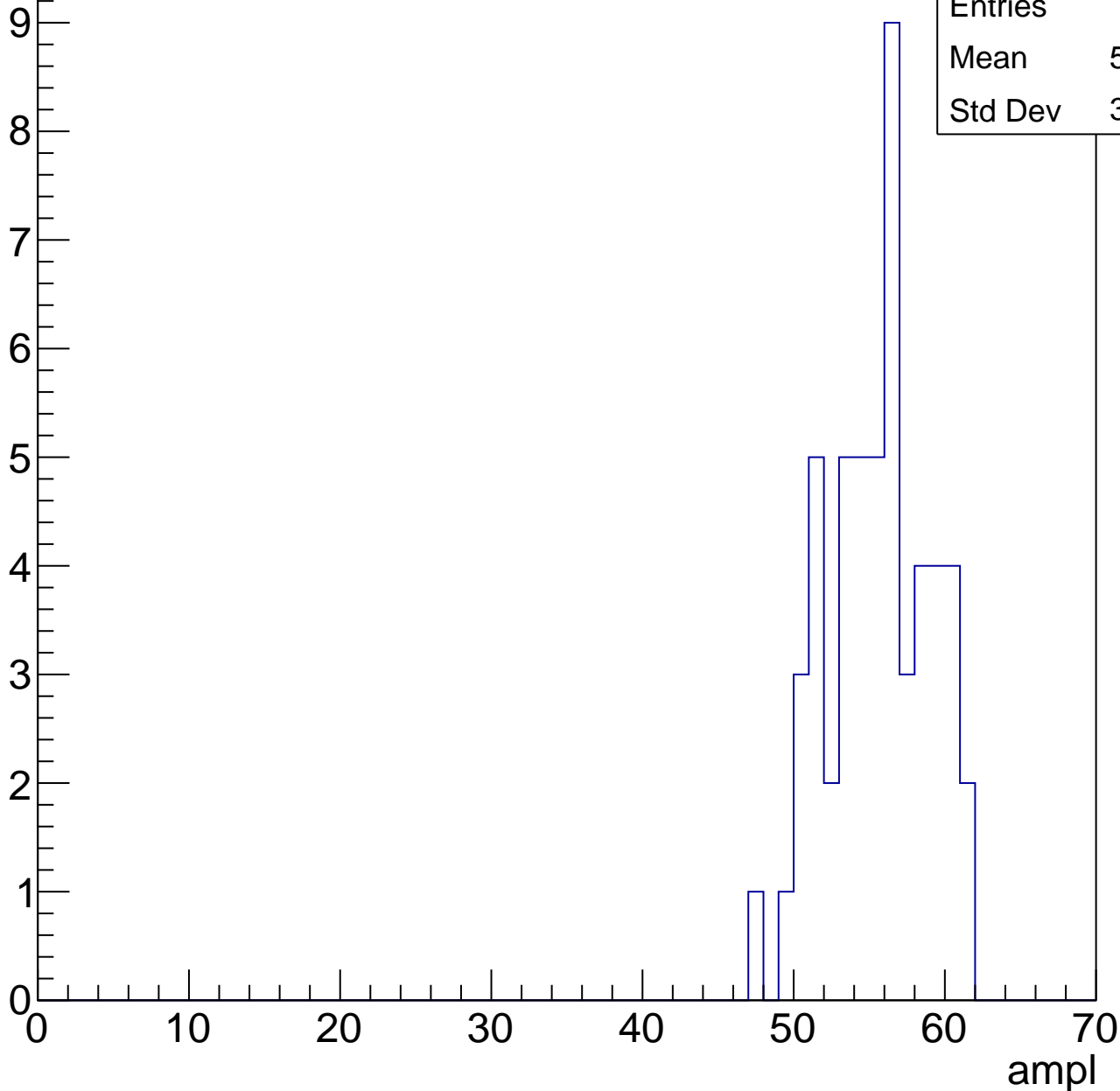


B1L102S, U18-ch117, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	53
Mean	55.09
Std Dev	3.338

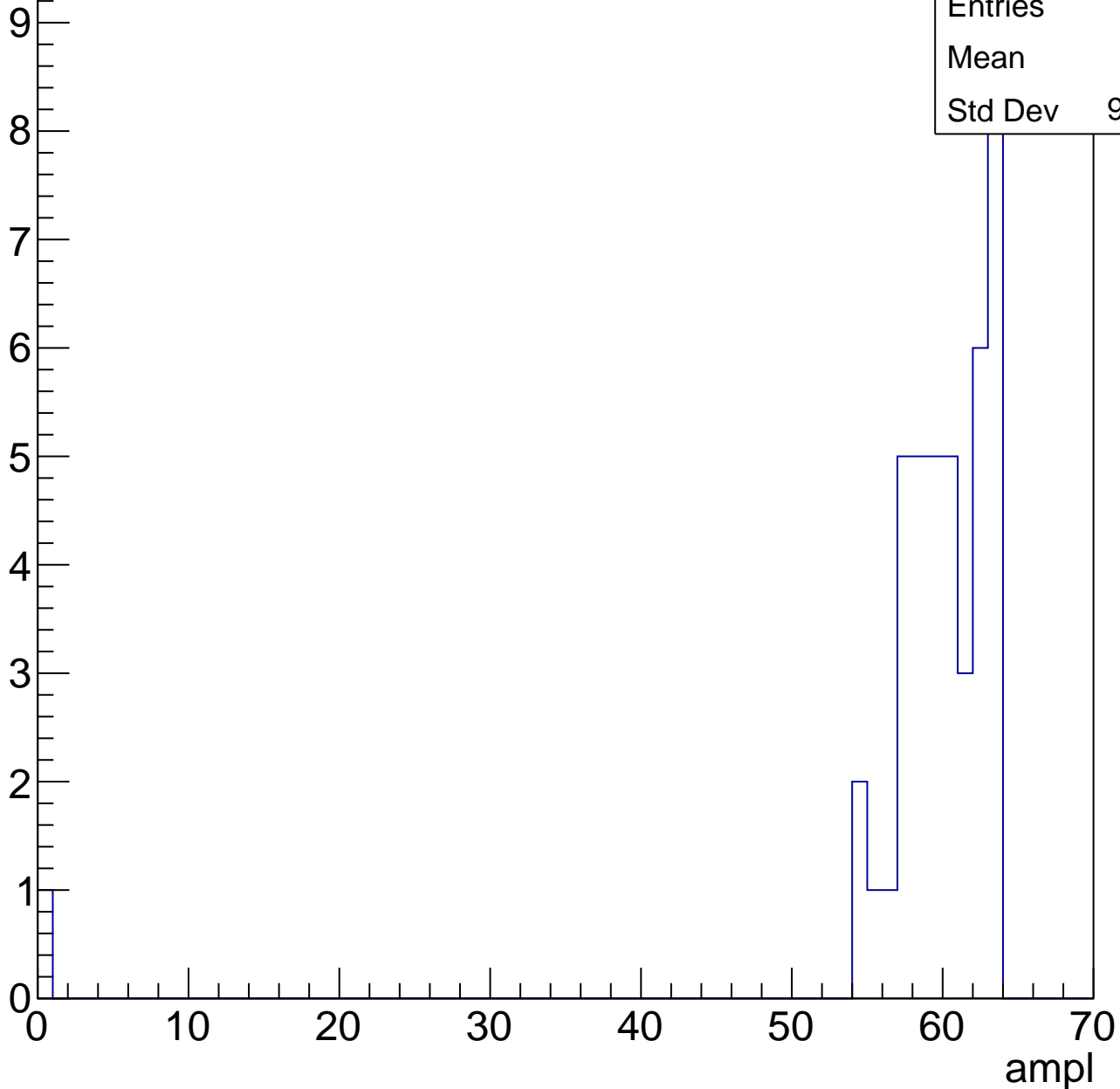


B1L102S, U18-ch117, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	43
Mean	58.4
Std Dev	9.376

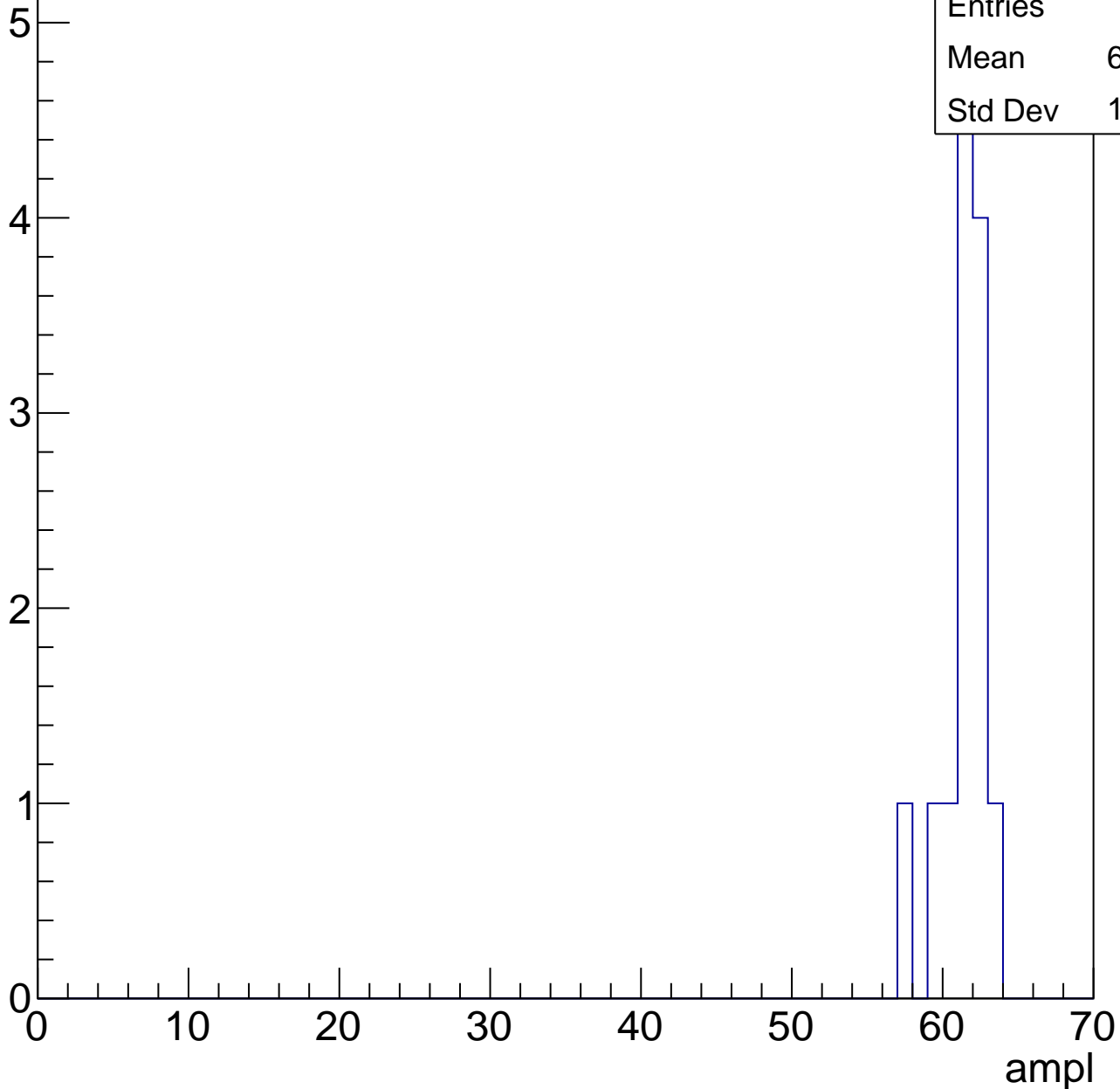


B1L102S, U18-ch117, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	13
Mean	60.92
Std Dev	1.492



B1L102S, U18-ch117, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch118, adc0

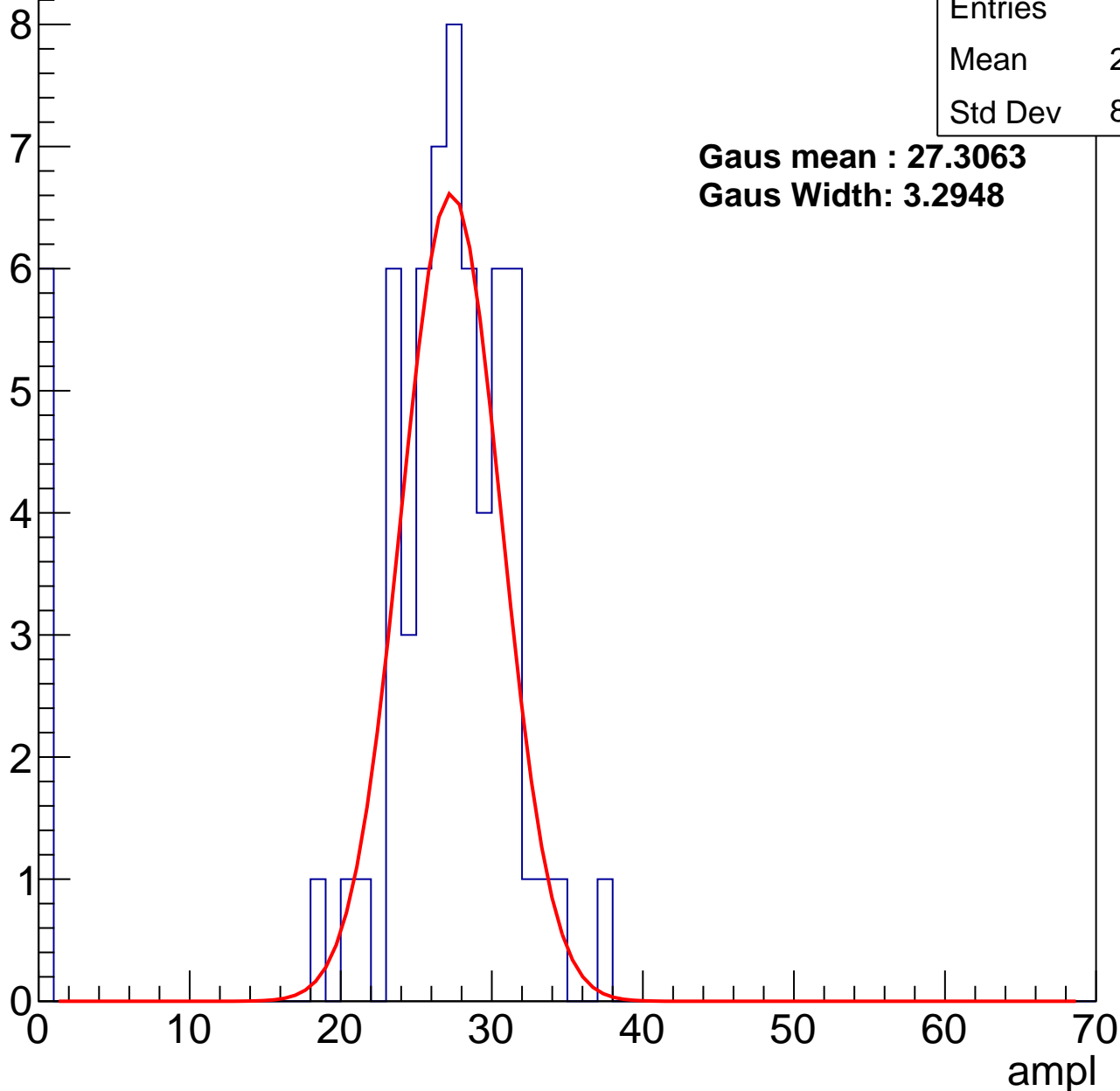
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	65
Mean	24.66
Std Dev	8.522

Gaus mean : 27.3063

Gaus Width: 3.2948



B1L102S, U18-ch118, adc1

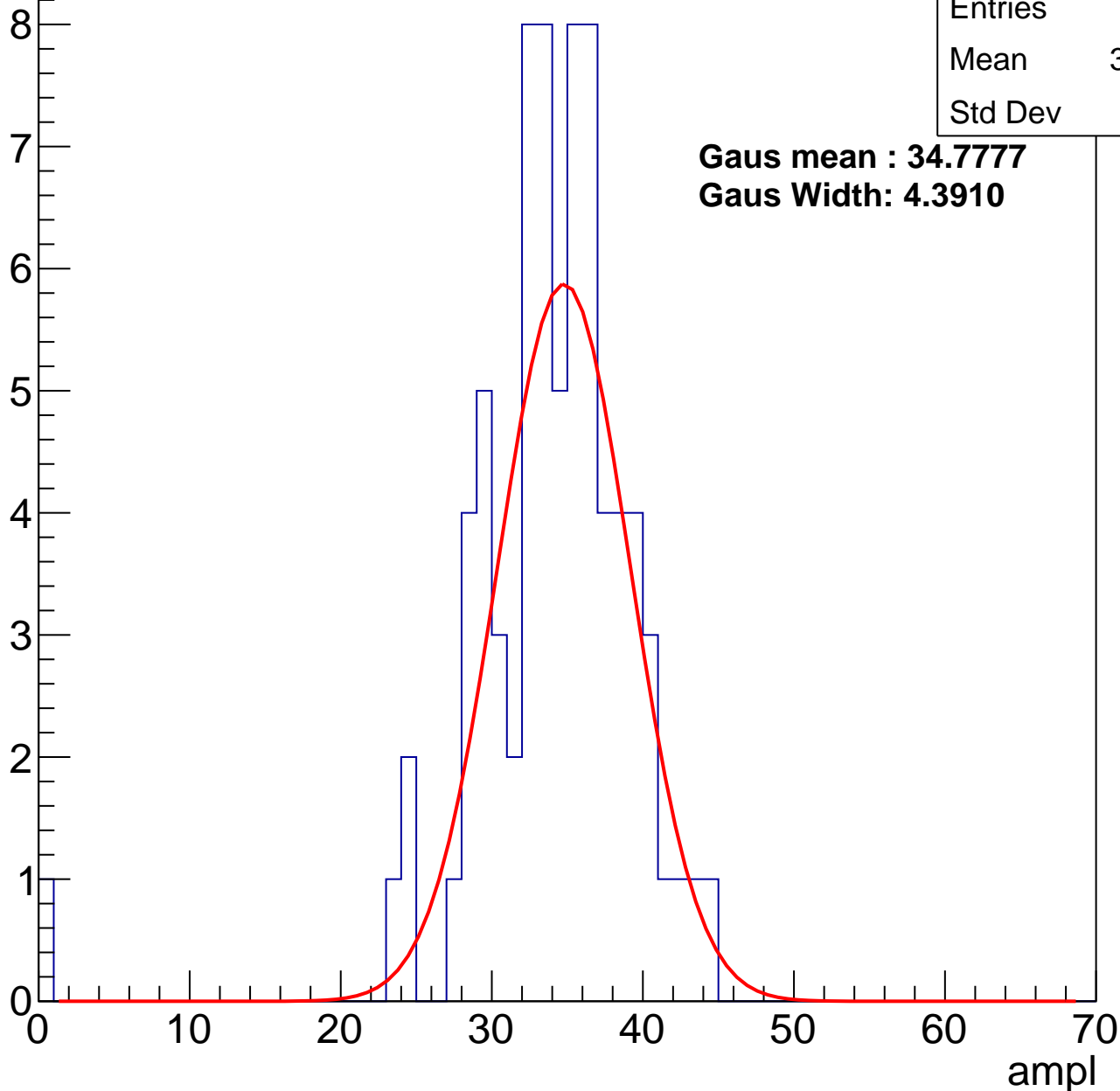
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	33.48
Std Dev	5.8

Gaus mean : 34.7777

Gaus Width: 4.3910



B1L102S, U18-ch118, adc2

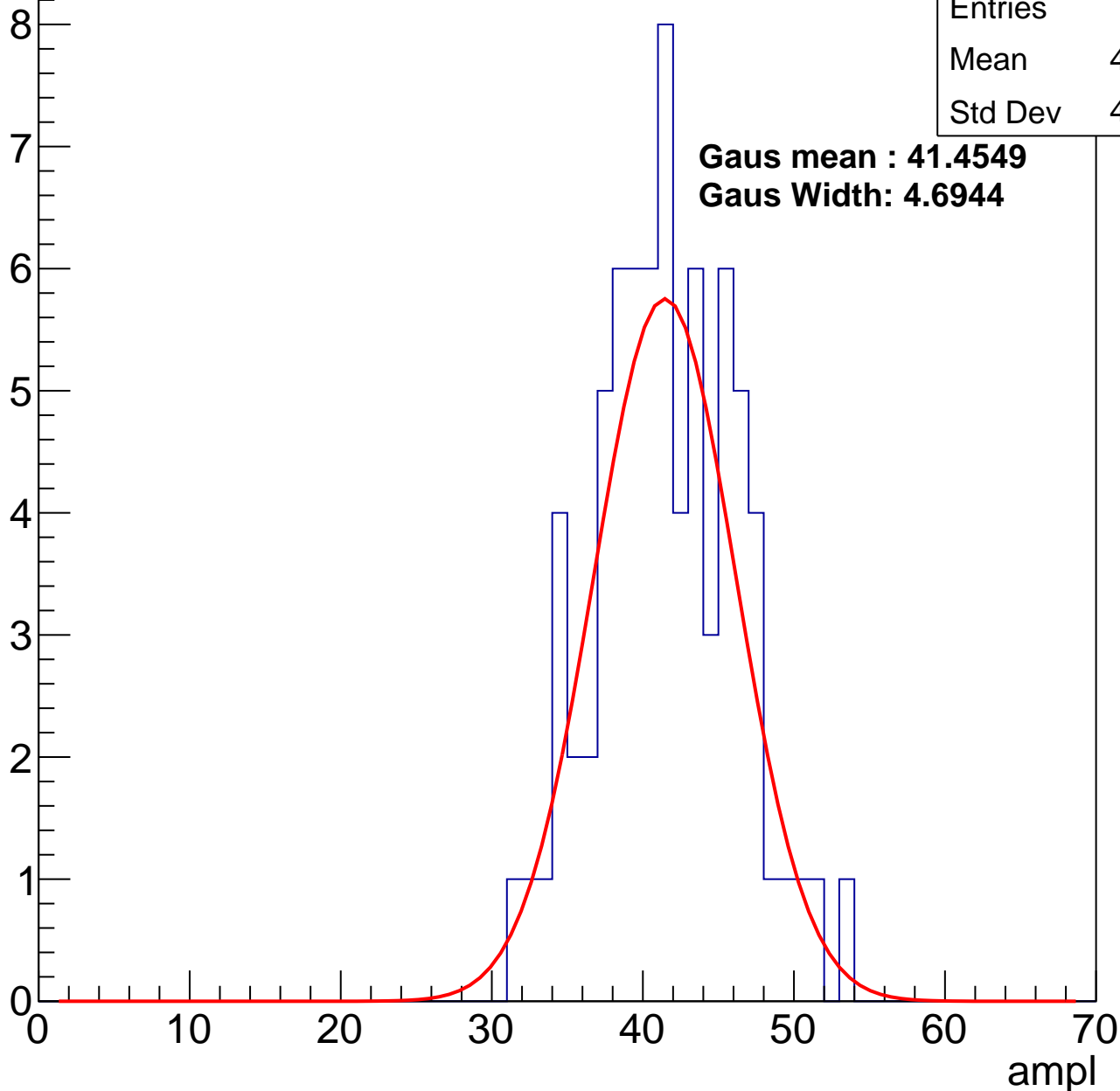
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	41.15
Std Dev	4.595

Gaus mean : 41.4549

Gaus Width: 4.6944

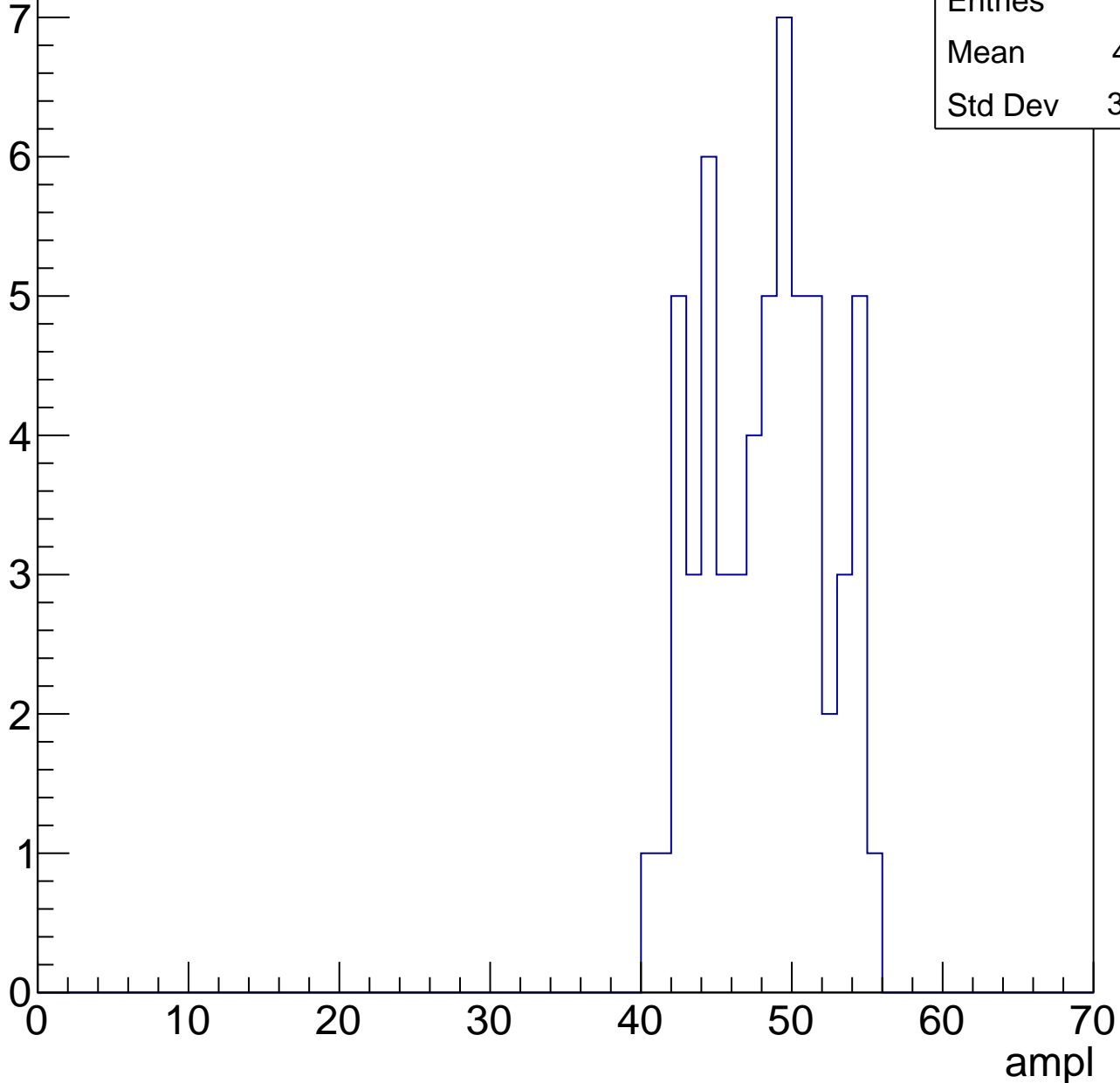


B1L102S, U18-ch118, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	47.81
Std Dev	3.934

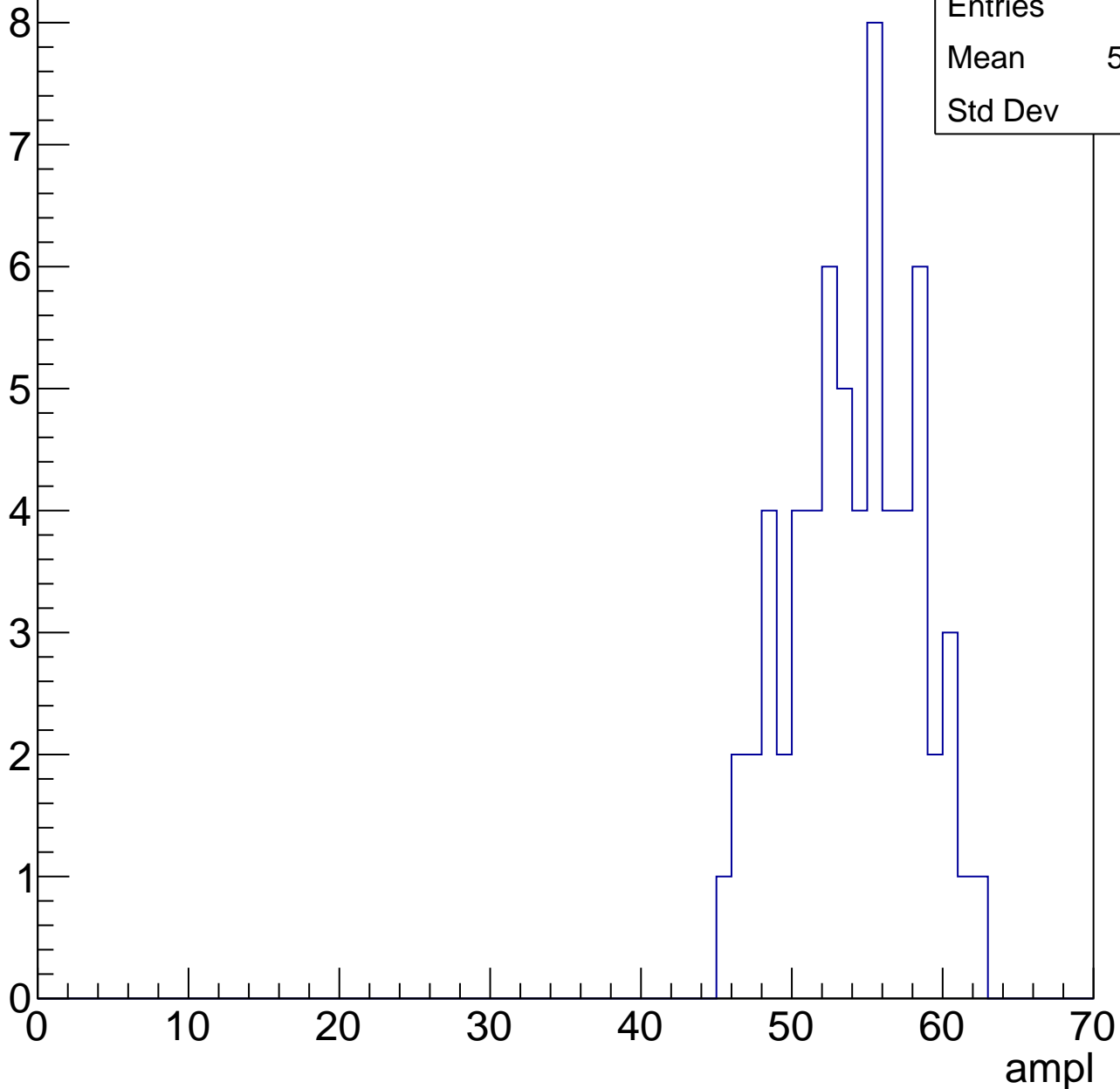


B1L102S, U18-ch118, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	53.63
Std Dev	4.08

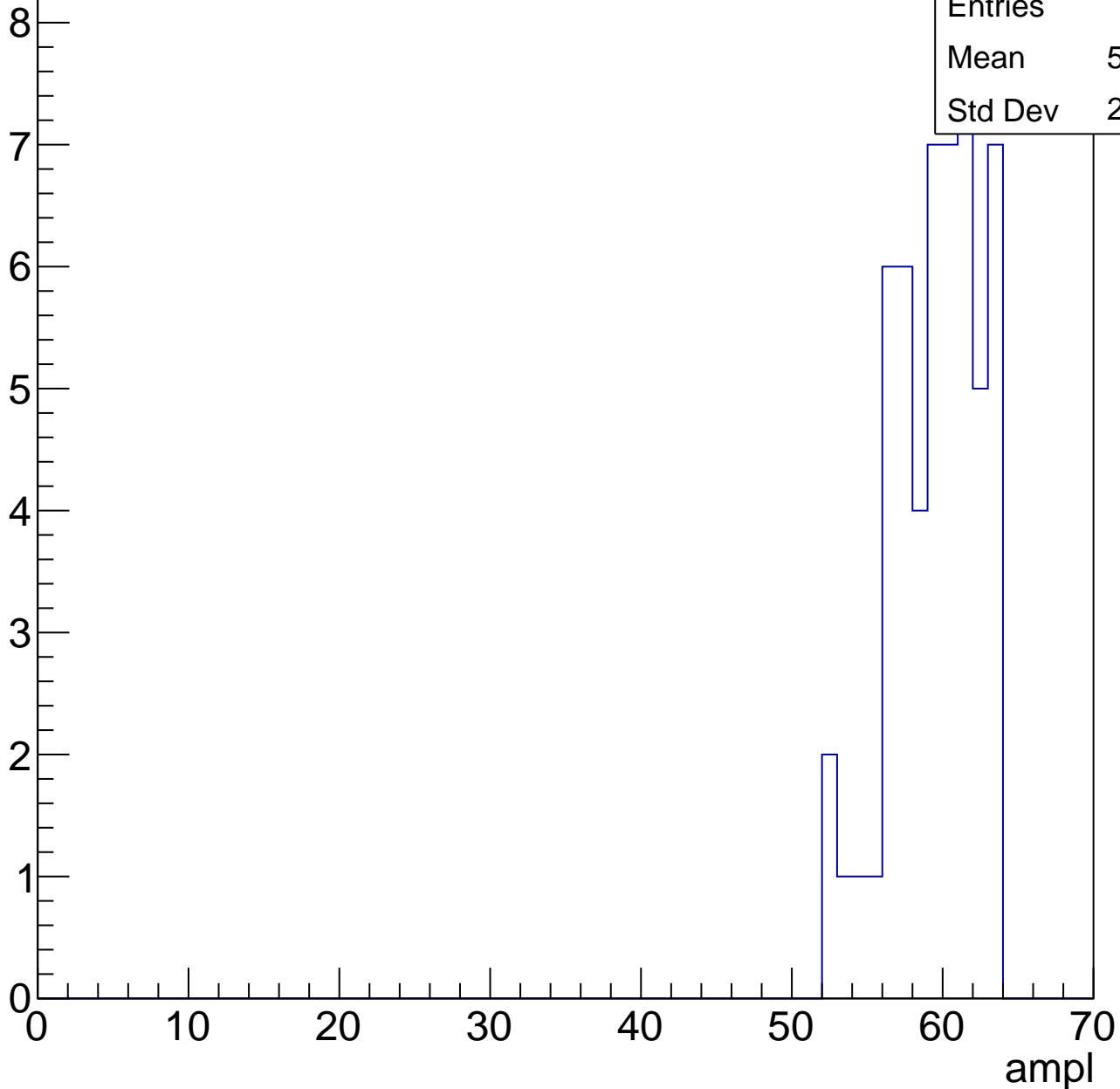


B1L102S, U18-ch118, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

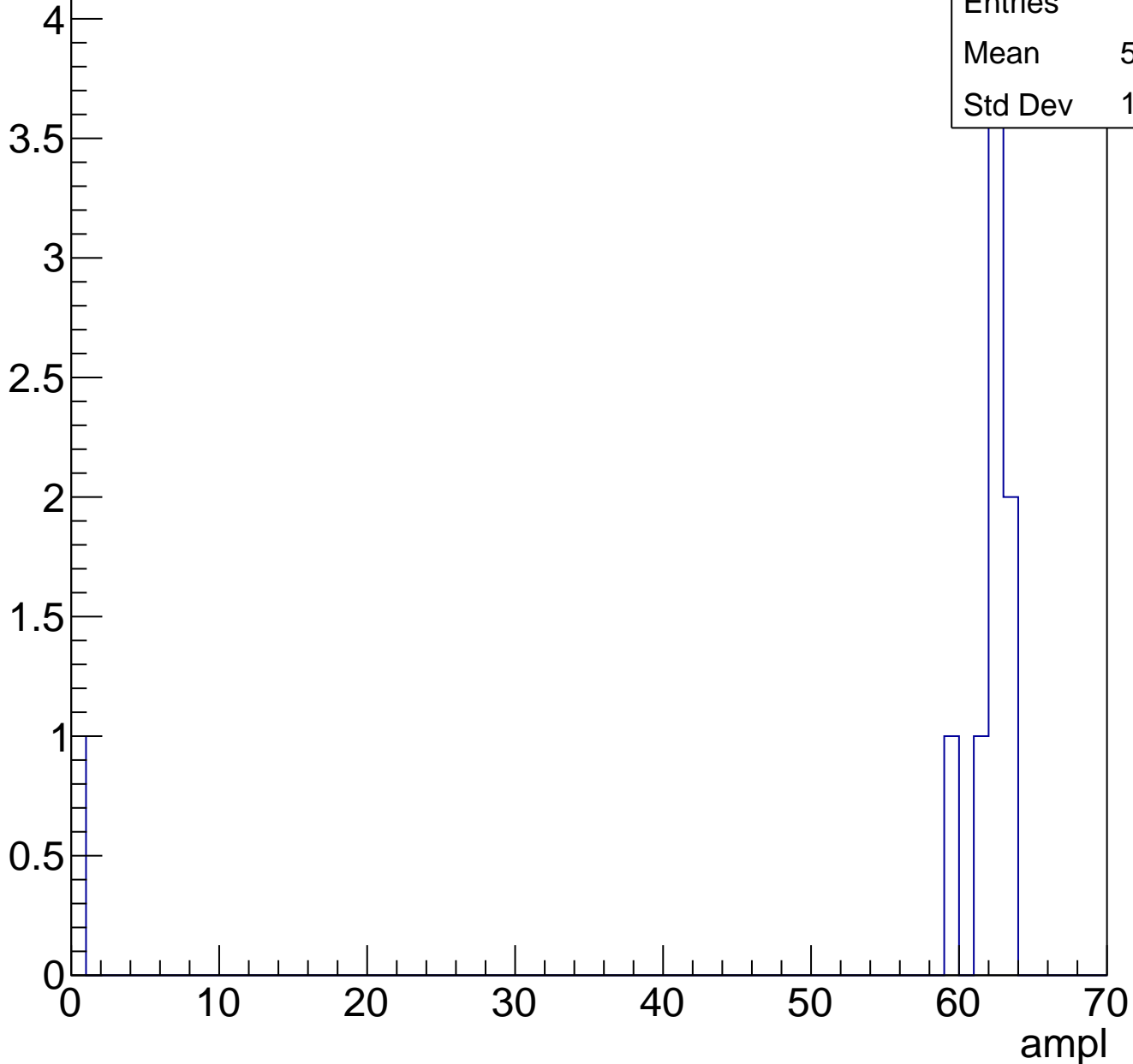
Entries	55
Mean	59.05
Std Dev	2.869



B1L102S, U18-ch118, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch118, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch119, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	27.95
Std Dev	4.846

Gaus mean : 28.7774

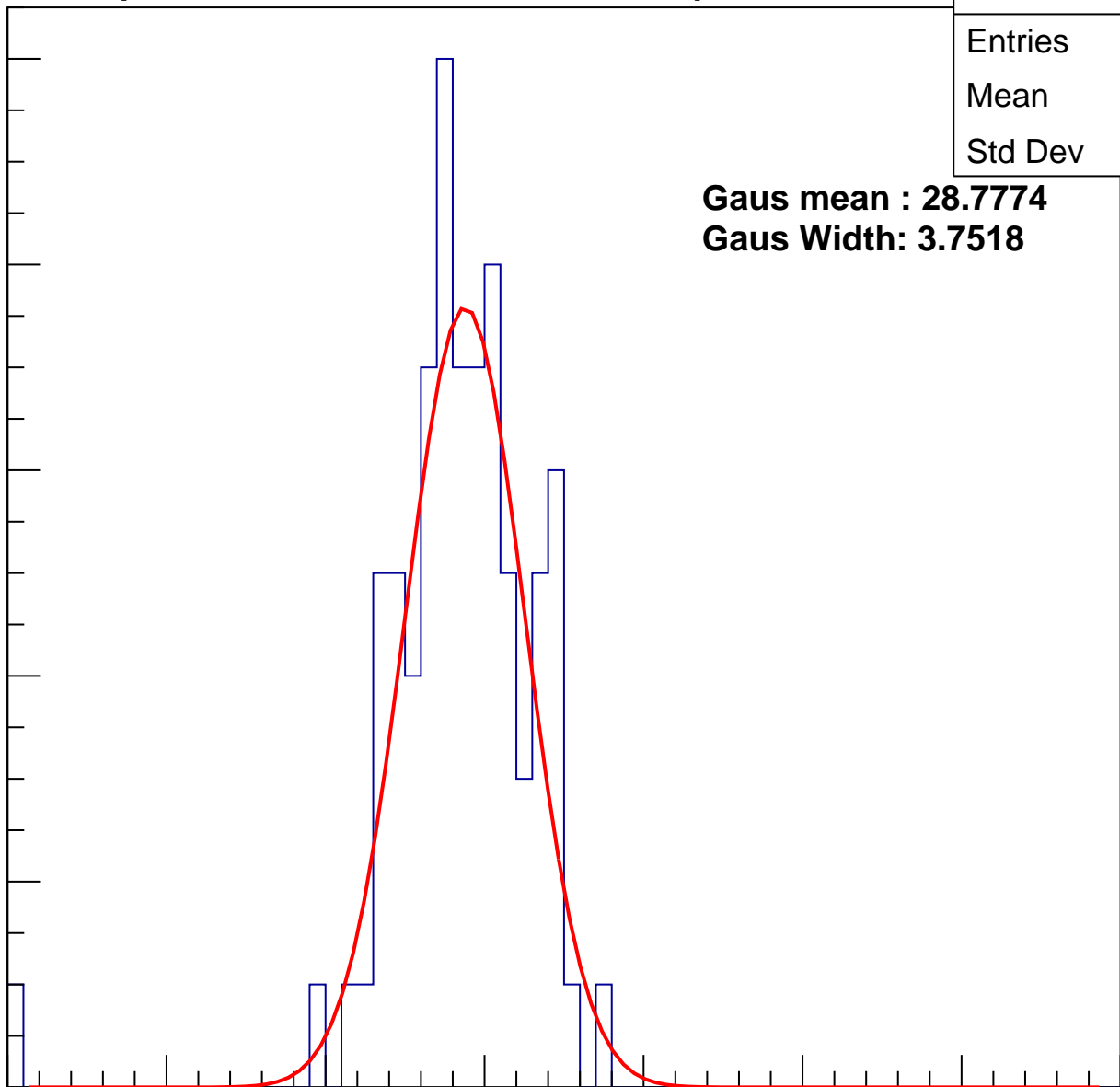
Gaus Width: 3.7518

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch119, adc1

calib_packv5_042523_0143.root, FC#11, port A2

Entries	76
Mean	35.58
Std Dev	3.625

Gaus mean : 36.1629

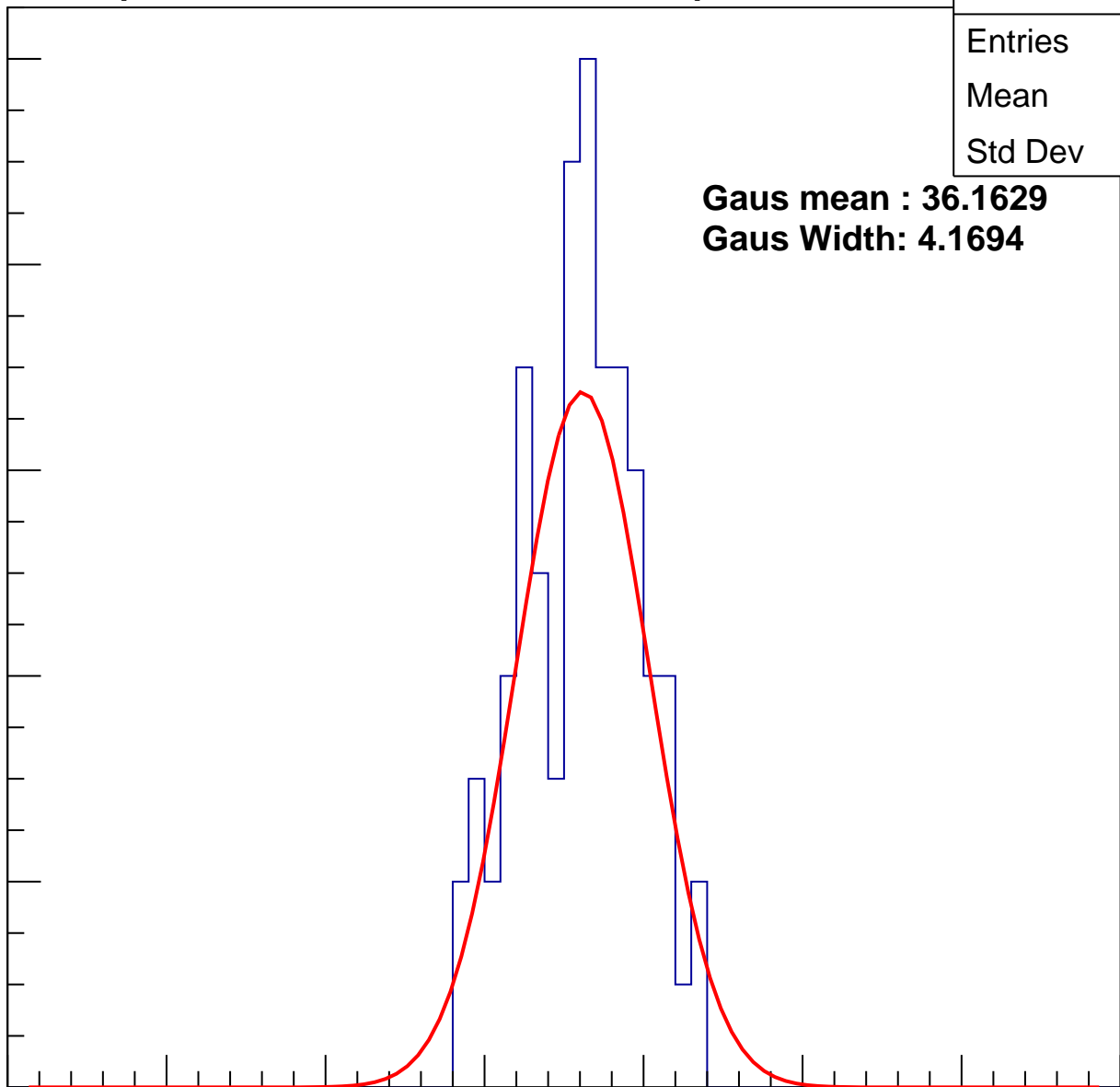
Gaus Width: 4.1694

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch119, adc2

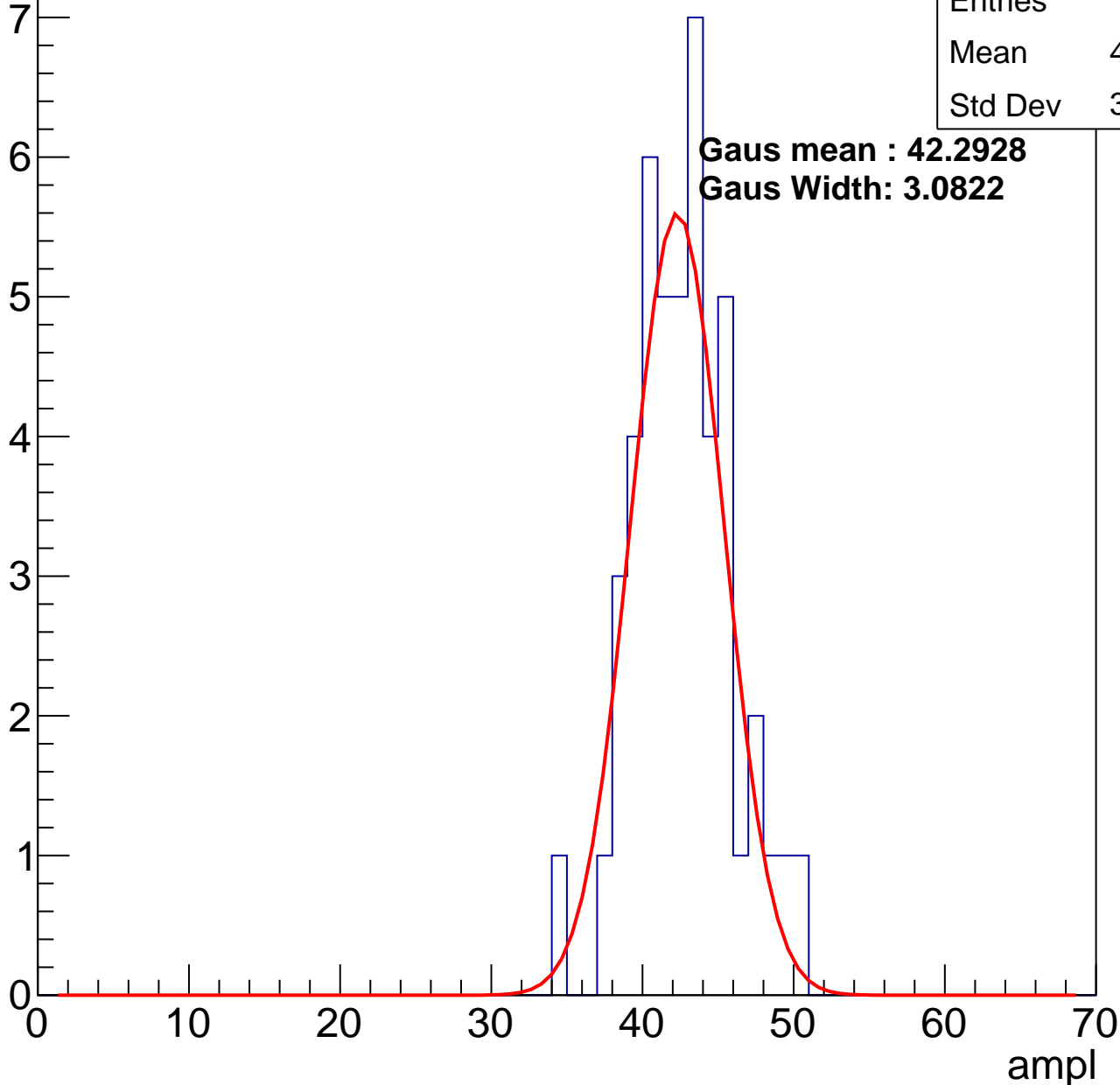
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	42.23
Std Dev	3.204

Gaus mean : 42.2928

Gaus Width: 3.0822

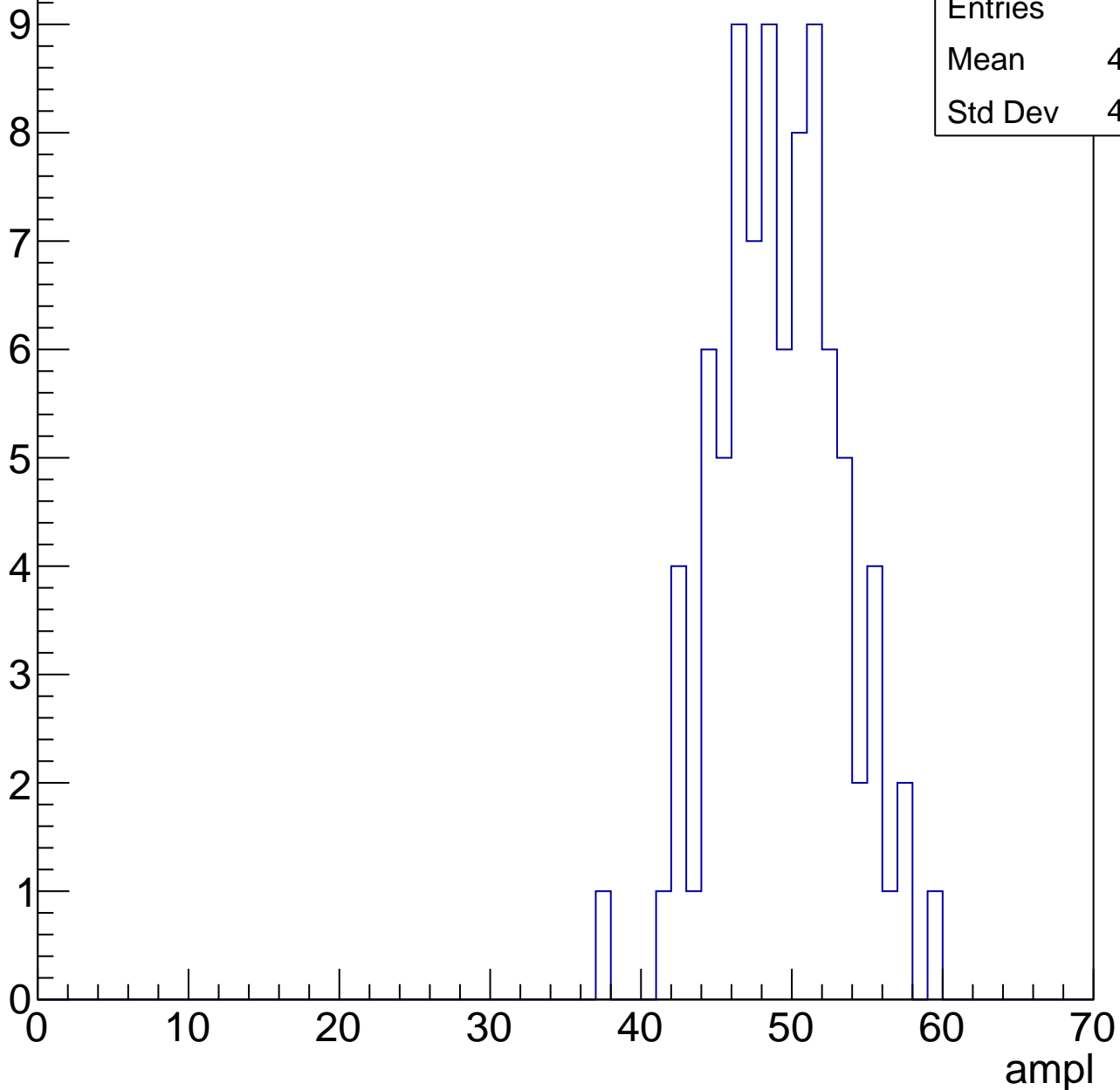


B1L102S, U18-ch119, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	87
Mean	48.74
Std Dev	4.056

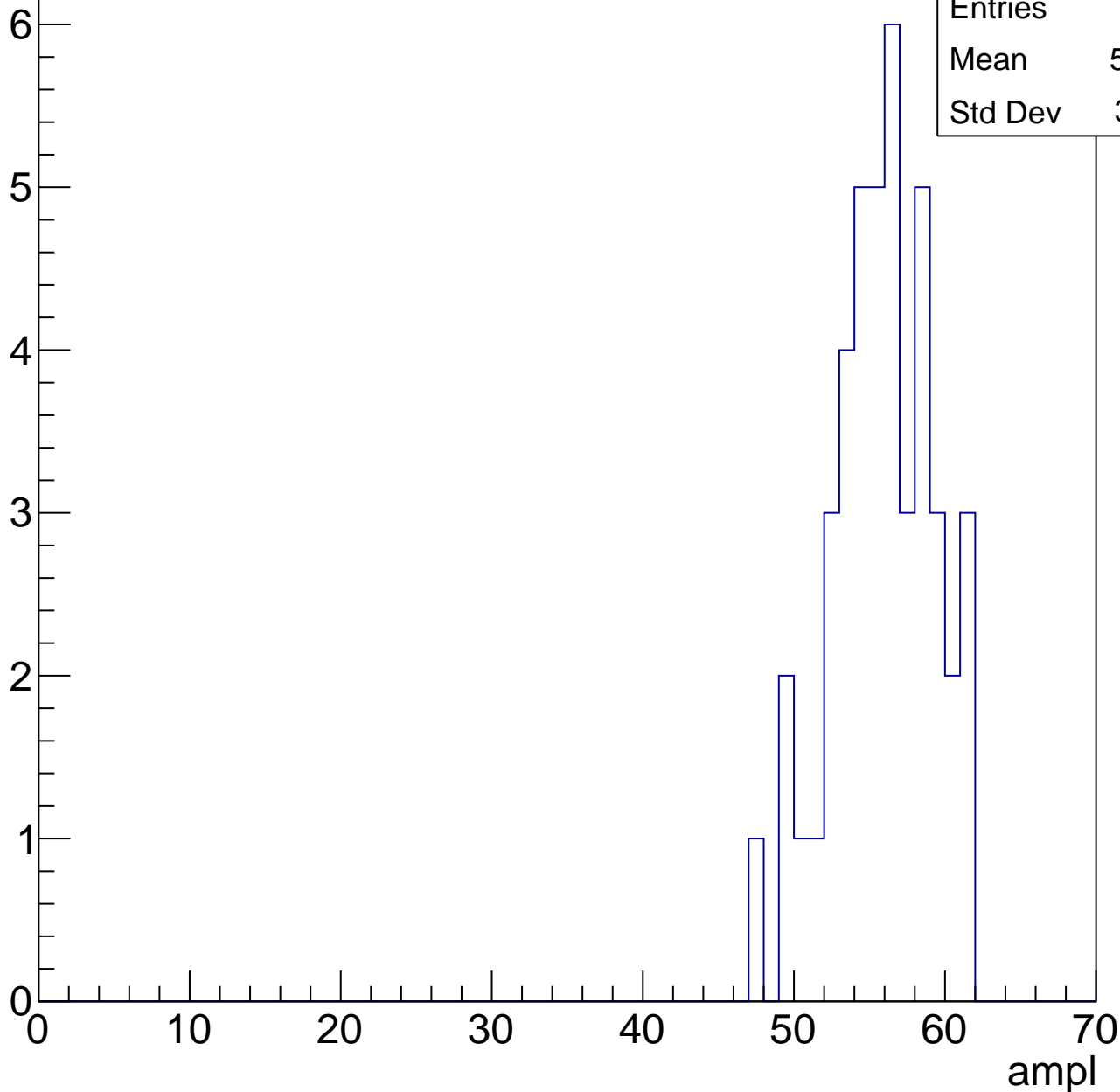


B1L102S, U18-ch119, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	44
Mean	55.36
Std Dev	3.331

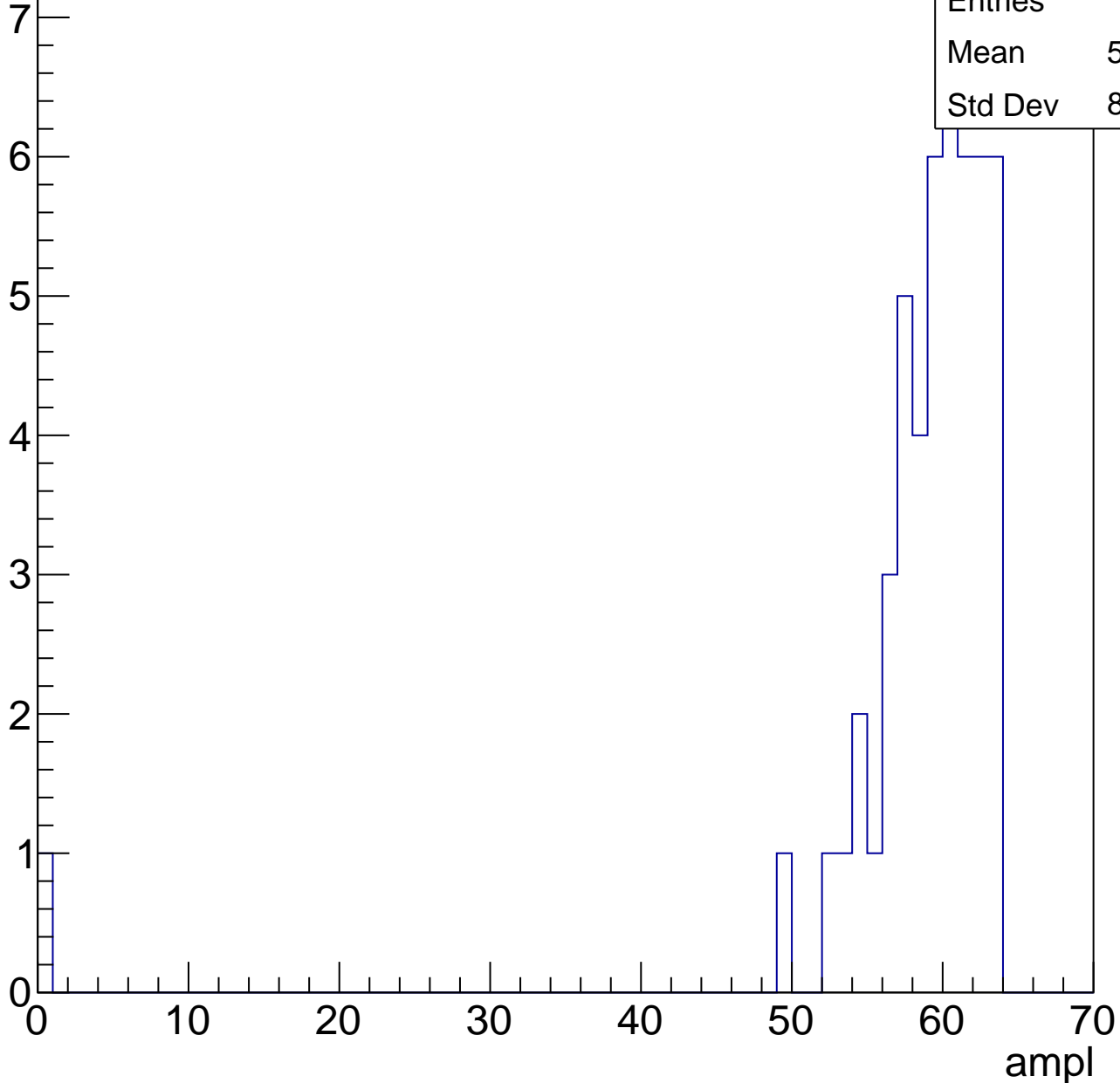


B1L102S, U18-ch119, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

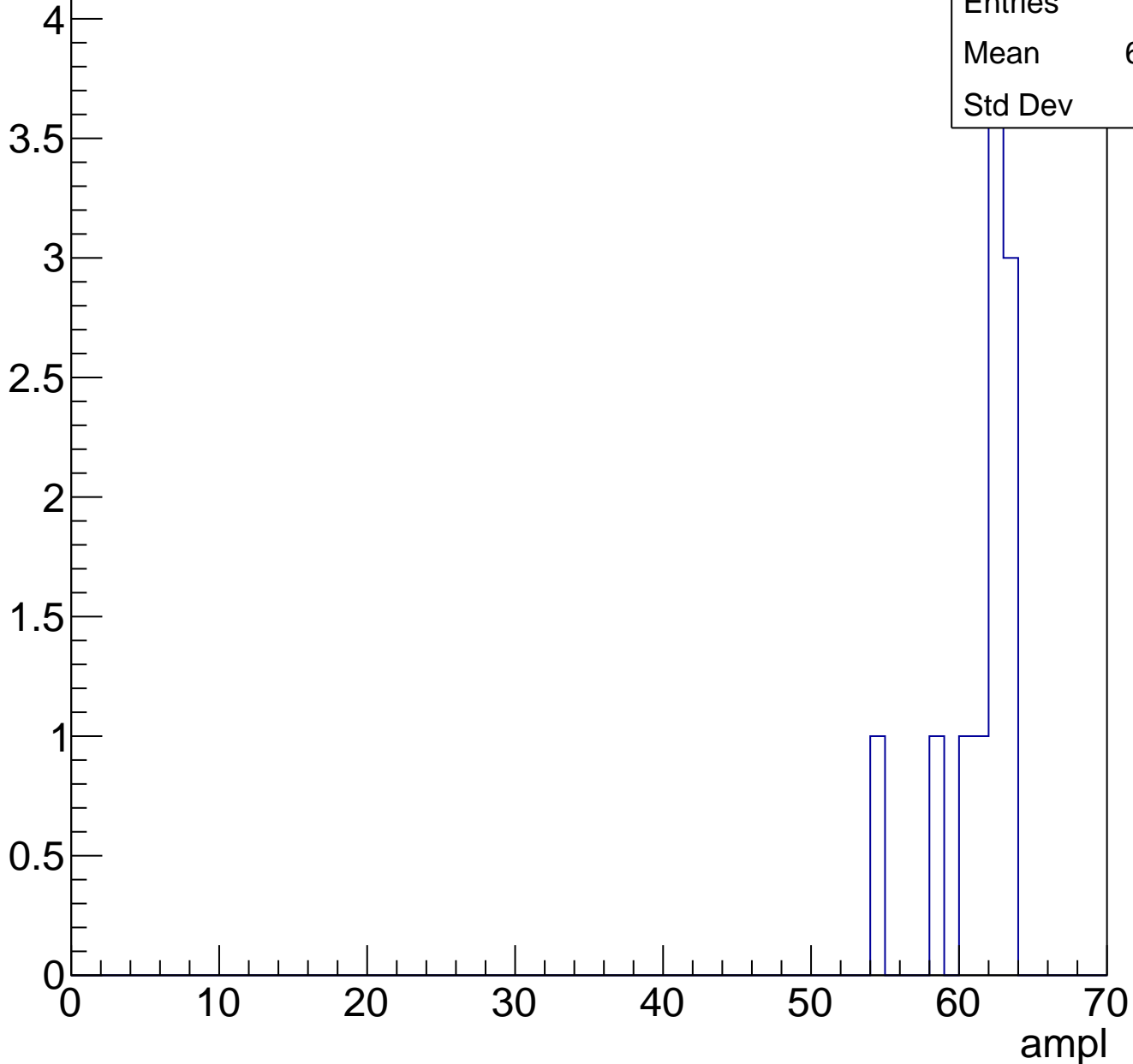
Entries	50
Mean	57.84
Std Dev	8.828



B1L102S, U18-ch119, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch119, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch120, adc0

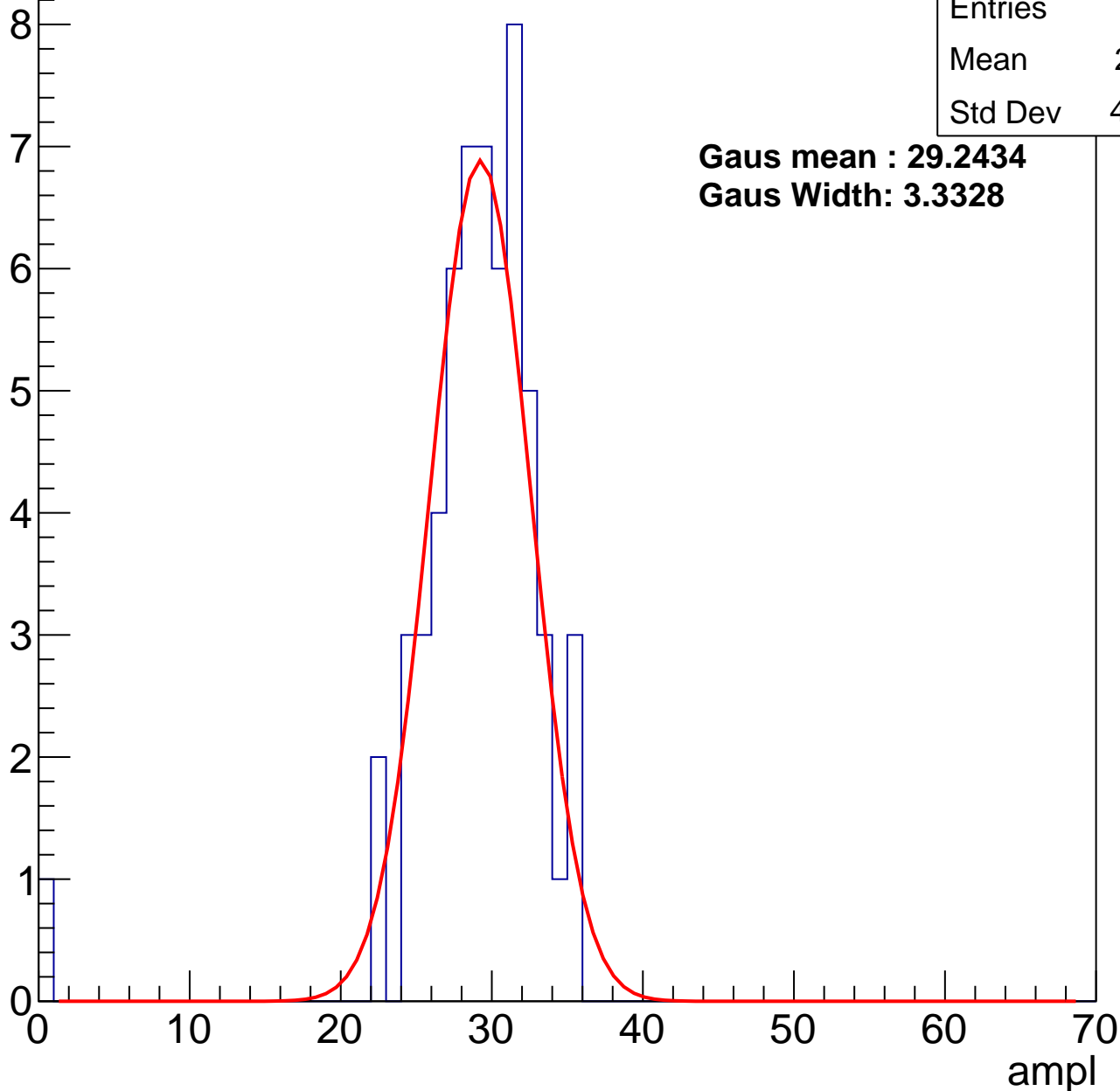
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	59
Mean	28.51
Std Dev	4.834

Gaus mean : 29.2434

Gaus Width: 3.3328



B1L102S, U18-ch120, adc1

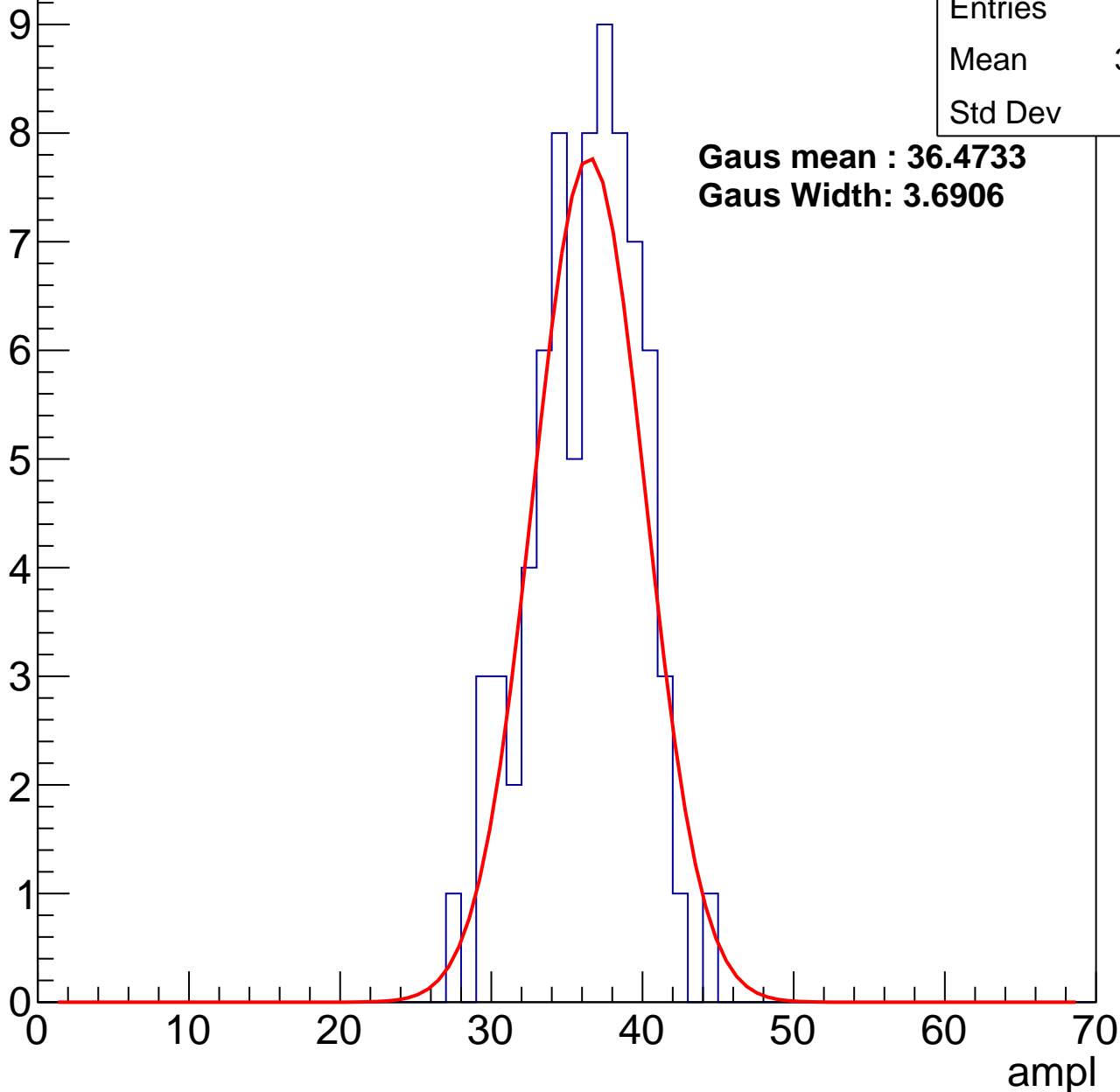
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	75
Mean	35.81
Std Dev	3.49

Gaus mean : 36.4733

Gaus Width: 3.6906



B1L102S, U18-ch120, adc2

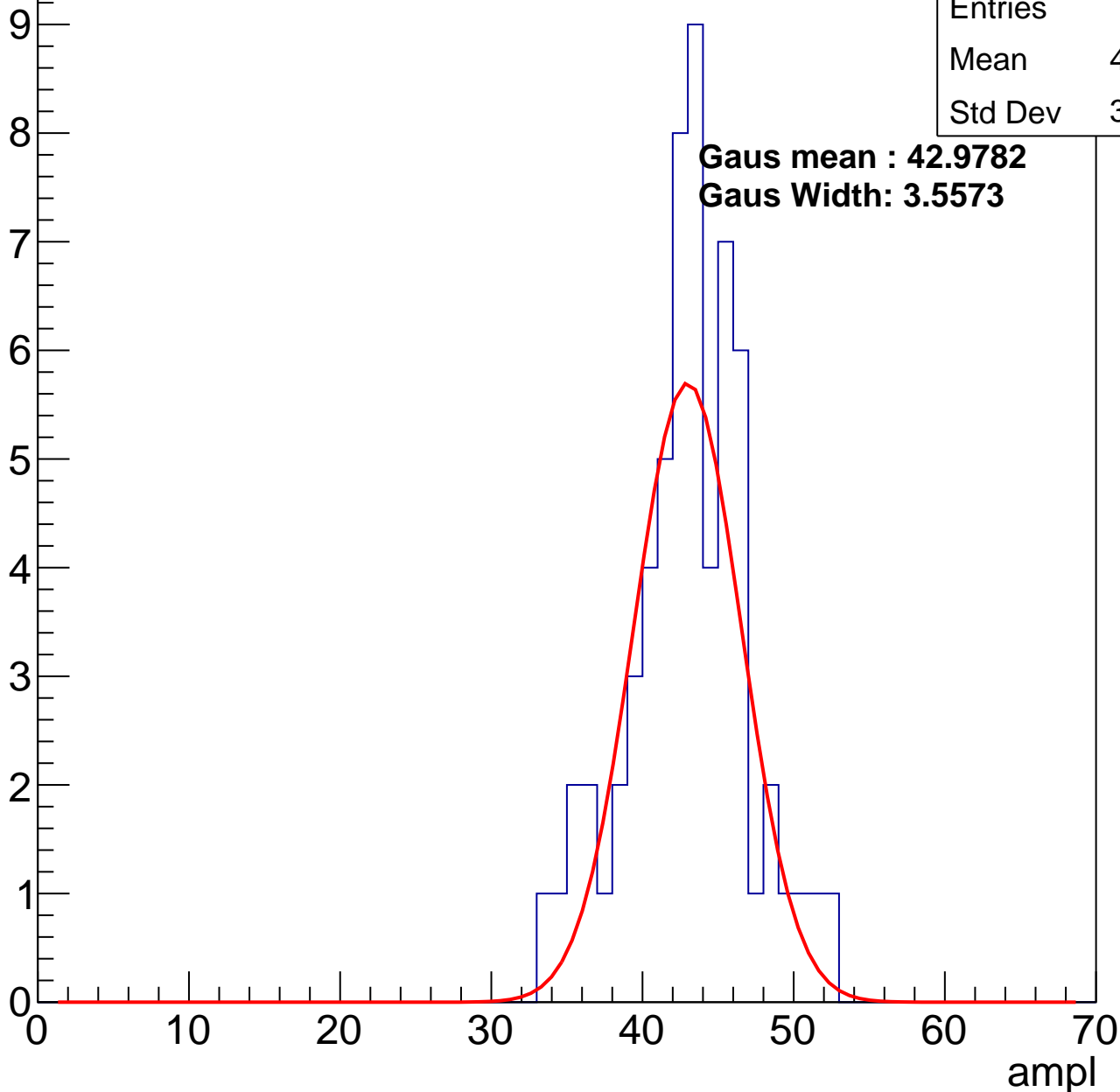
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	62
Mean	42.56
Std Dev	3.966

Gaus mean : 42.9782

Gaus Width: 3.5573

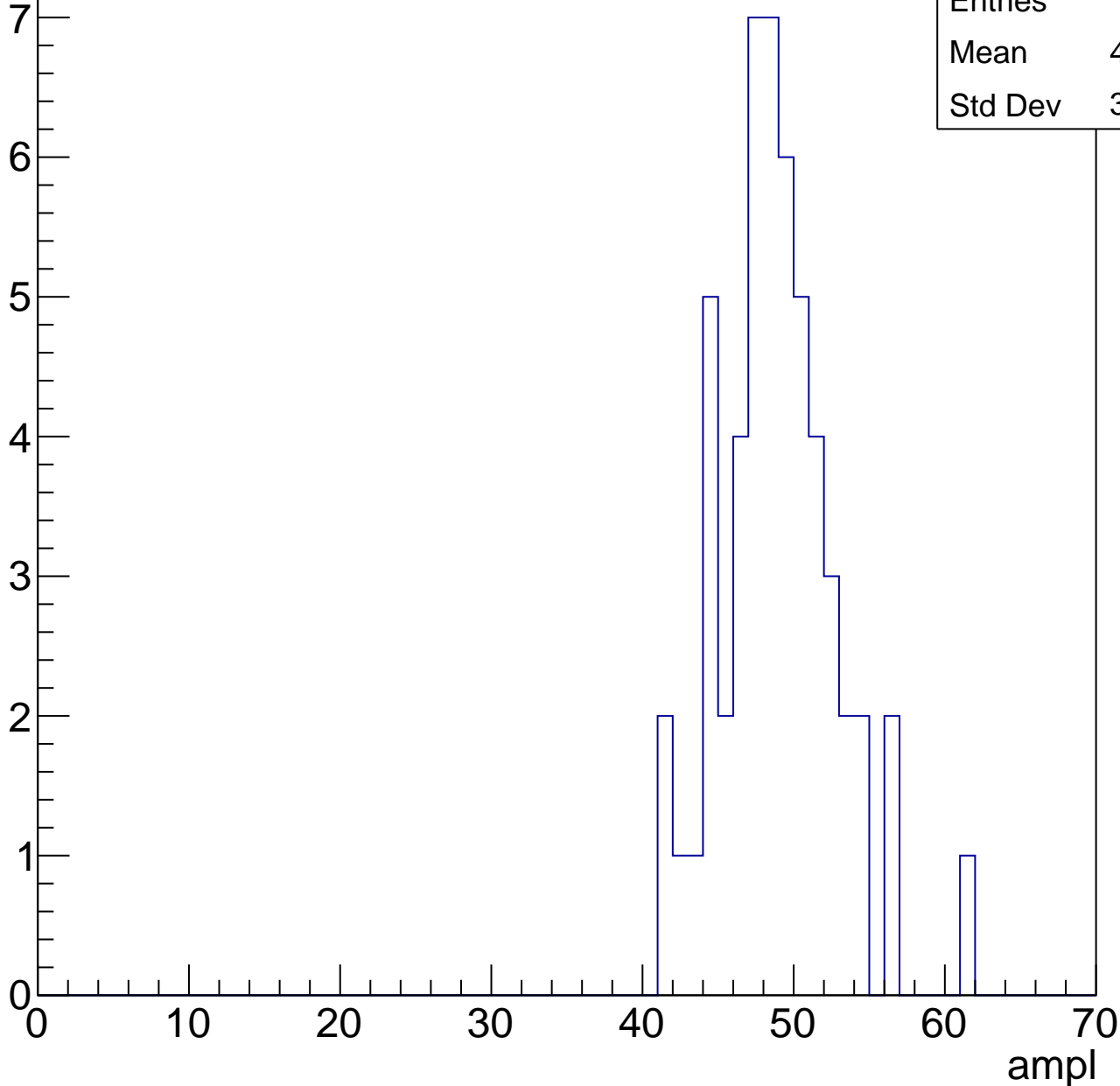


B1L102S, U18-ch120, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	48.46
Std Dev	3.843

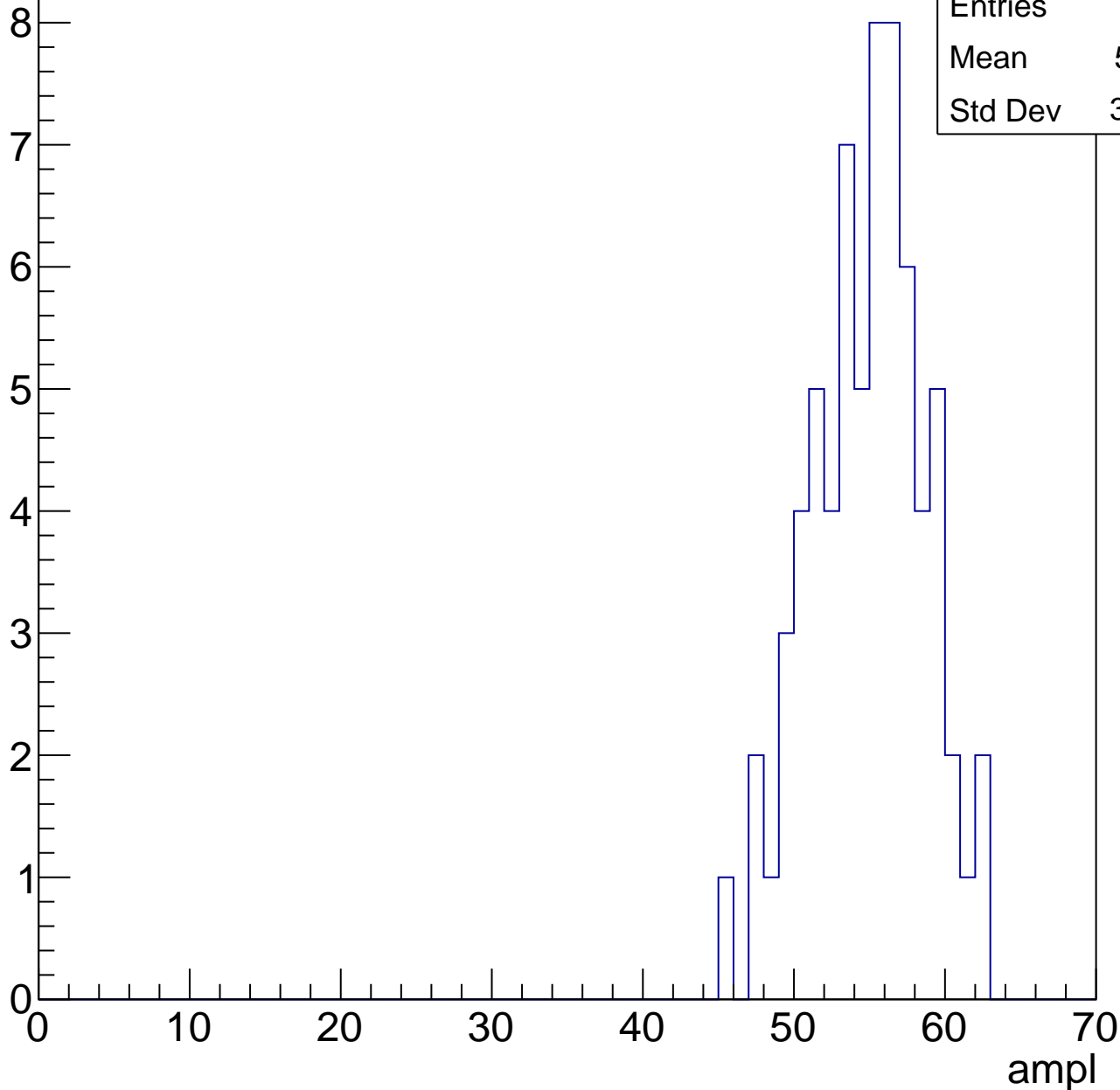


B1L102S, U18-ch120, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	54.41
Std Dev	3.723

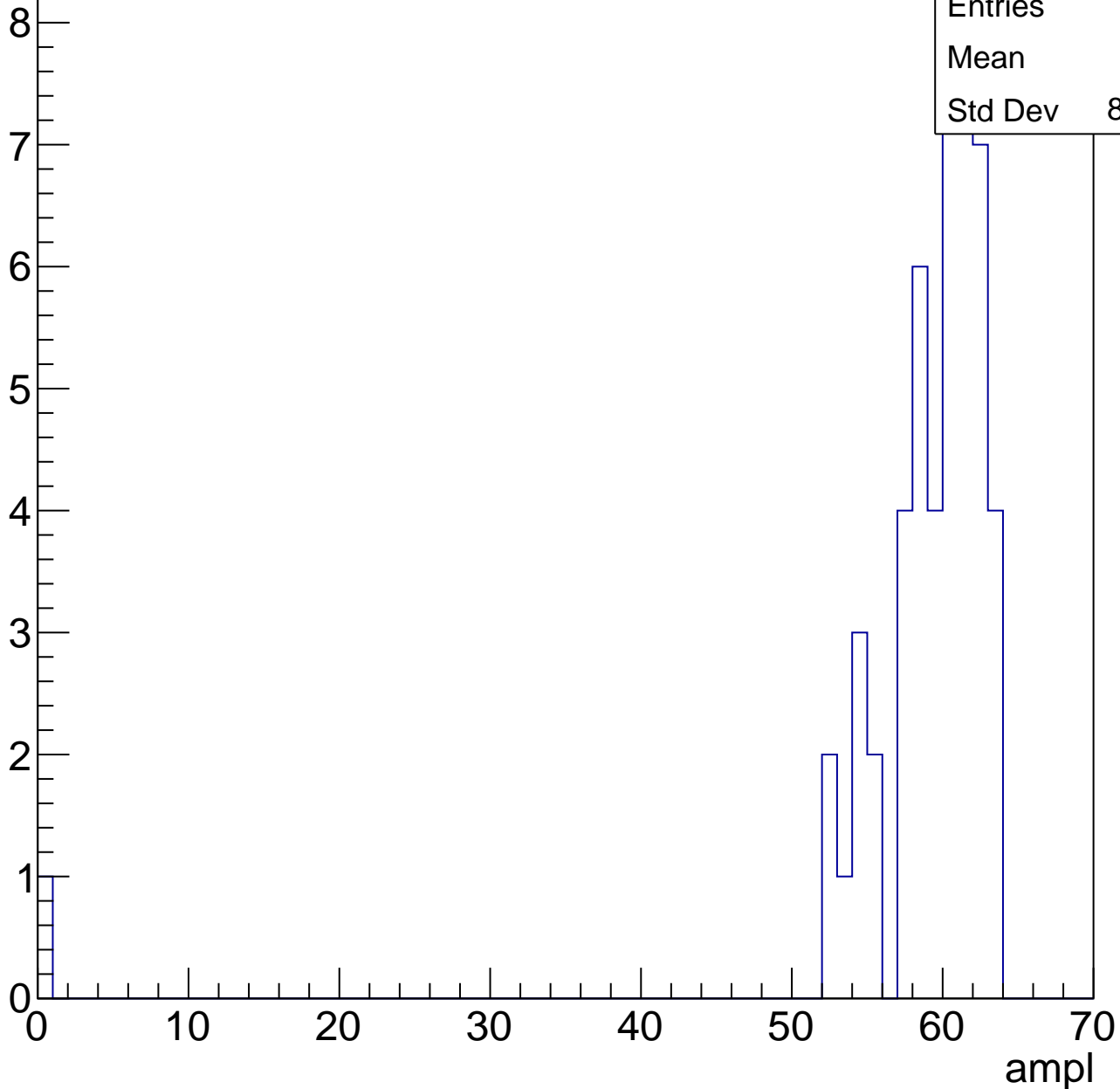


B1L102S, U18-ch120, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	57.9
Std Dev	8.776

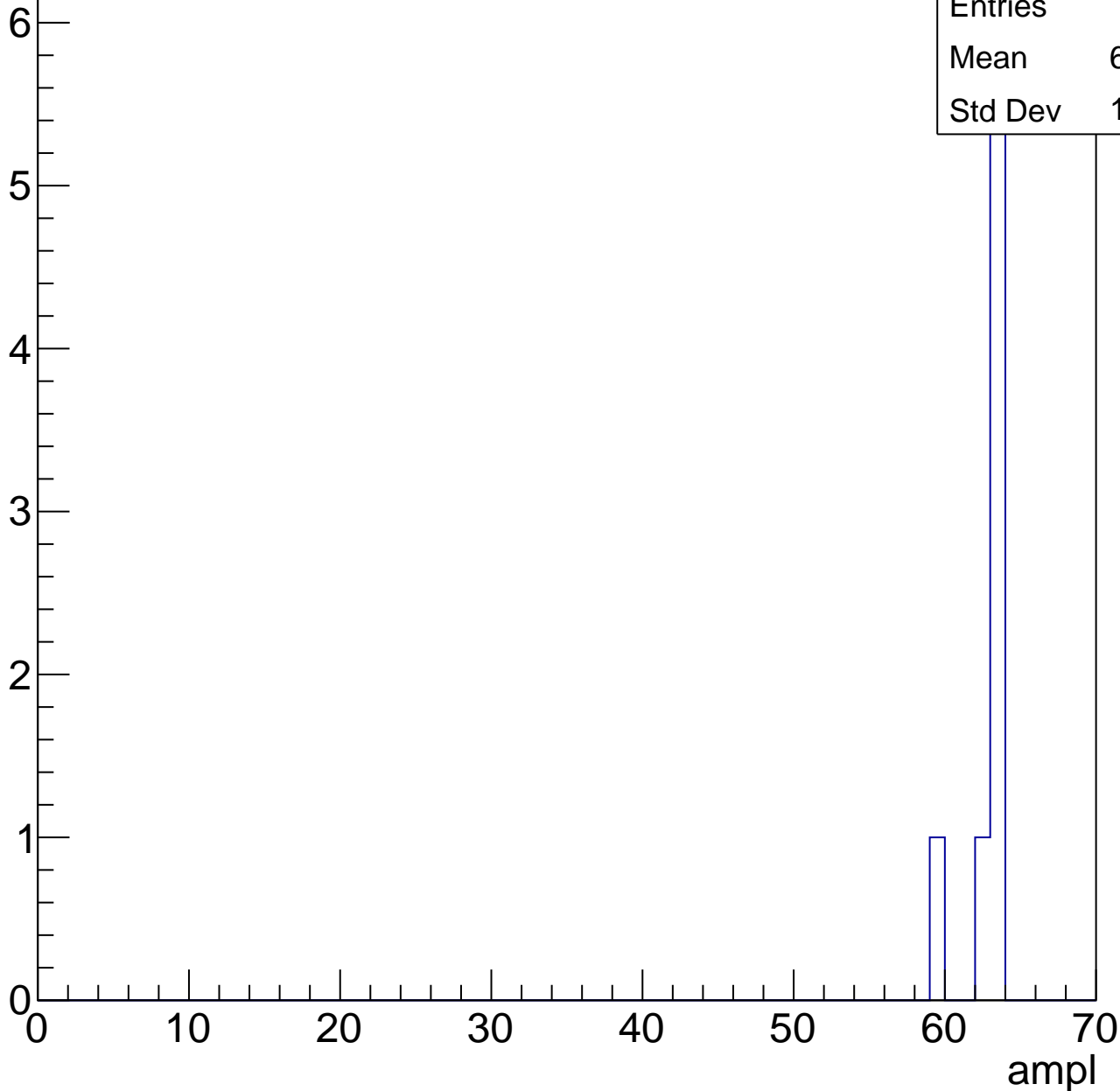


B1L102S, U18-ch120, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	8
Mean	62.38
Std Dev	1.317



B1L102S, U18-ch120, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch121, adc0

calib_packv5_042523_0143.root, FC#11, port A2

Entries	78
Mean	26.03
Std Dev	7.937

Gaus mean : 27.7686

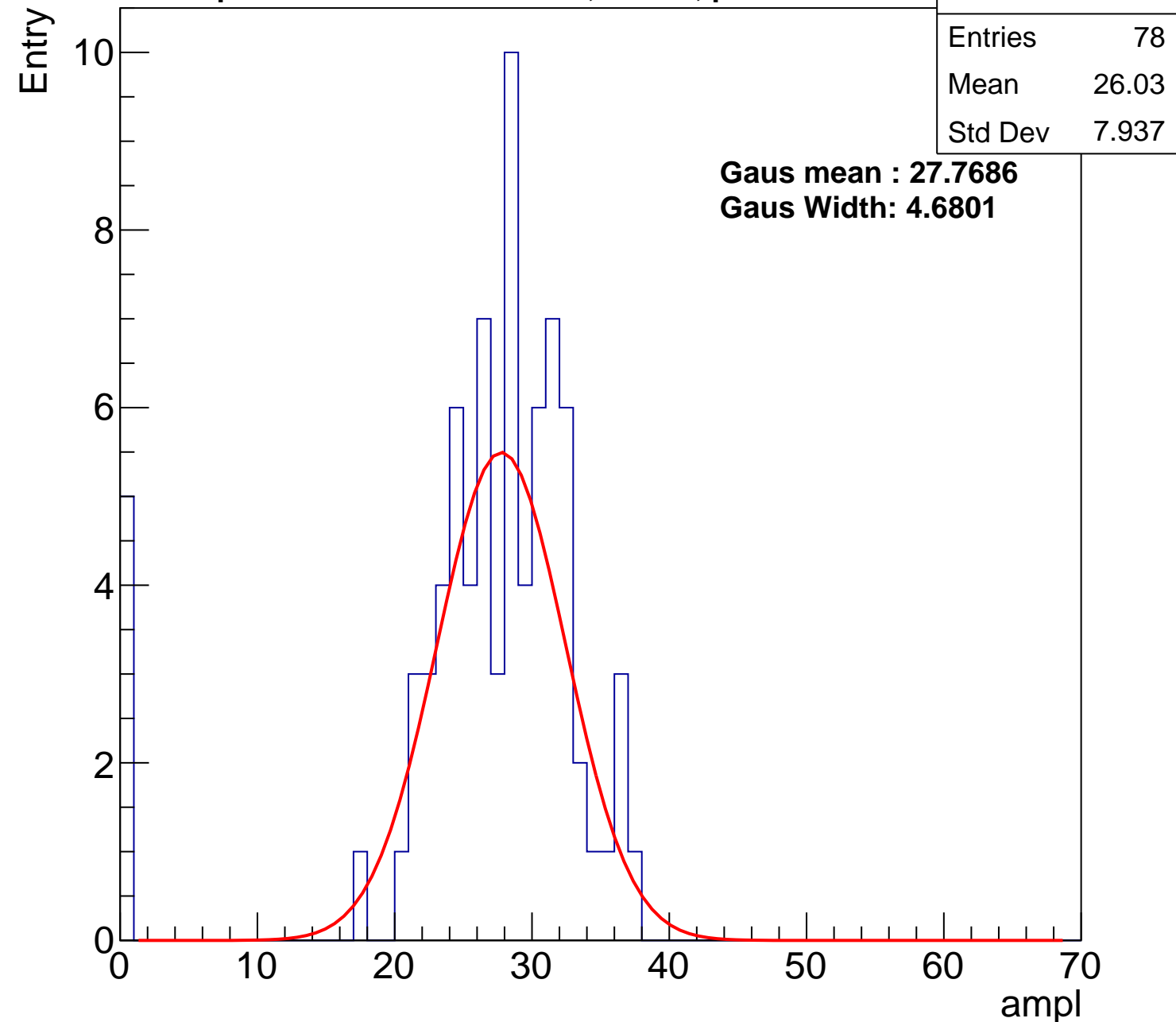
Gaus Width: 4.6801

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch121, adc1

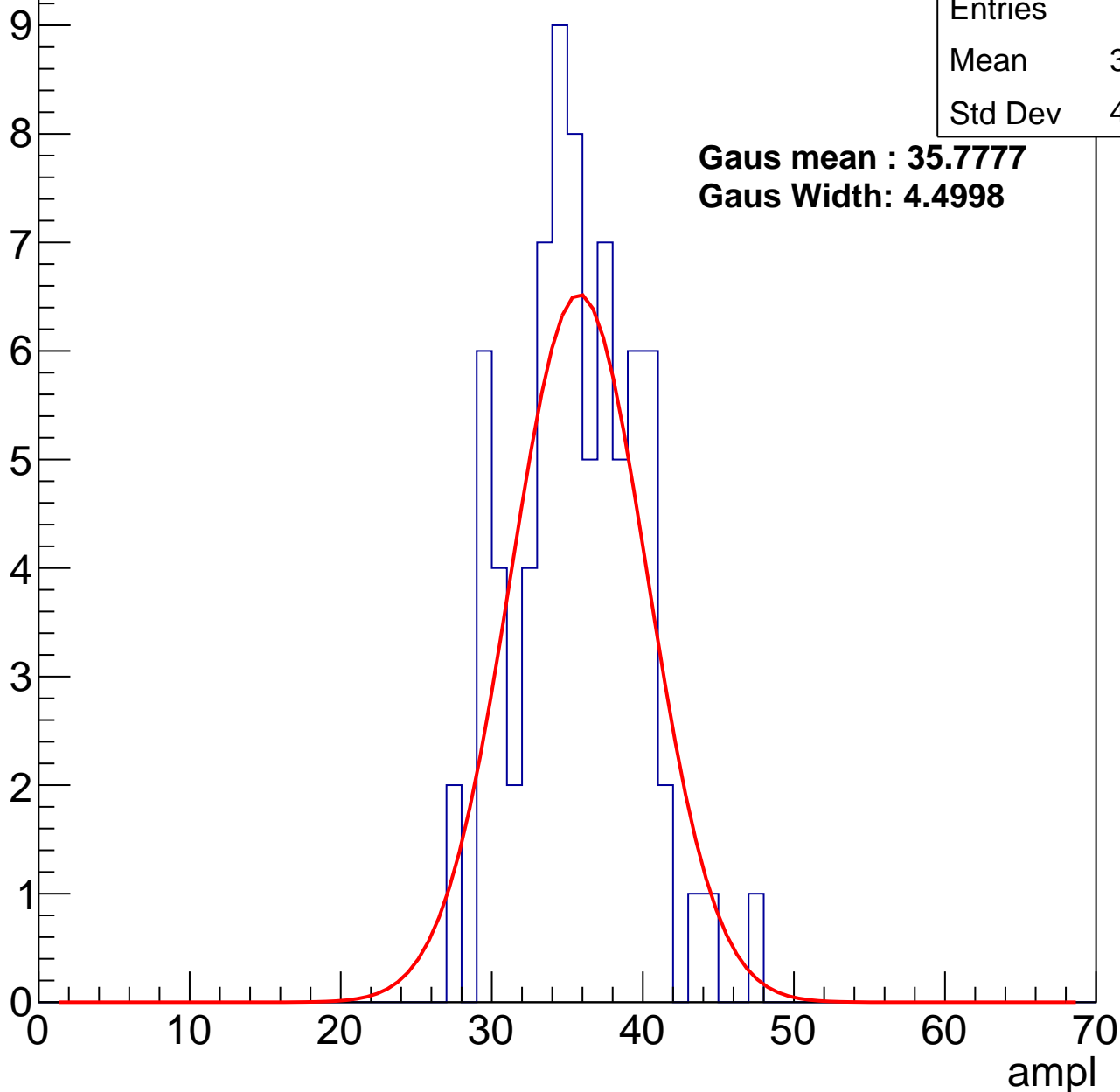
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	35.18
Std Dev	4.025

Gaus mean : 35.7777

Gaus Width: 4.4998



B1L102S, U18-ch121, adc2

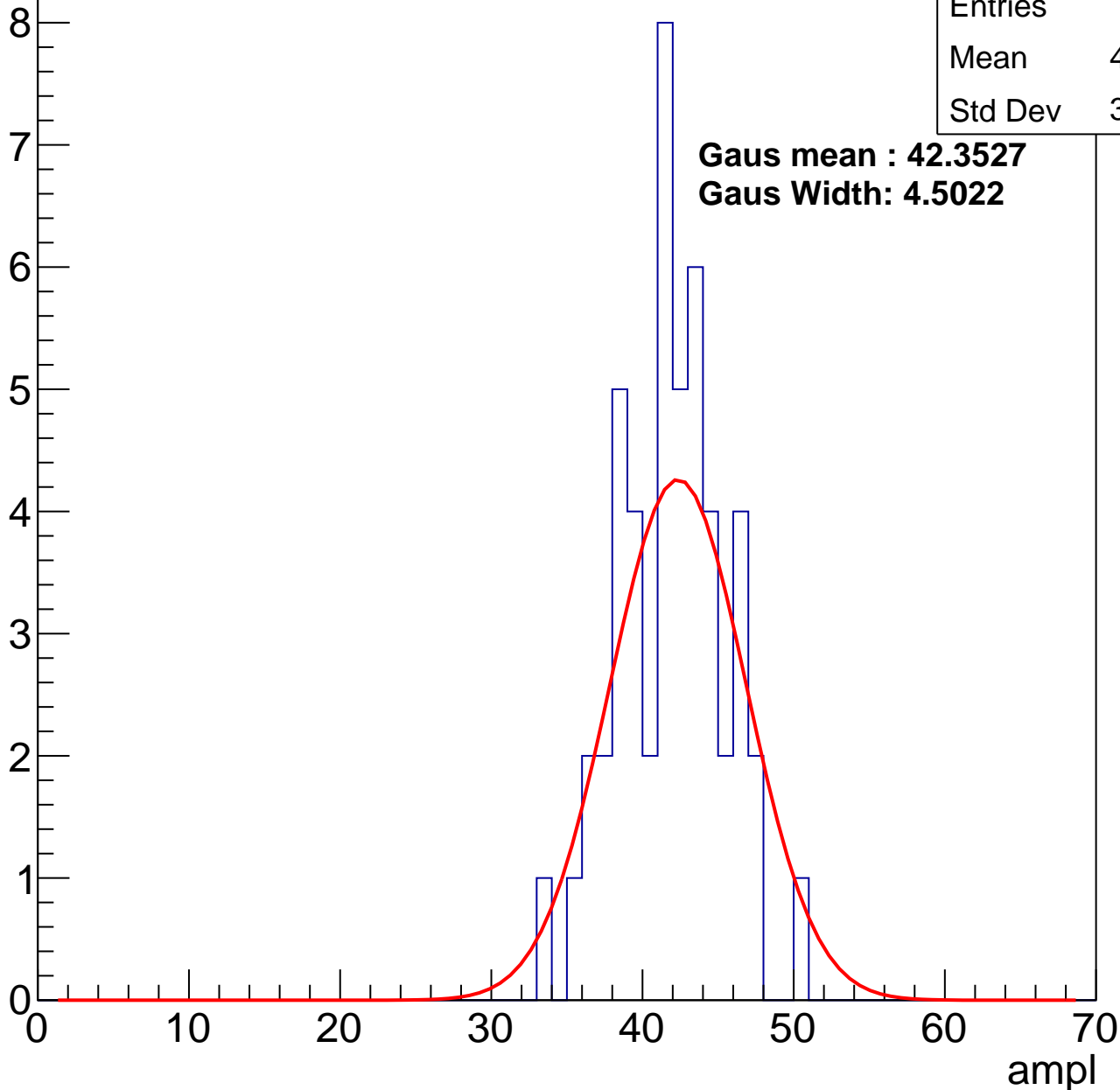
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	49
Mean	41.43
Std Dev	3.458

Gaus mean : 42.3527

Gaus Width: 4.5022

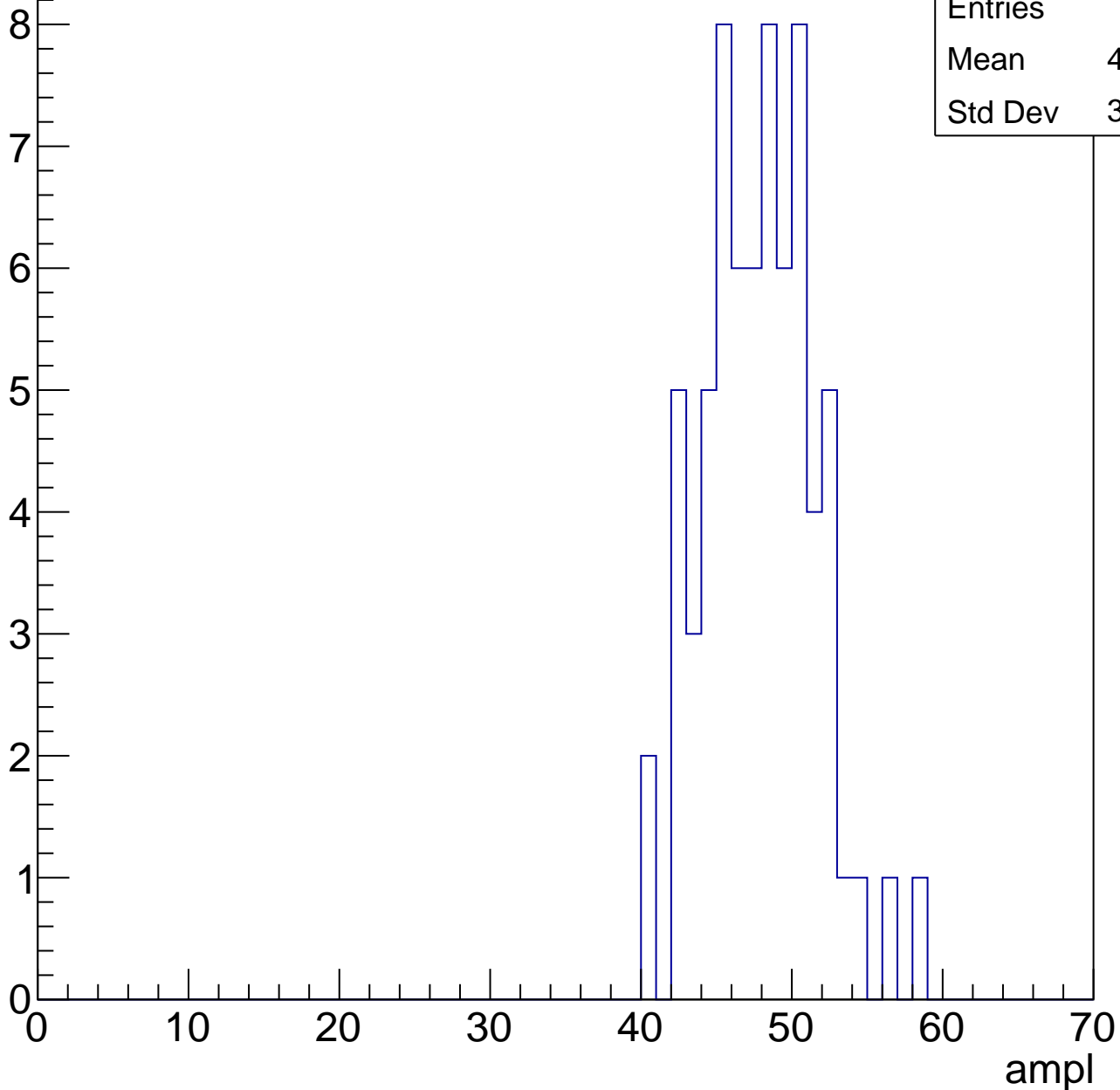


B1L102S, U18-ch121, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	47.43
Std Dev	3.628

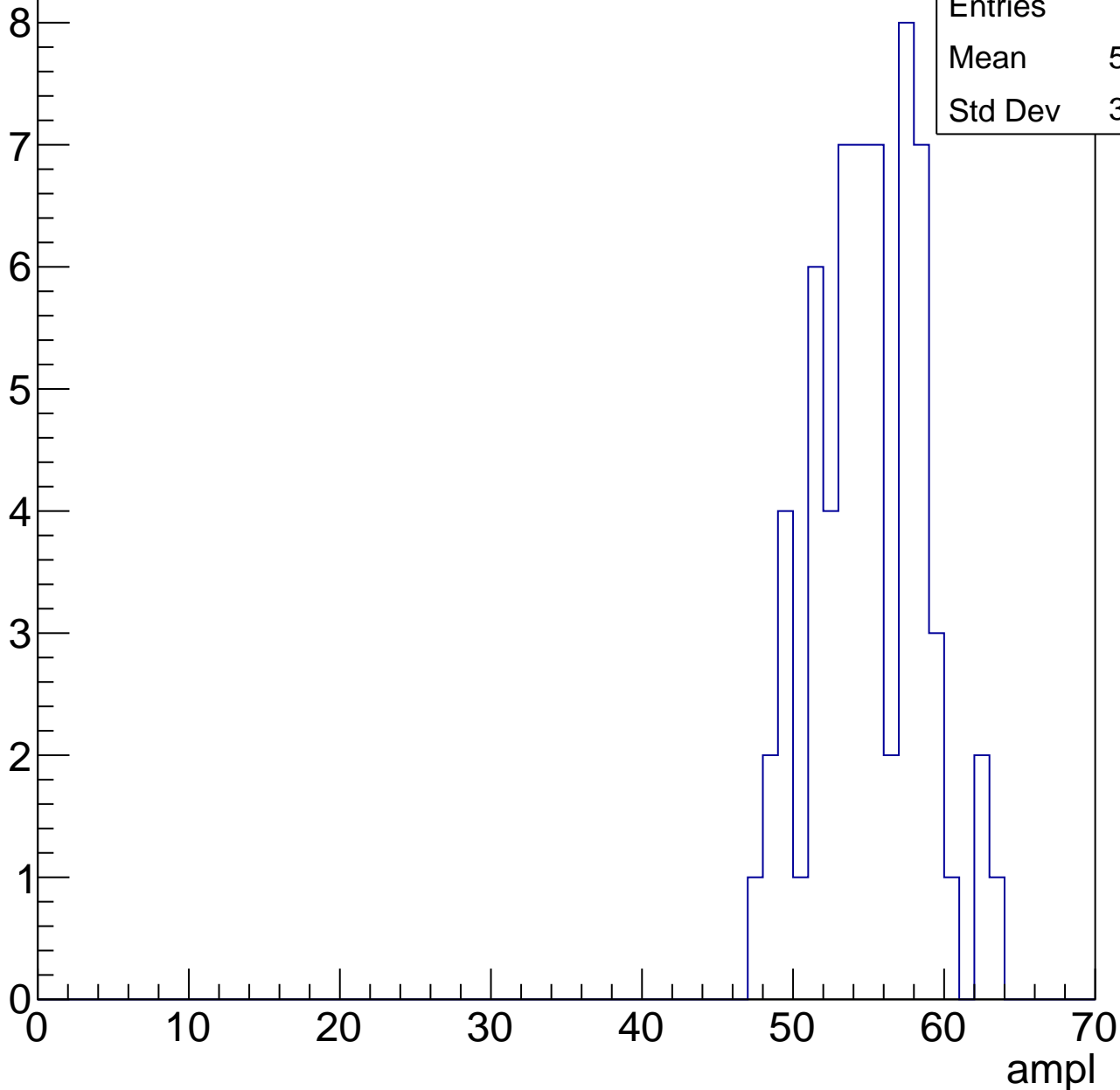


B1L102S, U18-ch121, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	63
Mean	54.52
Std Dev	3.598

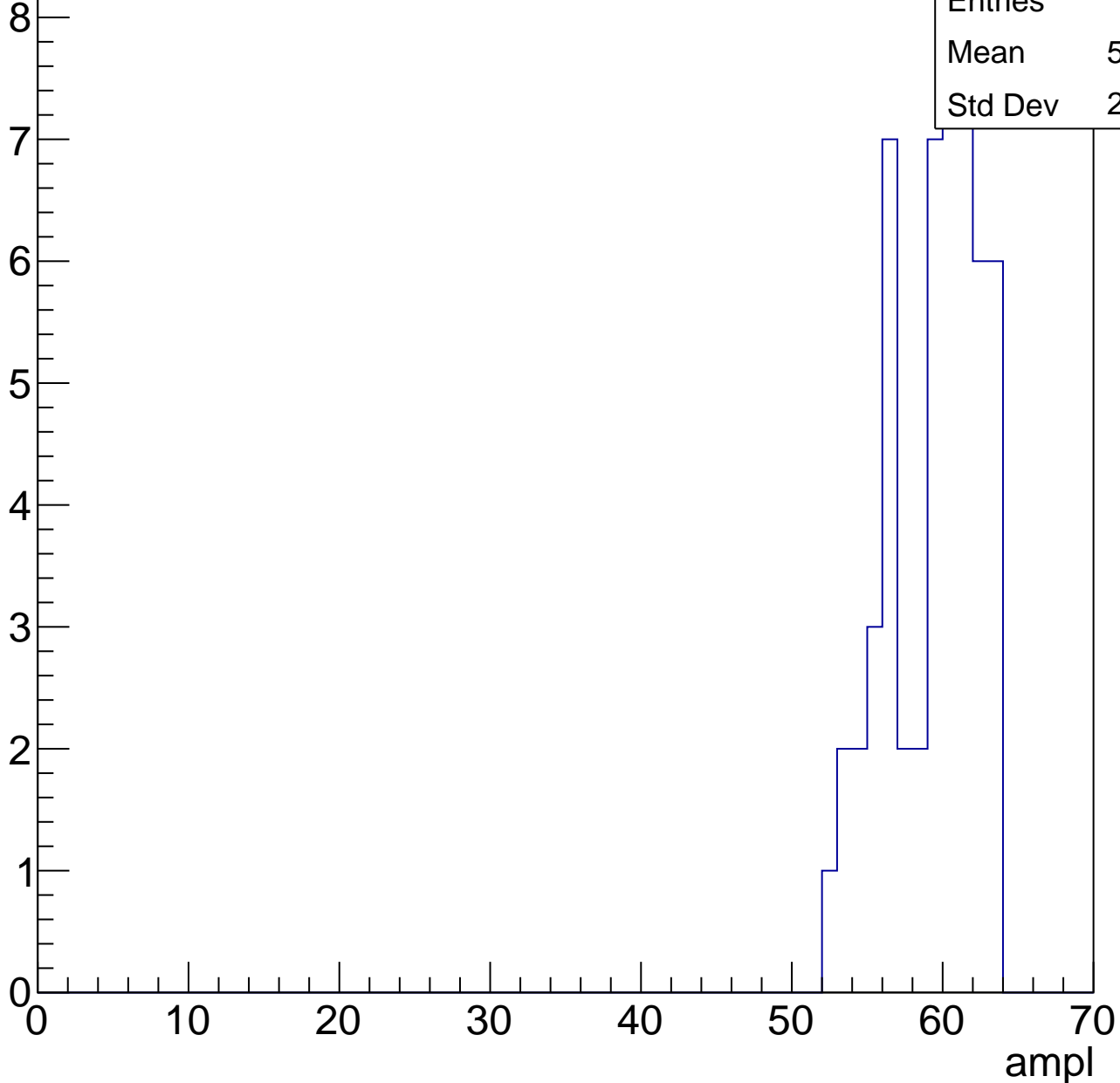


B1L102S, U18-ch121, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	54
Mean	58.96
Std Dev	2.987



B1L102S, U18-ch121, adc6

calib_packv5_042523_0143.root, FC#11, port A2

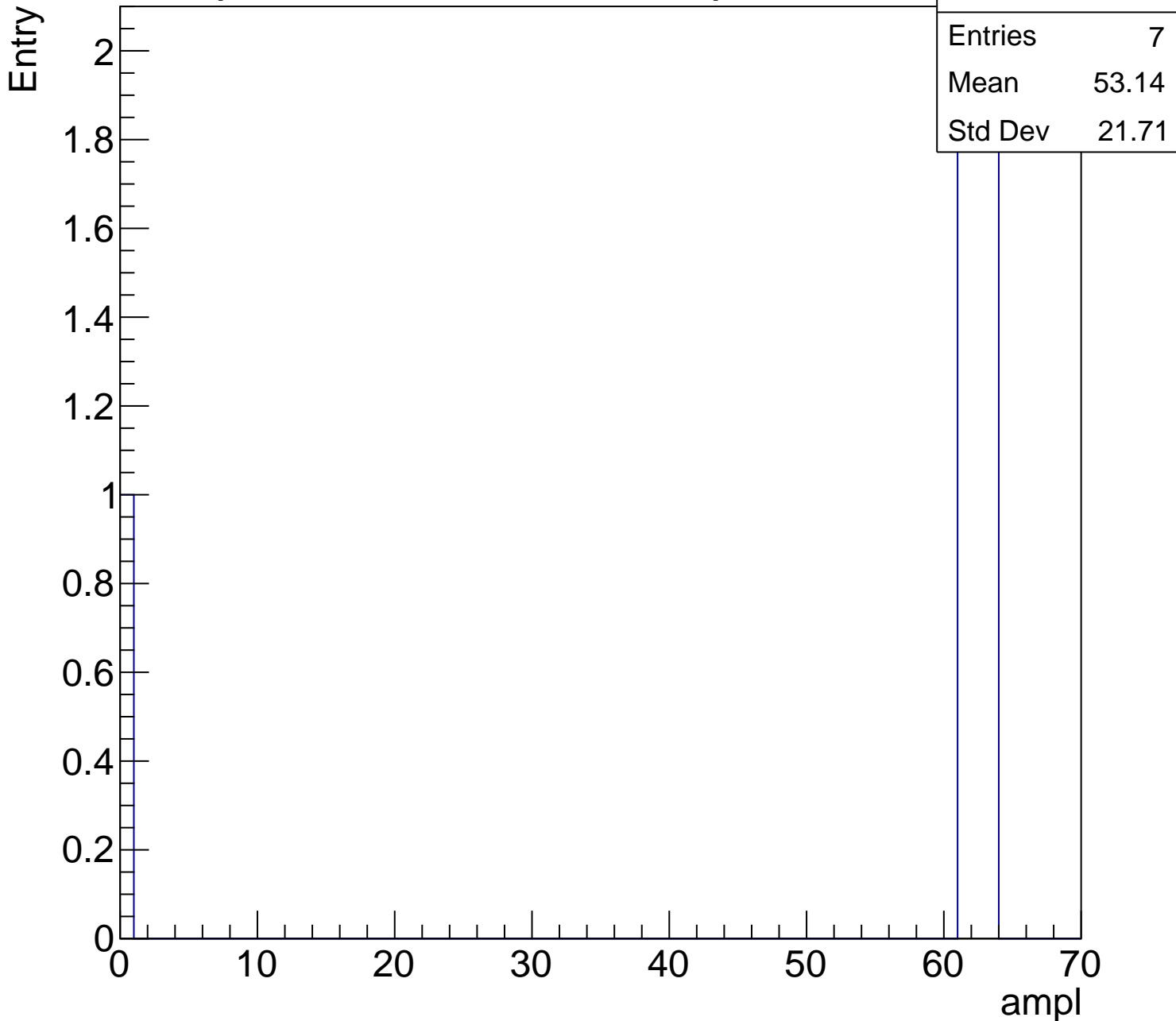
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	53.14
Std Dev	21.71

0 10 20 30 40 50 60 70

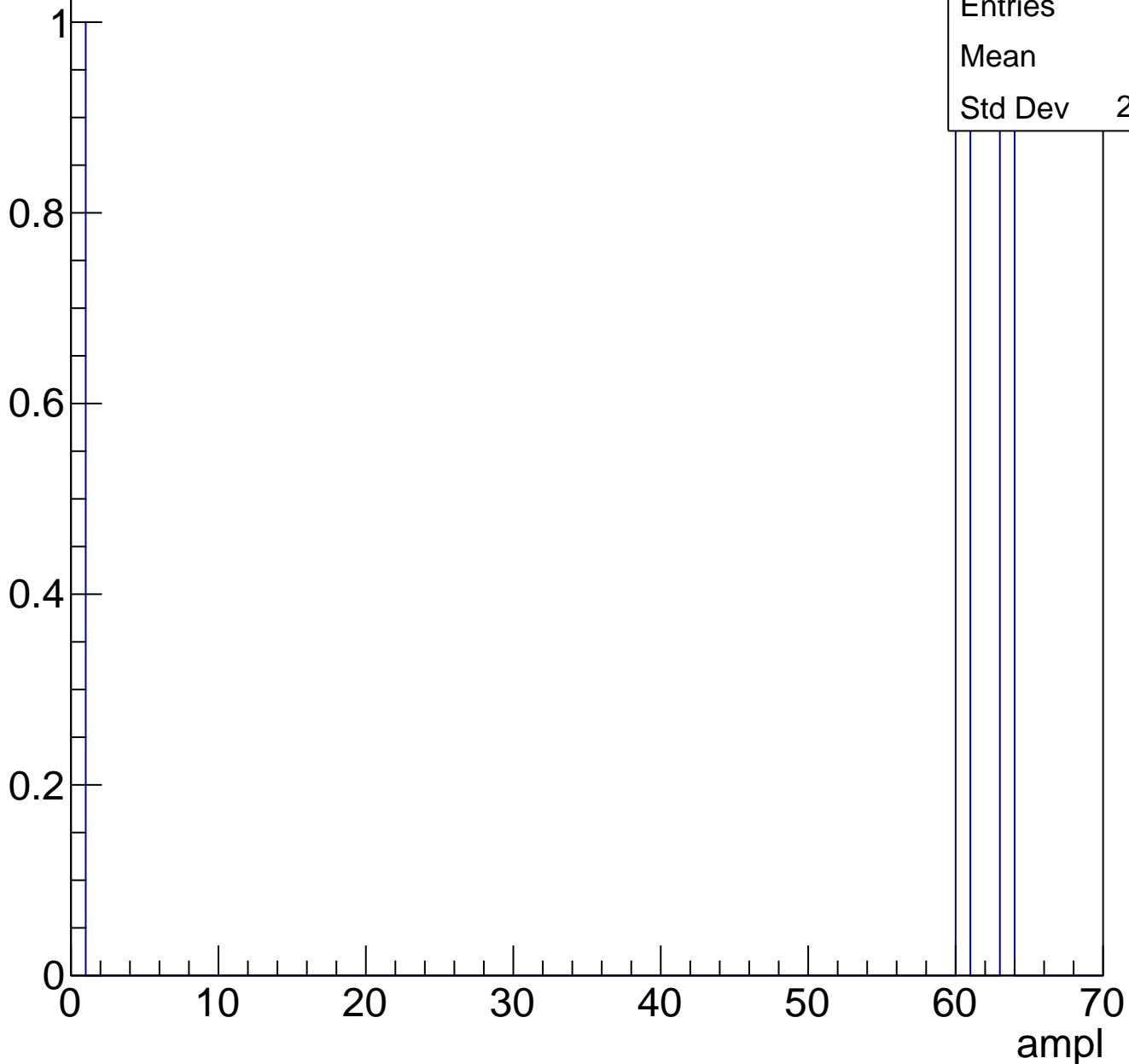
ampl



B1L102S, U18-ch121, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch122, adc0

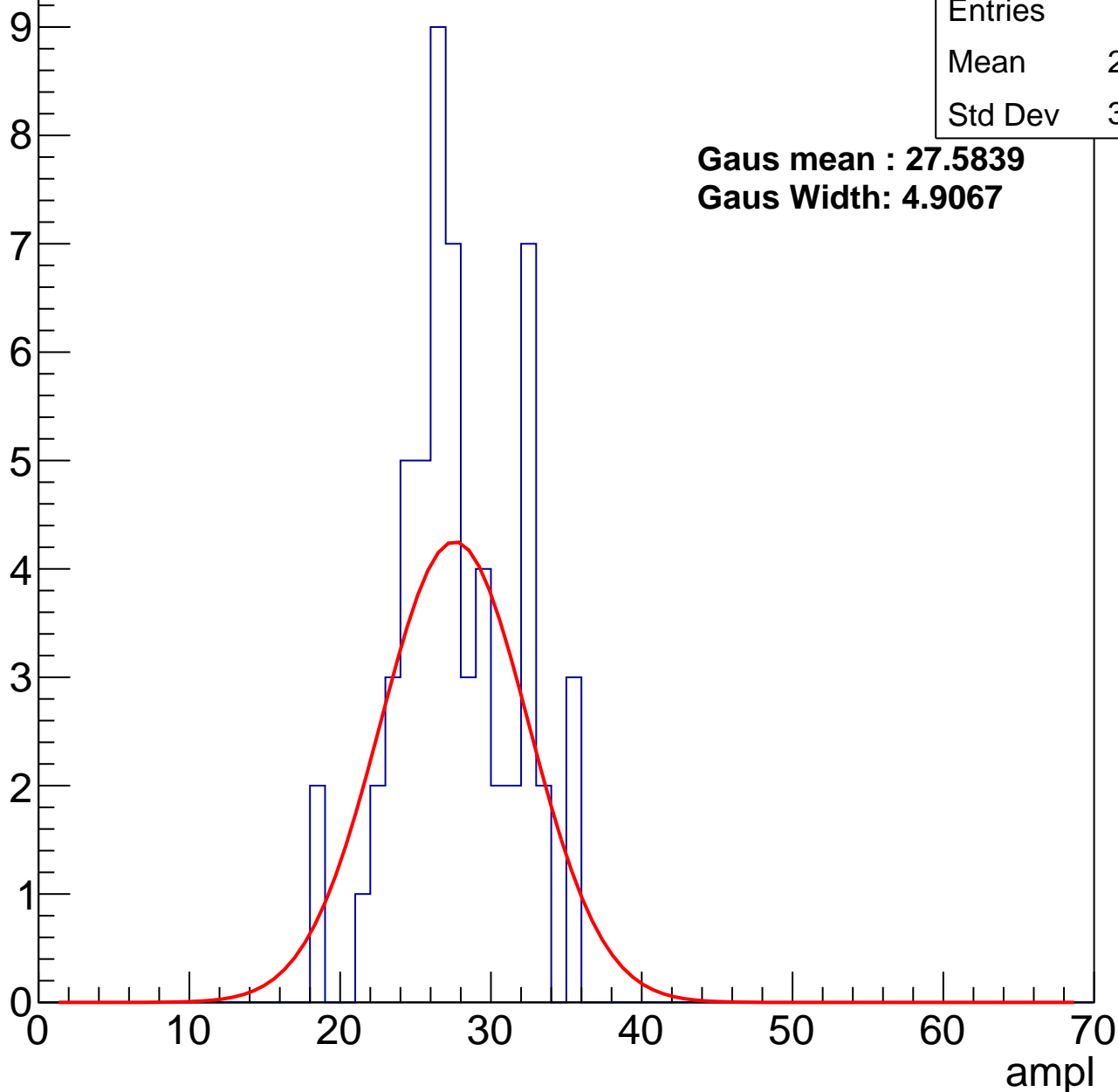
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.28
Std Dev	3.915

Gaus mean : 27.5839

Gaus Width: 4.9067



B1L102S, U18-ch122, adc1

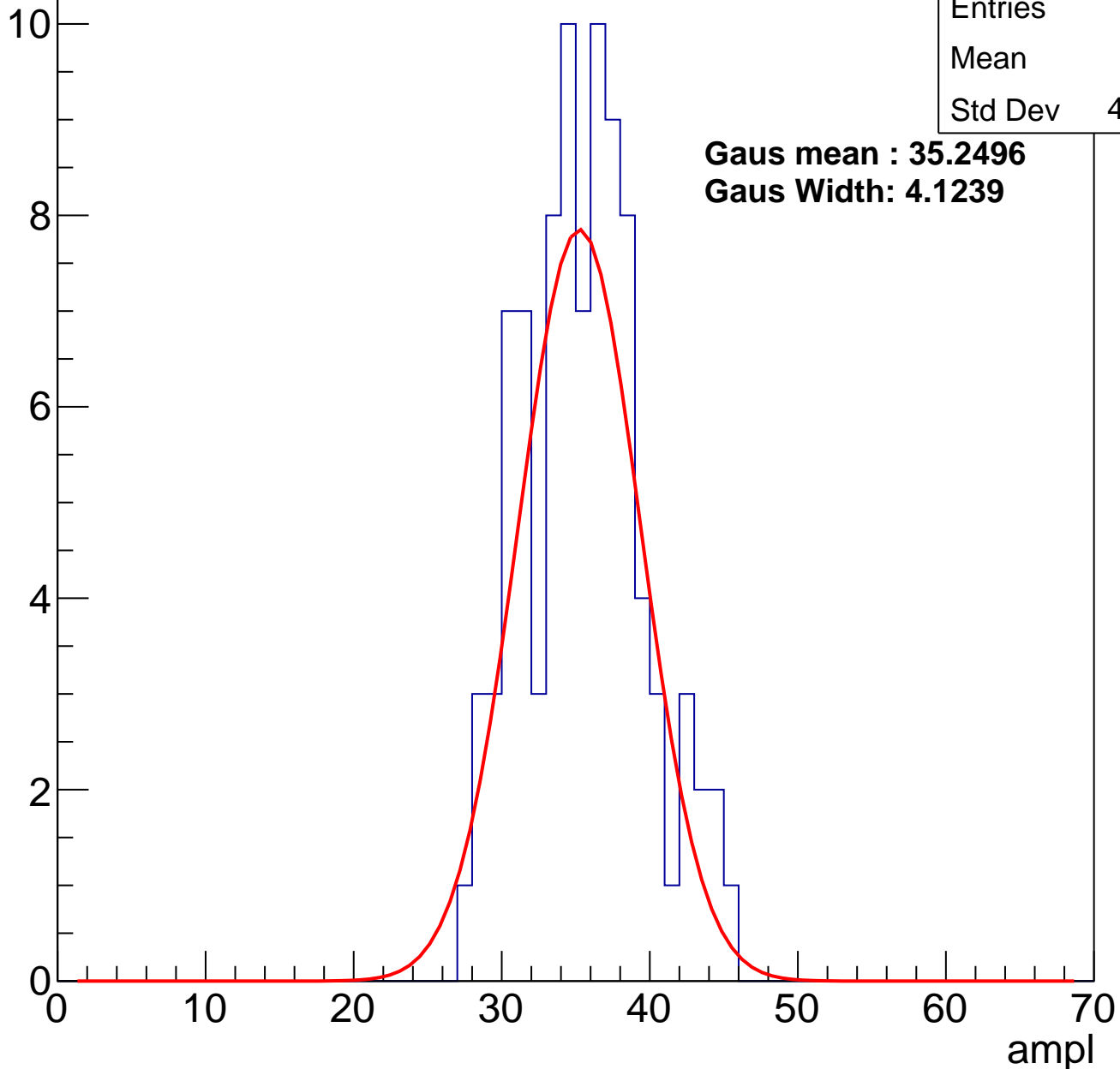
calib_packv5_042523_0143.root, FC#11, port A2

Entries	92
Mean	35.1
Std Dev	4.043

Gaus mean : 35.2496

Gaus Width: 4.1239

Entry



B1L102S, U18-ch122, adc2

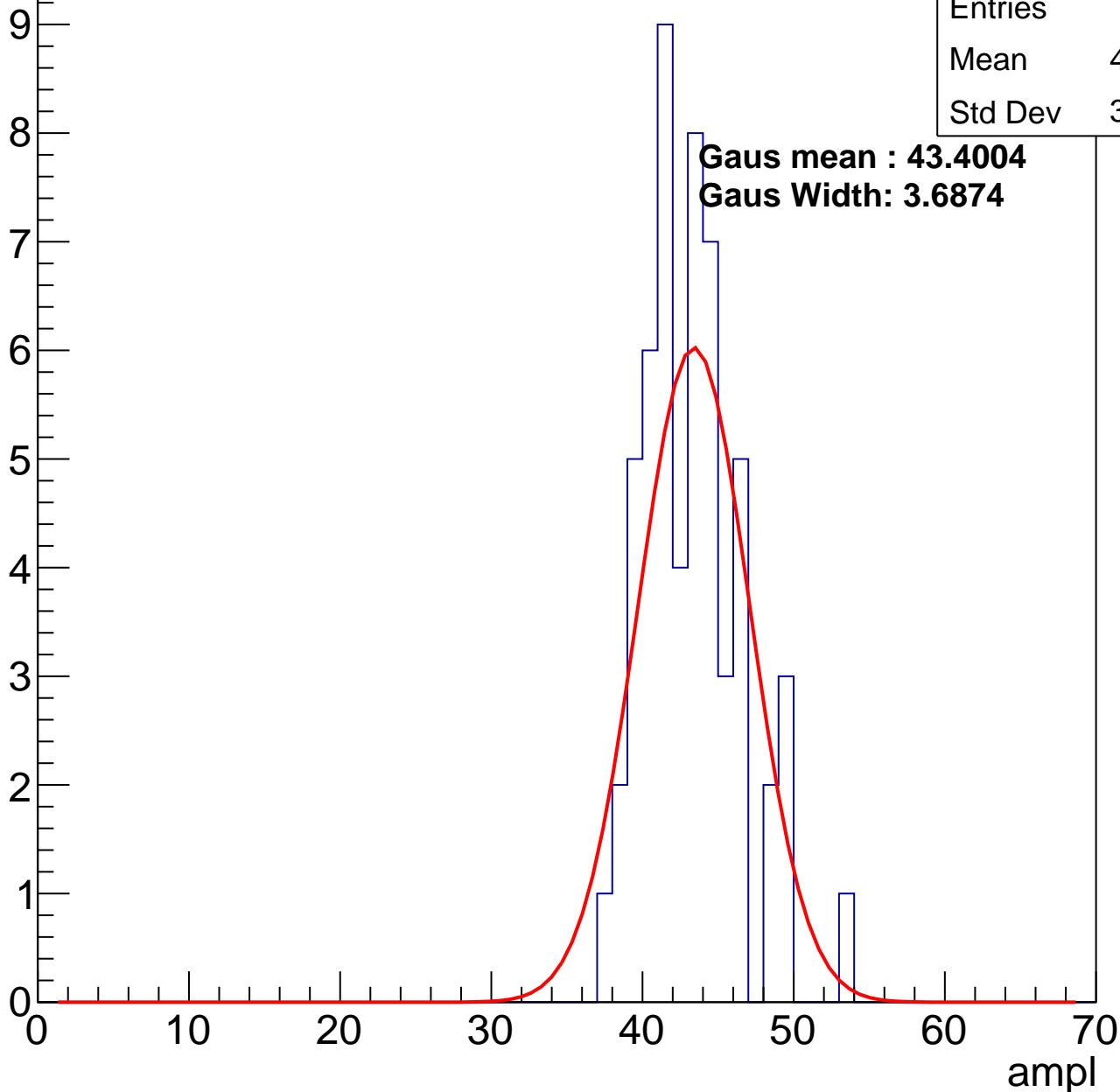
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	42.82
Std Dev	3.219

Gaus mean : 43.4004

Gaus Width: 3.6874

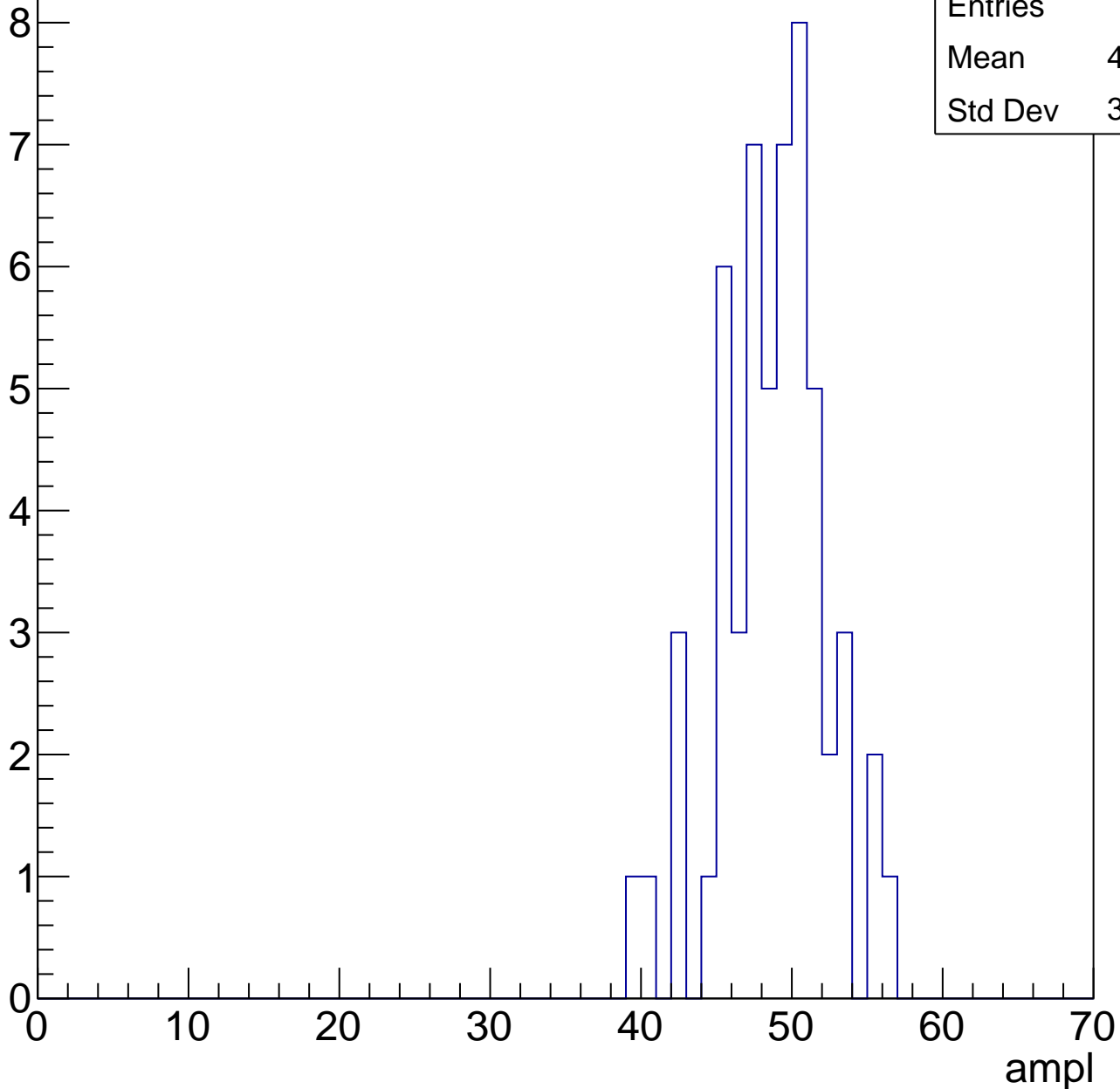


B1L102S, U18-ch122, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	55
Mean	48.24
Std Dev	3.547

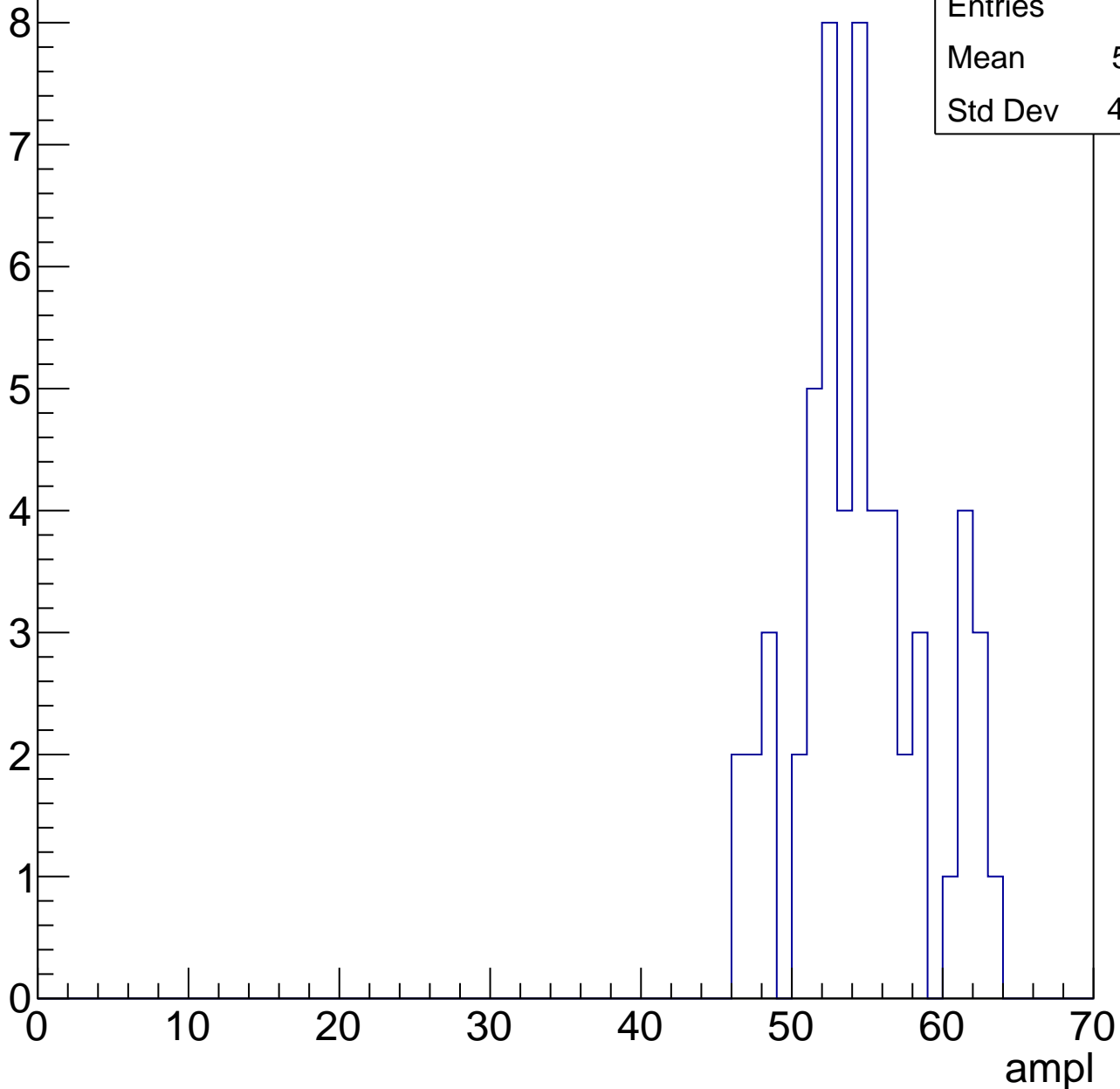


B1L102S, U18-ch122, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

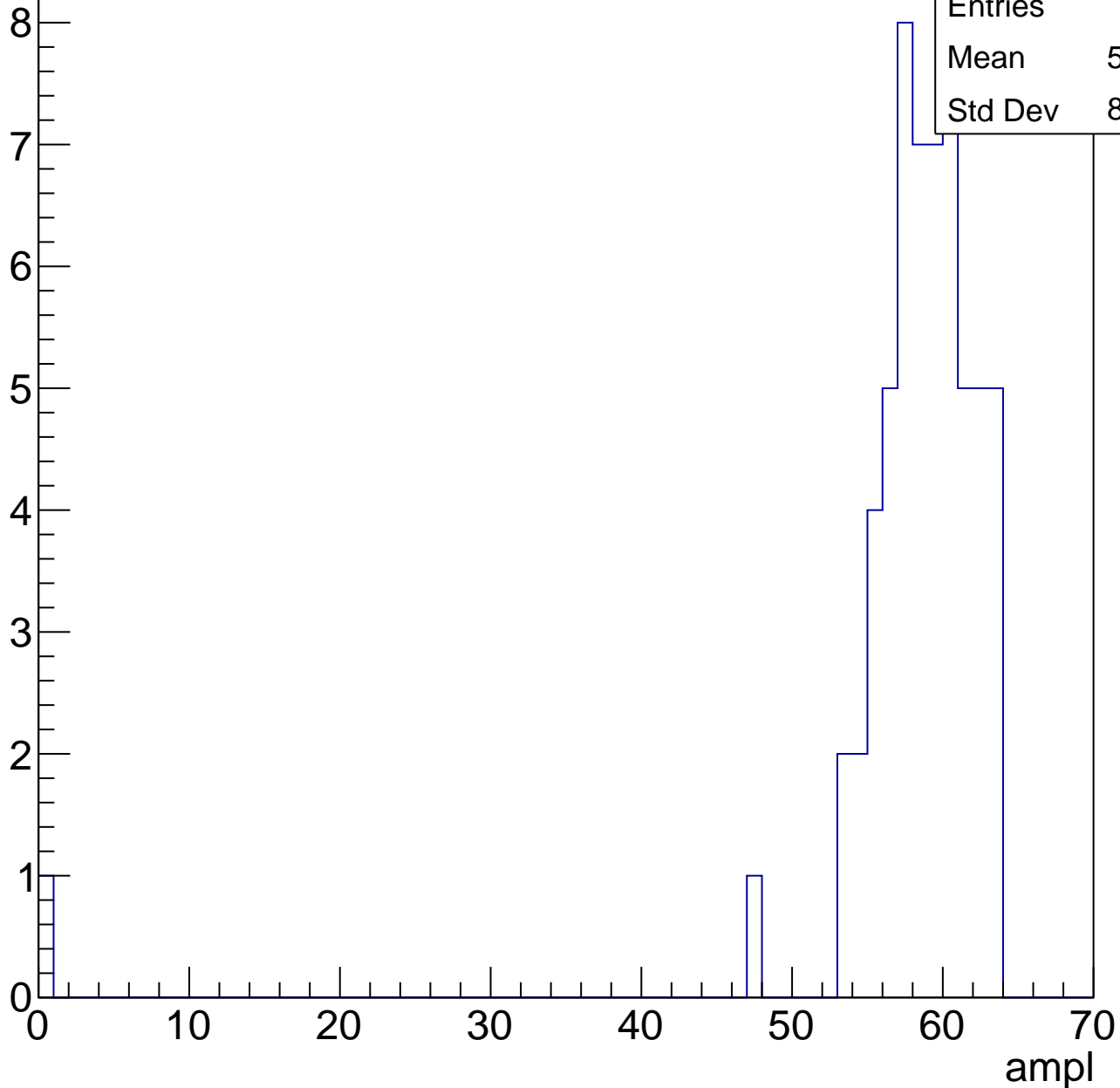
Entries	56
Mean	54.11
Std Dev	4.304



B1L102S, U18-ch122, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch122, adc6

calib_packv5_042523_0143.root, FC#11, port A2

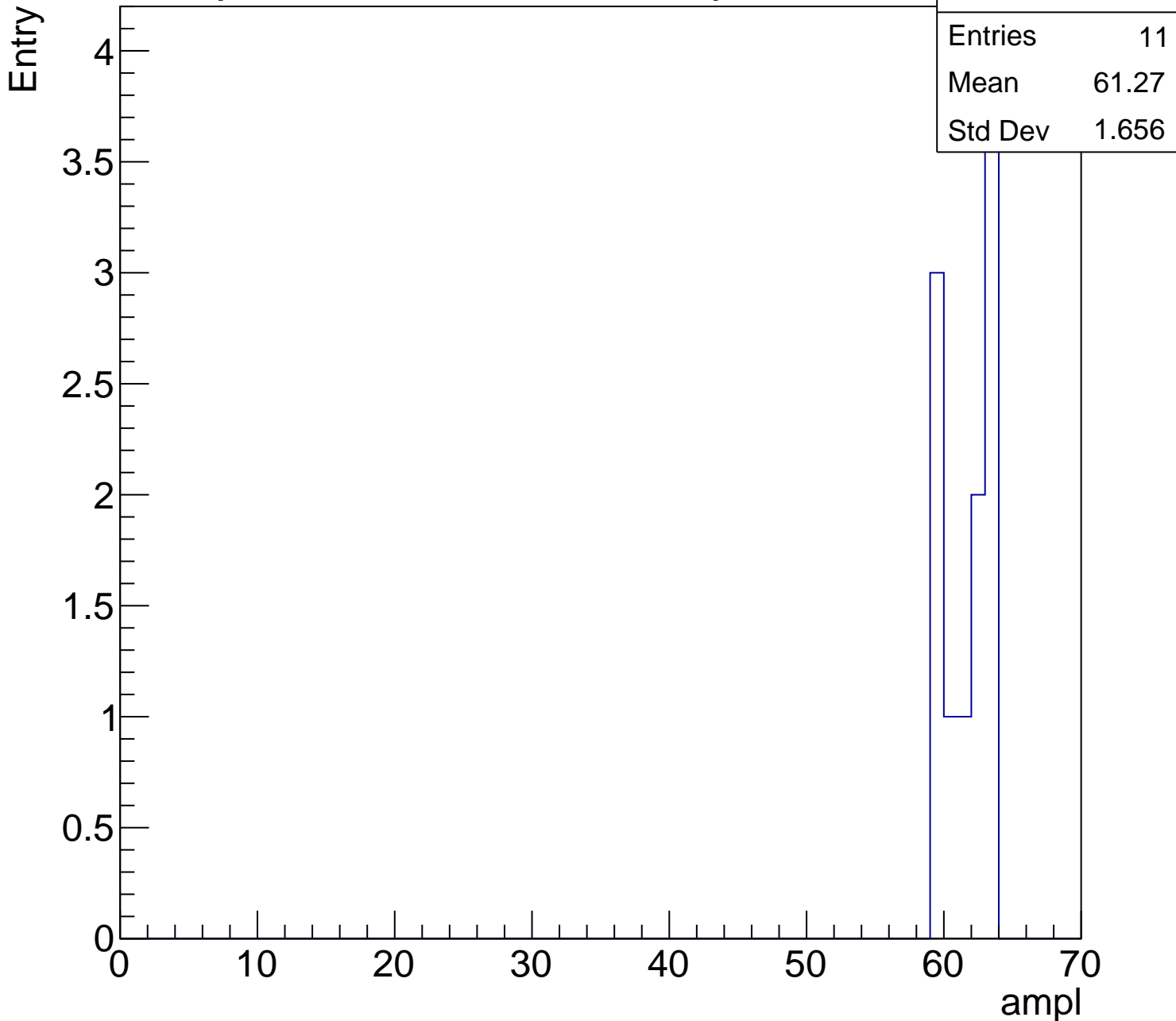
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	11
Mean	61.27
Std Dev	1.656

ampl

0 10 20 30 40 50 60 70



B1L102S, U18-ch122, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L102S, U18-ch123, adc0

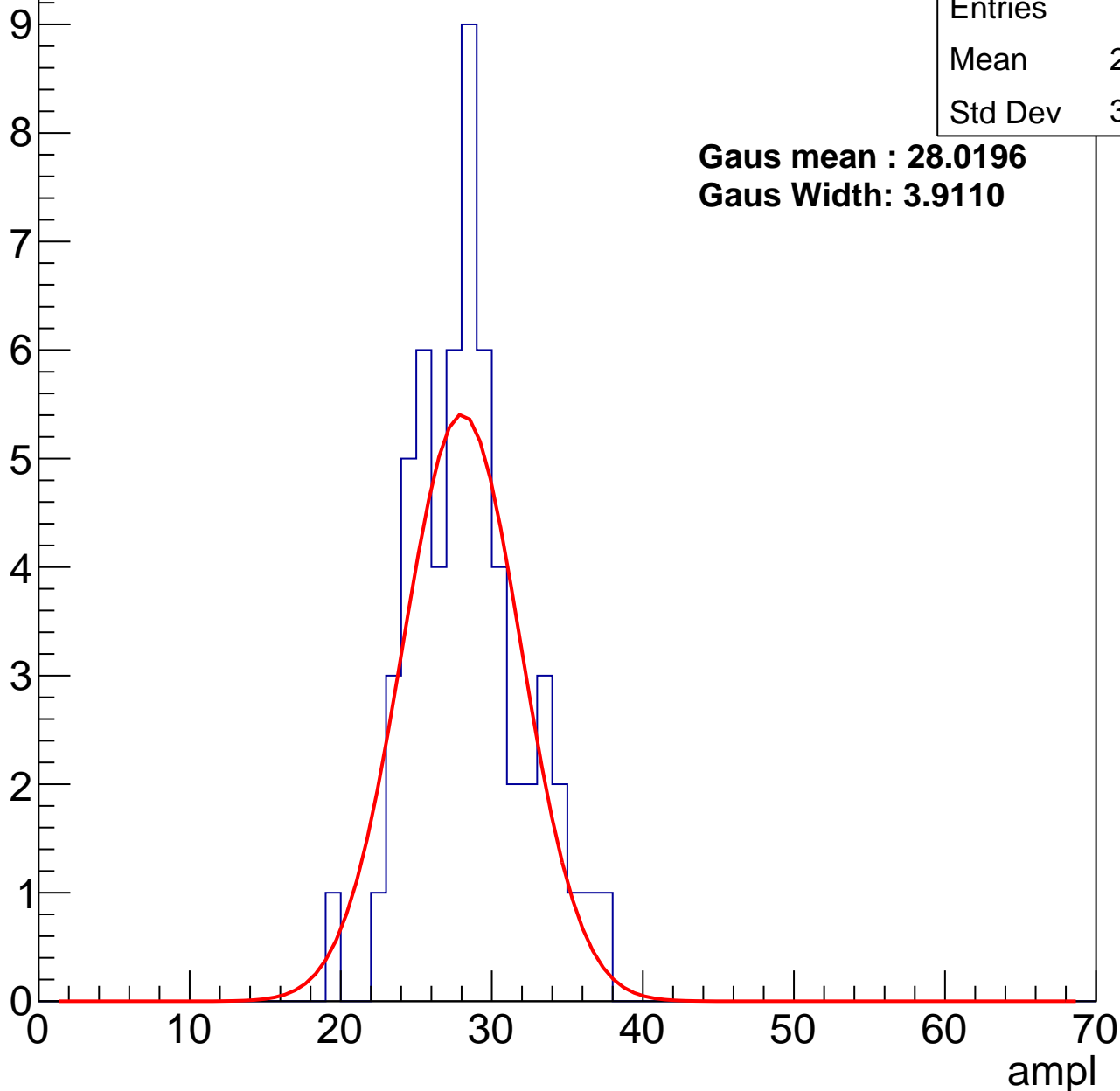
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	57
Mean	27.95
Std Dev	3.654

Gaus mean : 28.0196

Gaus Width: 3.9110



B1L102S, U18-ch123, adc1

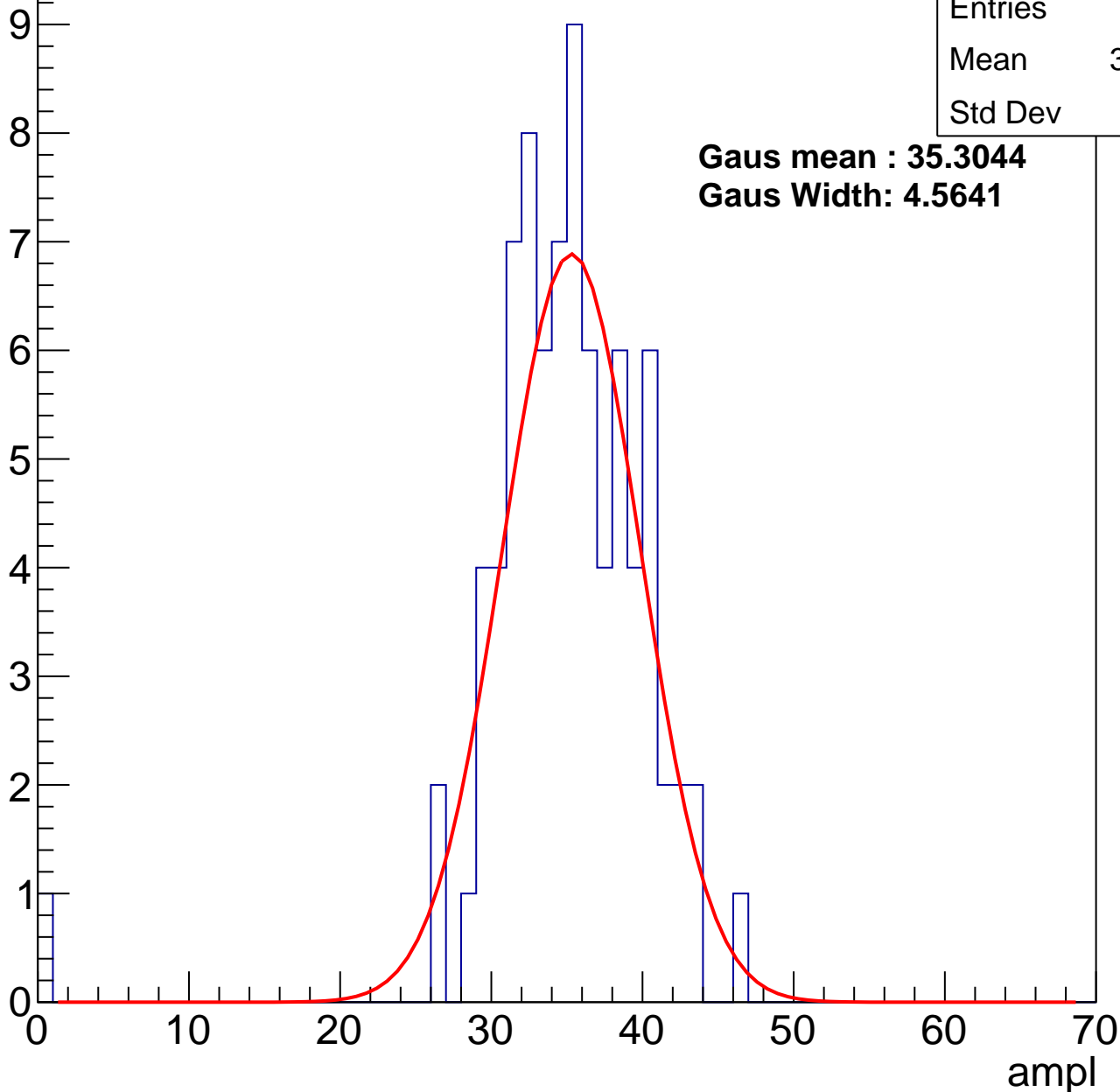
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	82
Mean	34.46
Std Dev	5.61

Gaus mean : 35.3044

Gaus Width: 4.5641



B1L102S, U18-ch123, adc2

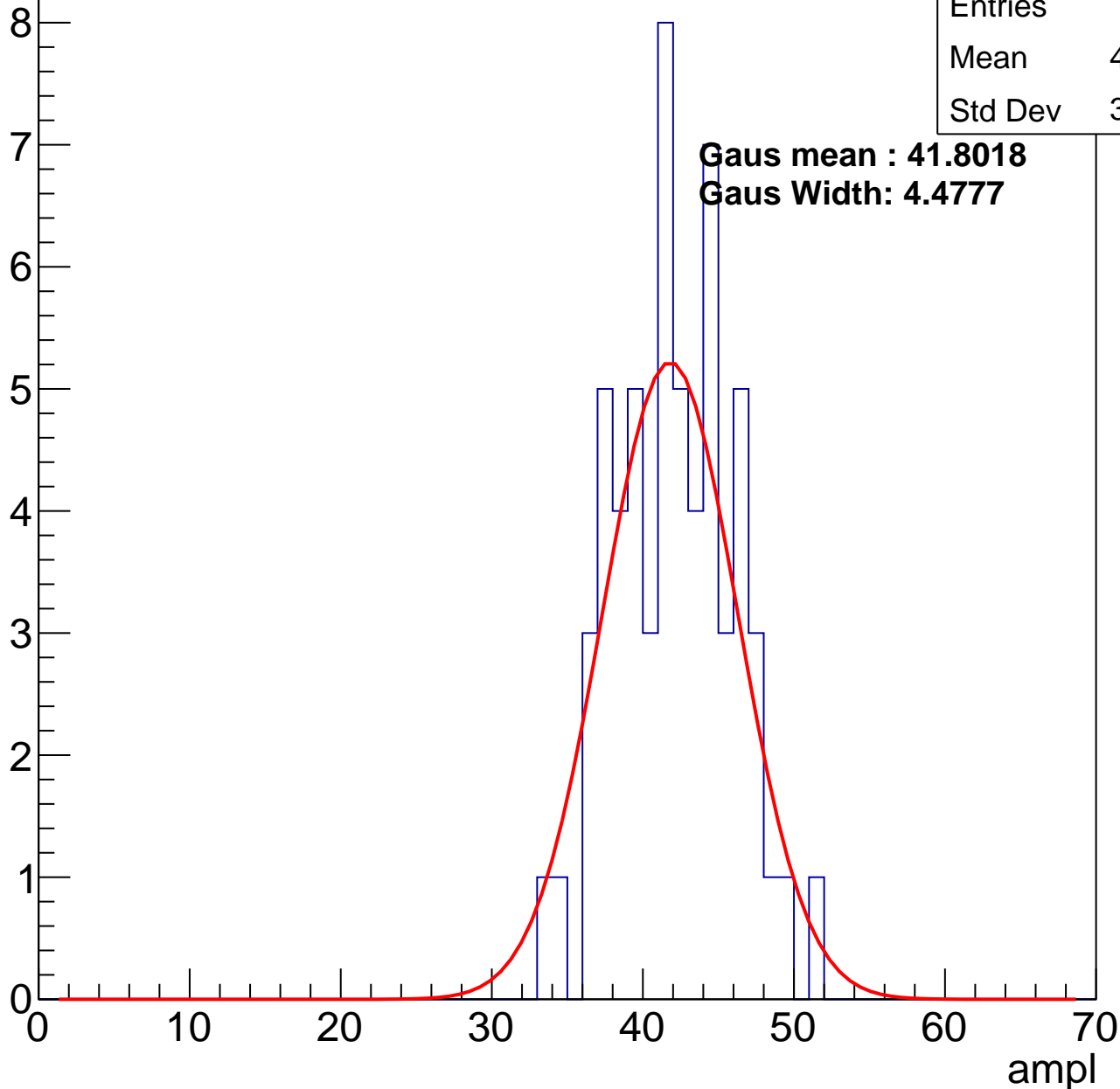
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	41.65
Std Dev	3.829

Gaus mean : 41.8018

Gaus Width: 4.4777

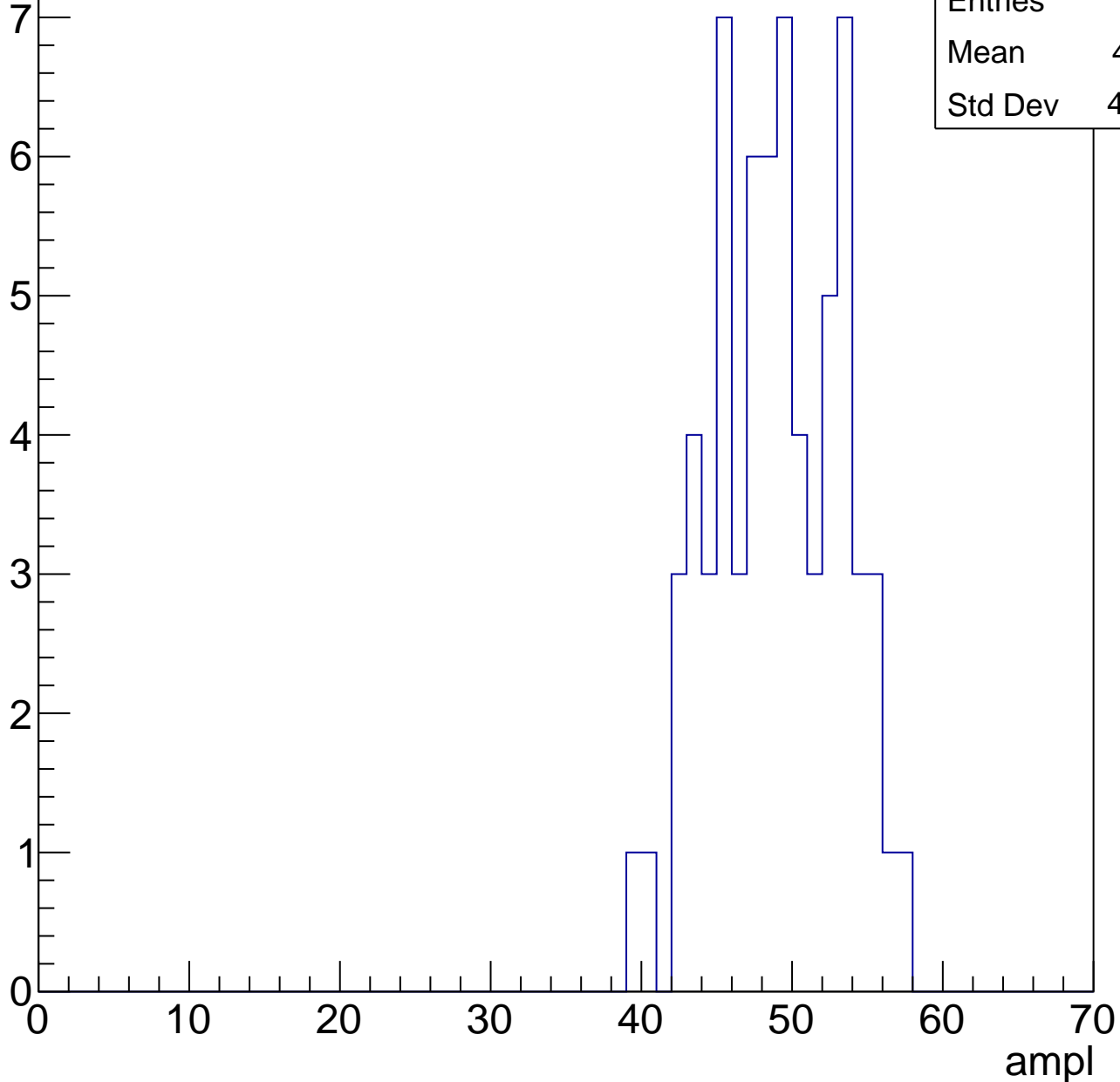


B1L102S, U18-ch123, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	68
Mean	48.51
Std Dev	4.146

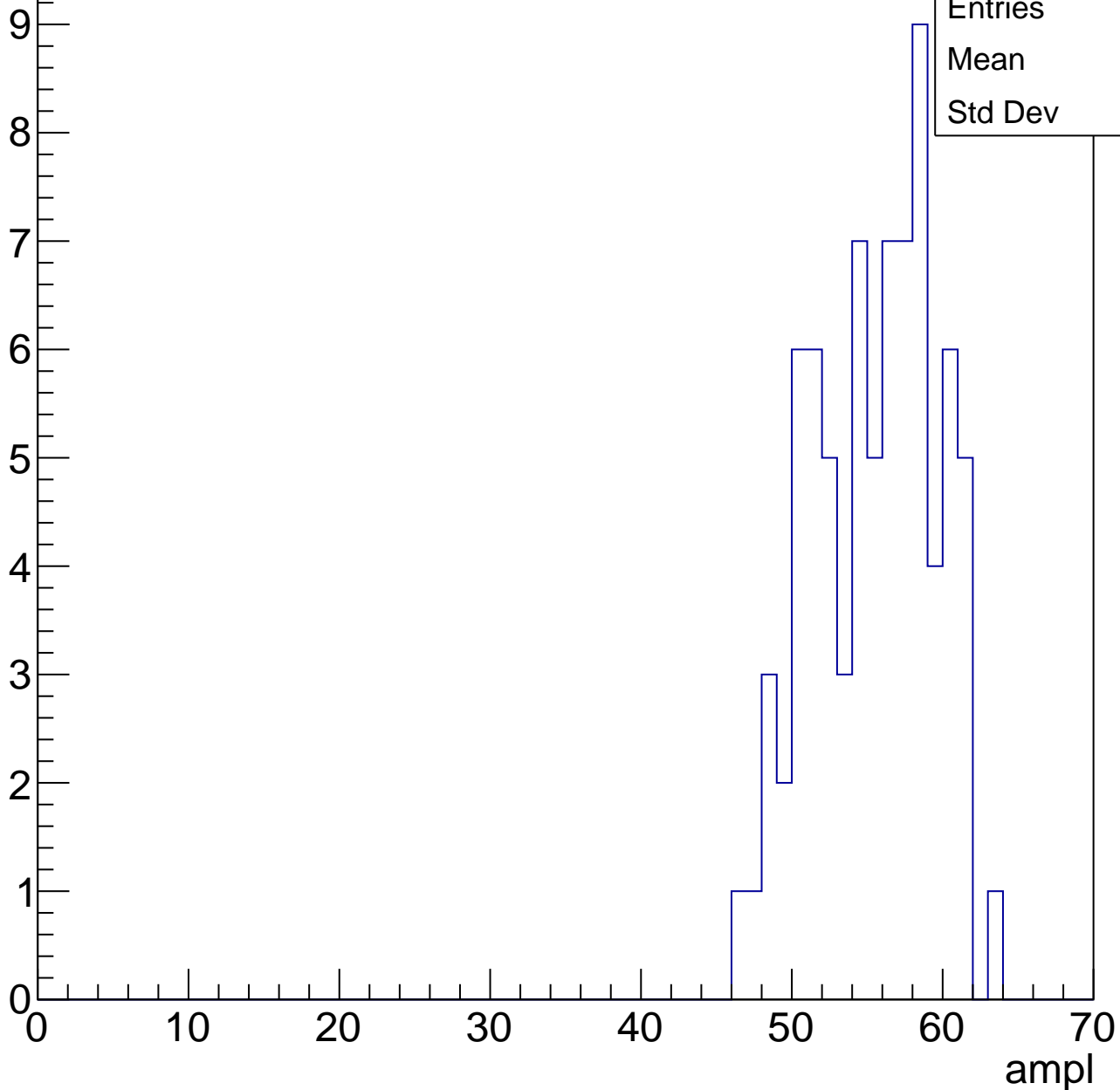


B1L102S, U18-ch123, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	55
Std Dev	4

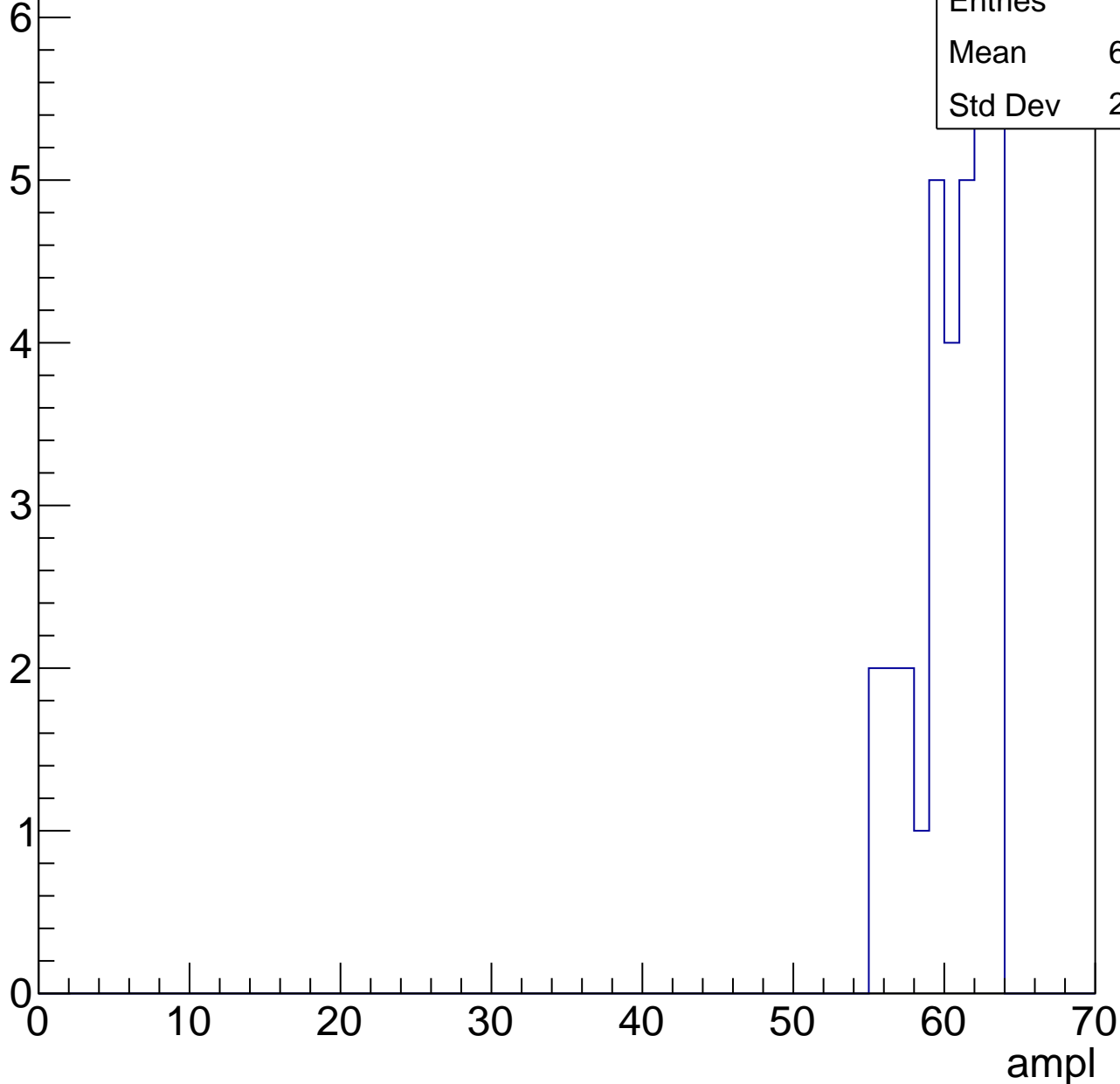


B1L102S, U18-ch123, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

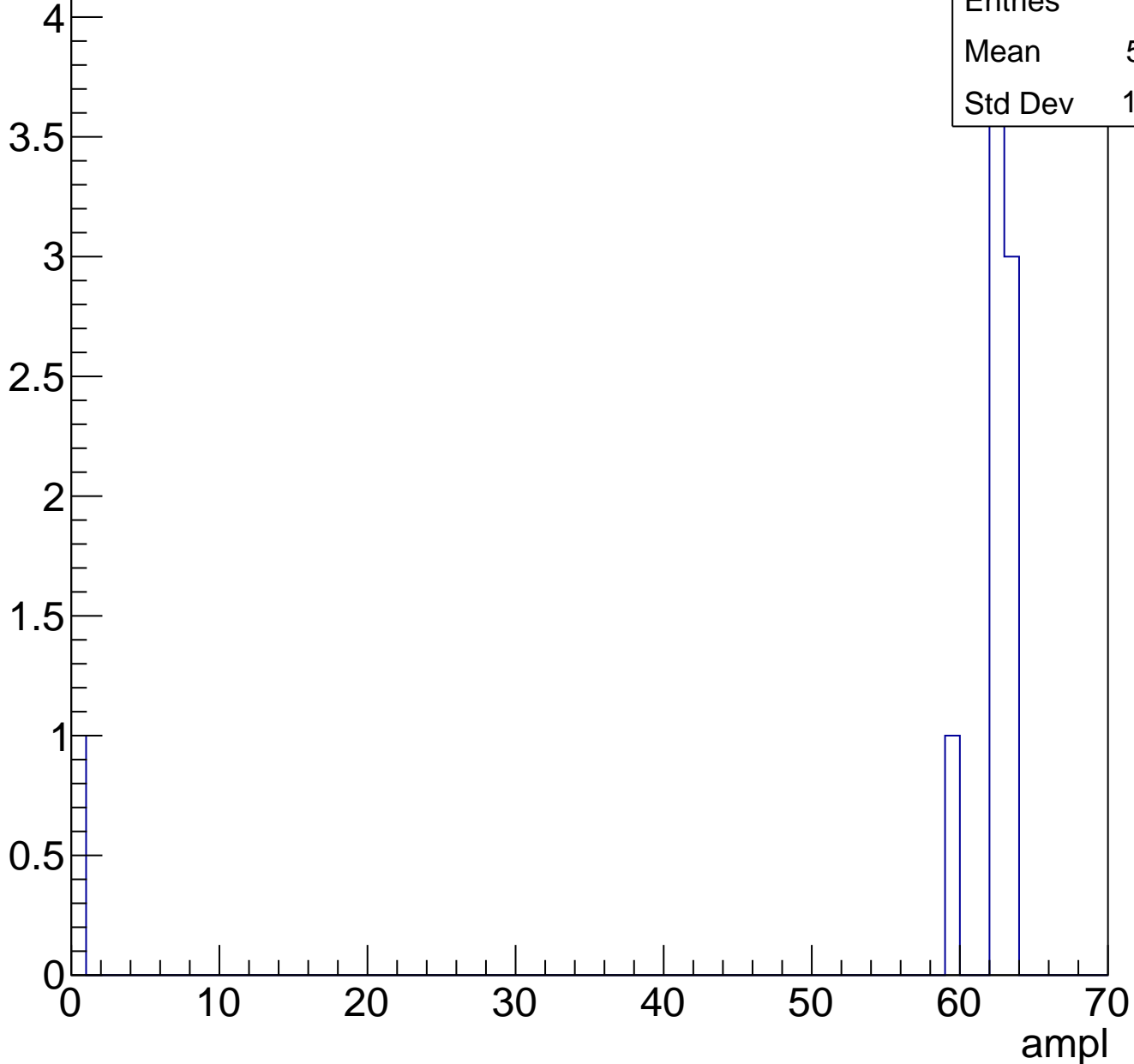
Entries	33
Mean	60.12
Std Dev	2.409



B1L102S, U18-ch123, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch123, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L102S, U18-ch124, adc0

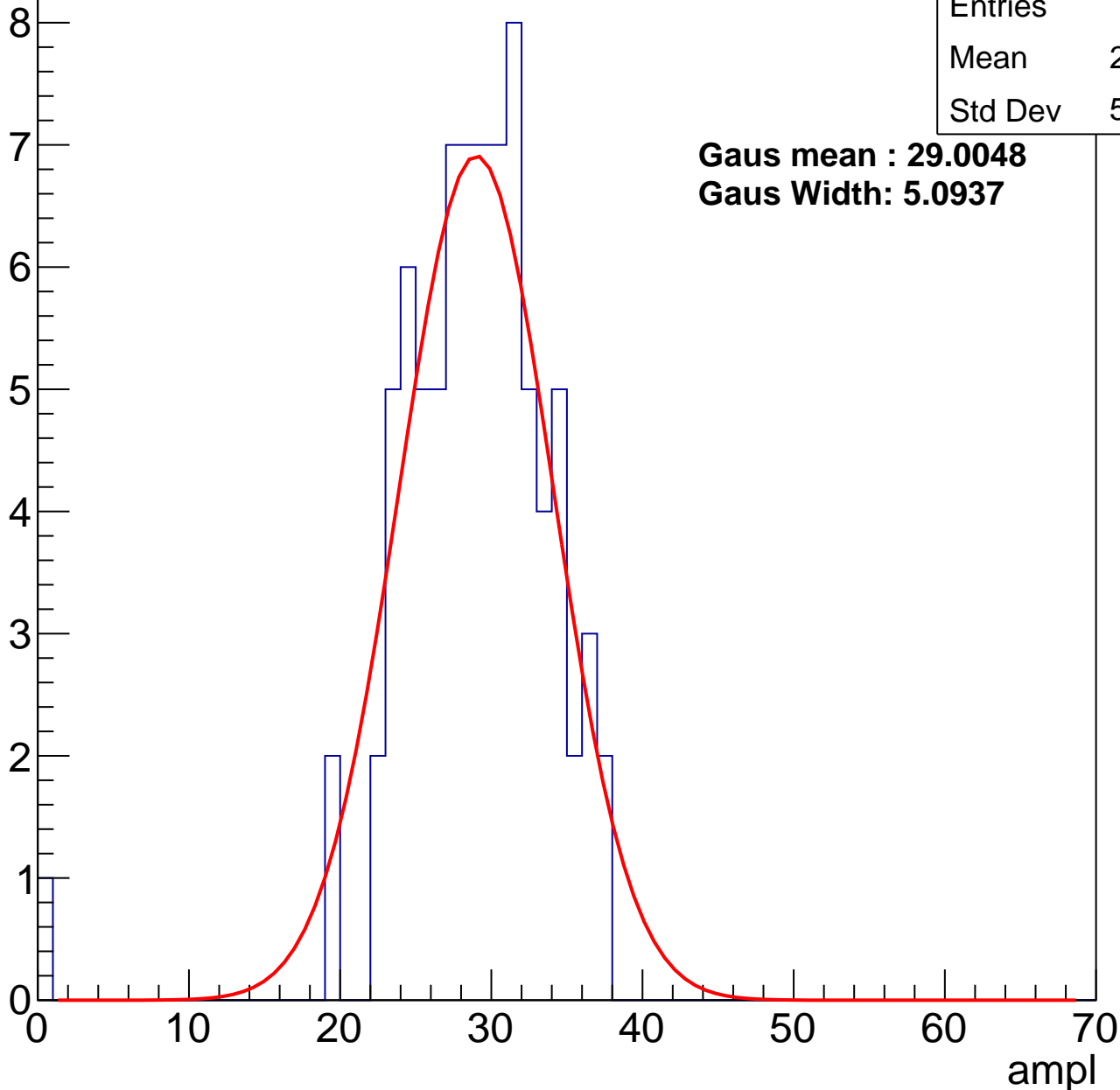
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	28.39
Std Dev	5.169

Gaus mean : 29.0048

Gaus Width: 5.0937



B1L102S, U18-ch124, adc1

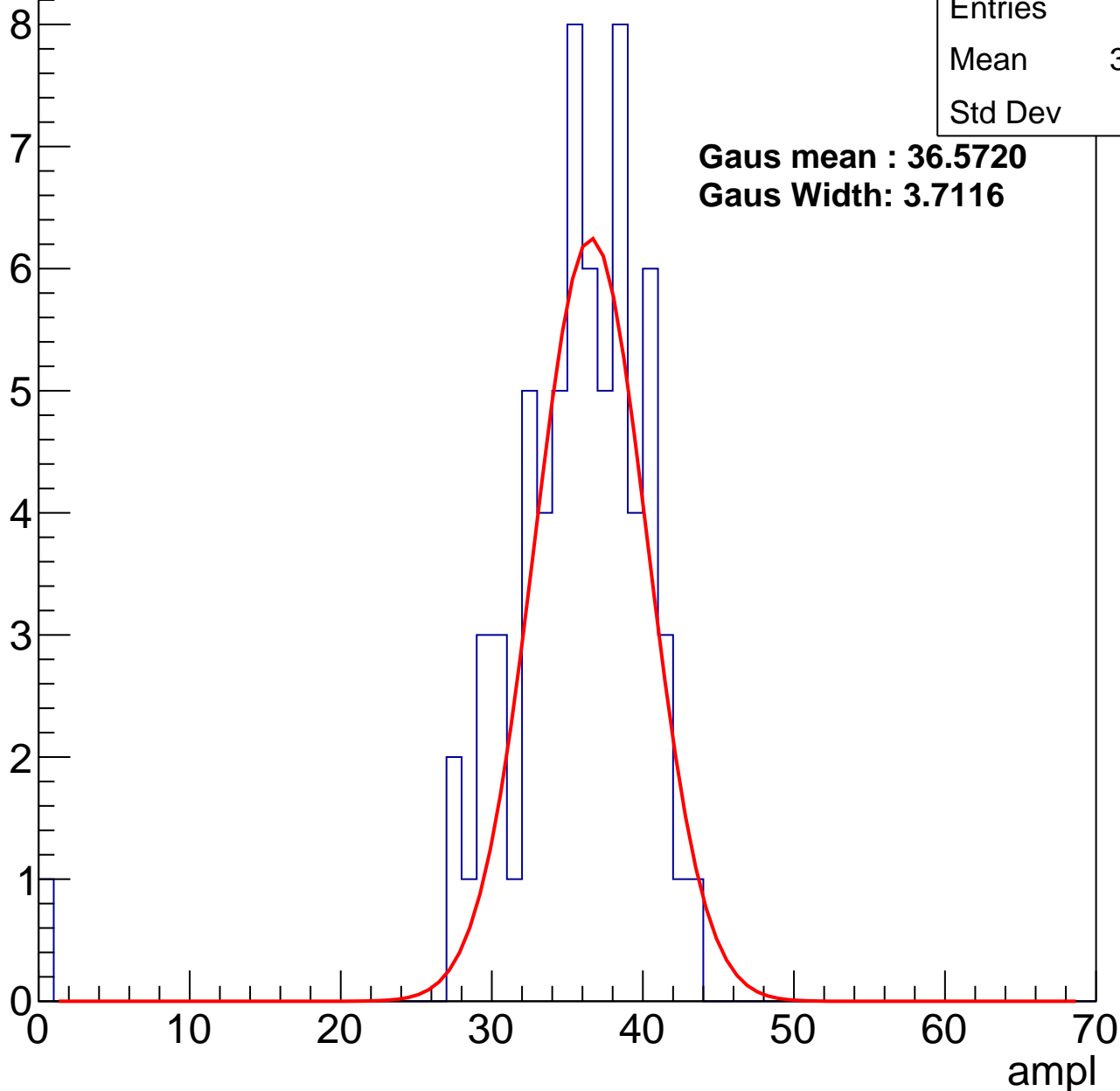
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	67
Mean	34.94
Std Dev	5.73

Gaus mean : 36.5720

Gaus Width: 3.7116



B1L102S, U18-ch124, adc2

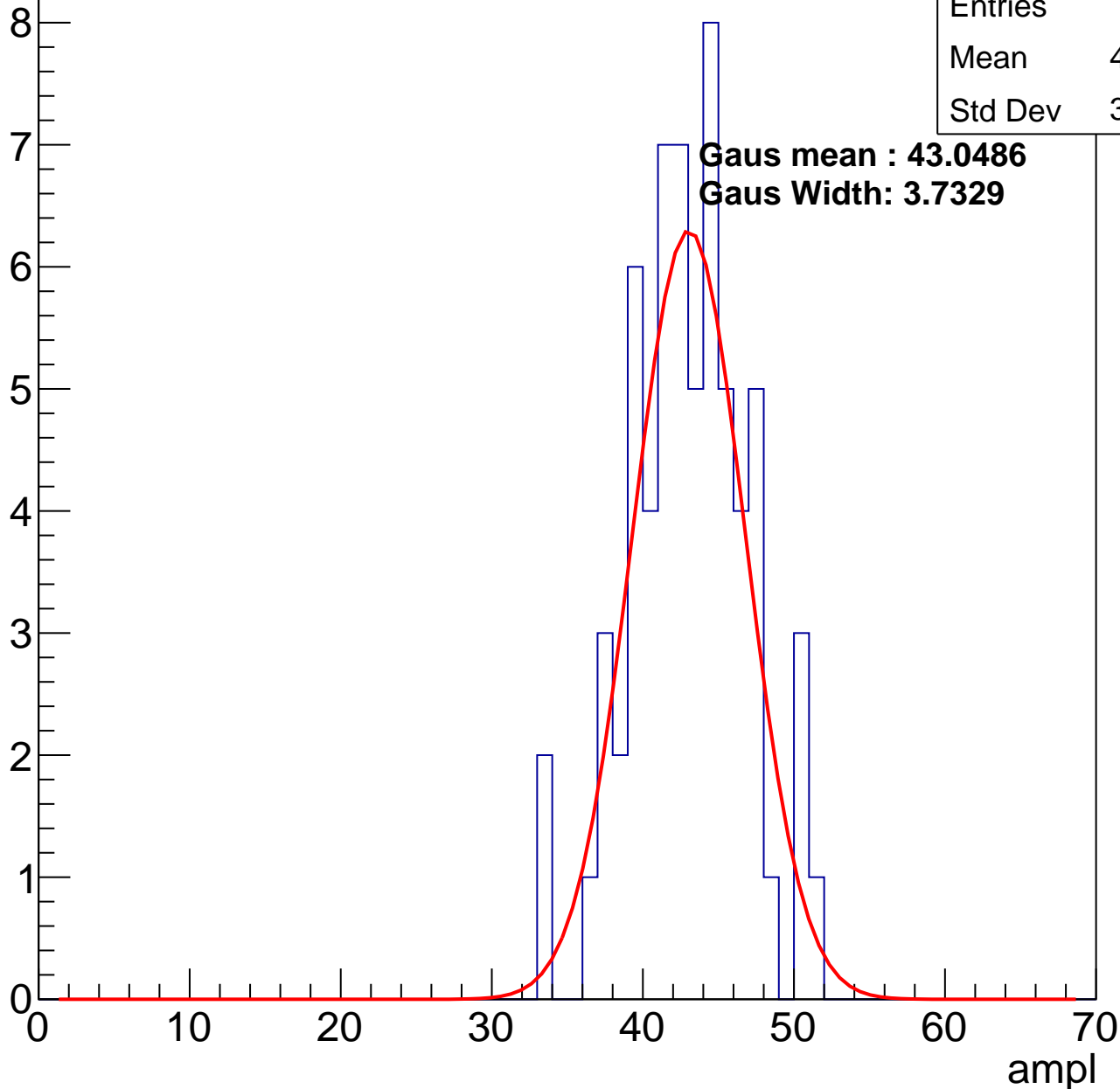
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	64
Mean	42.56
Std Dev	3.836

Gaus mean : 43.0486

Gaus Width: 3.7329

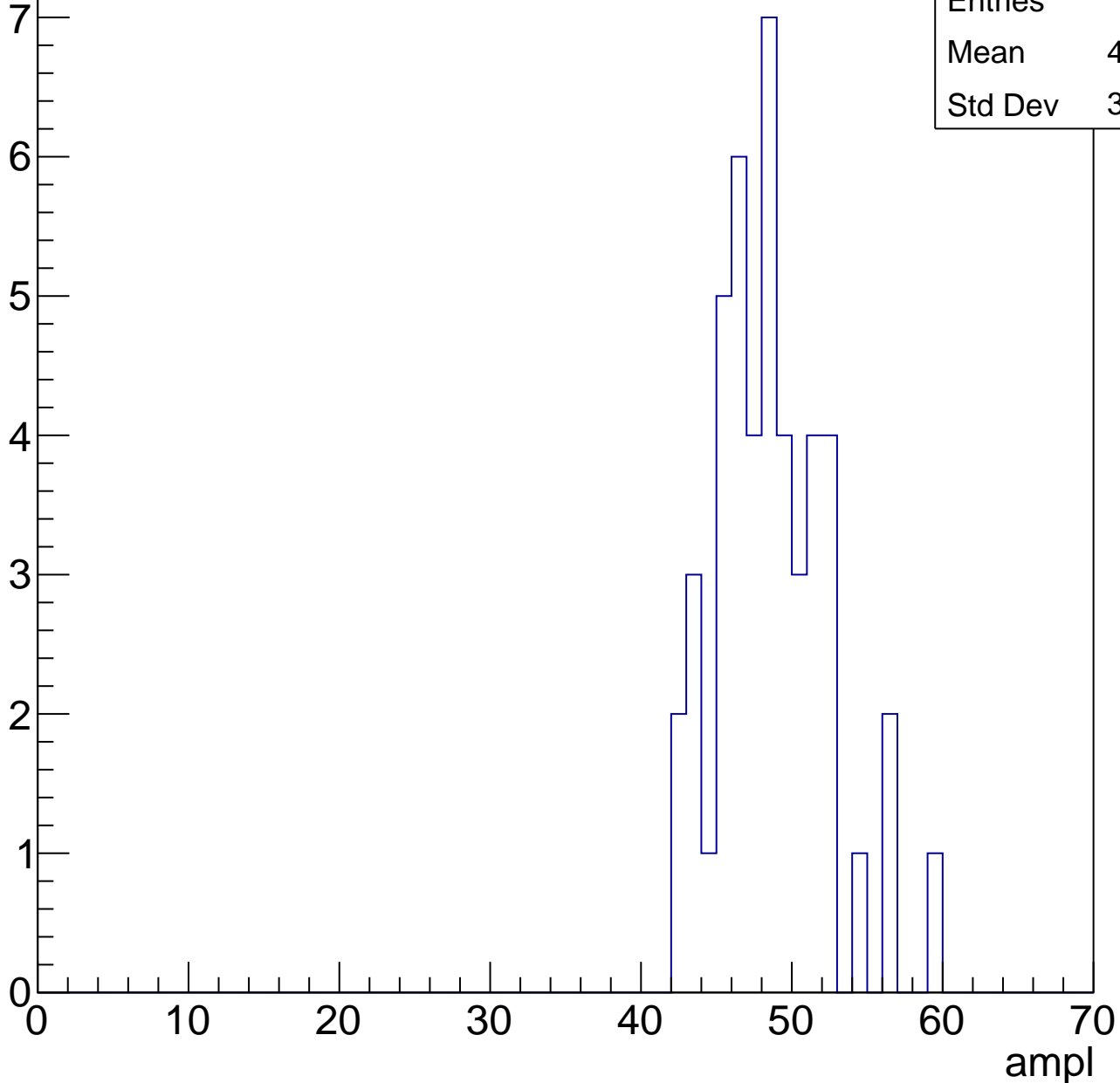


B1L102S, U18-ch124, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	47
Mean	48.19
Std Dev	3.676

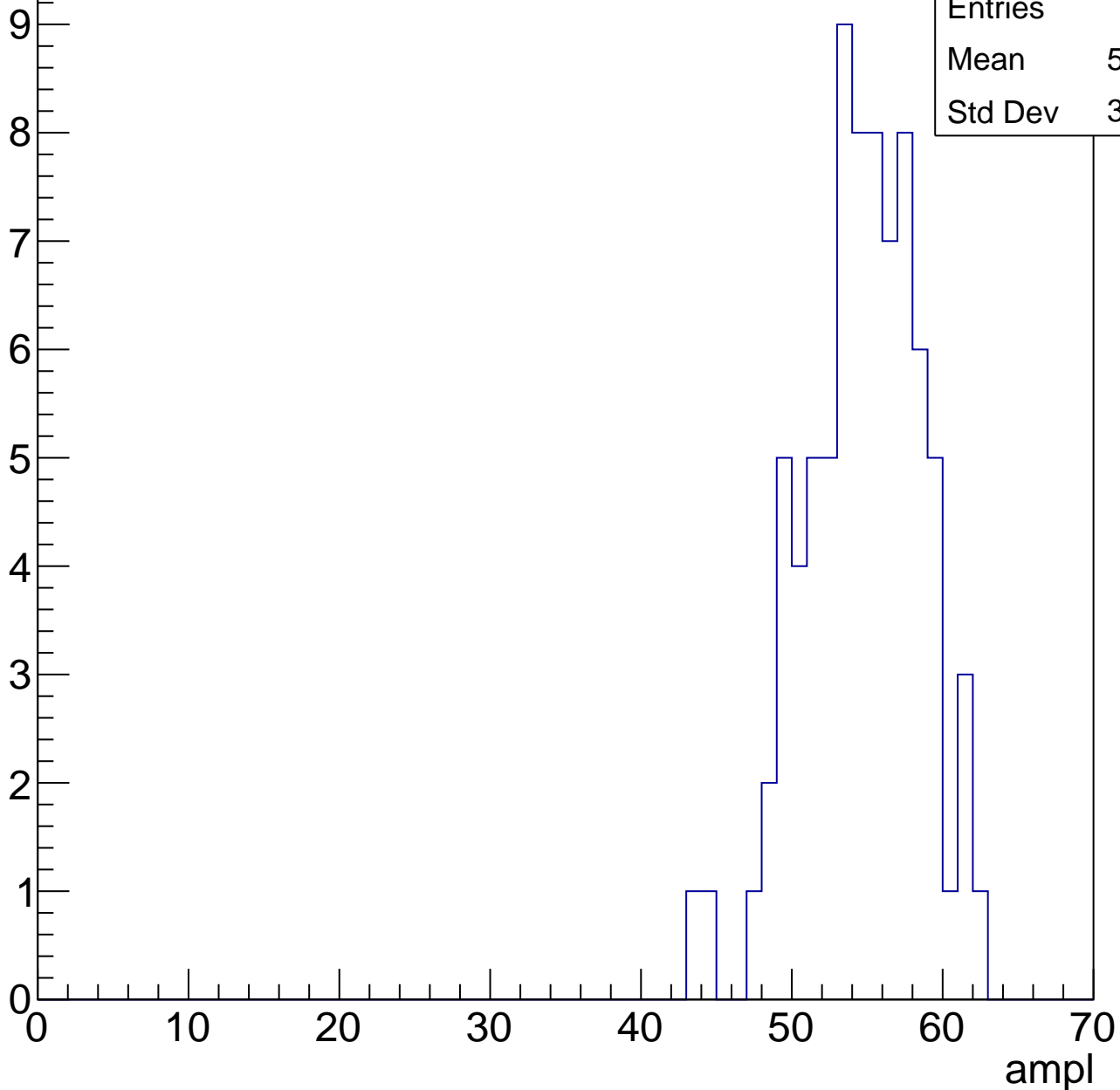


B1L102S, U18-ch124, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

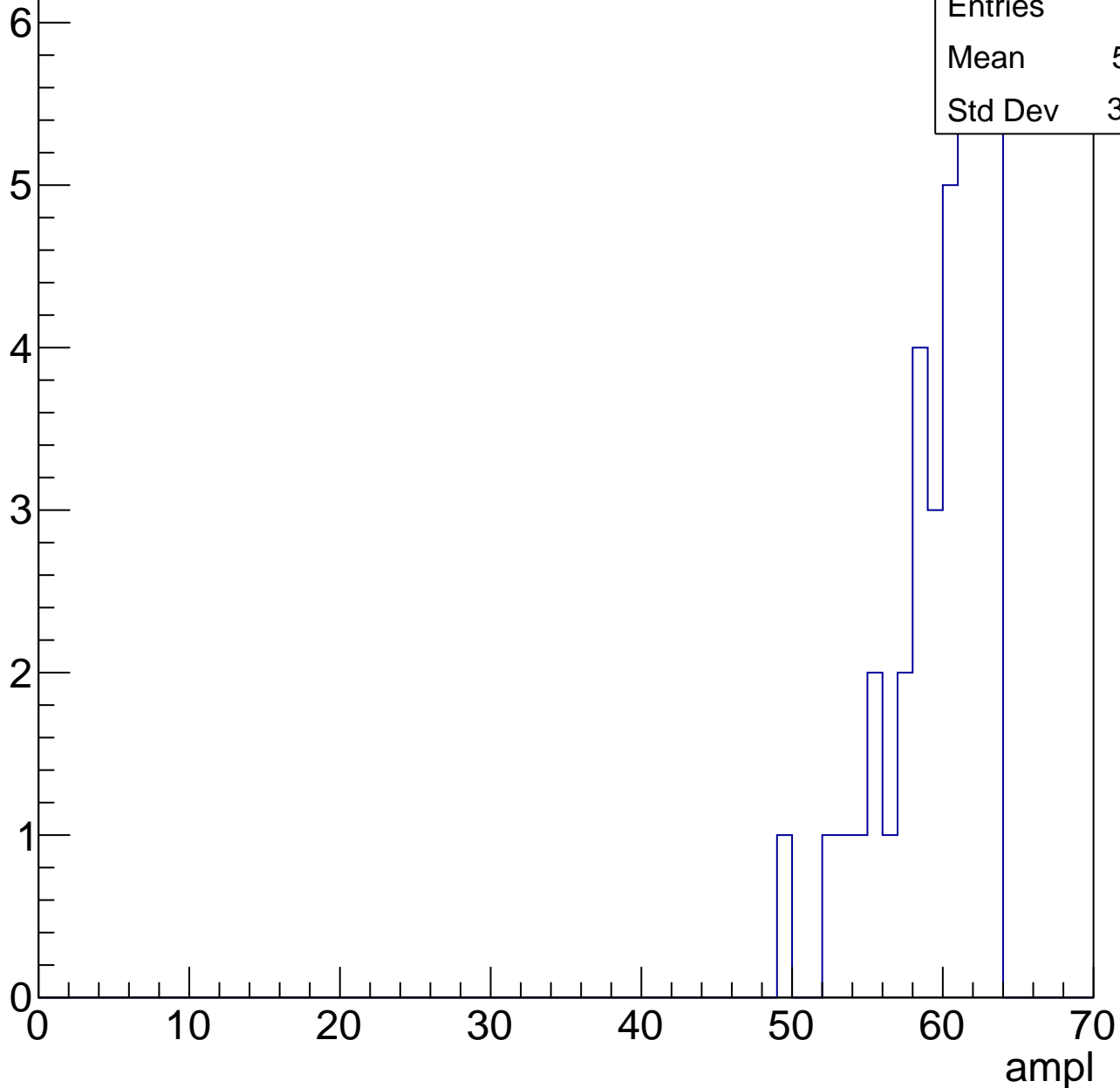
Entries	80
Mean	54.19
Std Dev	3.838



B1L102S, U18-ch124, adc5

calib_packv5_042523_0143.root, FC#11, port A2

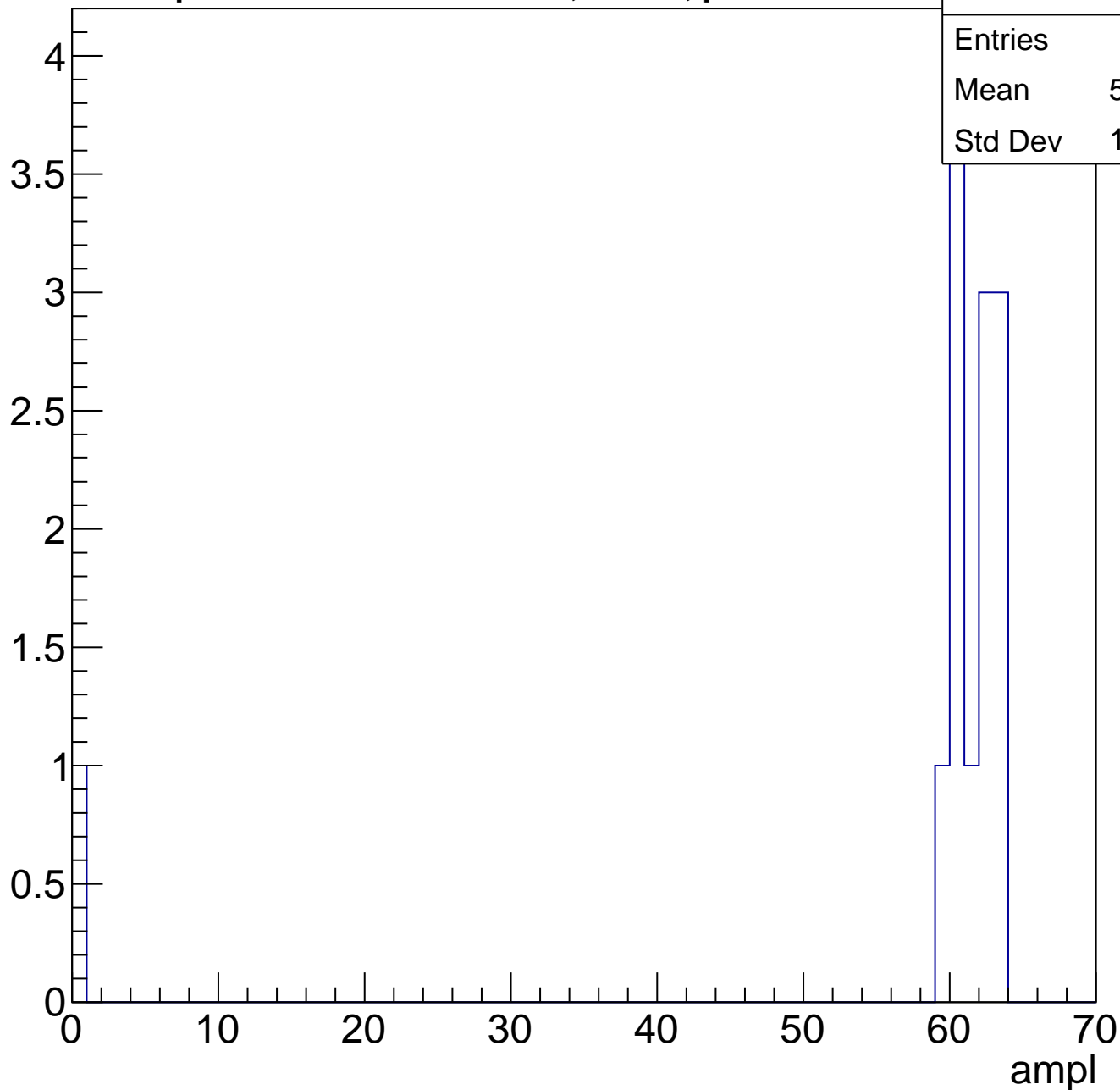
Entry



B1L102S, U18-ch124, adc6

calib_packv5_042523_0143.root, FC#11, port A2

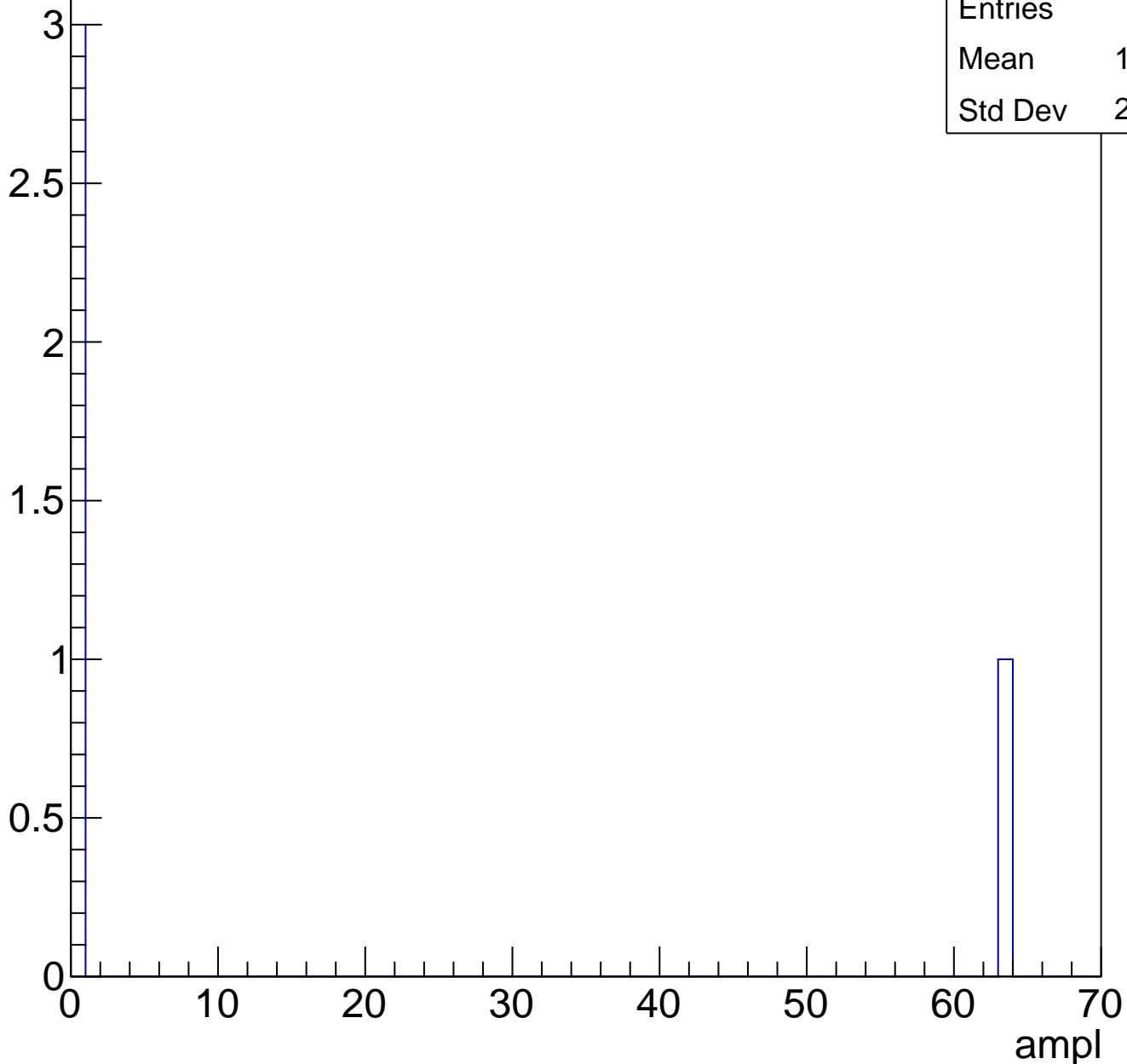
Entry



B1L102S, U18-ch124, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch125, adc0

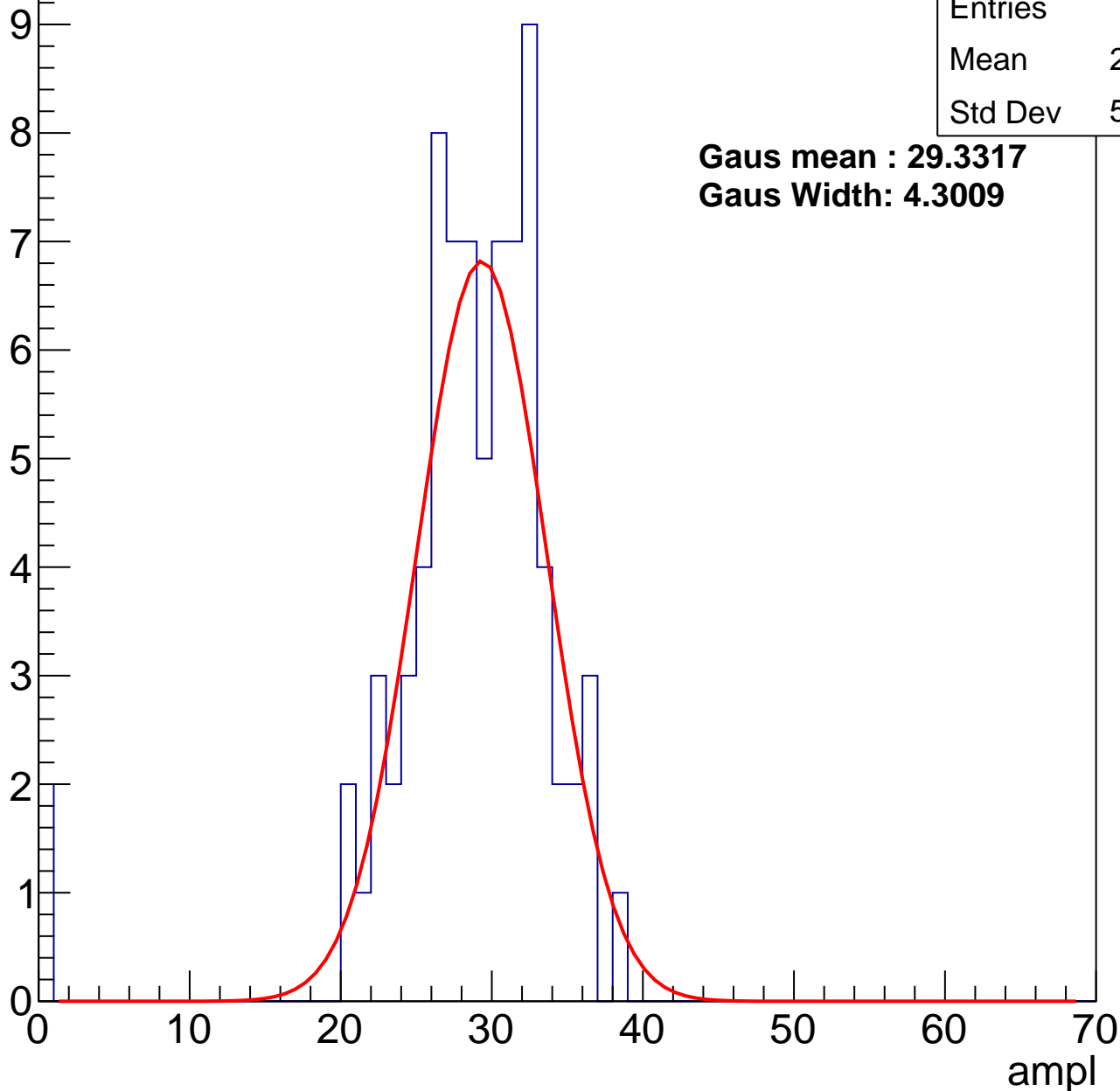
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	79
Mean	28.03
Std Dev	5.983

Gaus mean : 29.3317

Gaus Width: 4.3009



B1L102S, U18-ch125, adc1

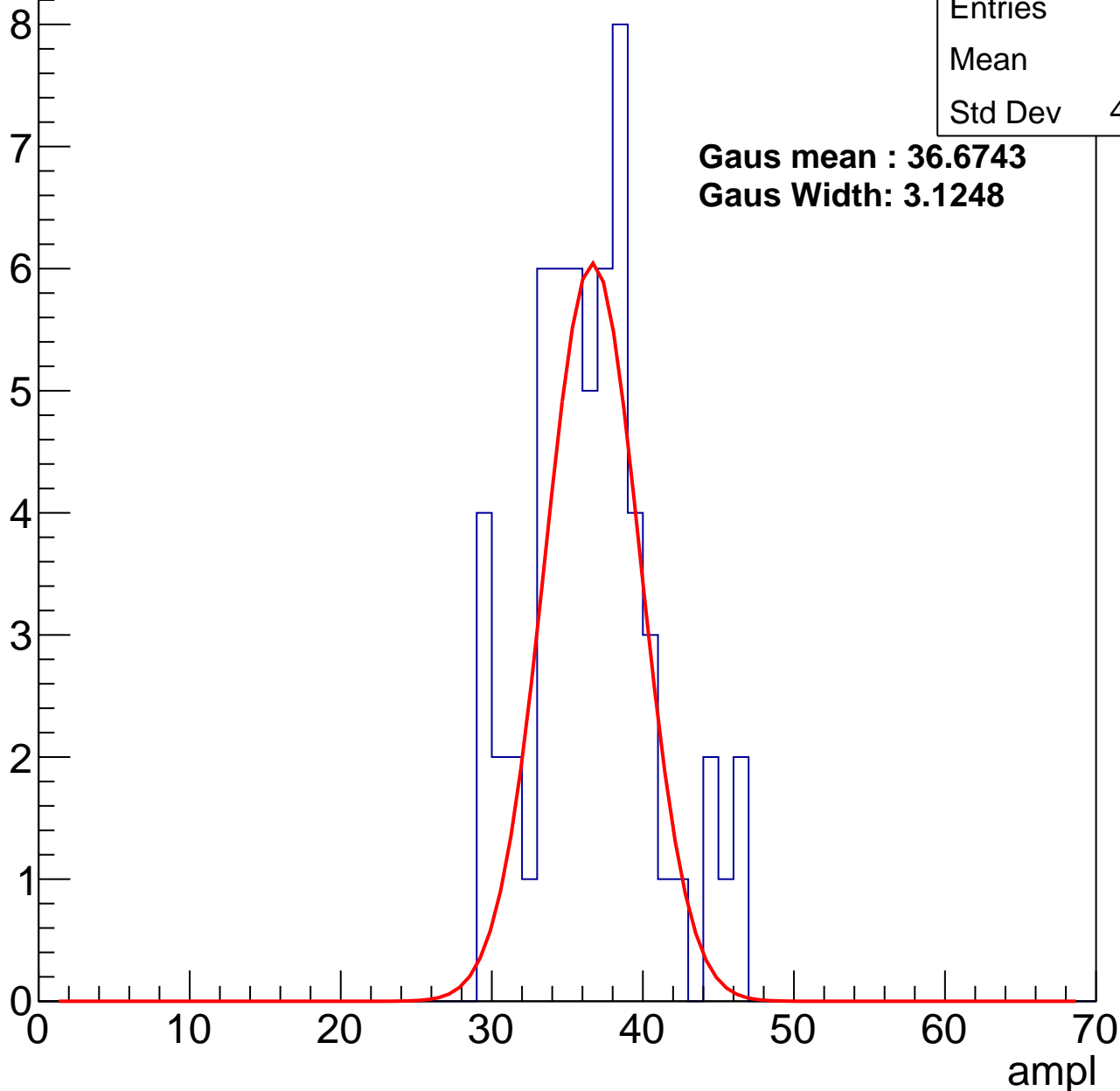
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.2
Std Dev	4.098

Gaus mean : 36.6743

Gaus Width: 3.1248



B1L102S, U18-ch125, adc2

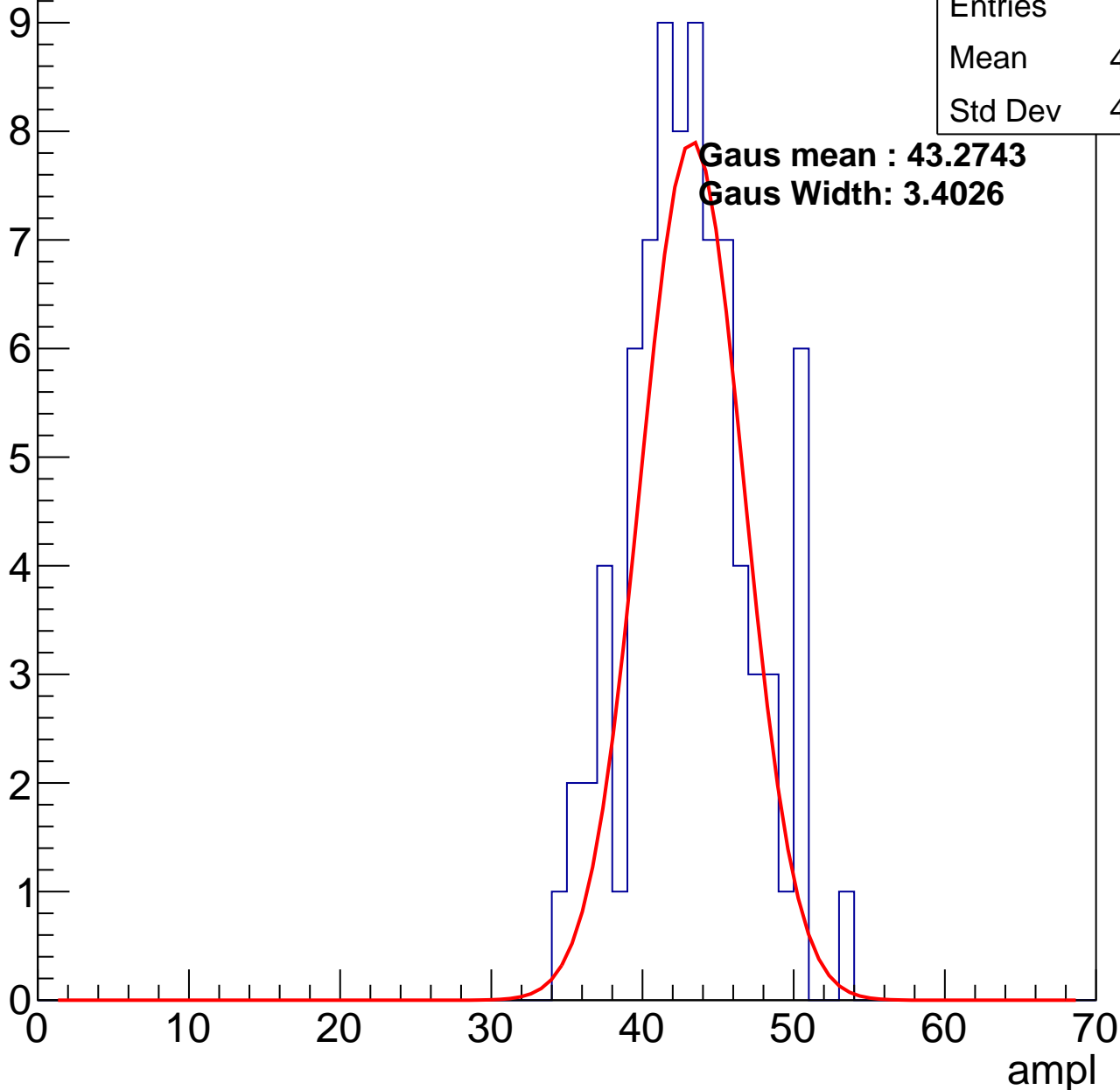
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	81
Mean	42.74
Std Dev	4.002

Gaus mean : 43.2743

Gaus Width: 3.4026

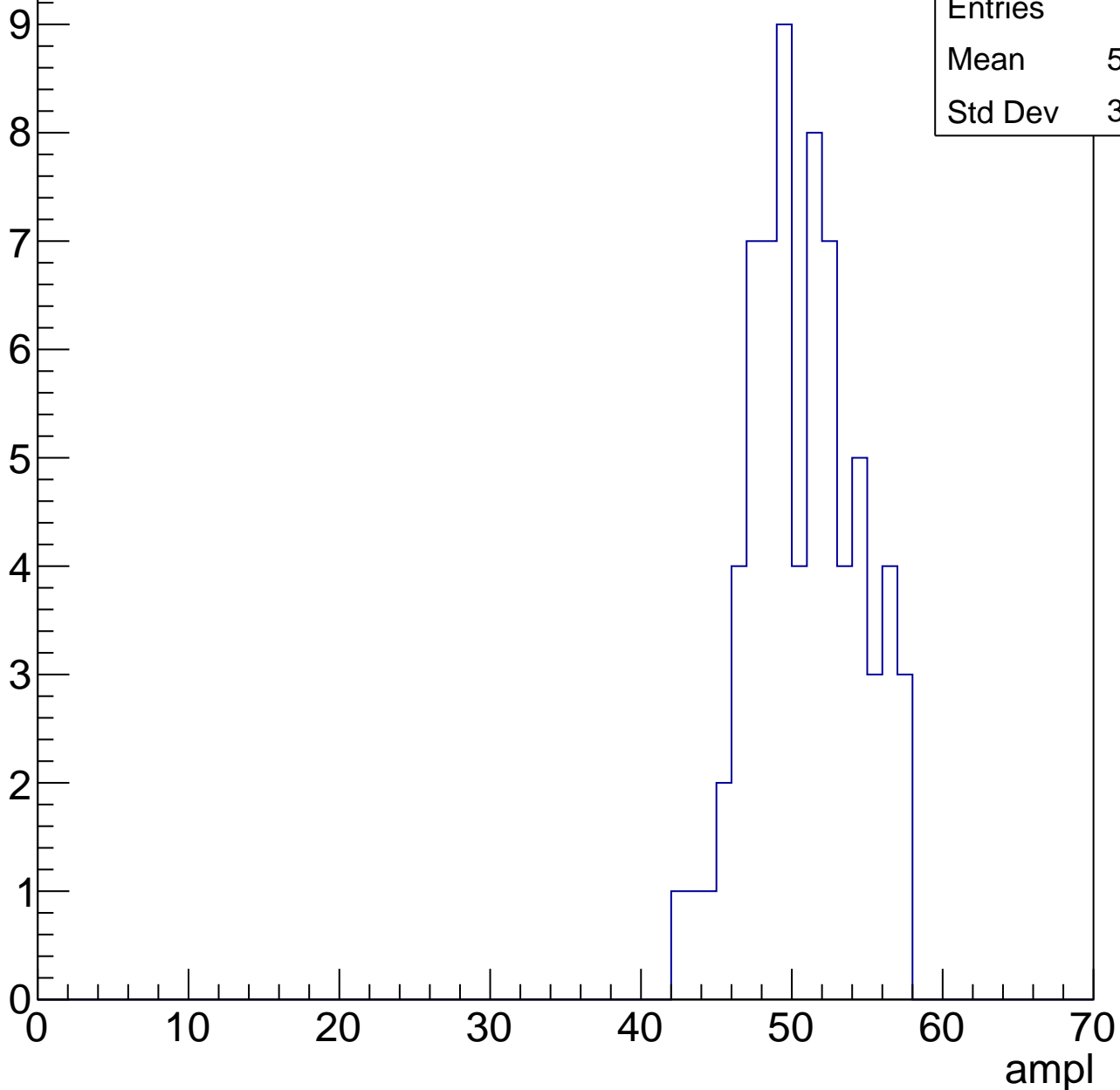


B1L102S, U18-ch125, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	50.33
Std Dev	3.532

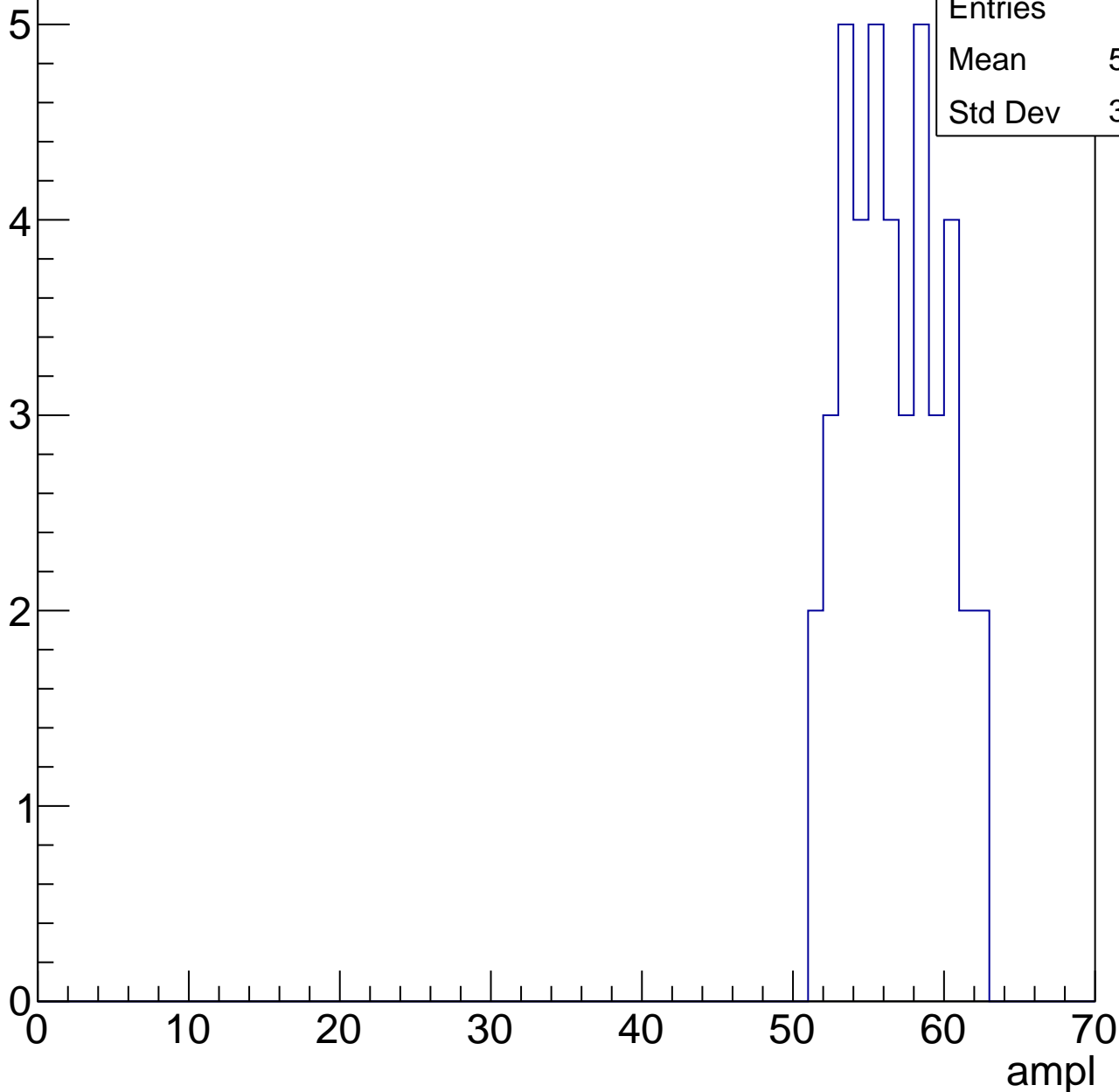


B1L102S, U18-ch125, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

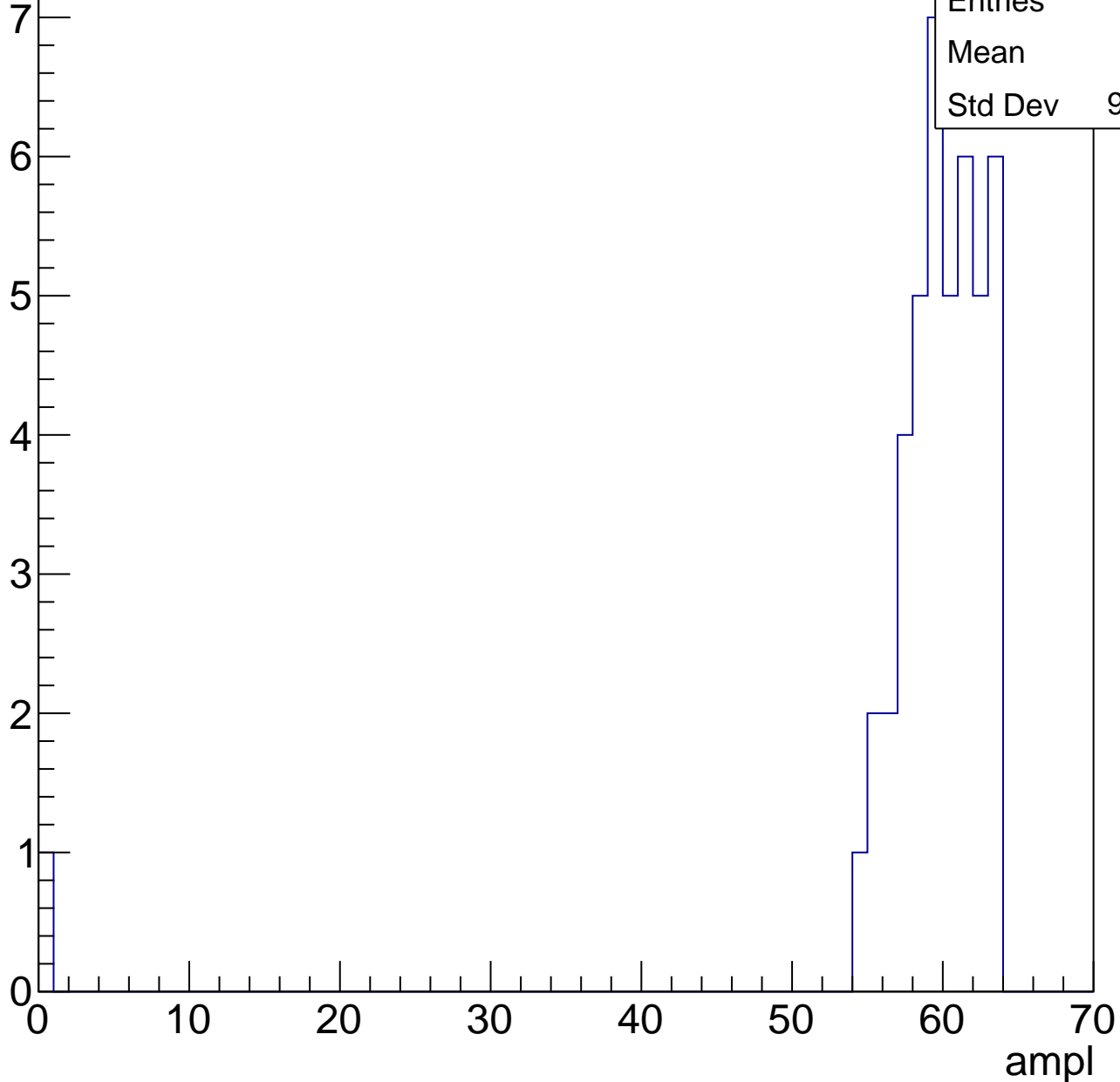
Entries	42
Mean	56.24
Std Dev	3.077



B1L102S, U18-ch125, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch125, adc6

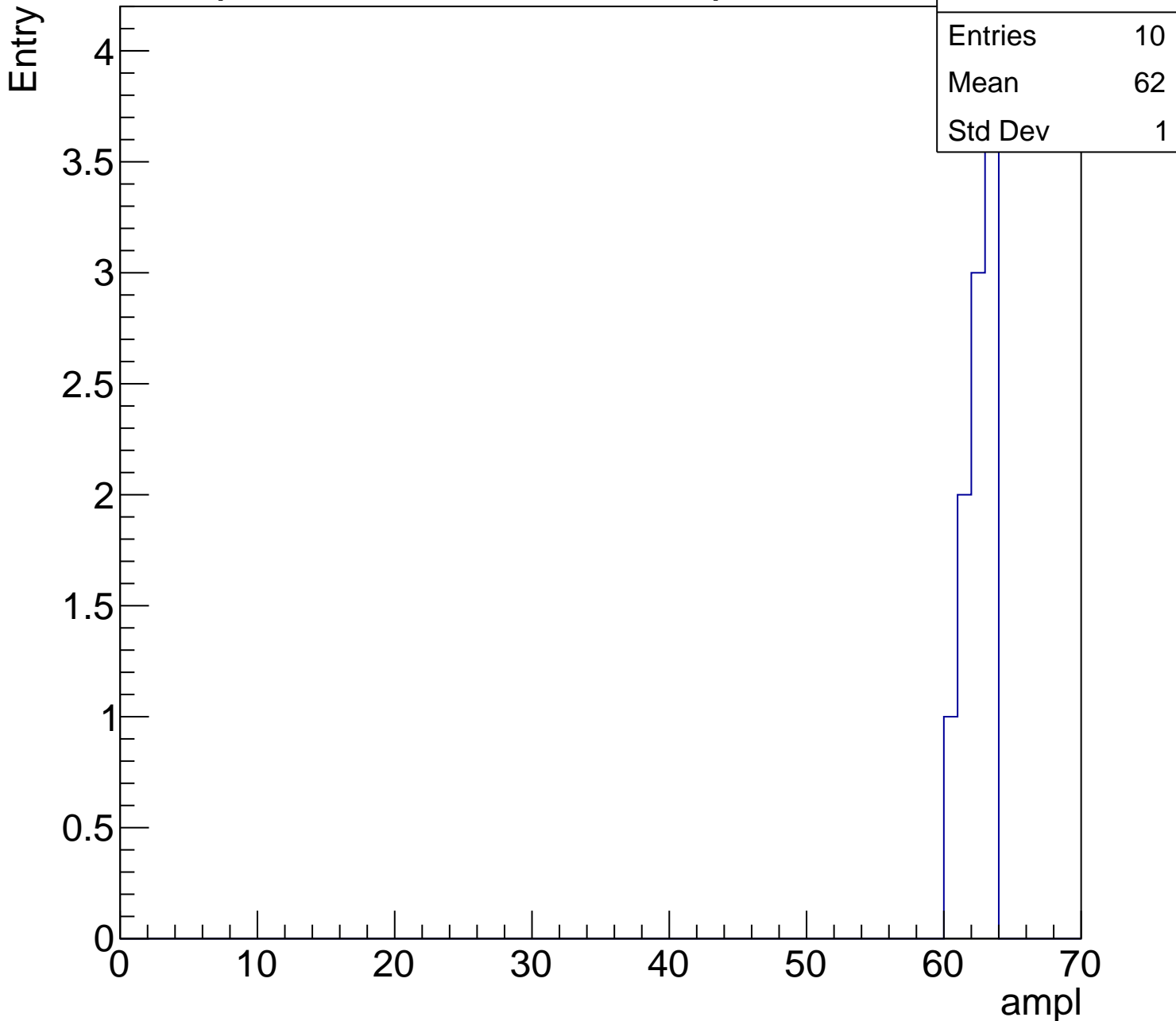
calib_packv5_042523_0143.root, FC#11, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	62
Std Dev	1

ampl



B1L102S, U18-ch125, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L102S, U18-ch126, adc0

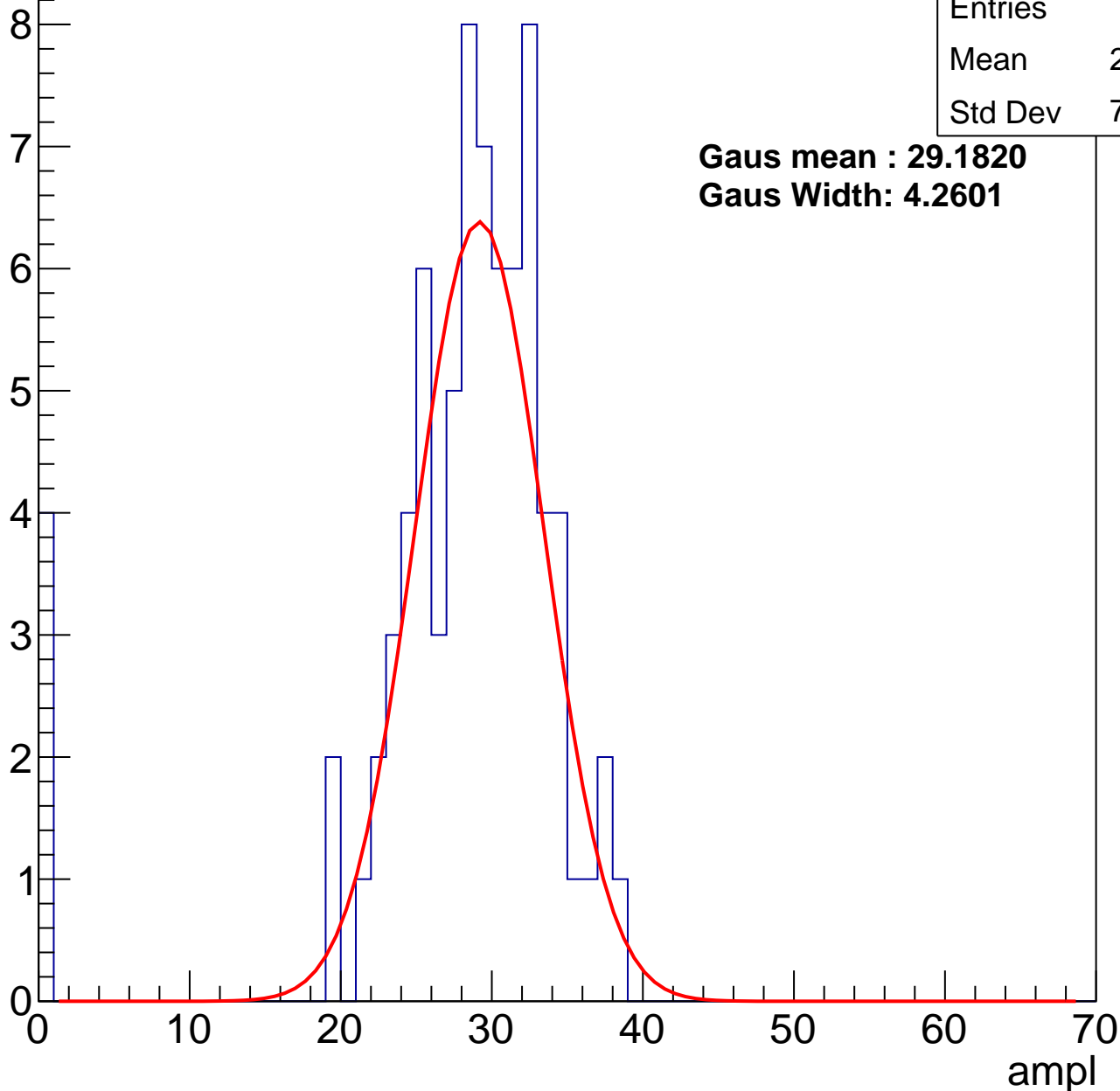
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	78
Mean	27.32
Std Dev	7.532

Gaus mean : 29.1820

Gaus Width: 4.2601



B1L102S, U18-ch126, adc1

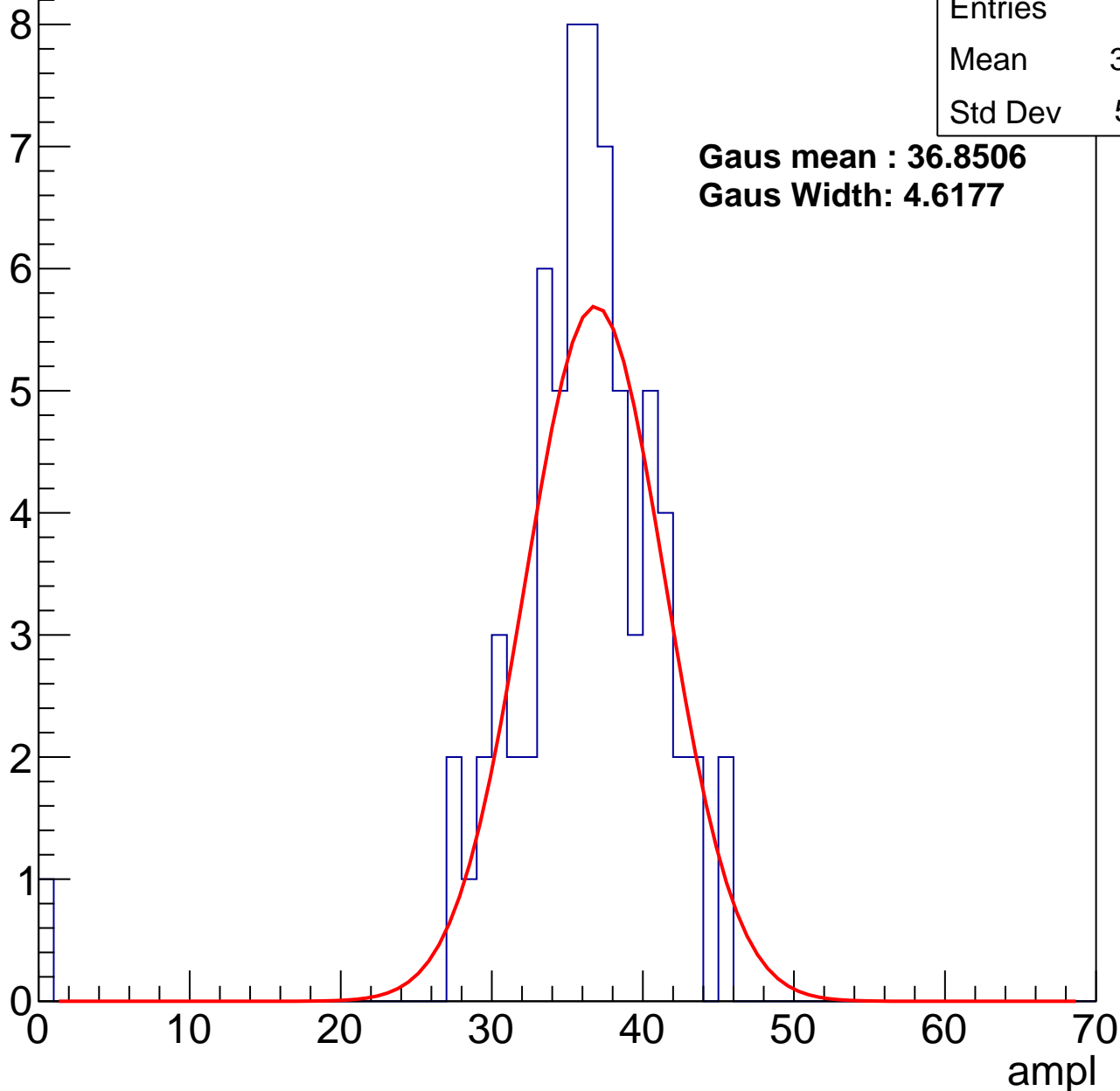
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	35.46
Std Dev	5.901

Gaus mean : 36.8506

Gaus Width: 4.6177

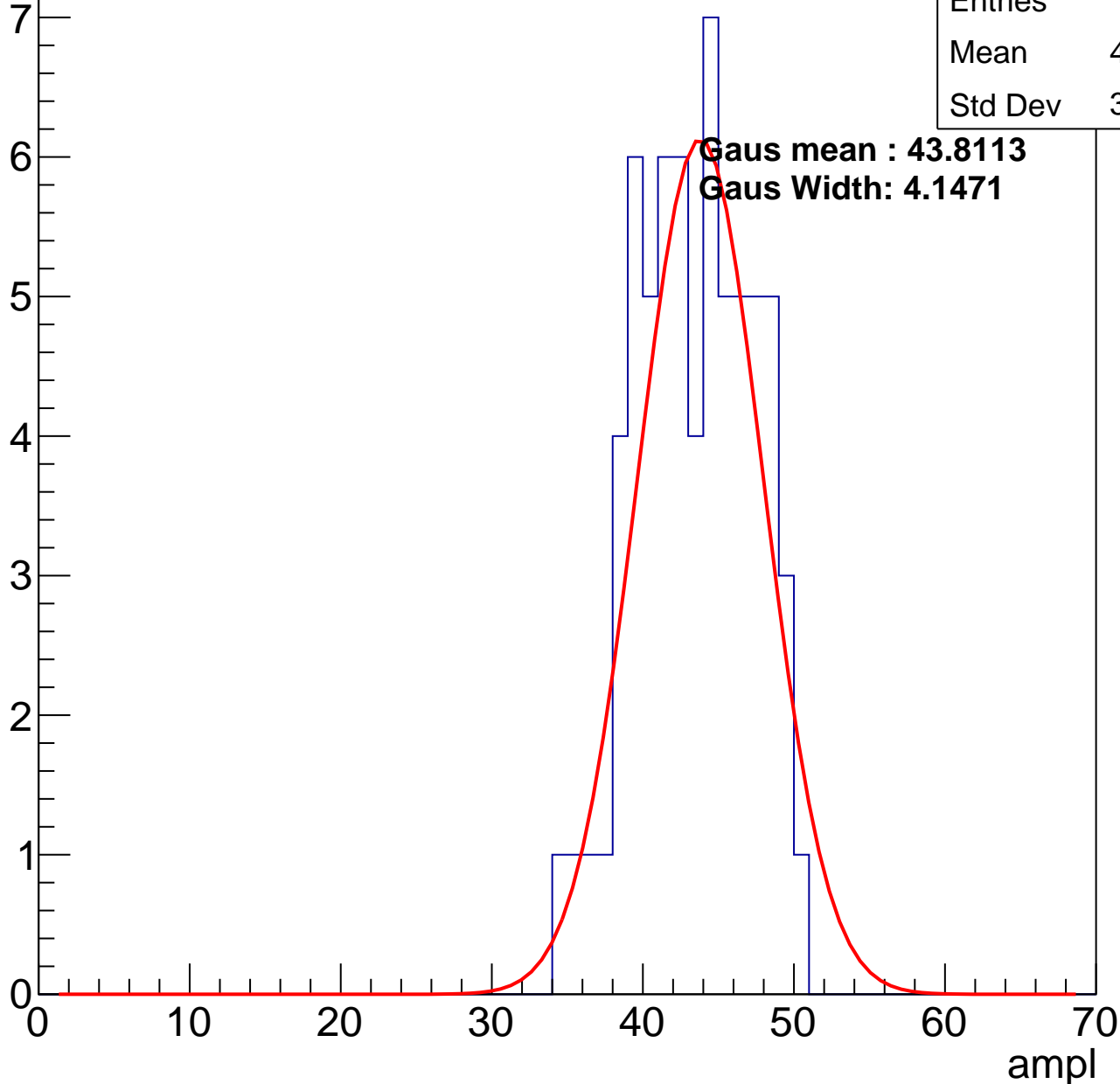


B1L102S, U18-ch126, adc2

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	66
Mean	42.92
Std Dev	3.763

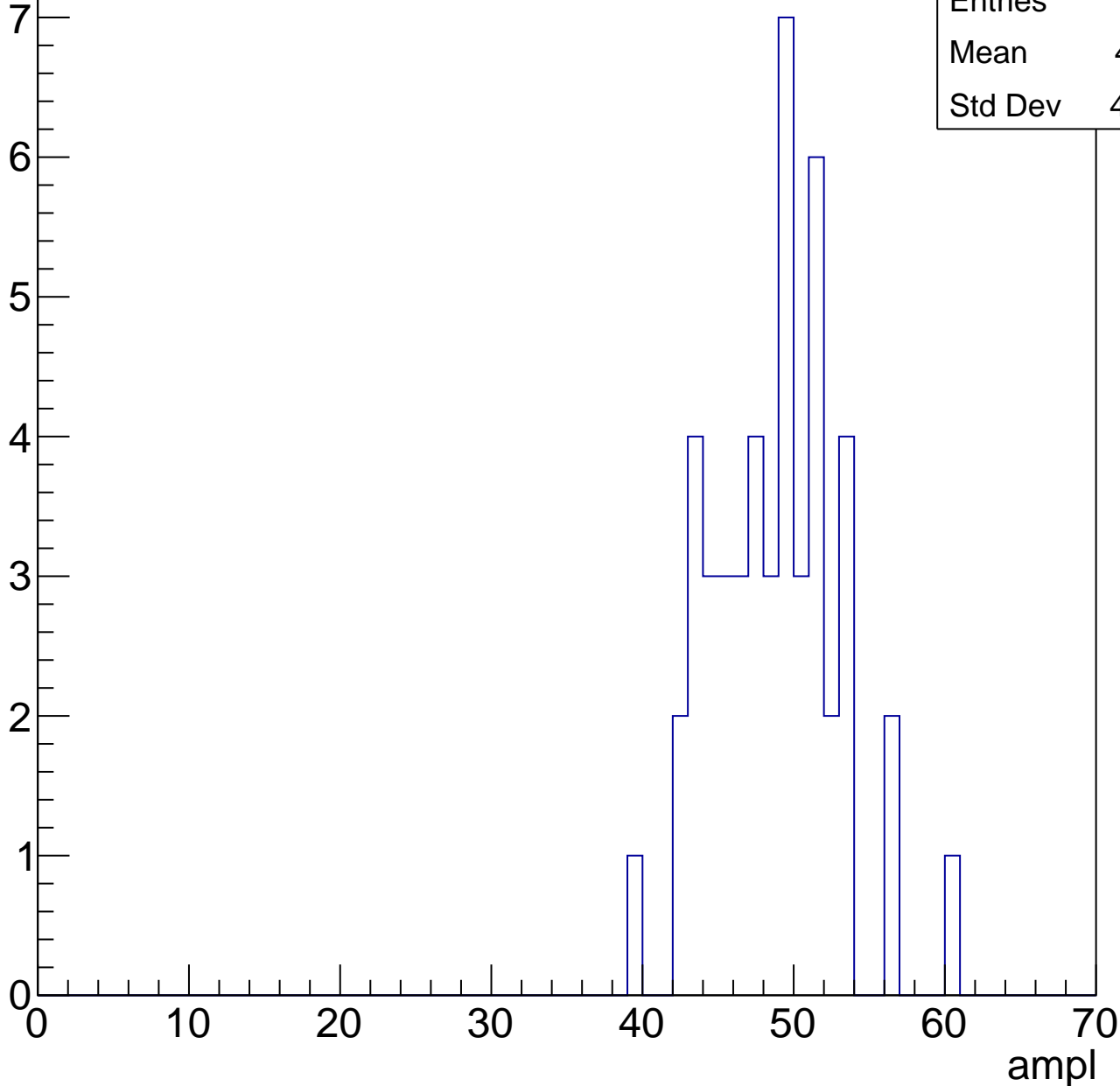


B1L102S, U18-ch126, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	48
Mean	48.31
Std Dev	4.124

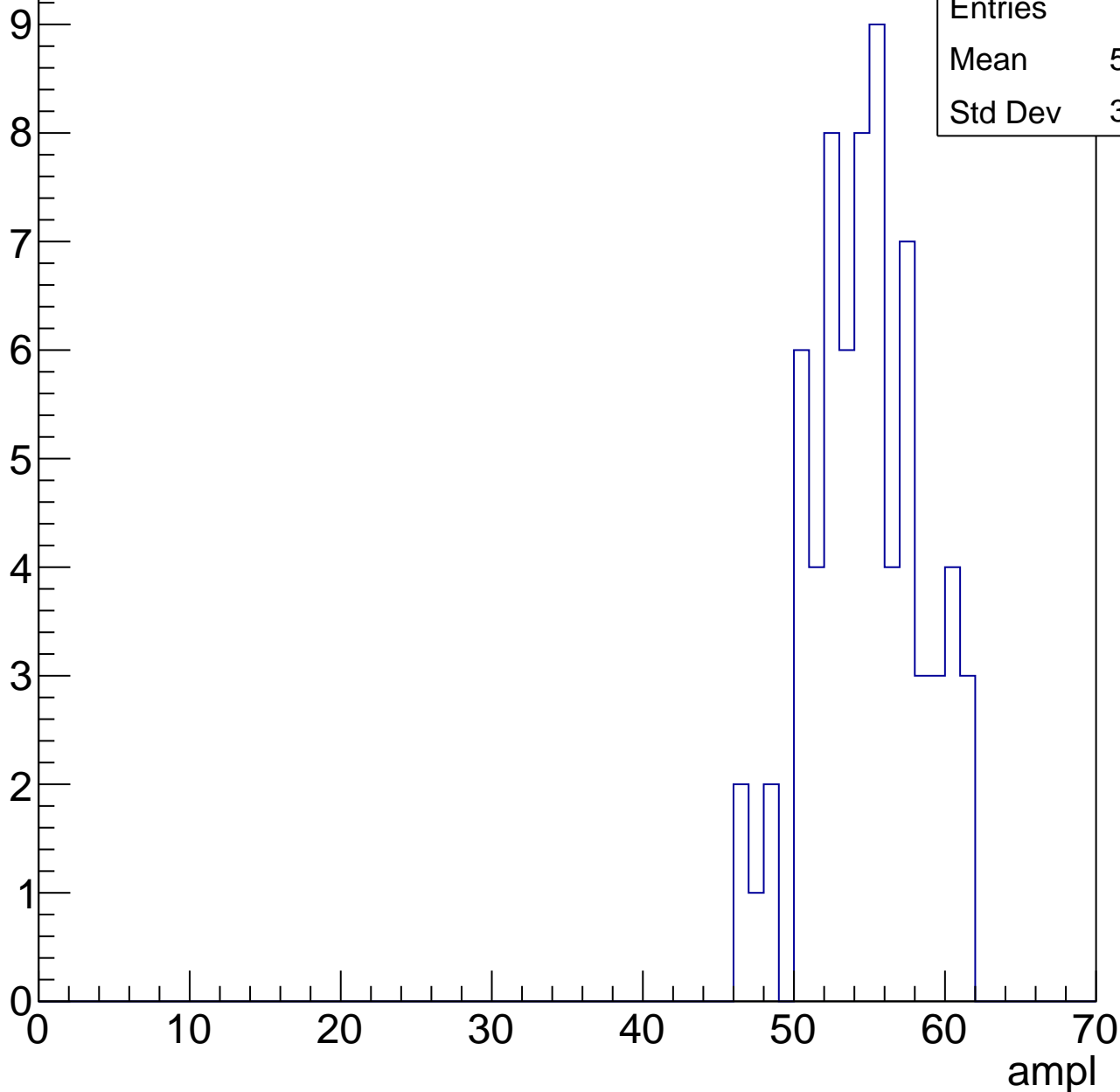


B1L102S, U18-ch126, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	70
Mean	54.24
Std Dev	3.619

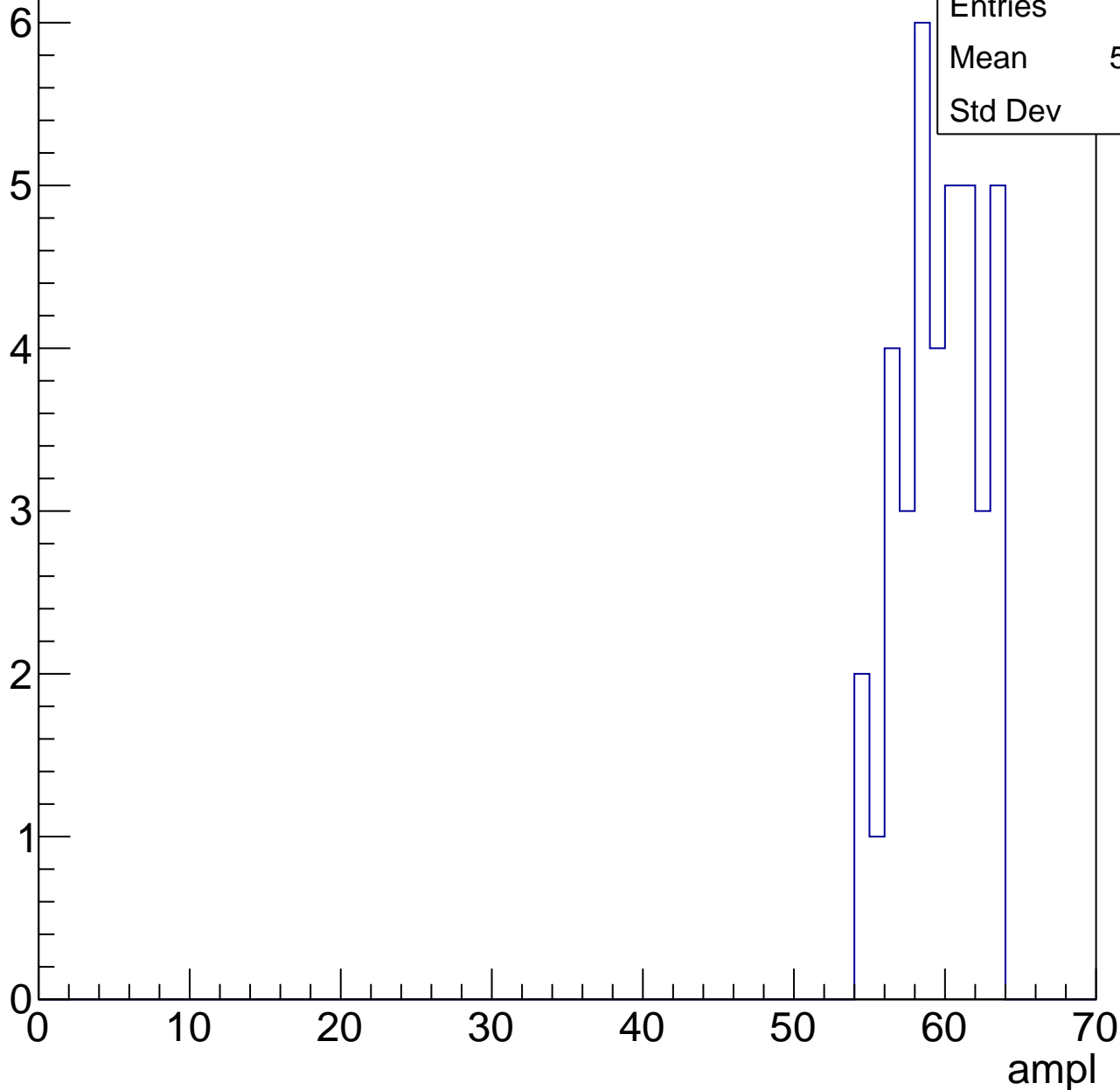


B1L102S, U18-ch126, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	38
Mean	59.16
Std Dev	2.57

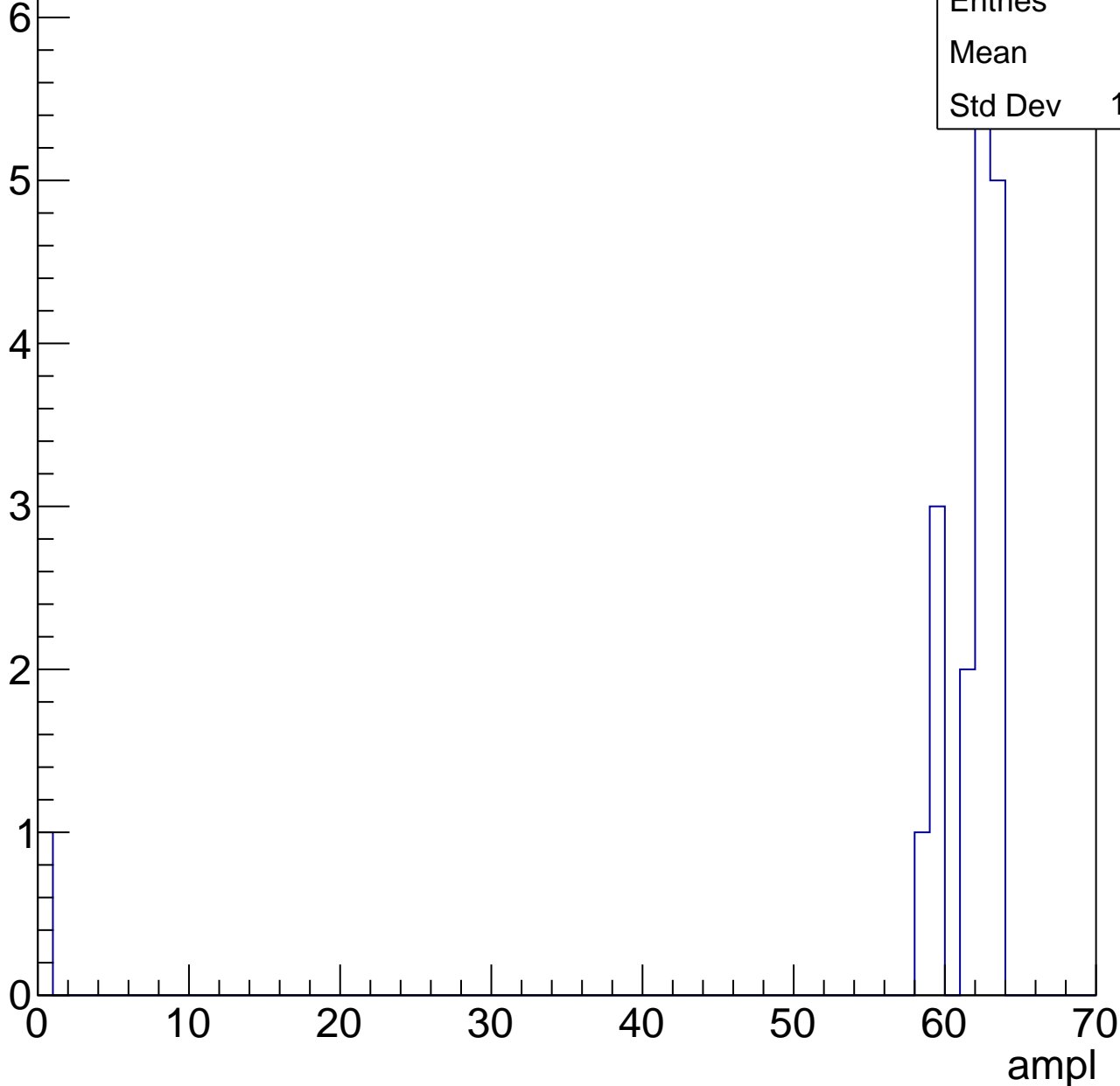


B1L102S, U18-ch126, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	18
Mean	58
Std Dev	14.15



B1L102S, U18-ch126, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch127, adc0

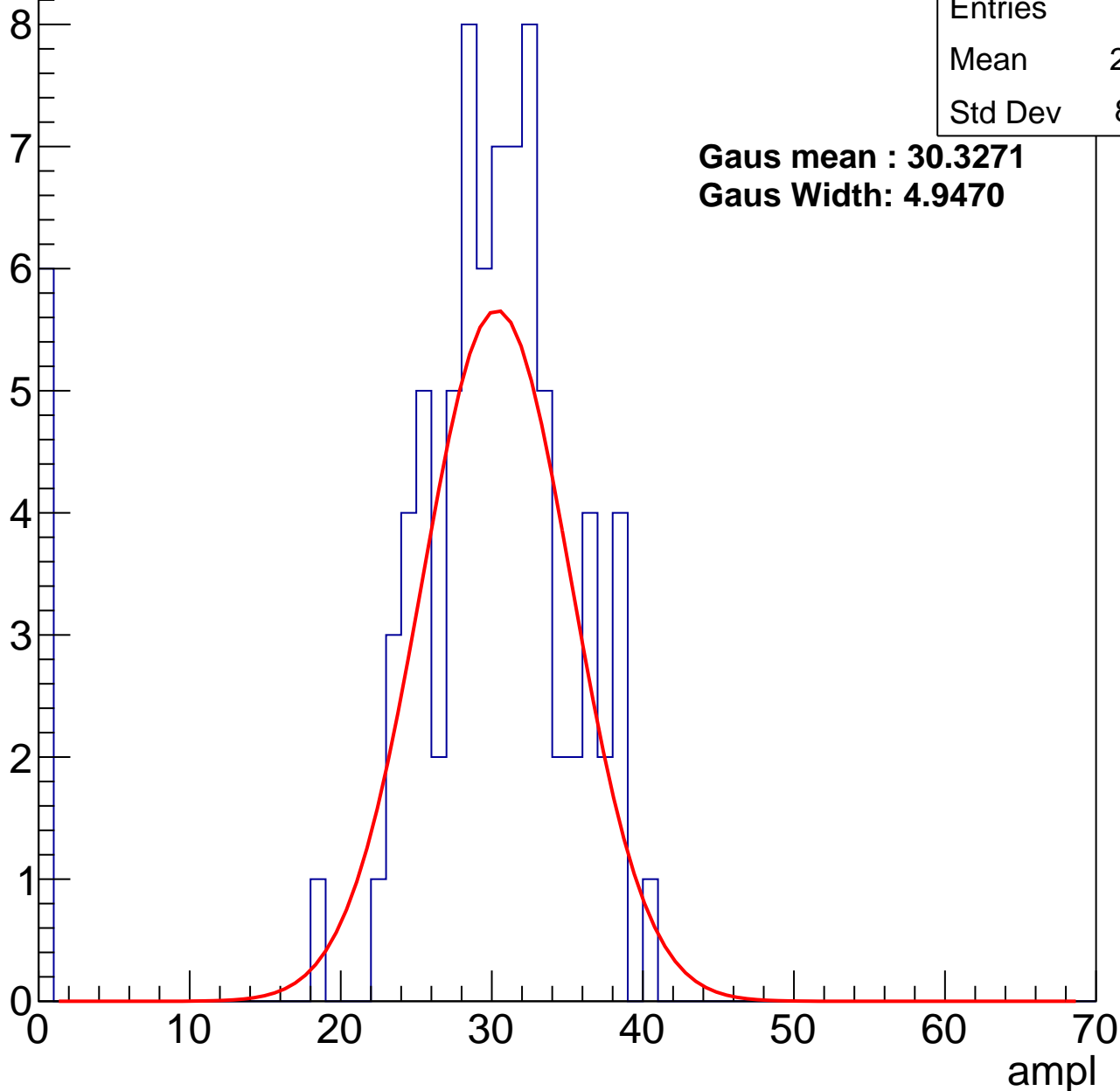
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	83
Mean	27.84
Std Dev	8.861

Gaus mean : 30.3271

Gaus Width: 4.9470



B1L102S, U18-ch127, adc1

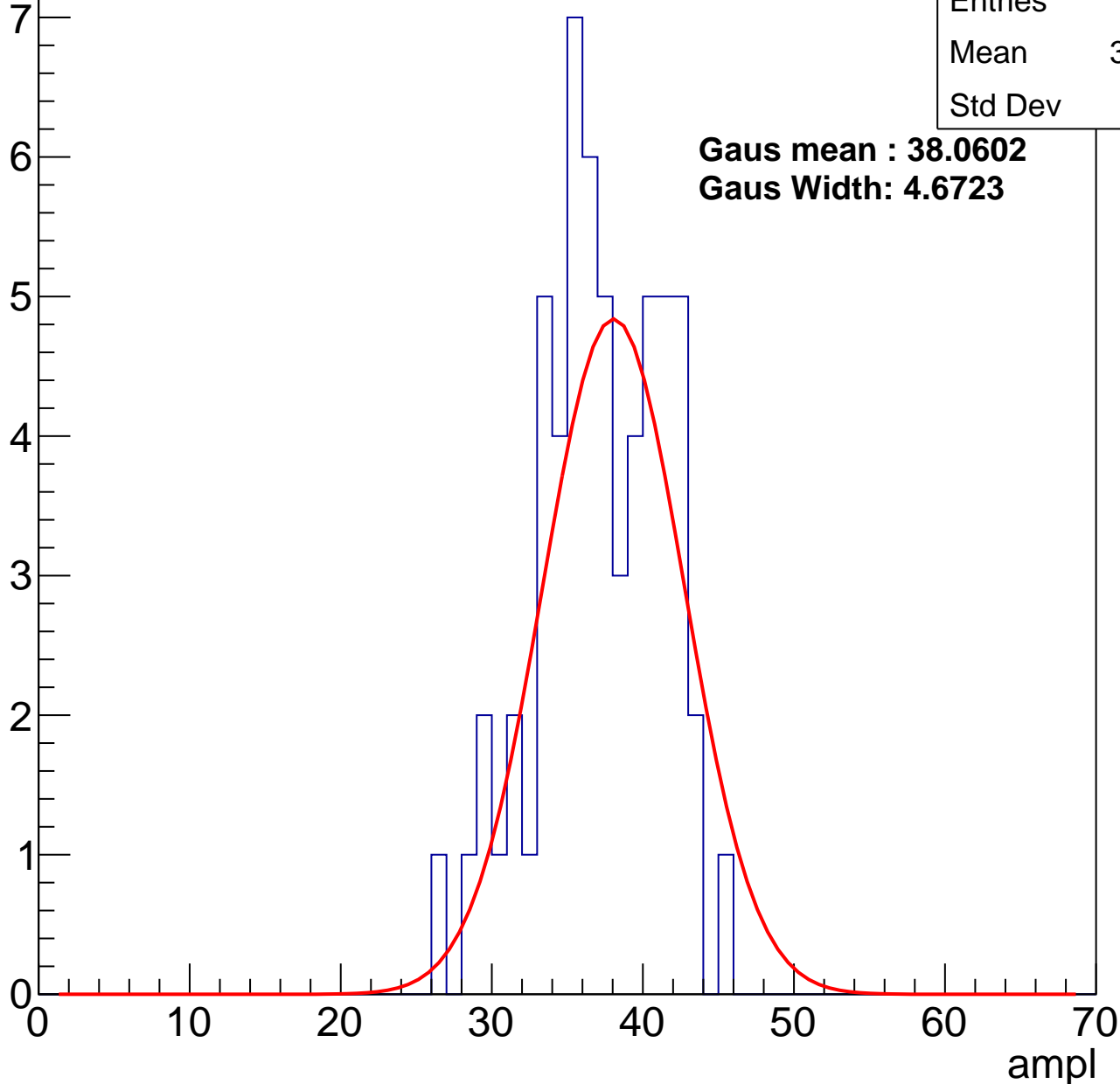
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	60
Mean	36.65
Std Dev	4.13

Gaus mean : 38.0602

Gaus Width: 4.6723



B1L102S, U18-ch127, adc2

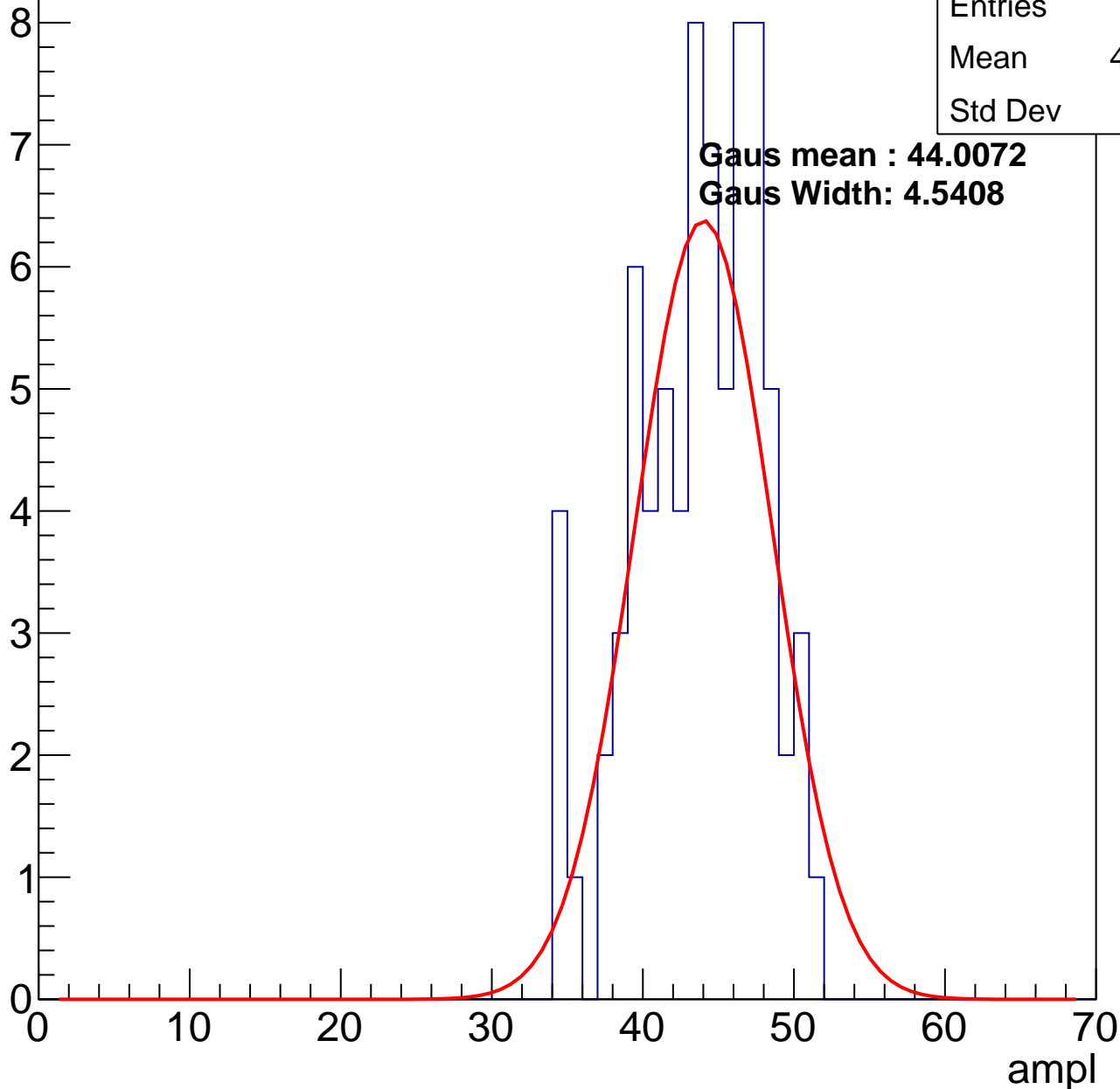
calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	76
Mean	43.24
Std Dev	4.17

Gaus mean : 44.0072

Gaus Width: 4.5408

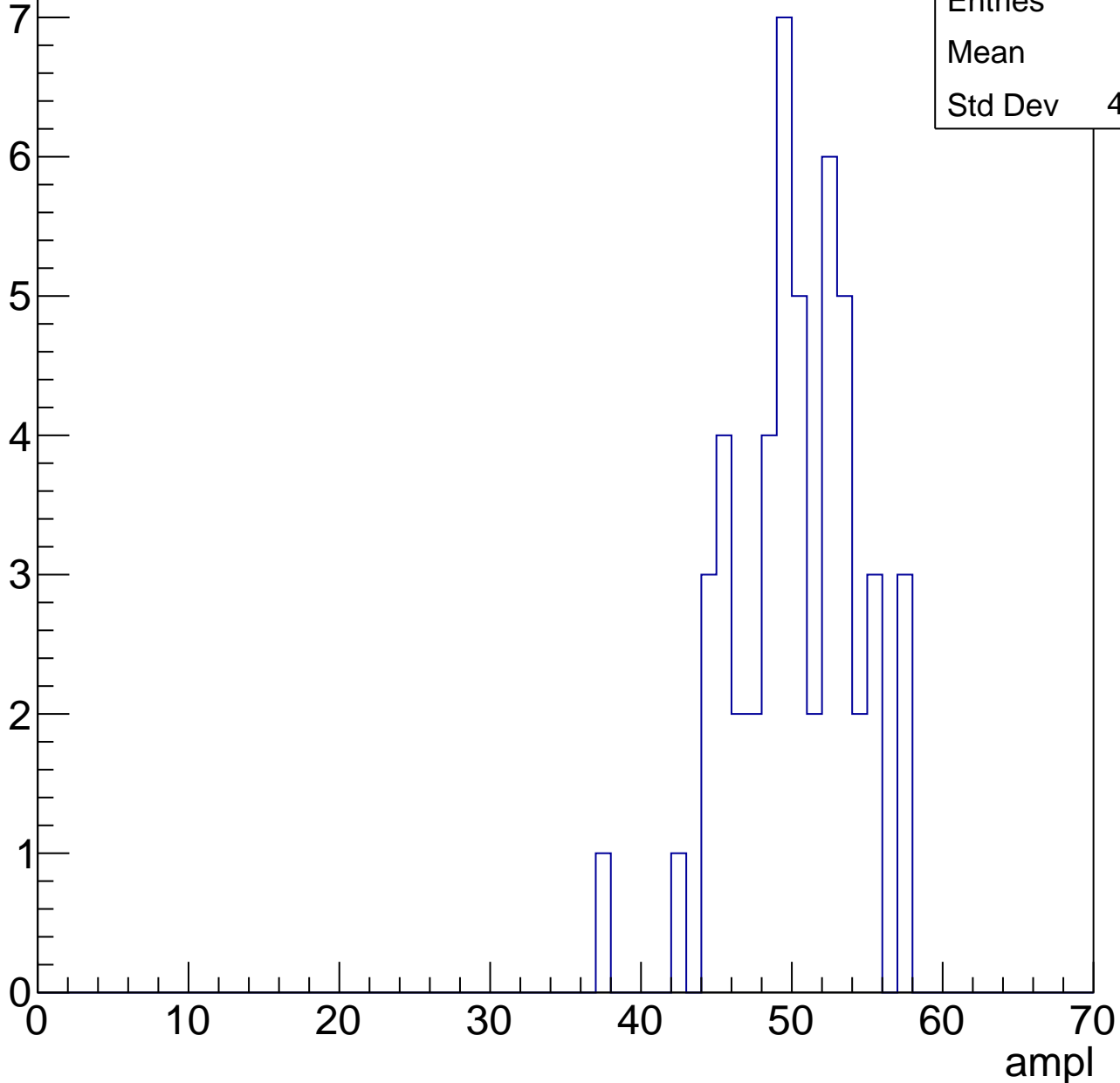


B1L102S, U18-ch127, adc3

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	50
Mean	49.7
Std Dev	4.085

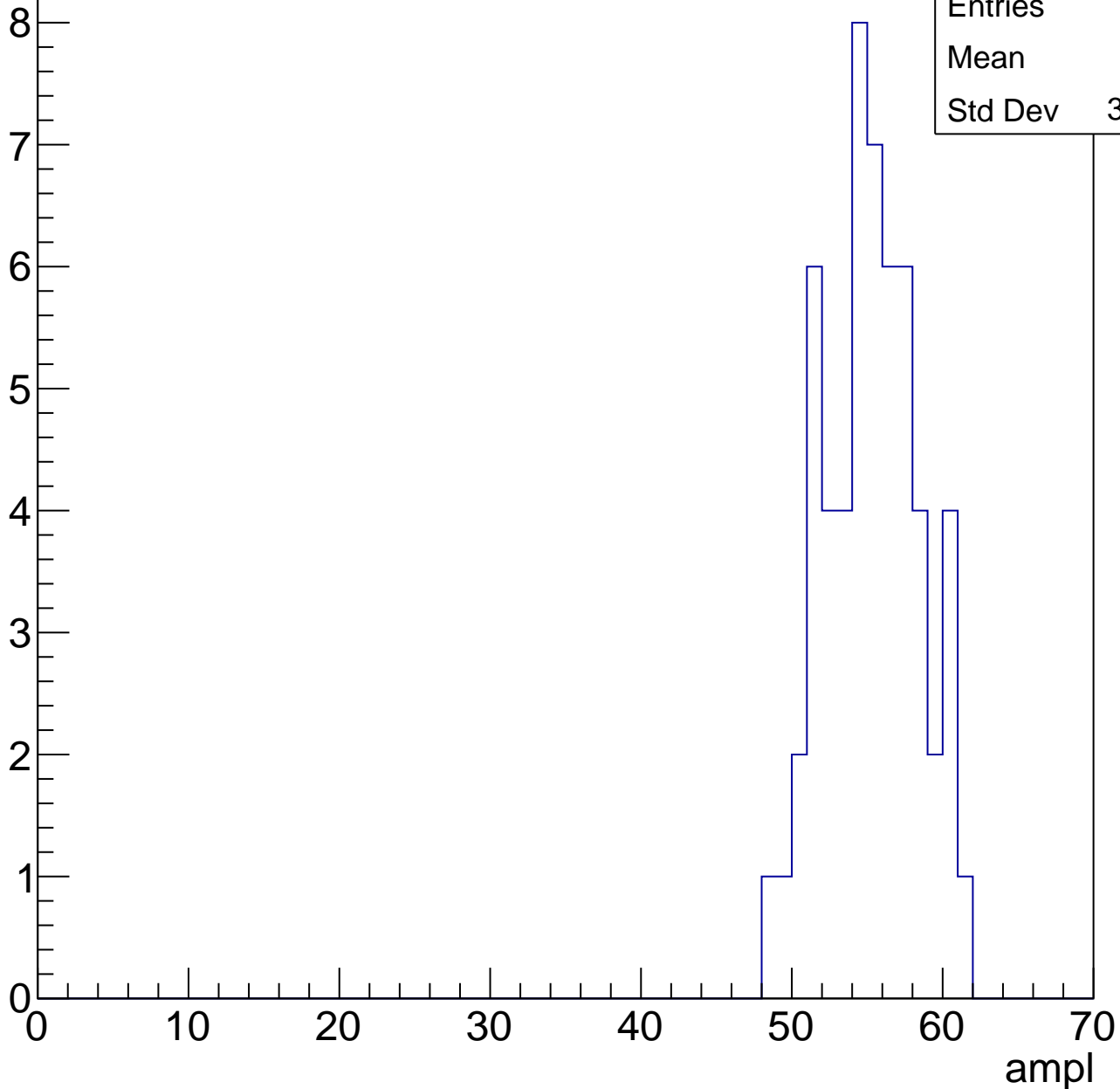


B1L102S, U18-ch127, adc4

calib_packv5_042523_0143.root, FC#11, port A2

Entry

Entries	56
Mean	54.8
Std Dev	3.056

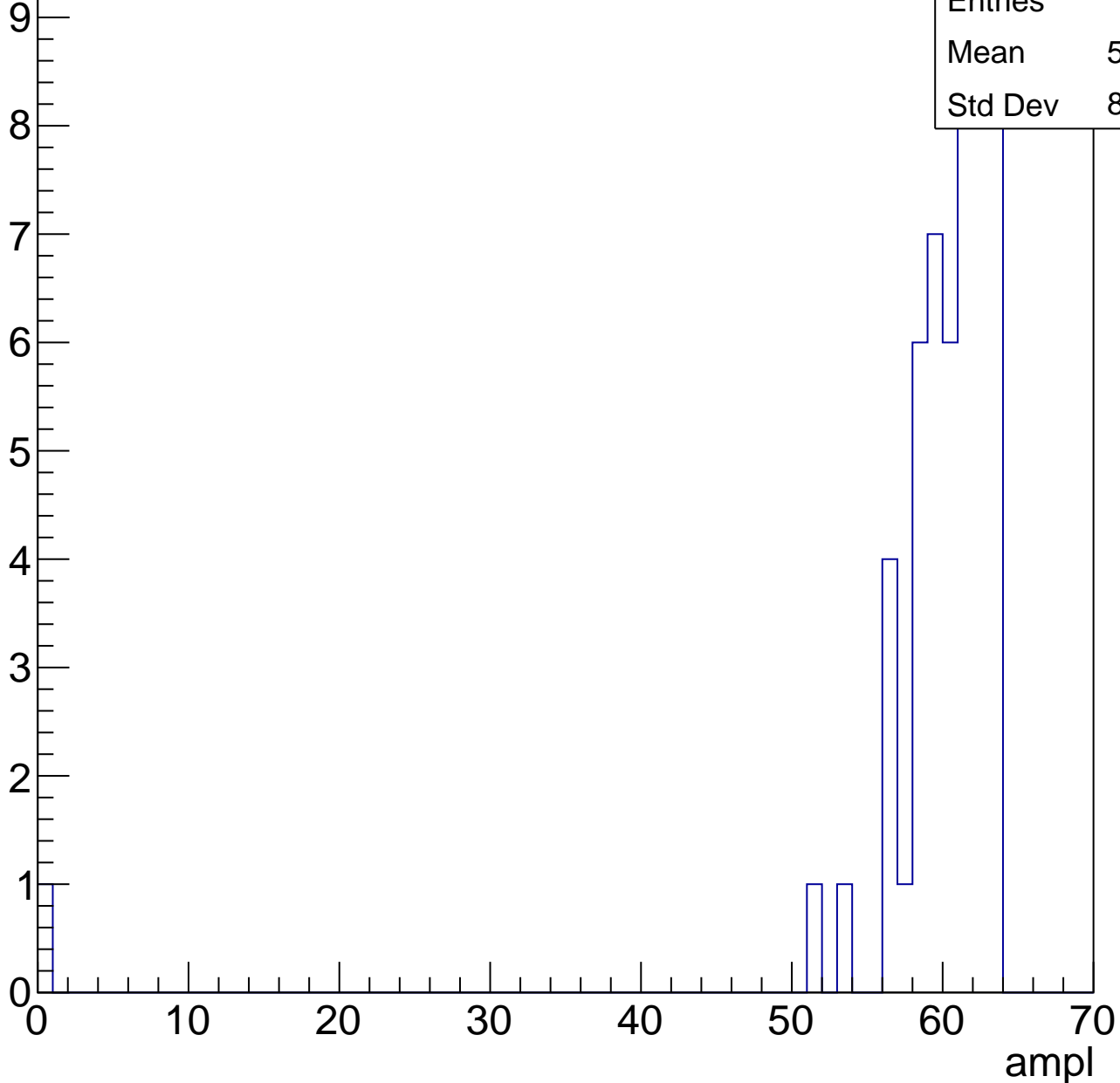


B1L102S, U18-ch127, adc5

calib_packv5_042523_0143.root, FC#11, port A2

Entry

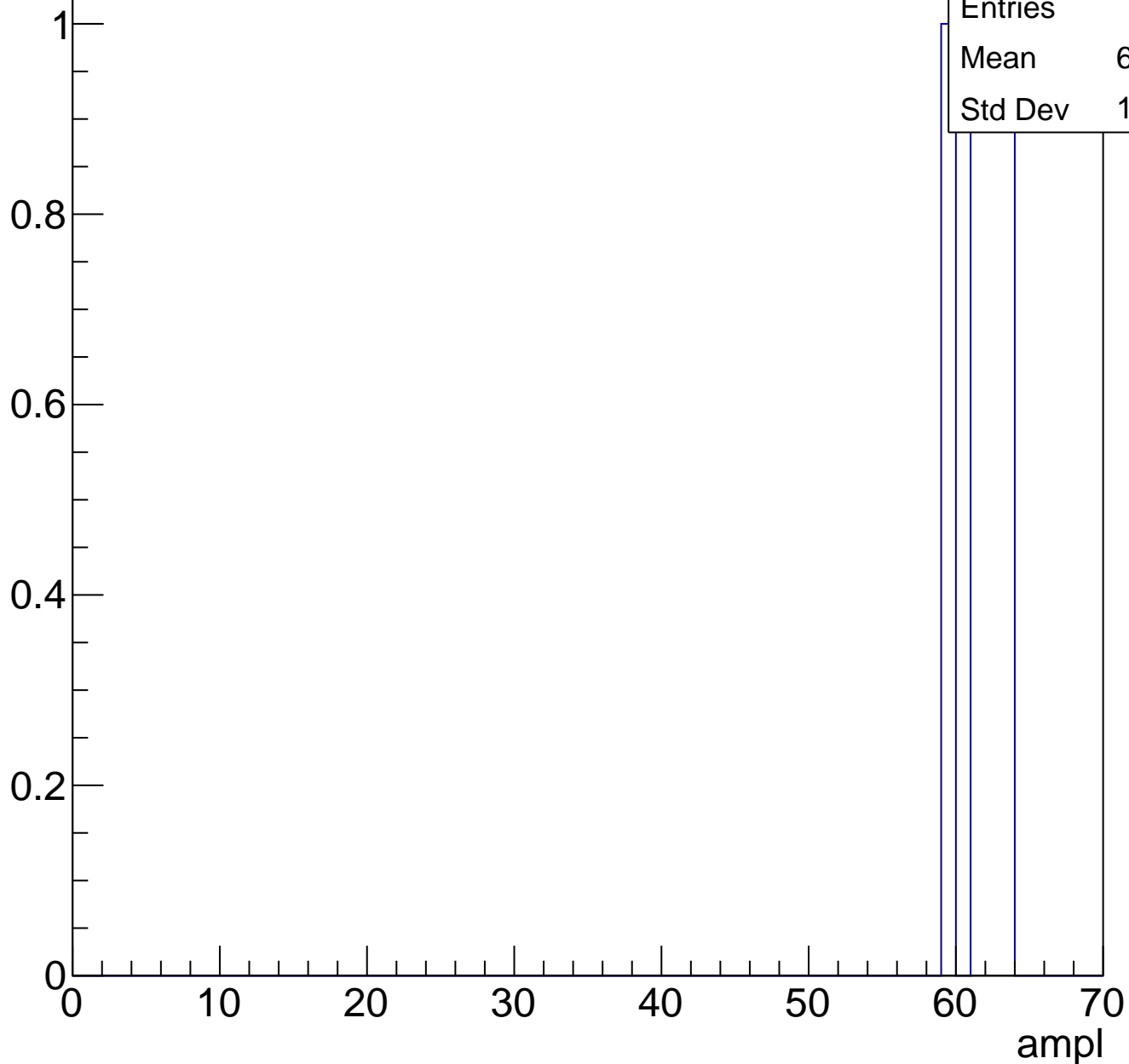
Entries	53
Mean	58.85
Std Dev	8.566



B1L102S, U18-ch127, adc6

calib_packv5_042523_0143.root, FC#11, port A2

Entry



B1L102S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L102S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#11, port A2

Entry



Entries	0
Mean	0
Std Dev	0