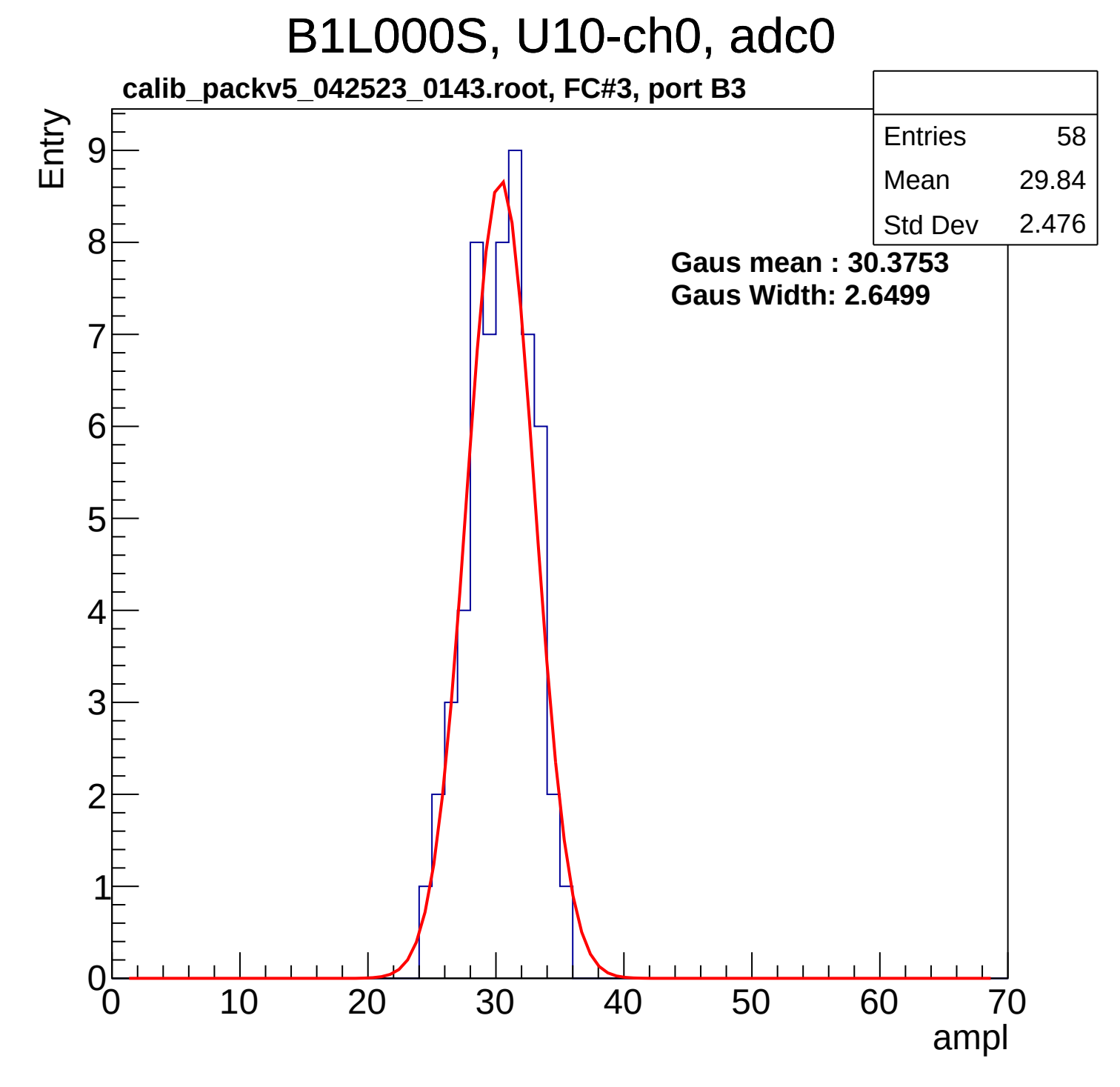
B1L000S, U10-ch0, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 29.84
Std Dev 2.476
Gaus mean : 30.3753
Gaus Width: 2.6499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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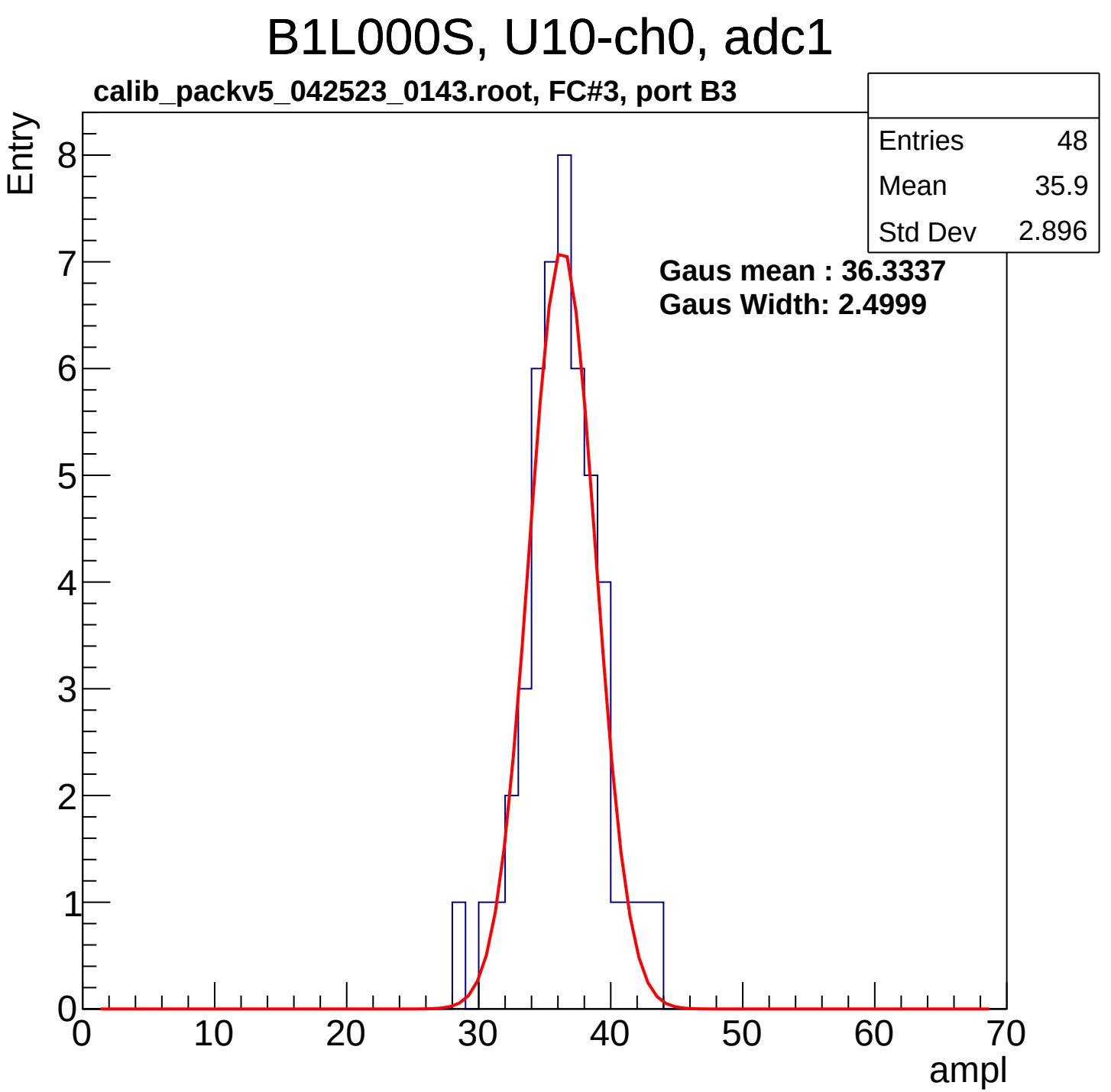
B1L000S, U10-ch0, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 35.9
Std Dev 2.896
Gaus mean : 36.3337
Gaus Width: 2.4999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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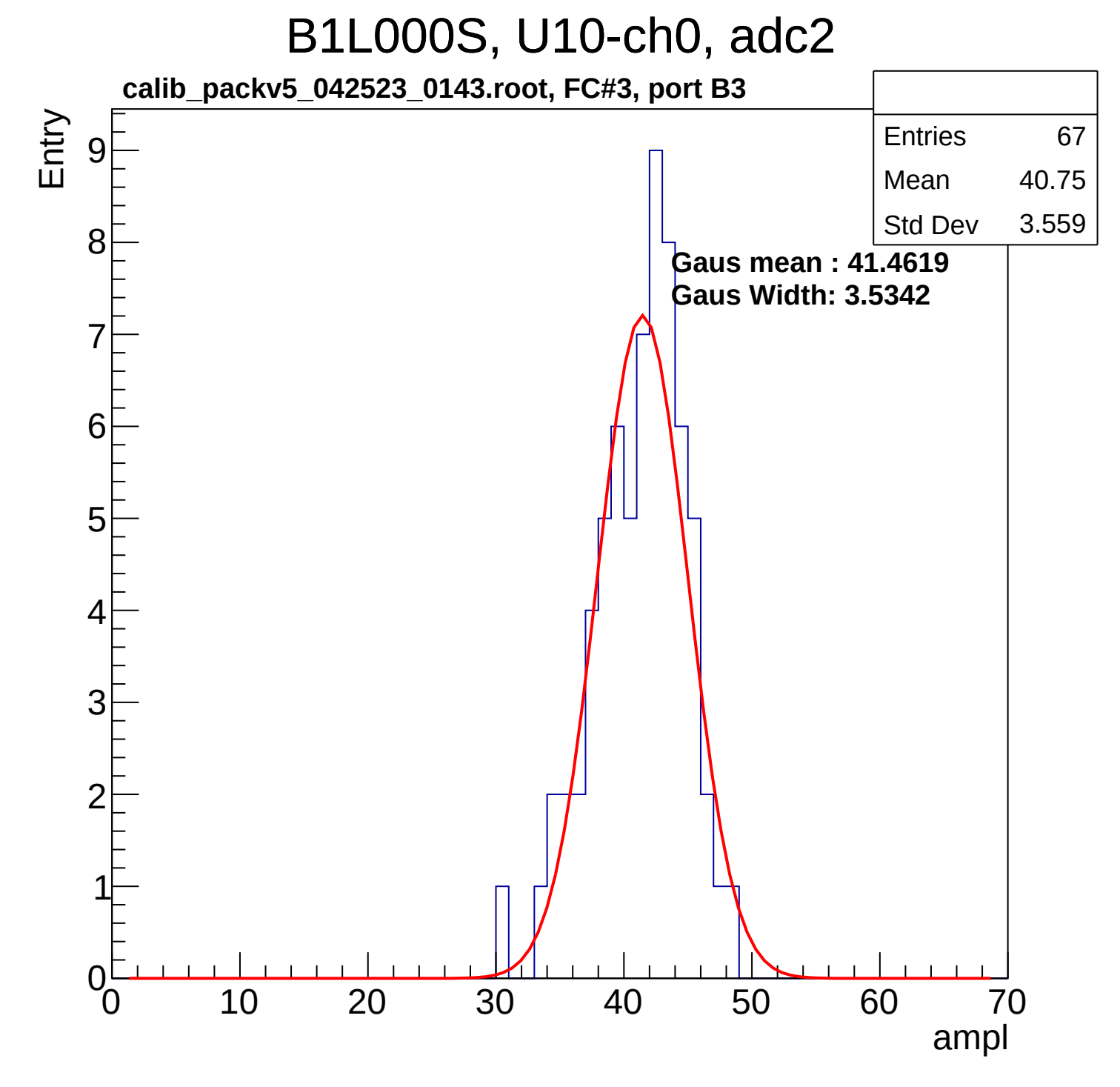
B1L000S, U10-ch0, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.75
Std Dev 3.559
Gaus mean : 41.4619
Gaus Width: 3.5342
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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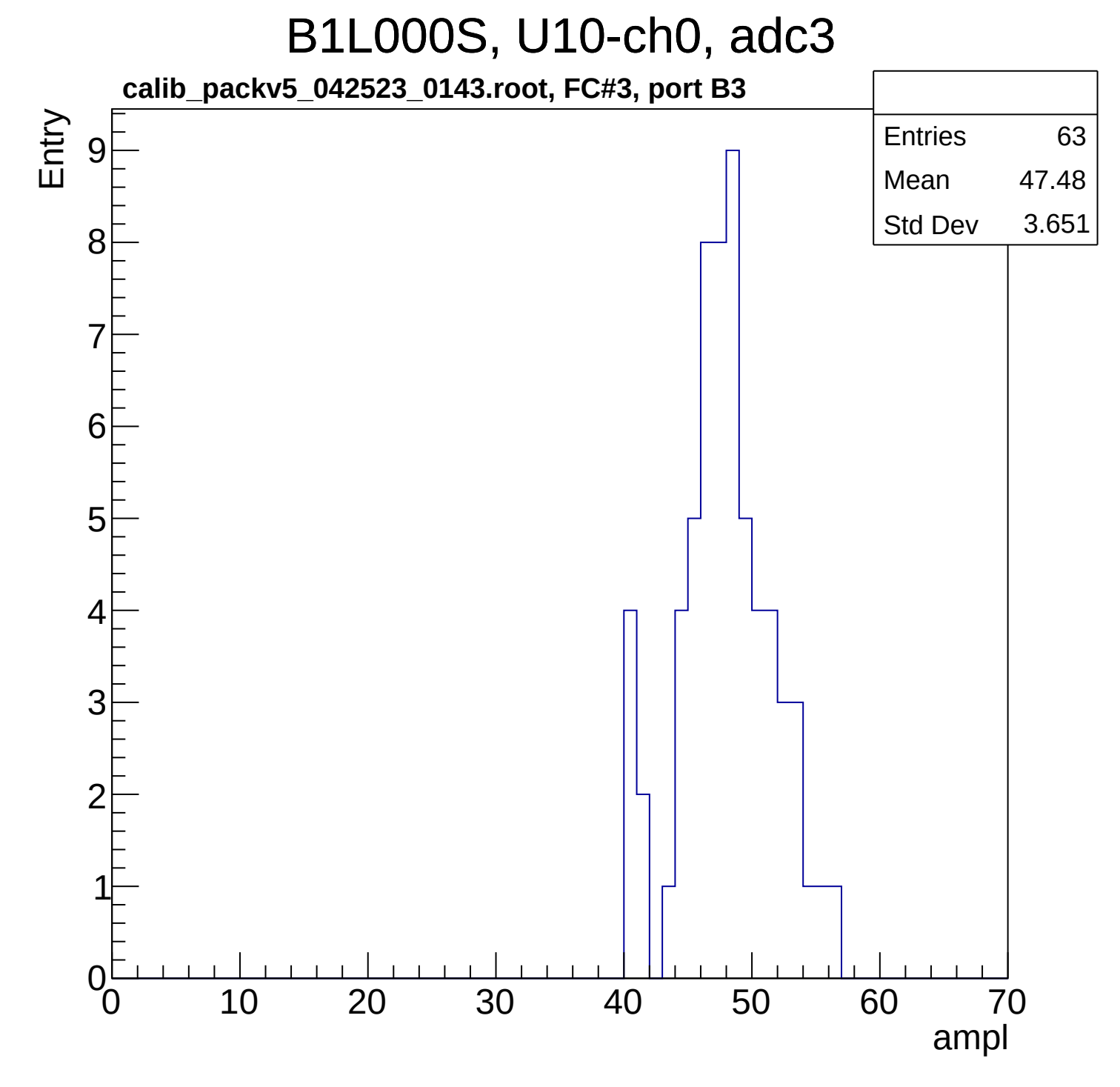
B1L000S, U10-ch0, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.48
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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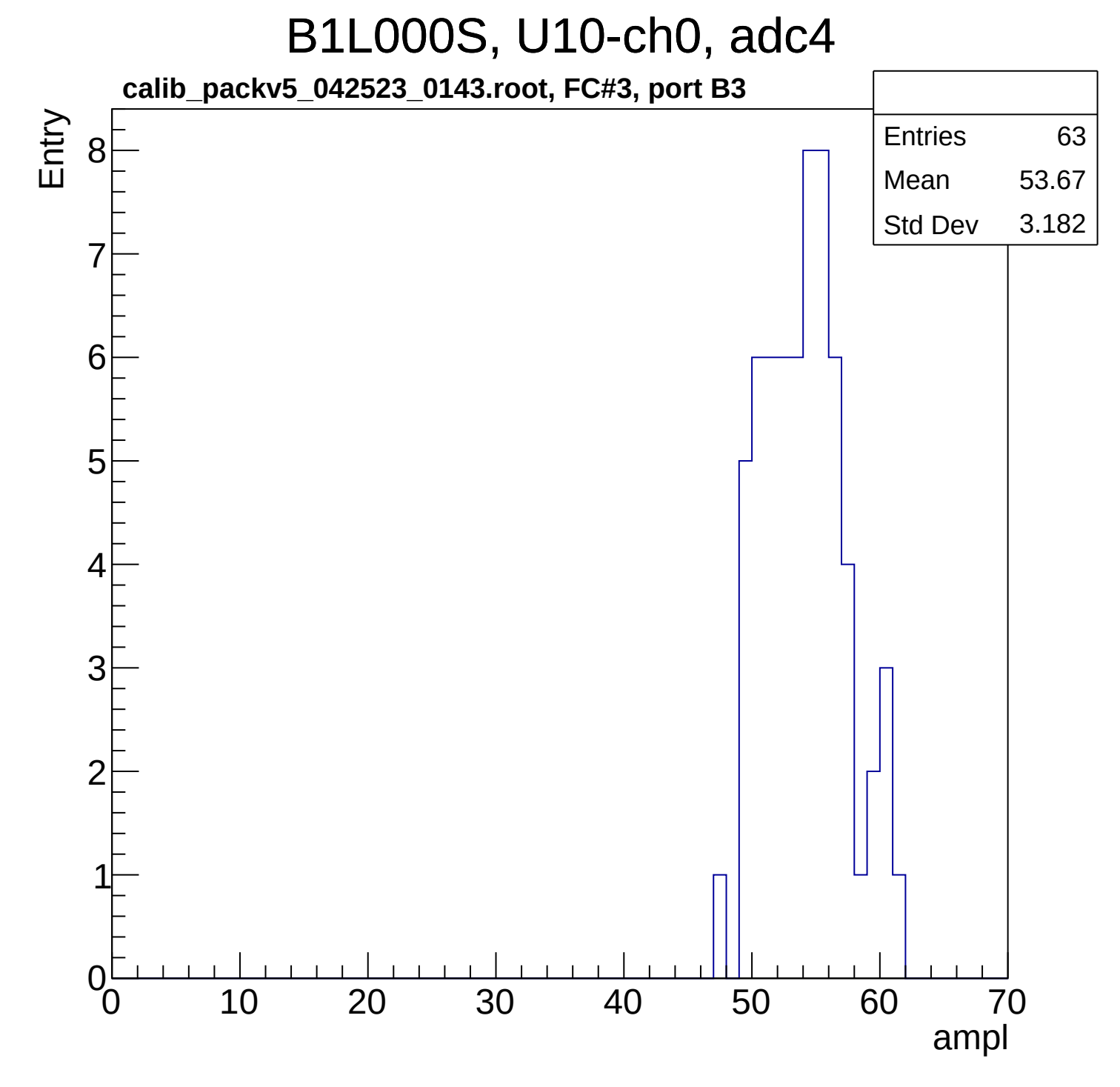
B1L000S, U10-ch0, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 53.67
Std Dev 3.182
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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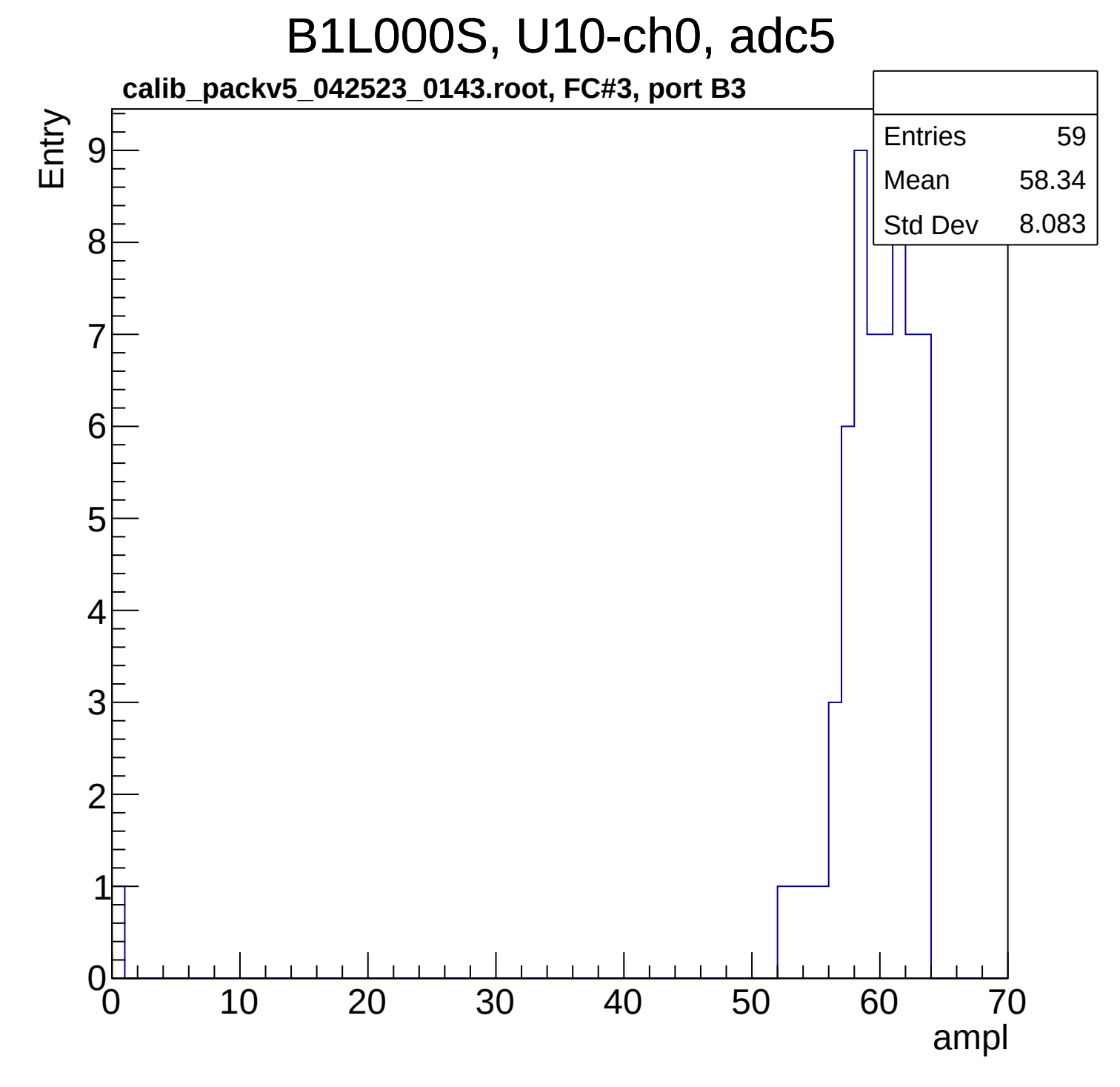
B1L000S, U10-ch0, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.34
Std Dev 8.083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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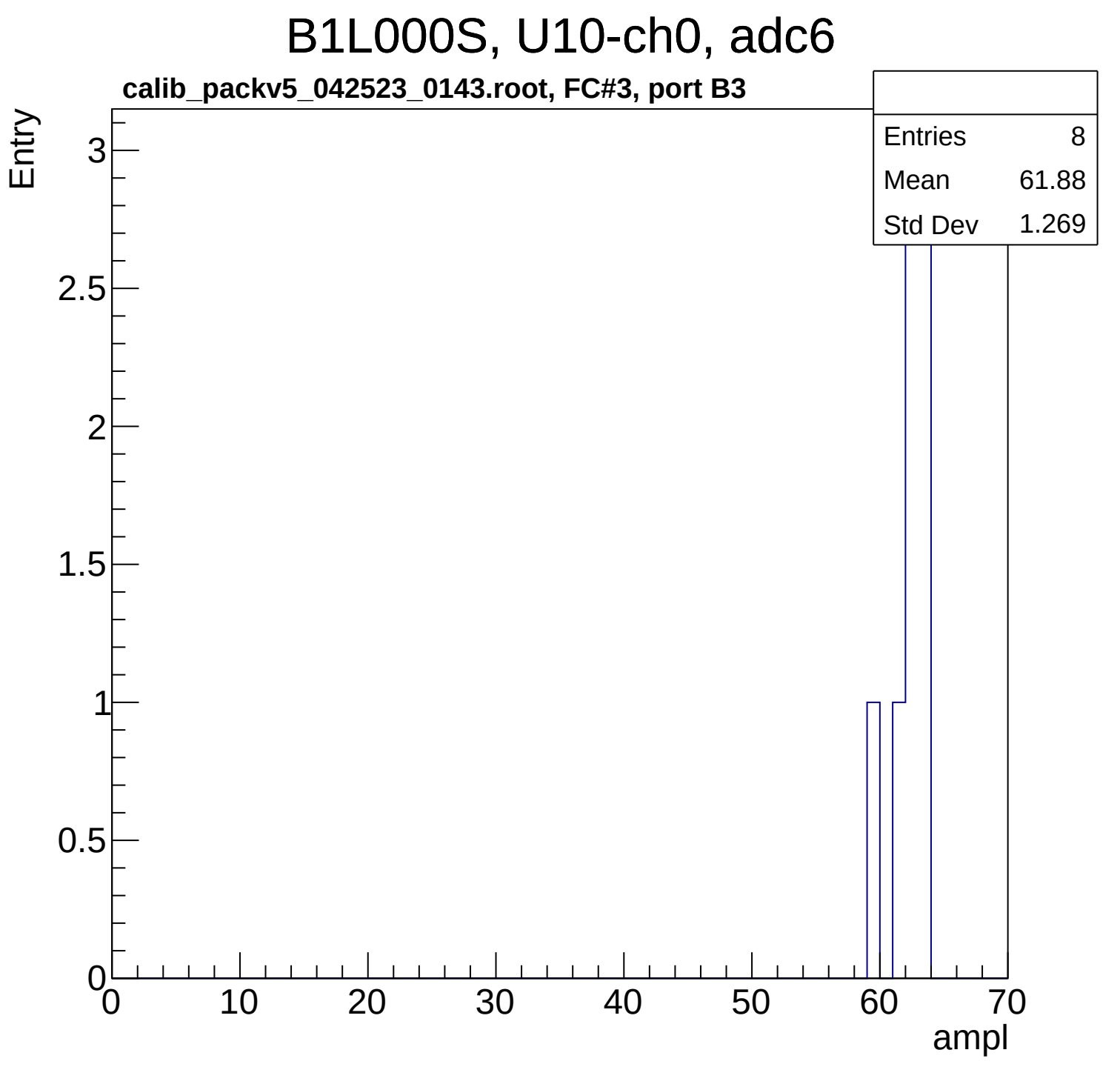
B1L000S, U10-ch0, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.88
Std Dev 1.269
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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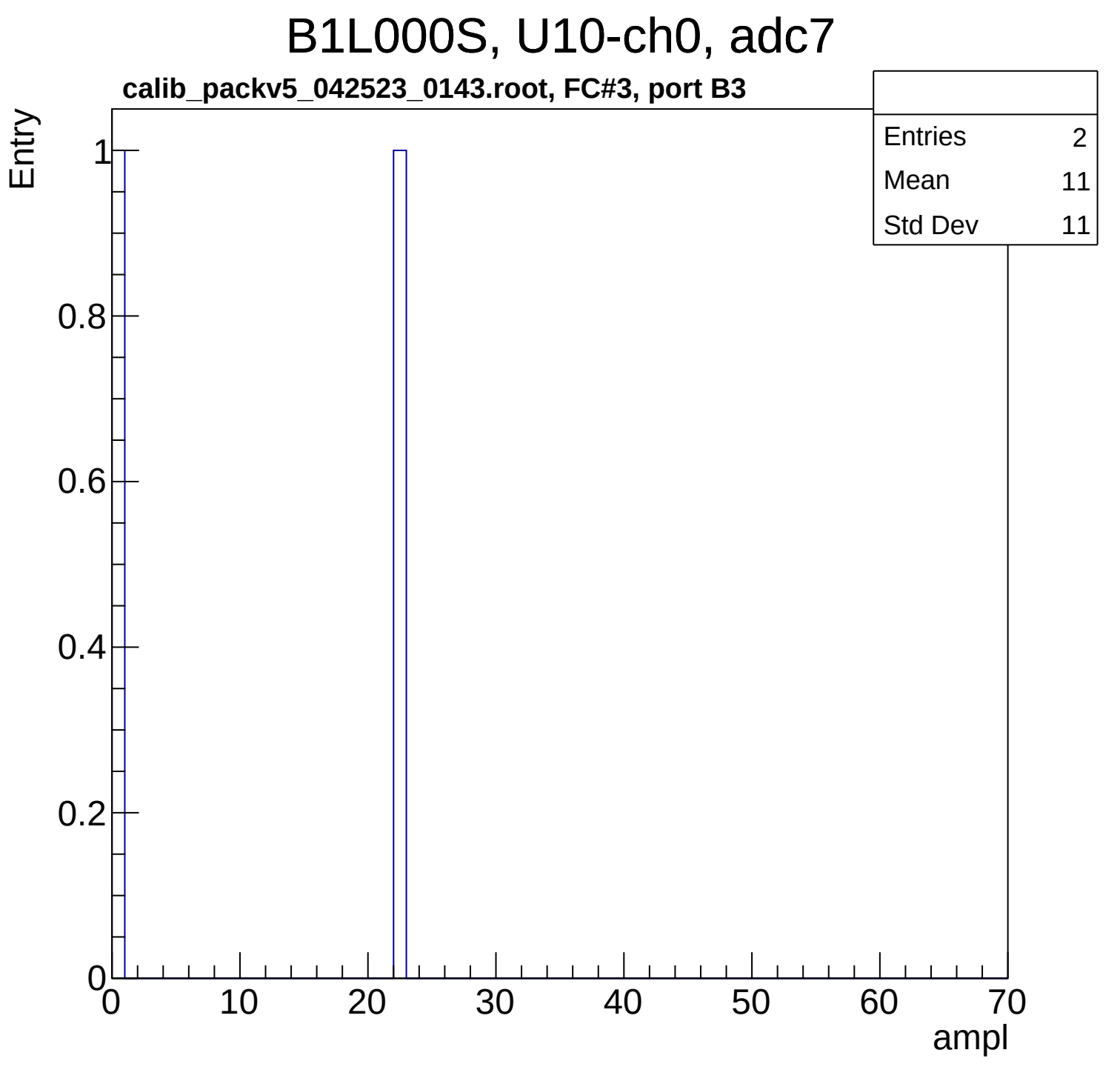
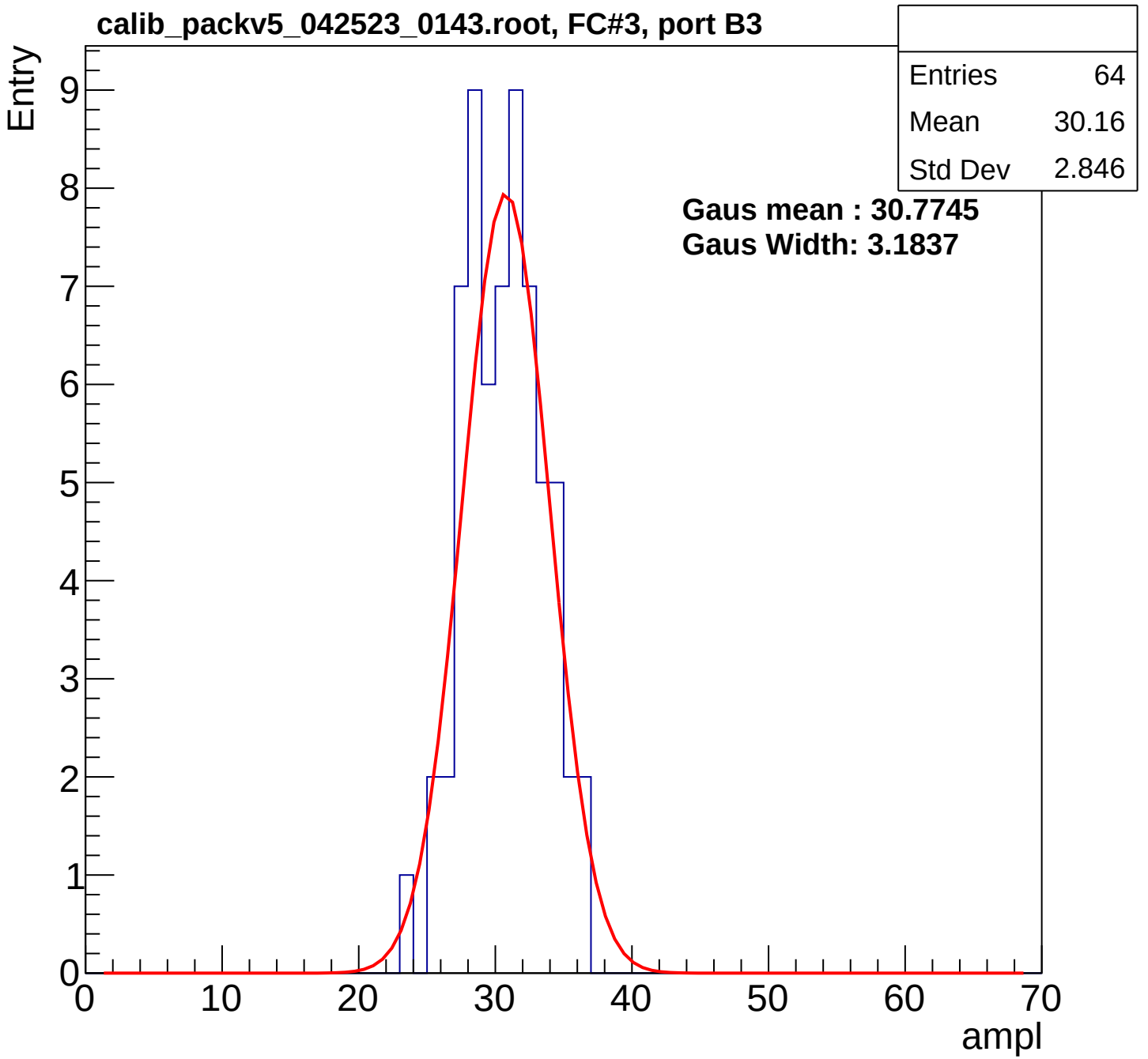

B1L000S, U10-ch0, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch1, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 30.16
Std Dev 2.846
Gaus mean : 30.7745
Gaus Width: 3.1837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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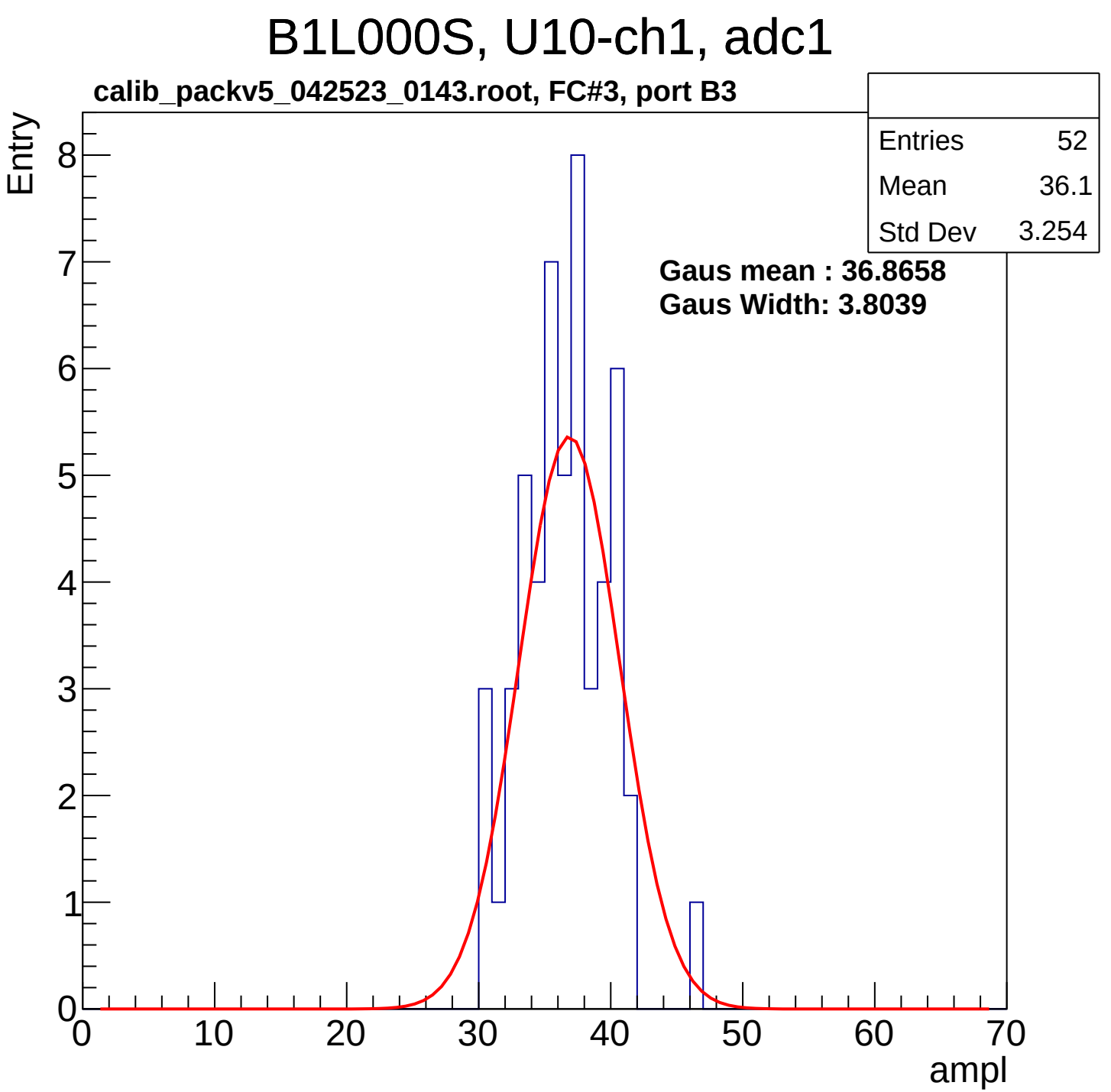
B1L000S, U10-ch1, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 36.1
Std Dev 3.254
Gaus mean : 36.8658
Gaus Width: 3.8039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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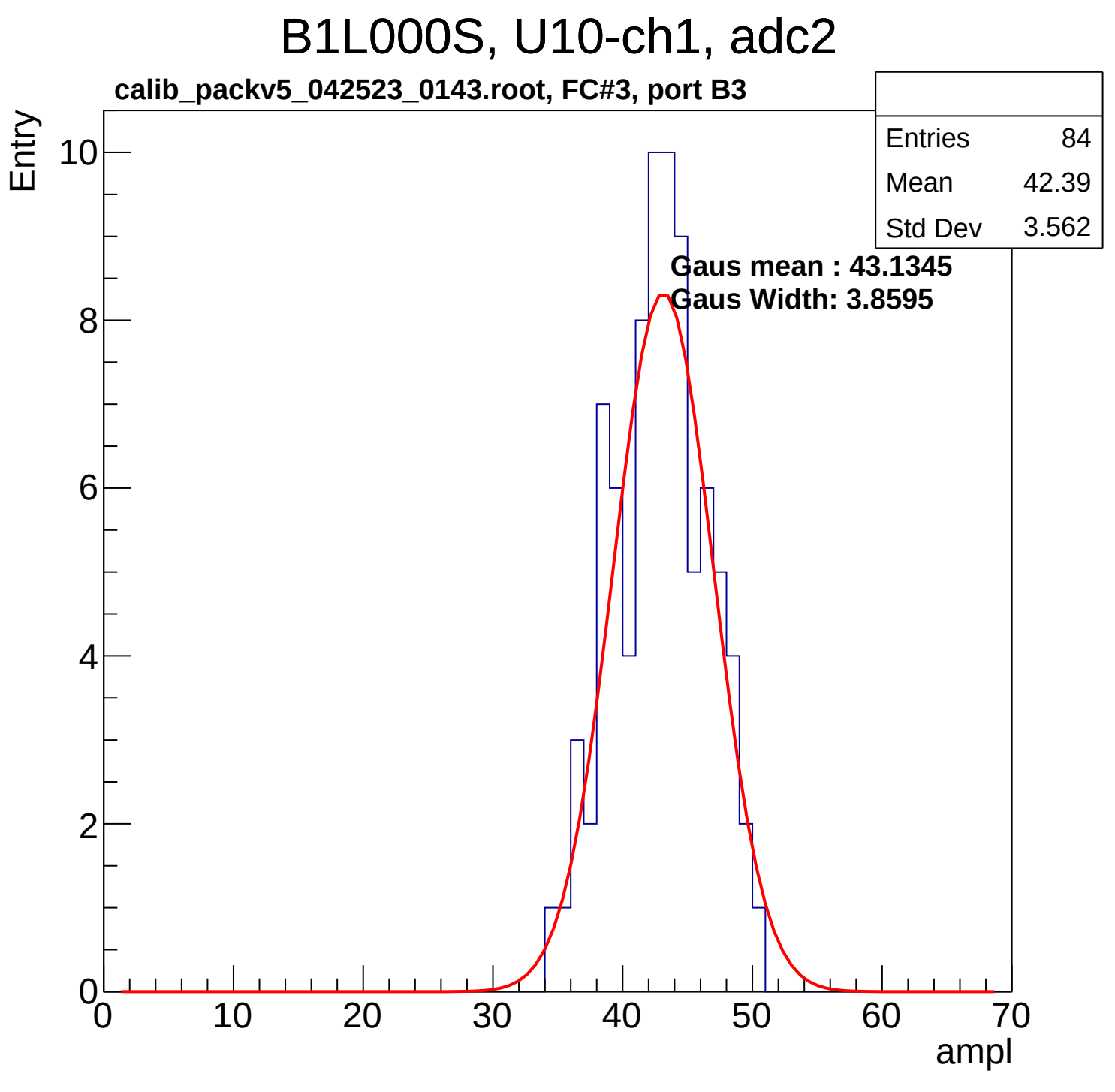
B1L000S, U10-ch1, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 42.39
Std Dev 3.562
Gaus mean : 43.1345
Gaus Width: 3.8595
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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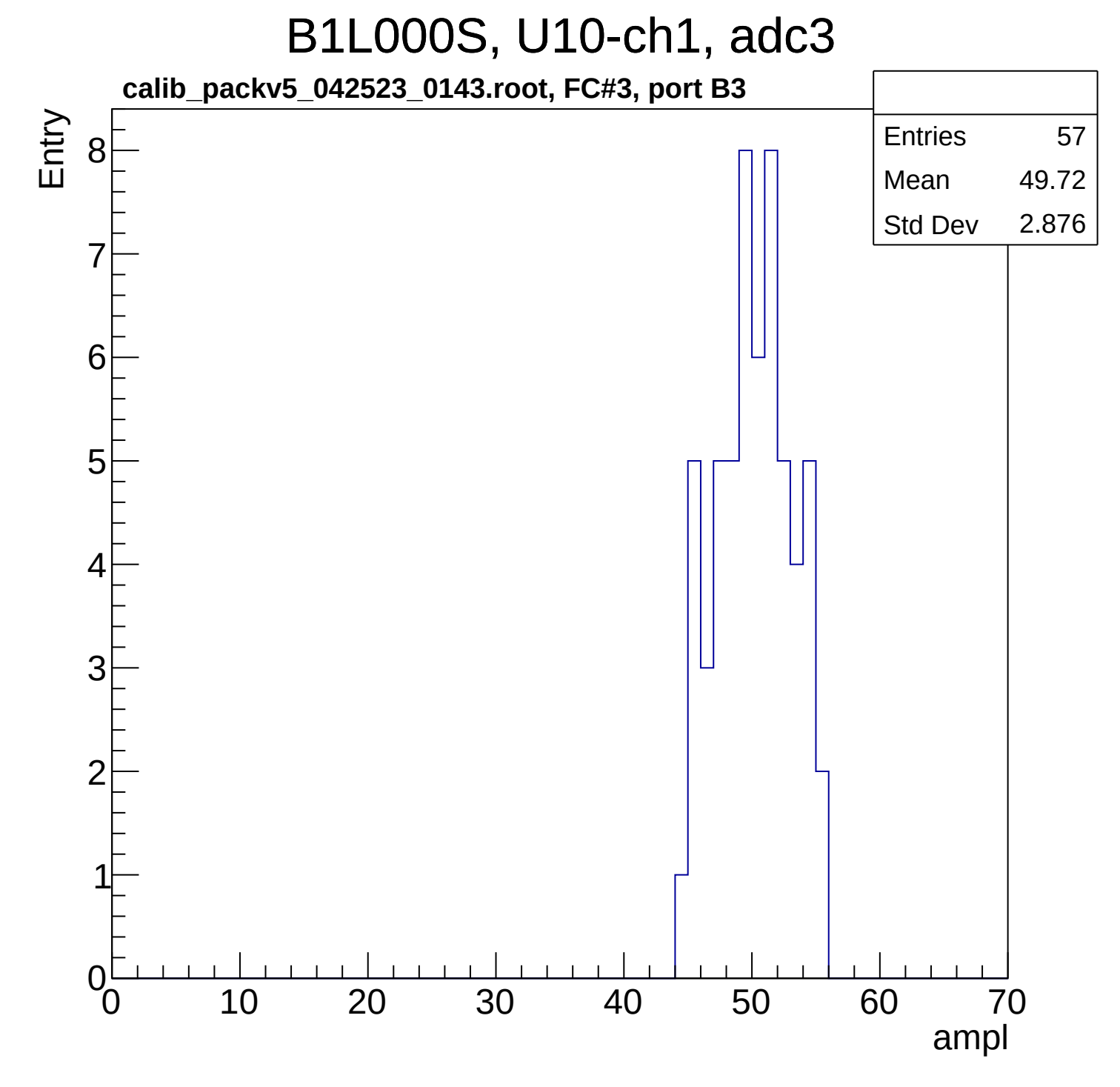
B1L000S, U10-ch1, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 49.72
Std Dev 2.876
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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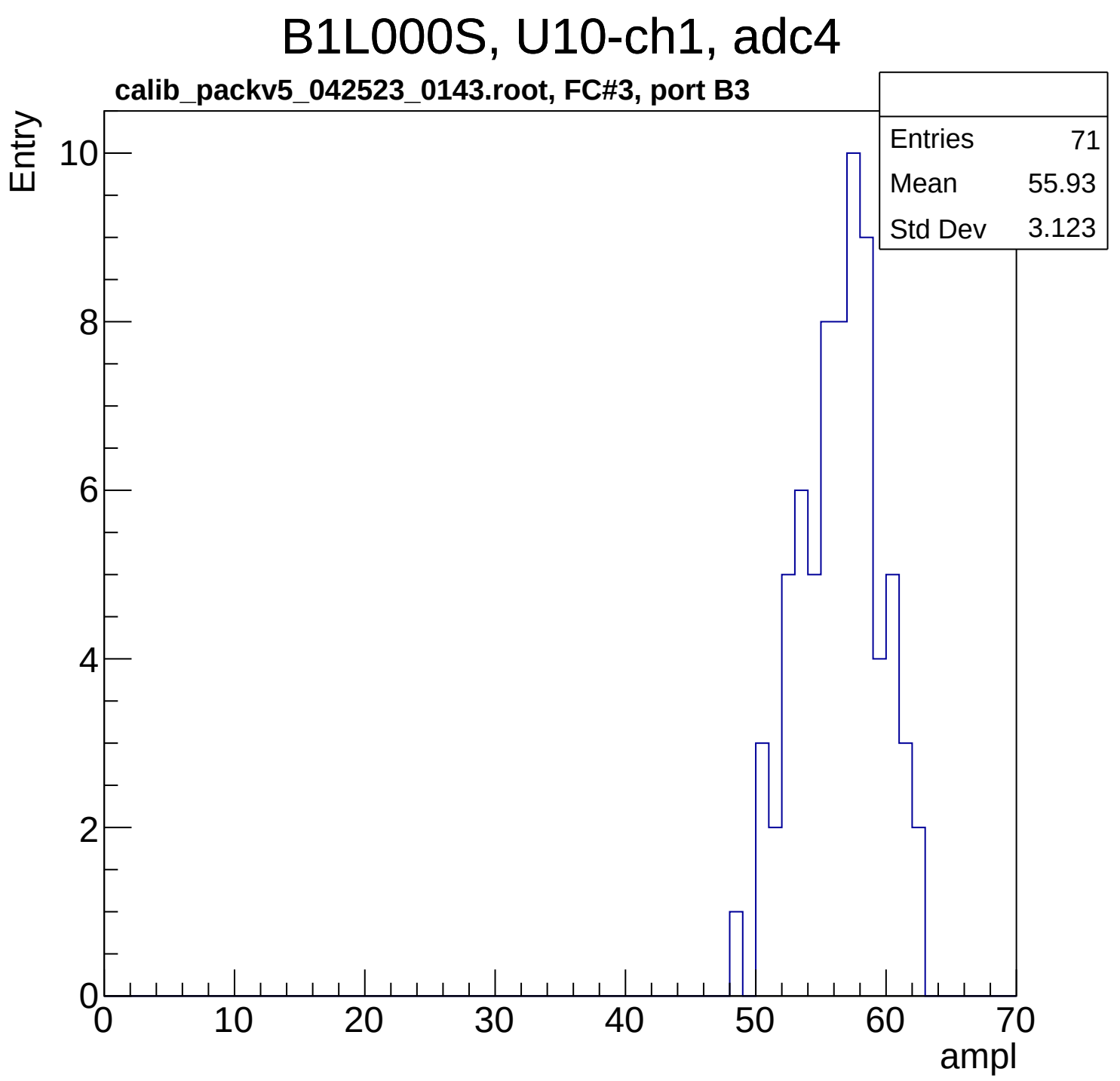
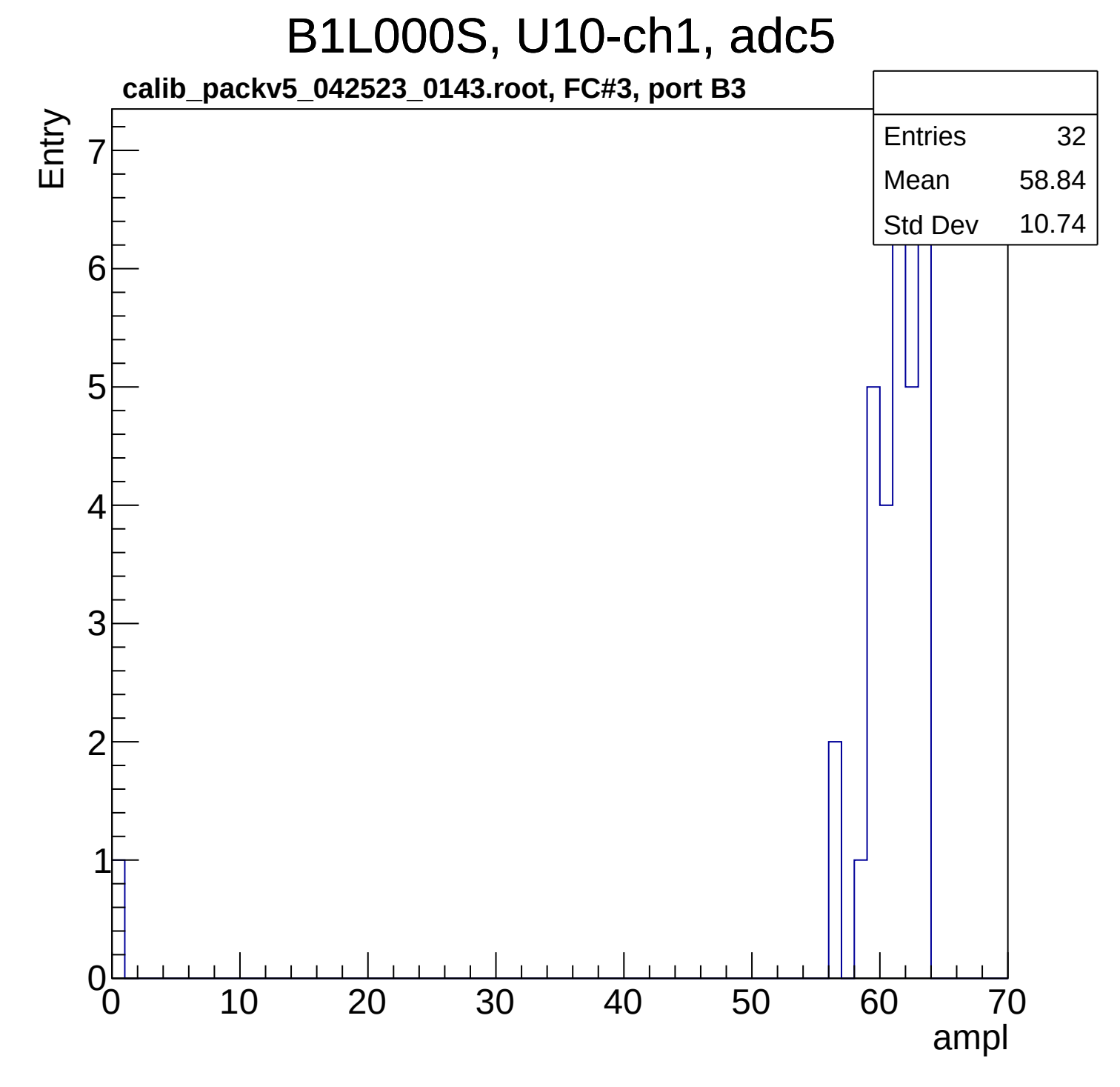

B1L000S, U10-ch1, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 55.93
Std Dev 3.123
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch1, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 58.84
Std Dev 10.74
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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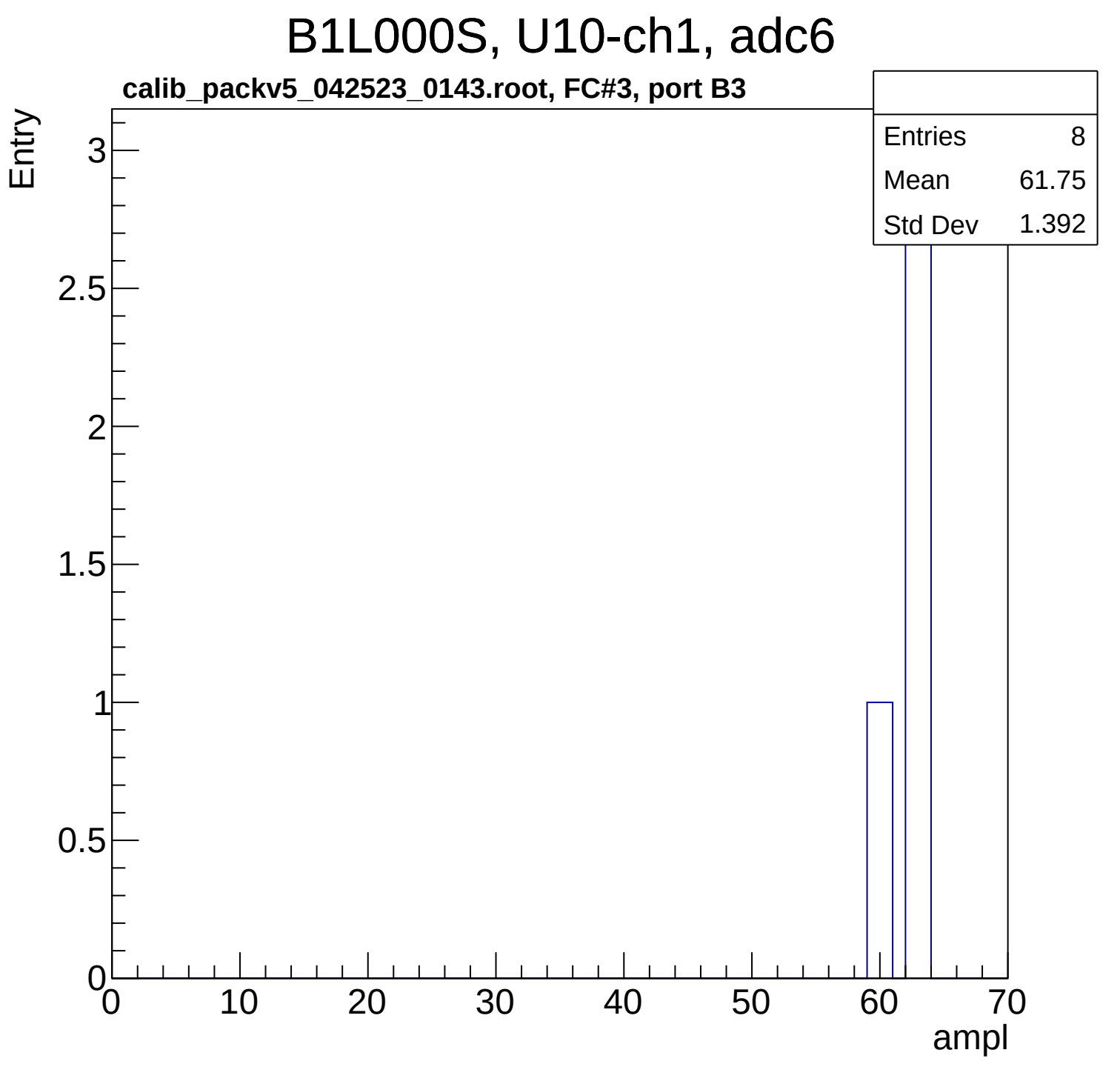
B1L000S, U10-ch1, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.75
Std Dev 1.392
Entry
ampl
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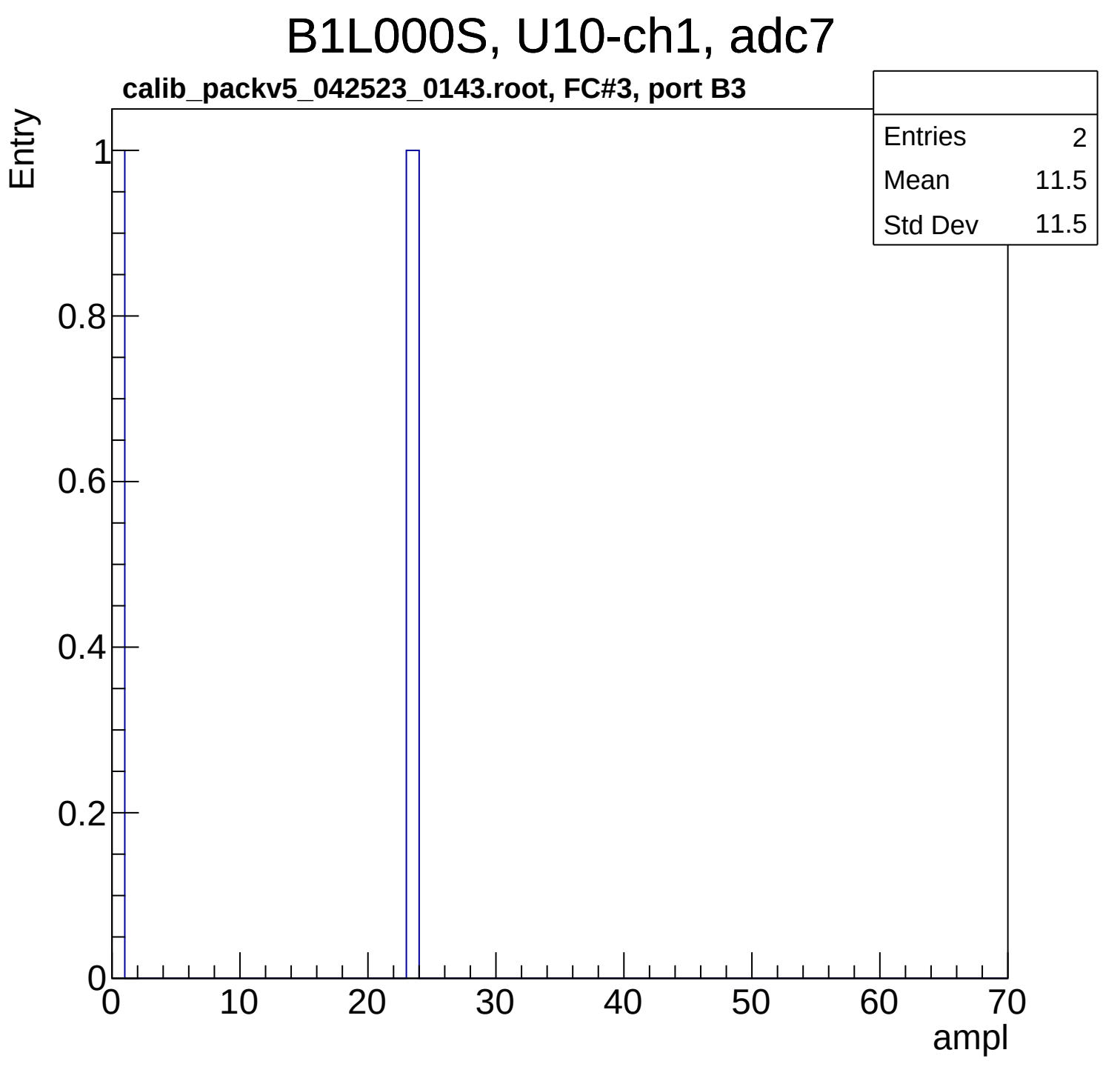
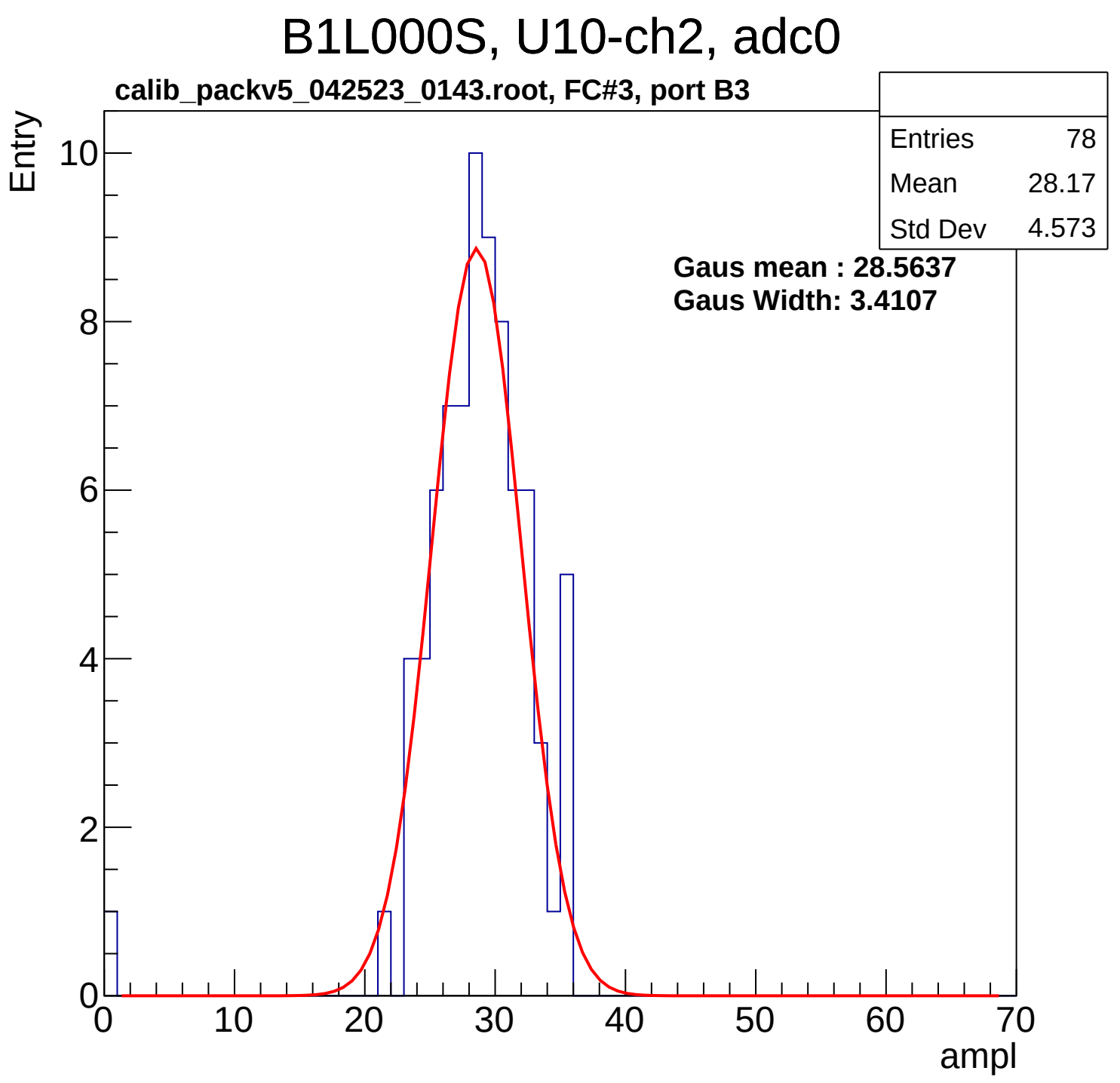
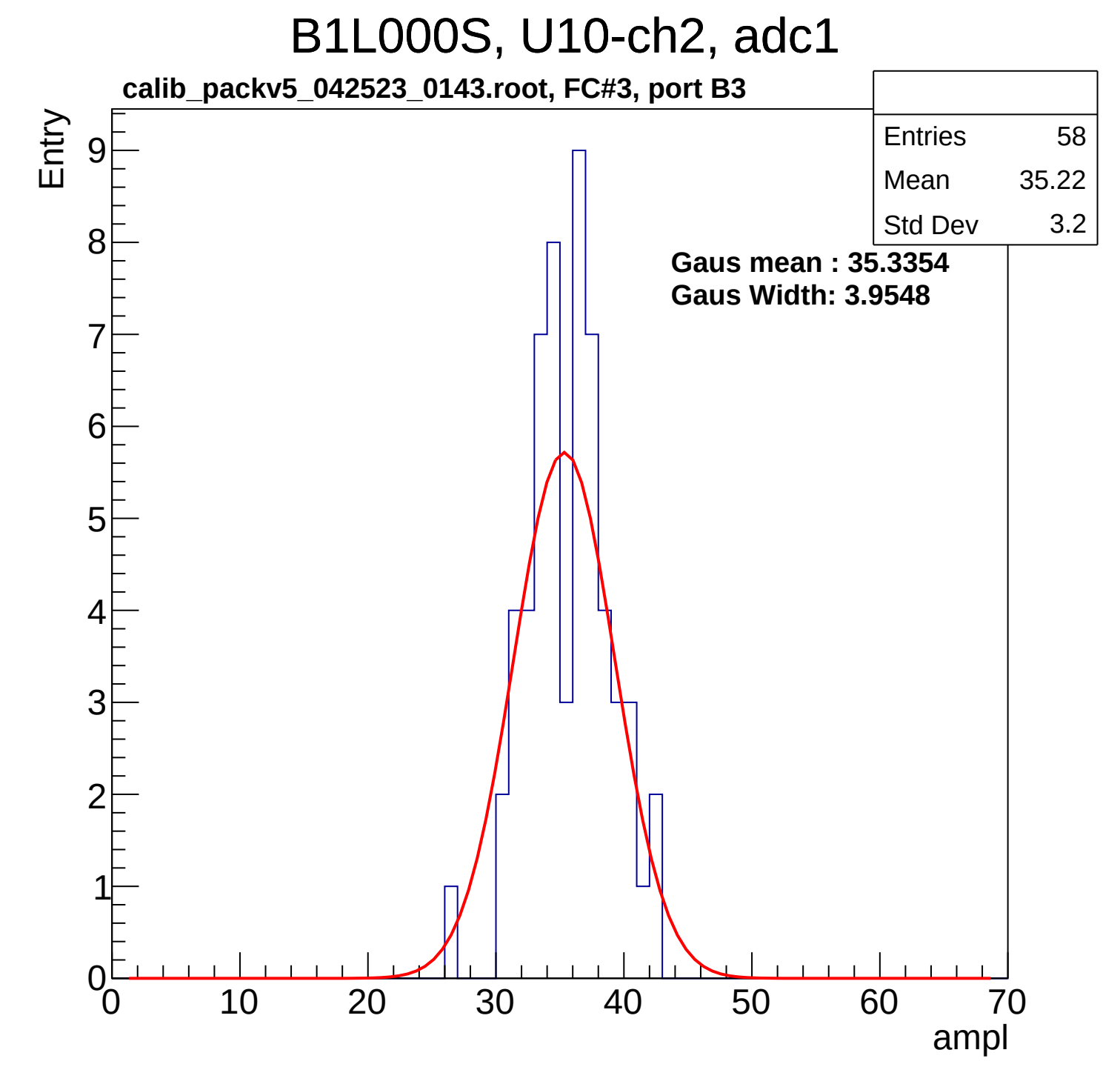
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch1, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch2, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 28.17
Std Dev 4.573
Gaus mean : 28.5637
Gaus Width: 3.4107
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch2, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.22
Std Dev 3.2
Gaus mean : 35.3354
Gaus Width: 3.9548
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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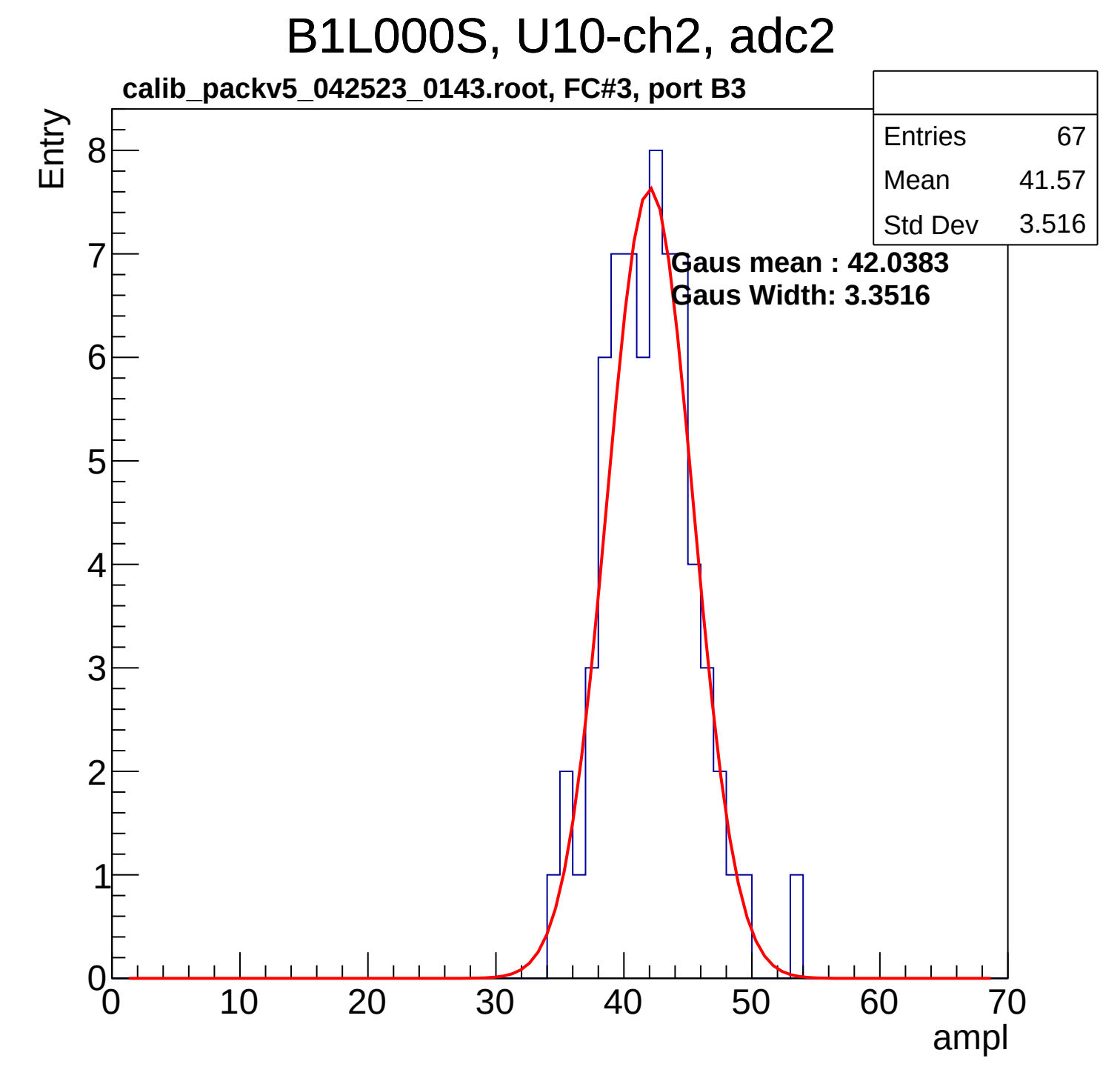
B1L000S, U10-ch2, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.57
Std Dev 3.516
Gaus mean : 42.0383
Gaus Width: 3.3516
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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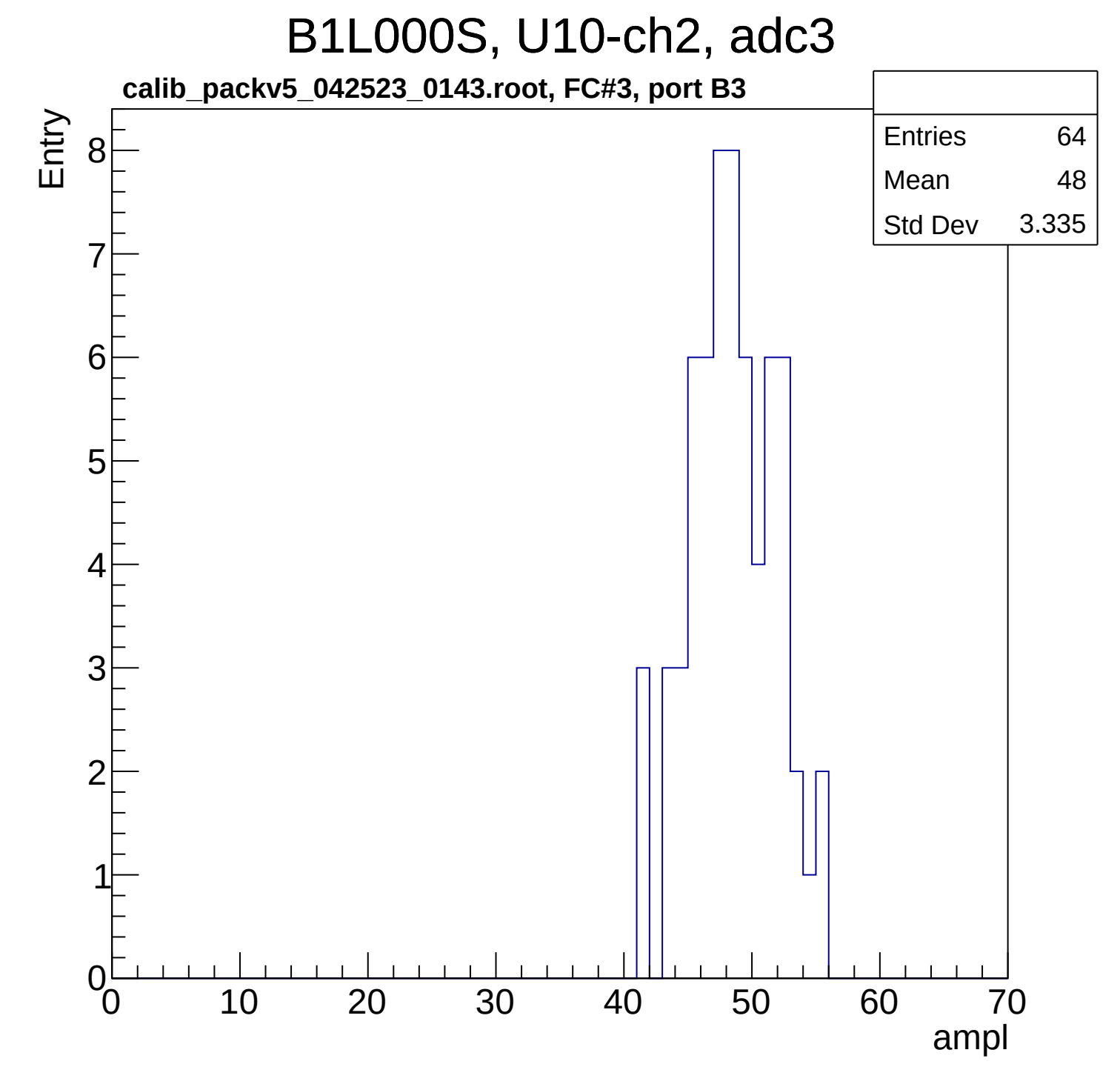
B1L000S, U10-ch2, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 48
Std Dev 3.335
Entry
ampl
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4
5
6
7
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0
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60
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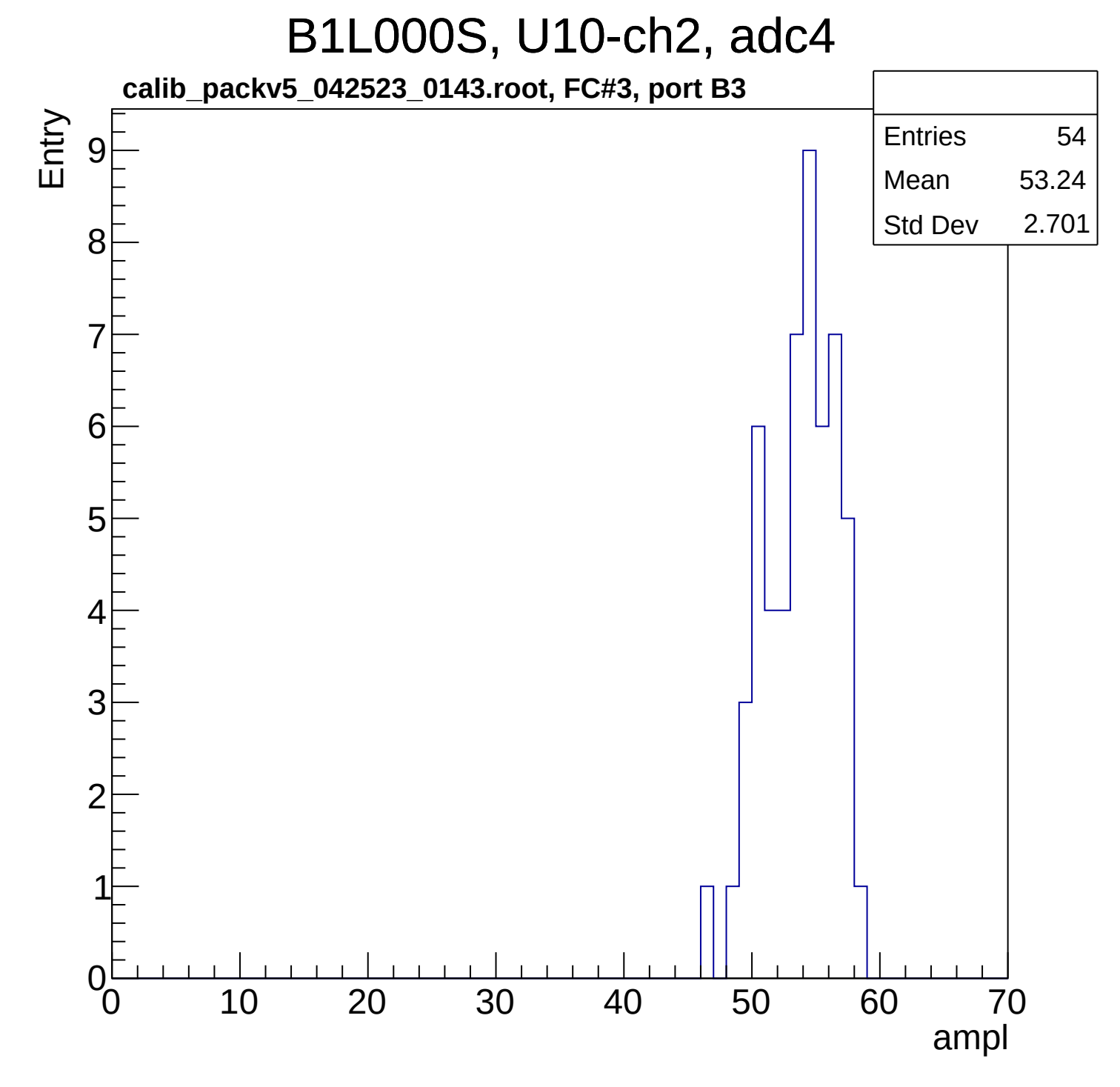
B1L000S, U10-ch2, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.24
Std Dev 2.701
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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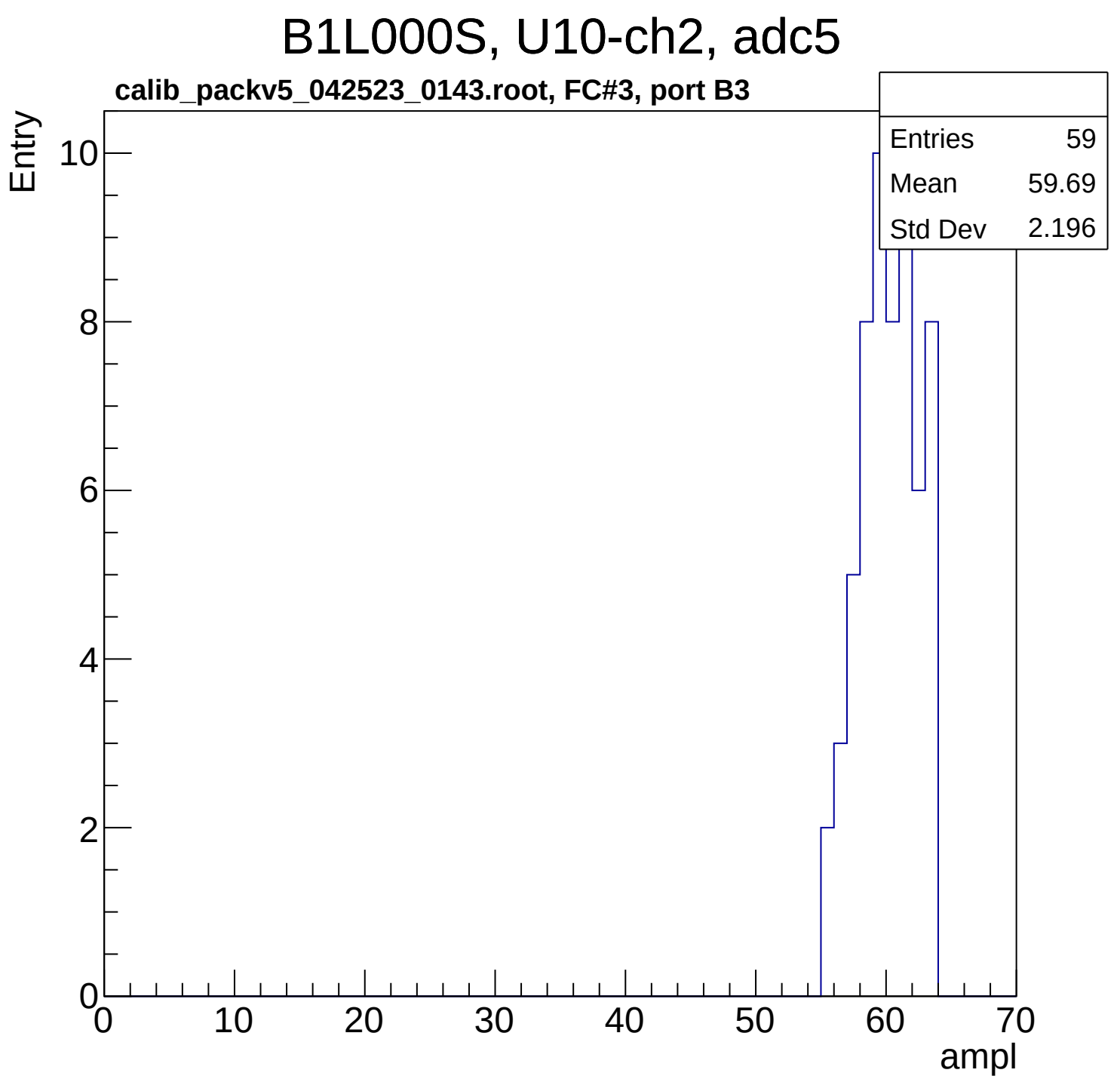
B1L000S, U10-ch2, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 59.69
Std Dev 2.196
Entry
ampl
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2
4
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8
10
0
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20
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40
50
60
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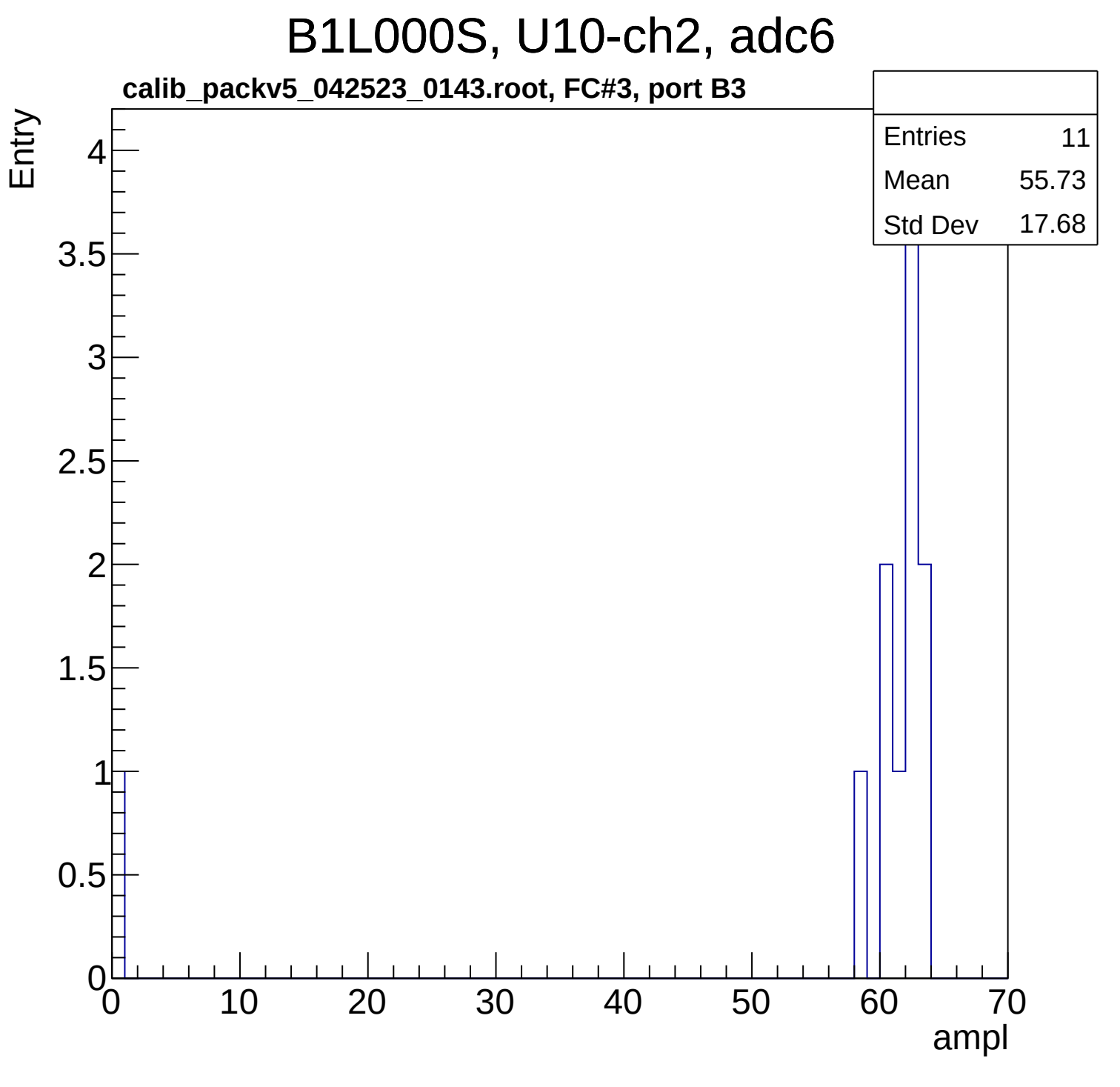
B1L000S, U10-ch2, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 55.73
Std Dev 17.68
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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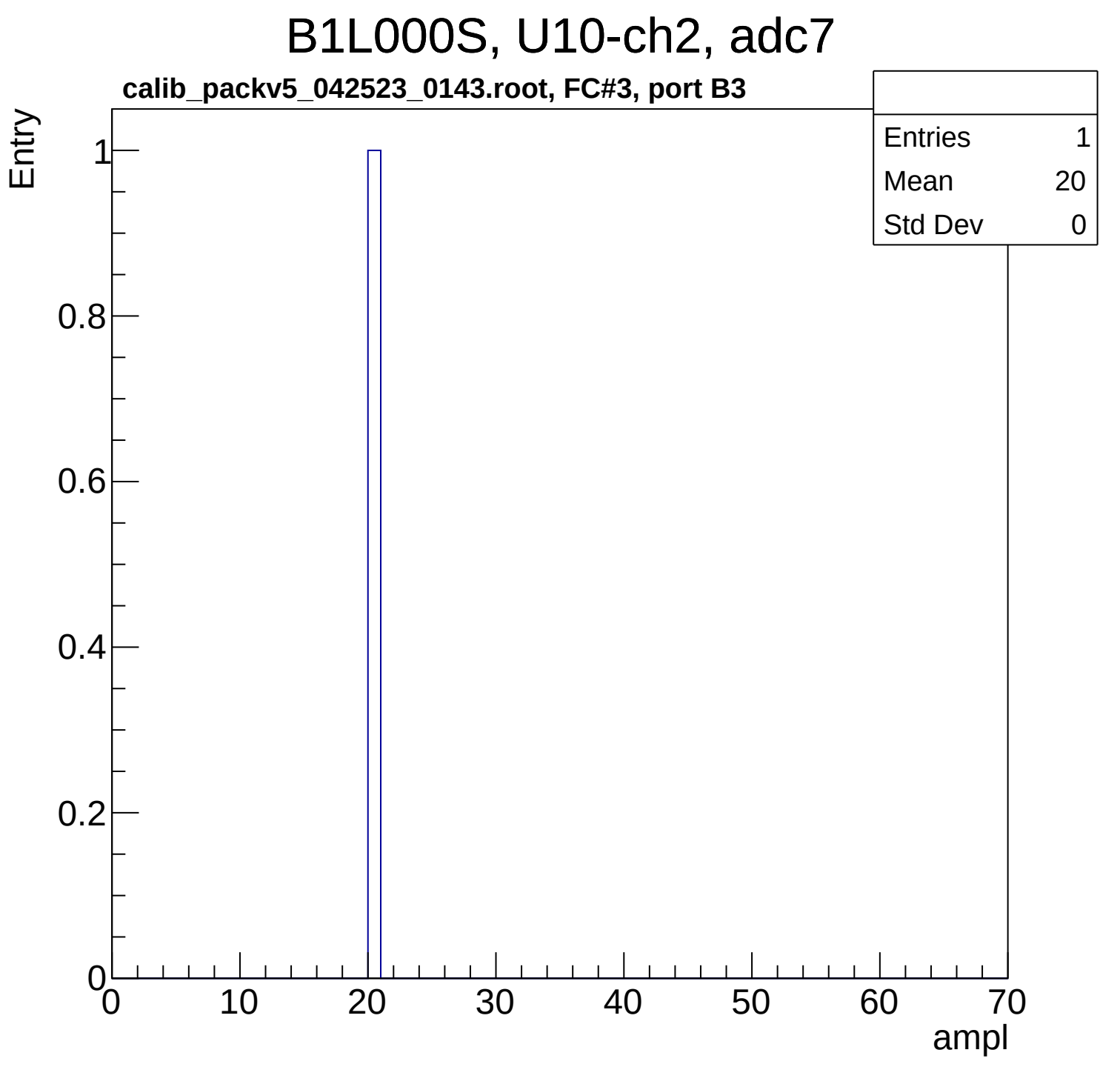
B1L000S, U10-ch2, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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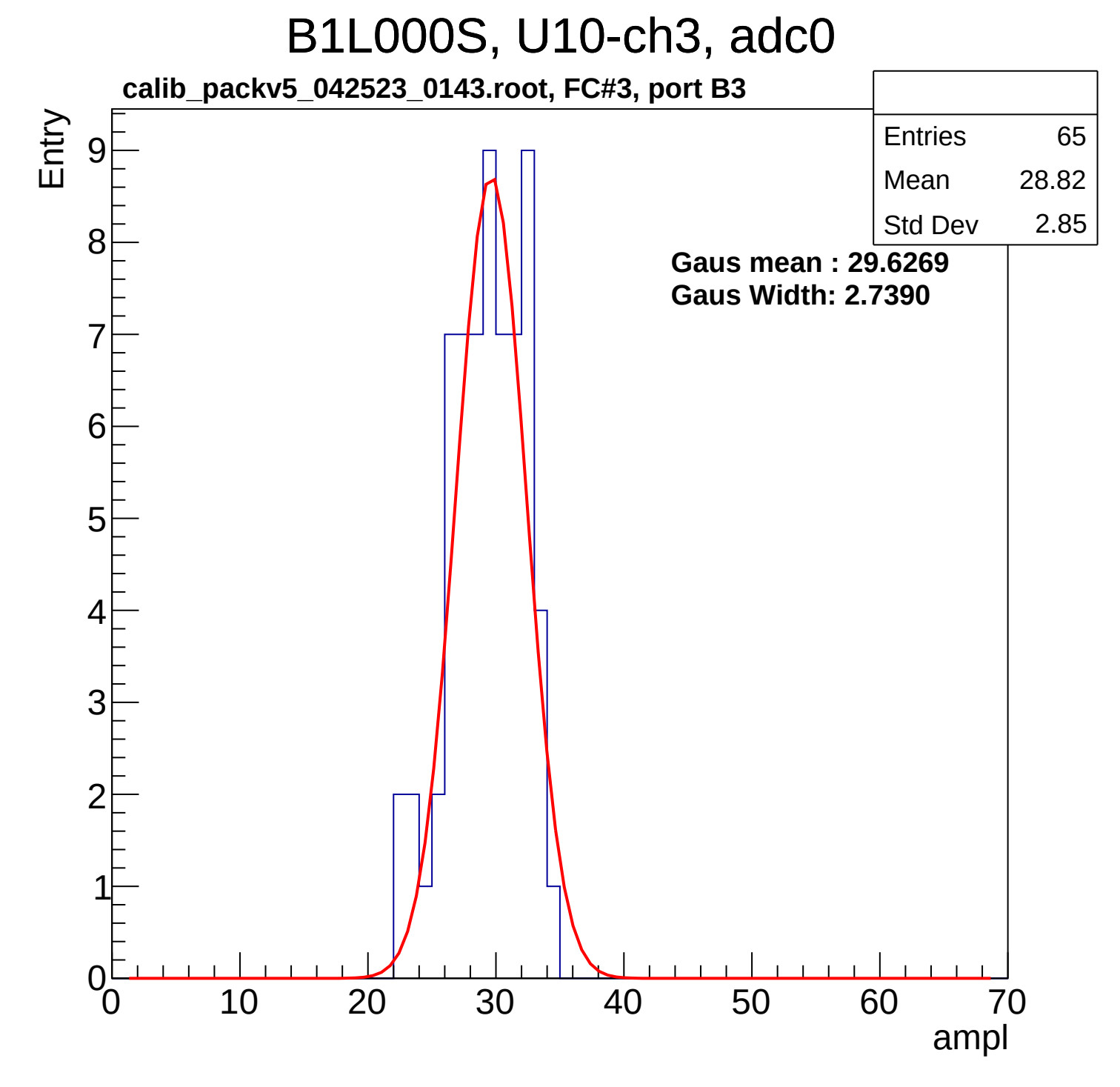
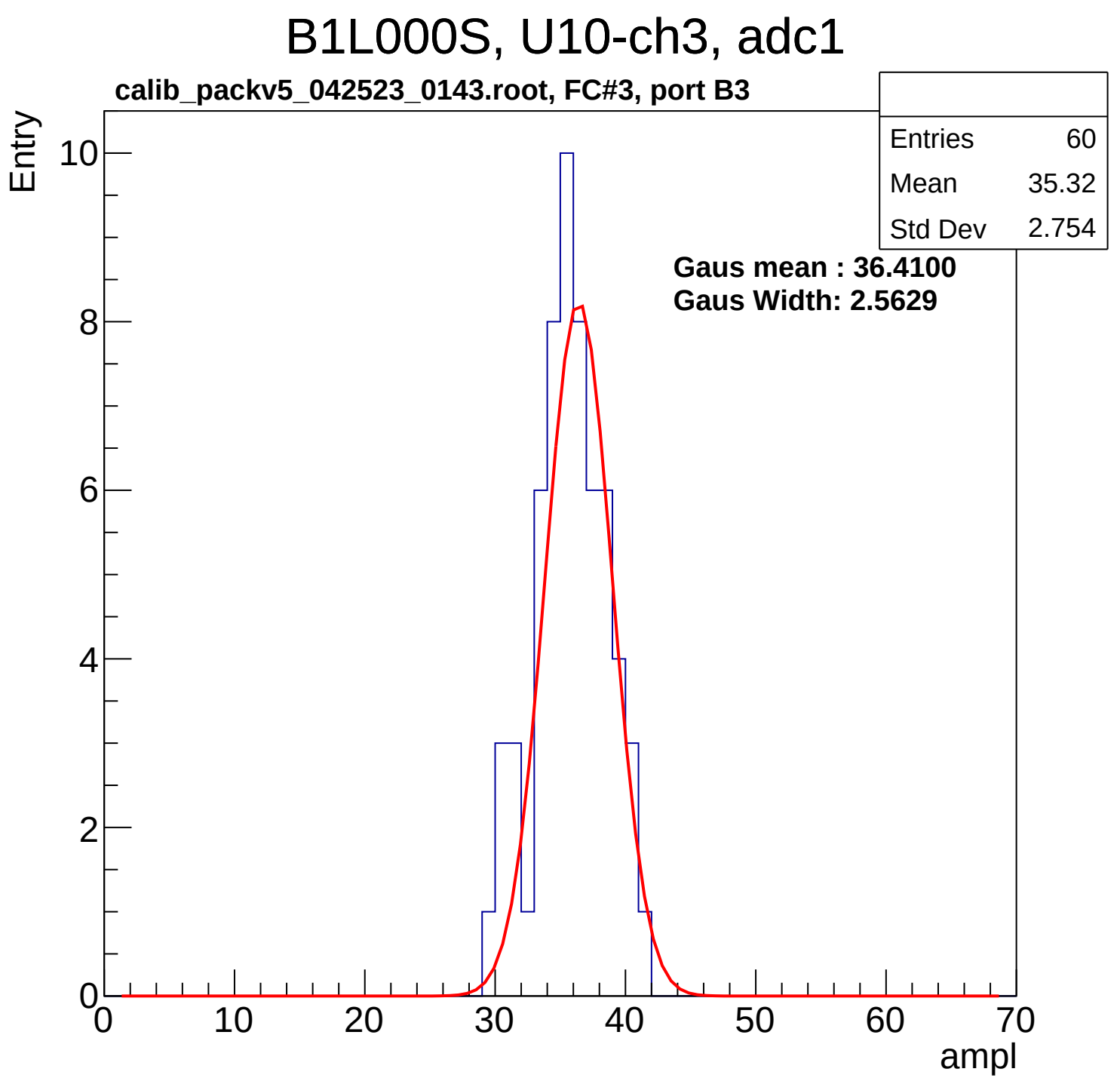
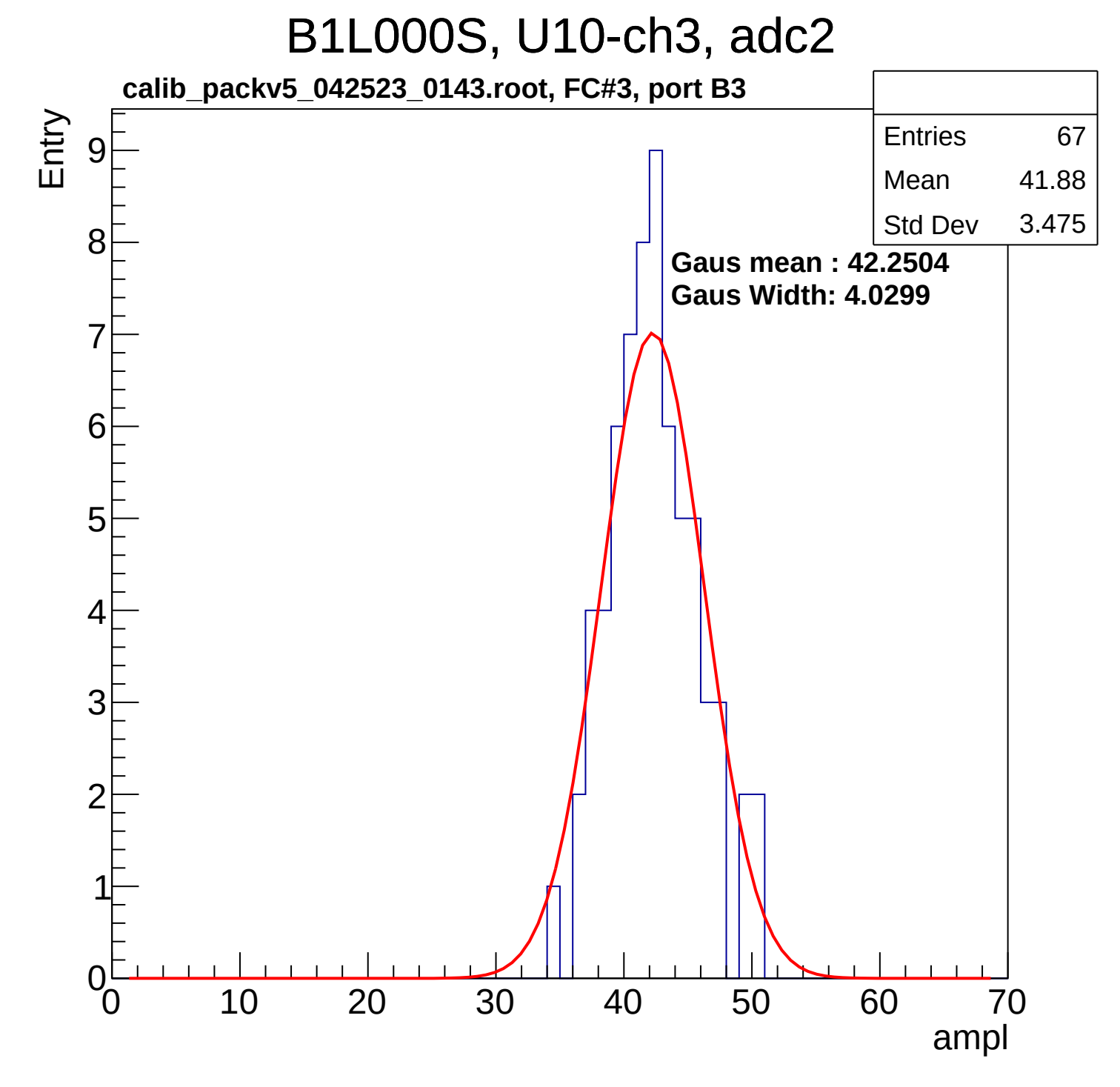
40
50
60
70
ampl


B1L000S, U10-ch3, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.82
Std Dev 2.85
Gaus mean : 29.6269
Gaus Width: 2.7390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch3, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 35.32
Std Dev 2.754
Gaus mean : 36.4100
Gaus Width: 2.5629
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch3, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.88
Std Dev 3.475
Gaus mean : 42.2504
Gaus Width: 4.0299
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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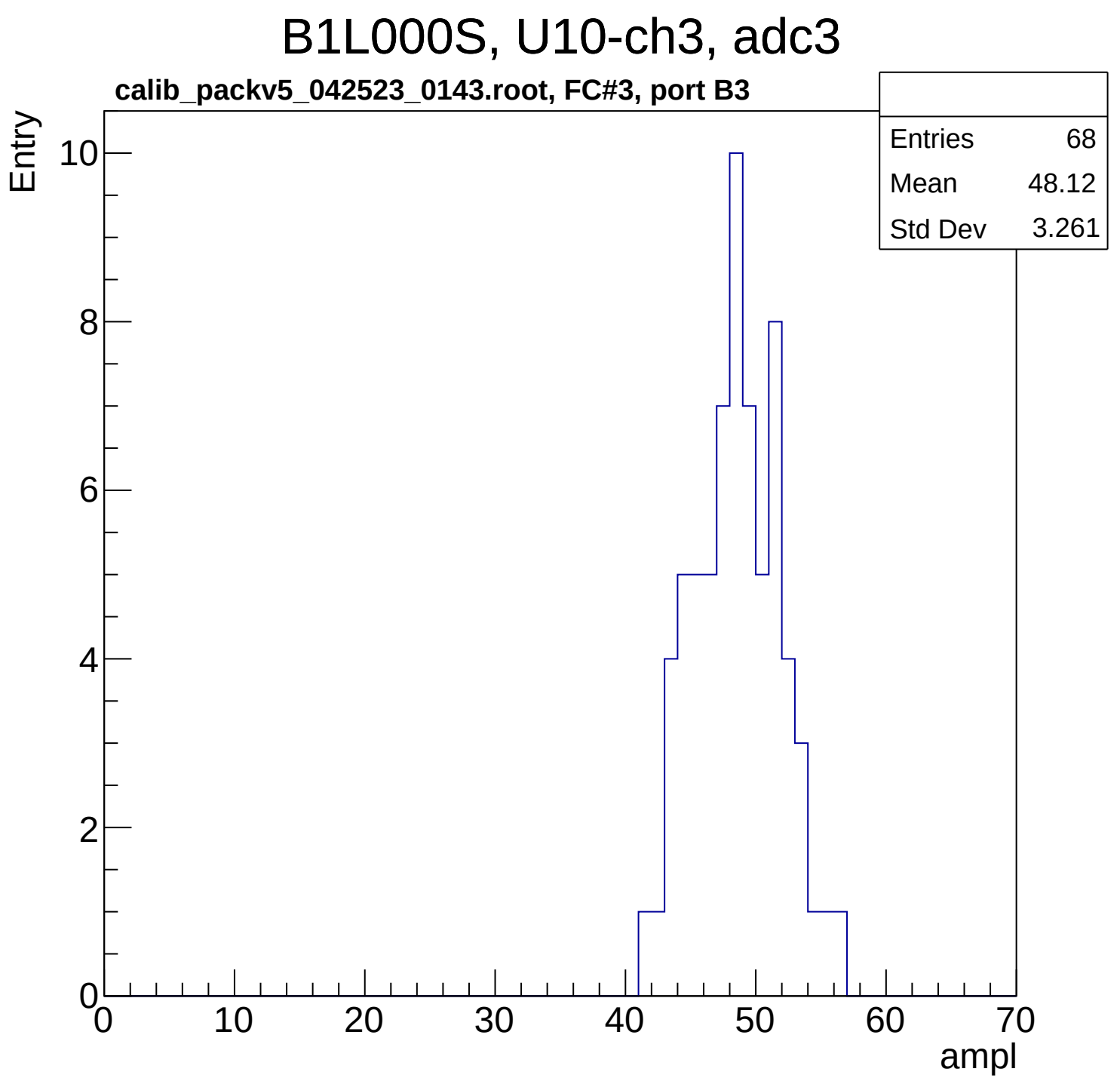
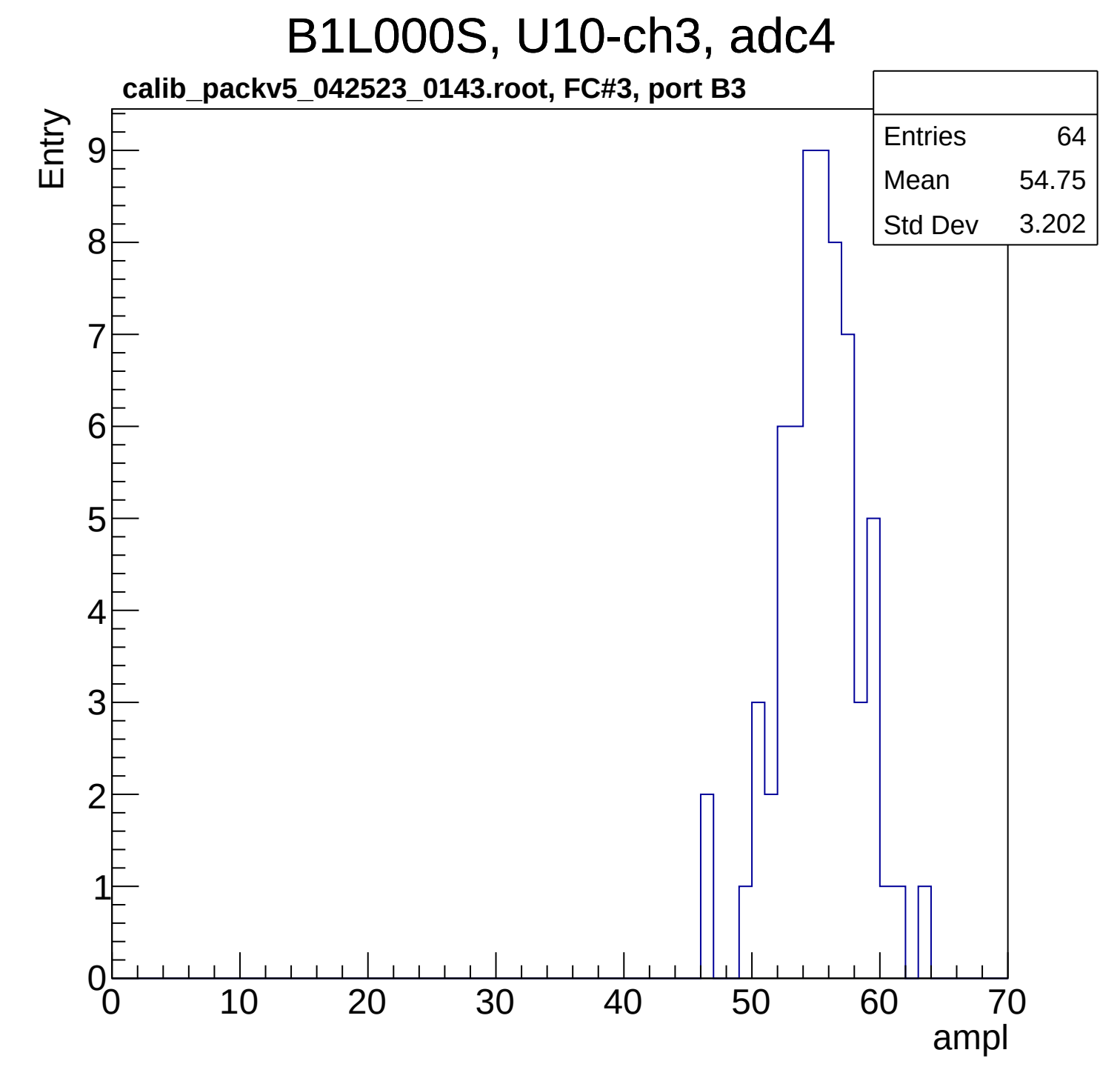
60
70


B1L000S, U10-ch3, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 48.12
Std Dev 3.261
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch3, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 54.75
Std Dev 3.202
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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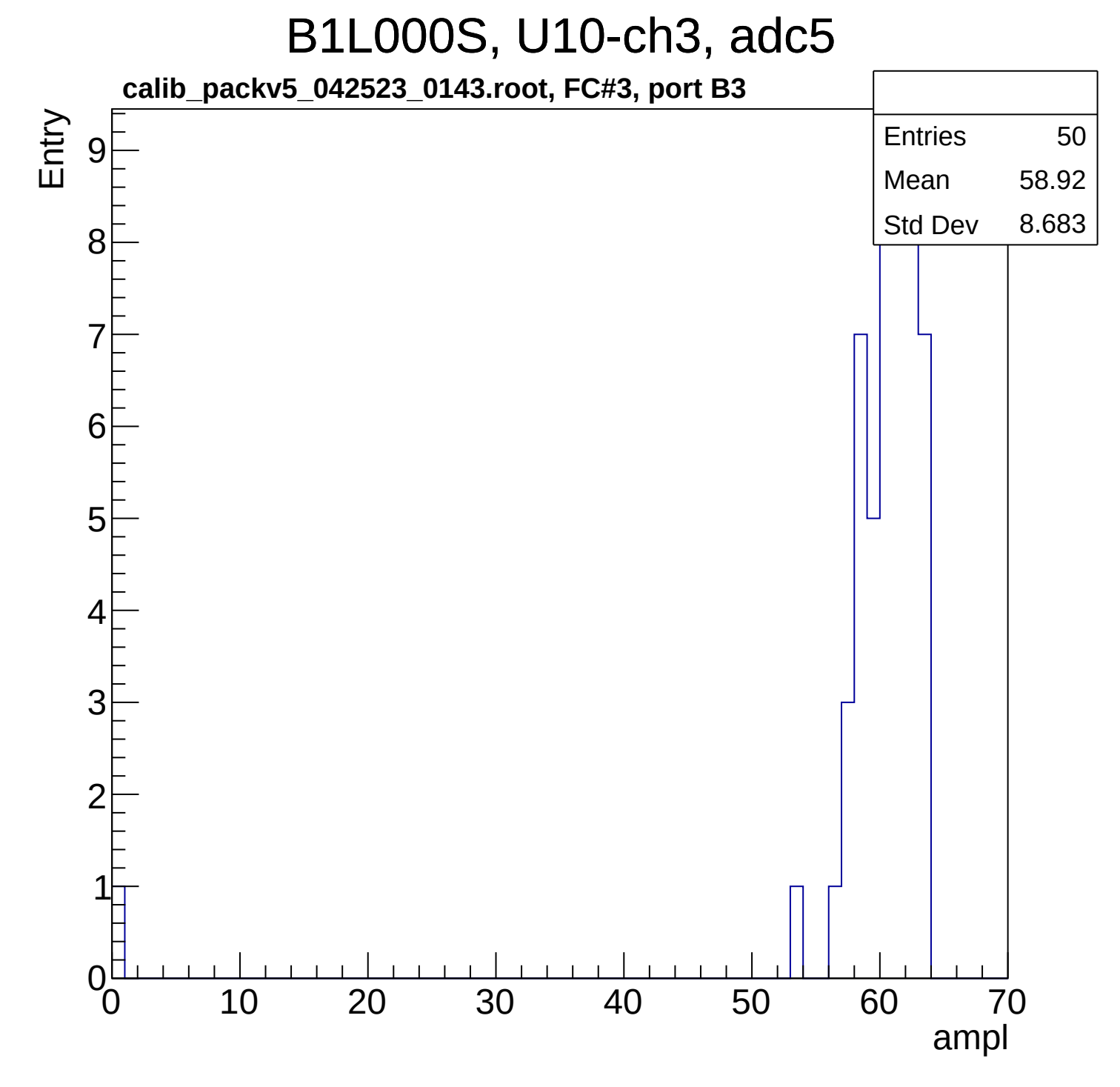
B1L000S, U10-ch3, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.92
Std Dev 8.683
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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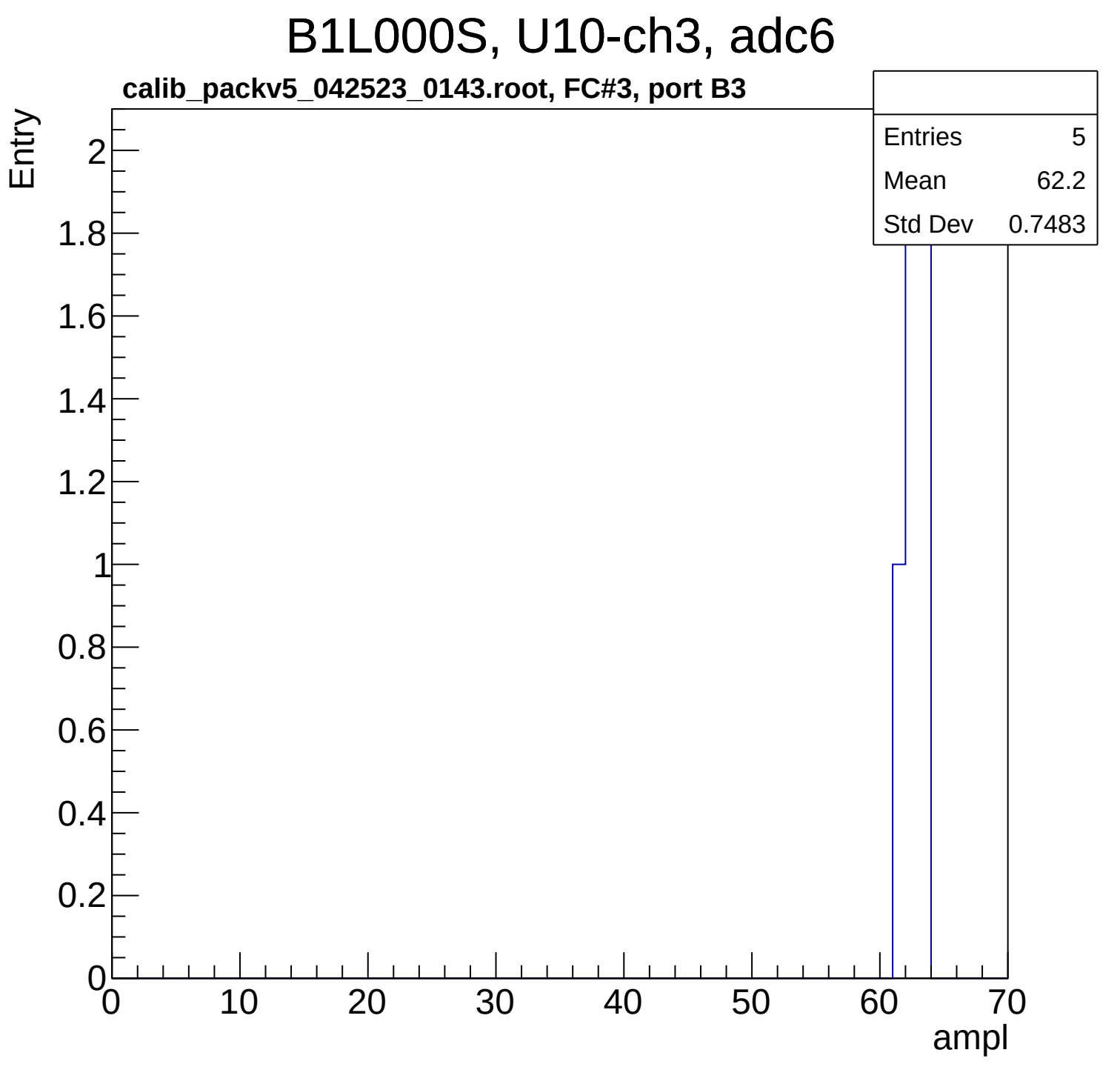
B1L000S, U10-ch3, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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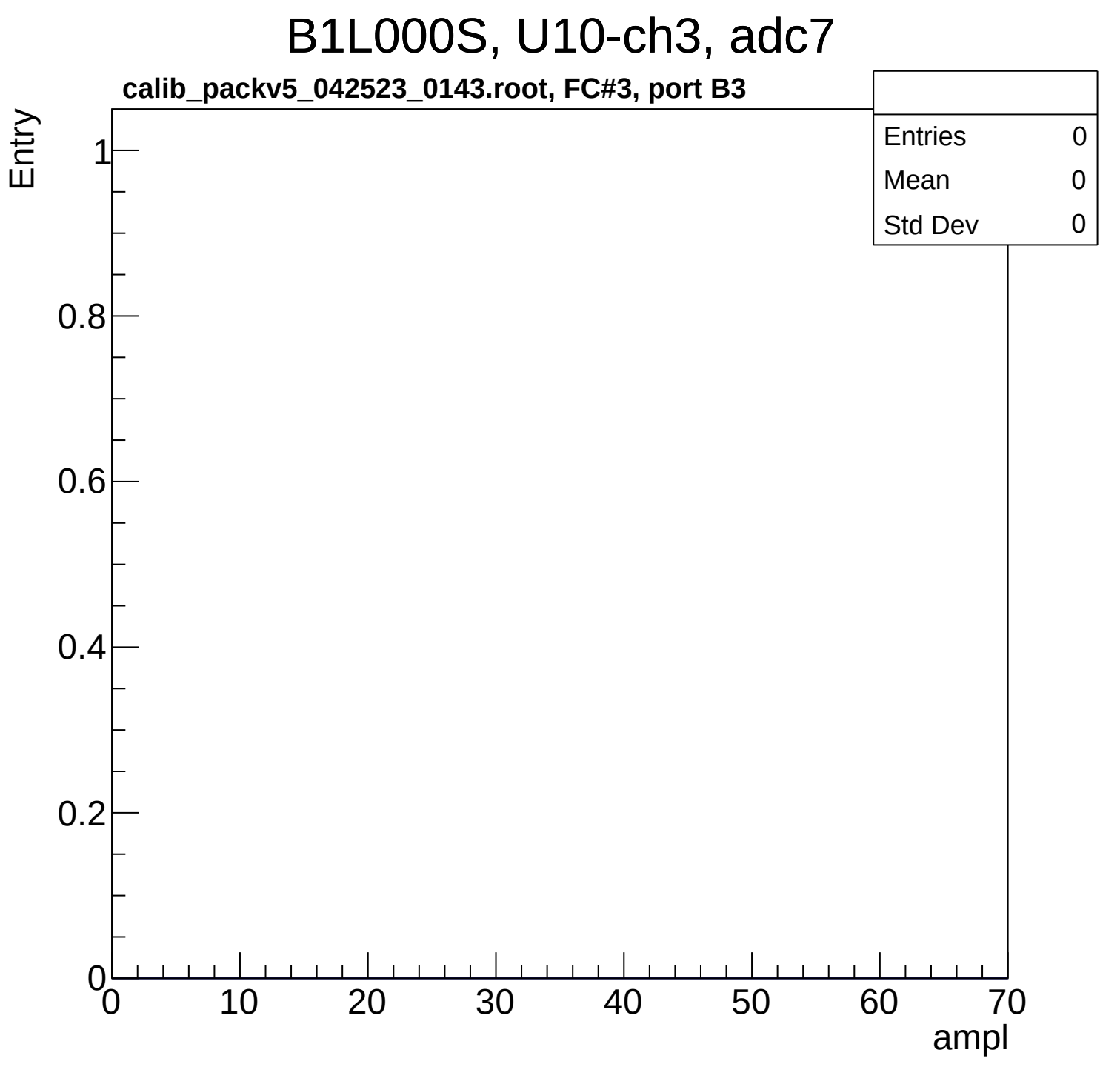
B1L000S, U10-ch3, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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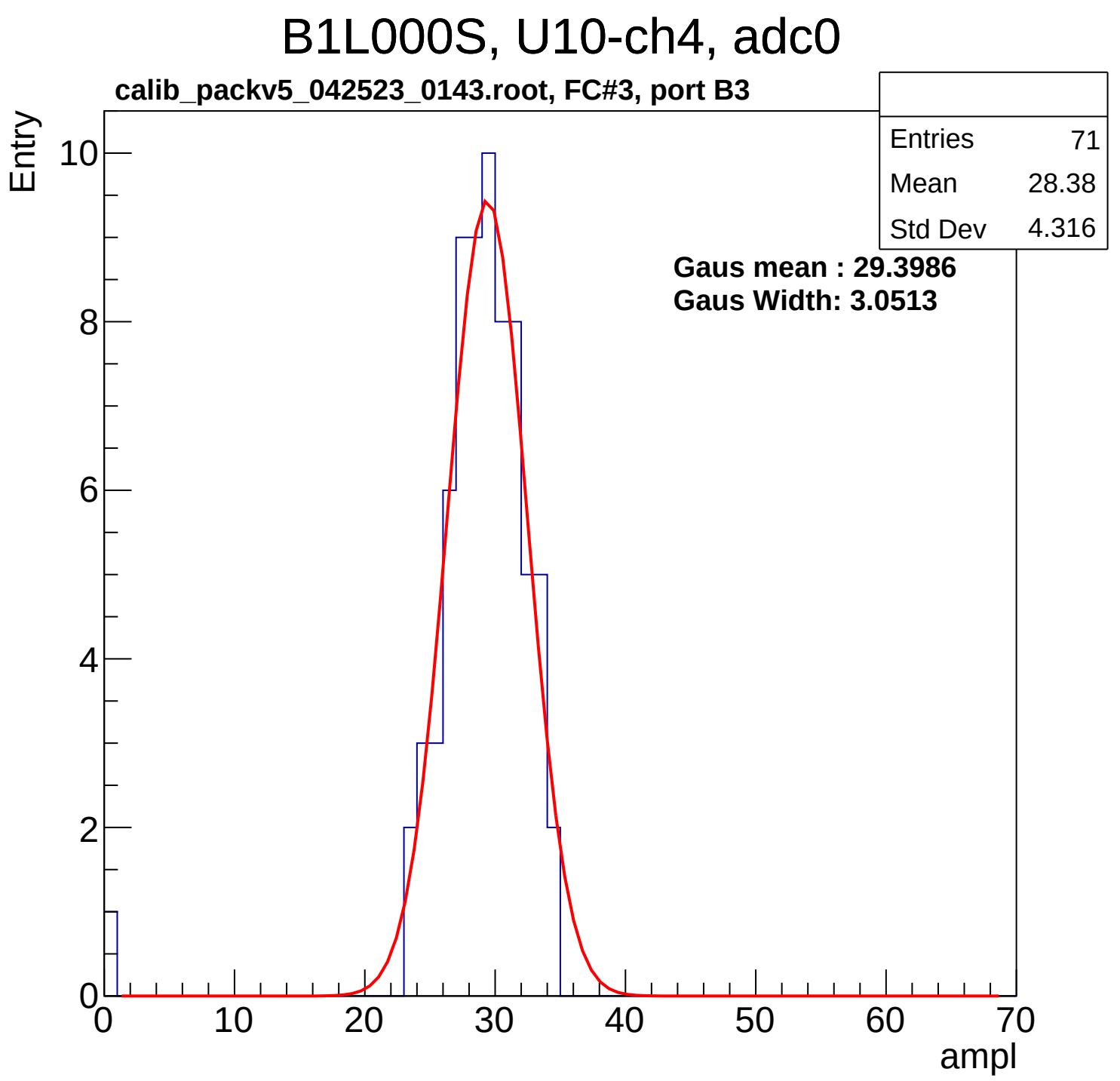
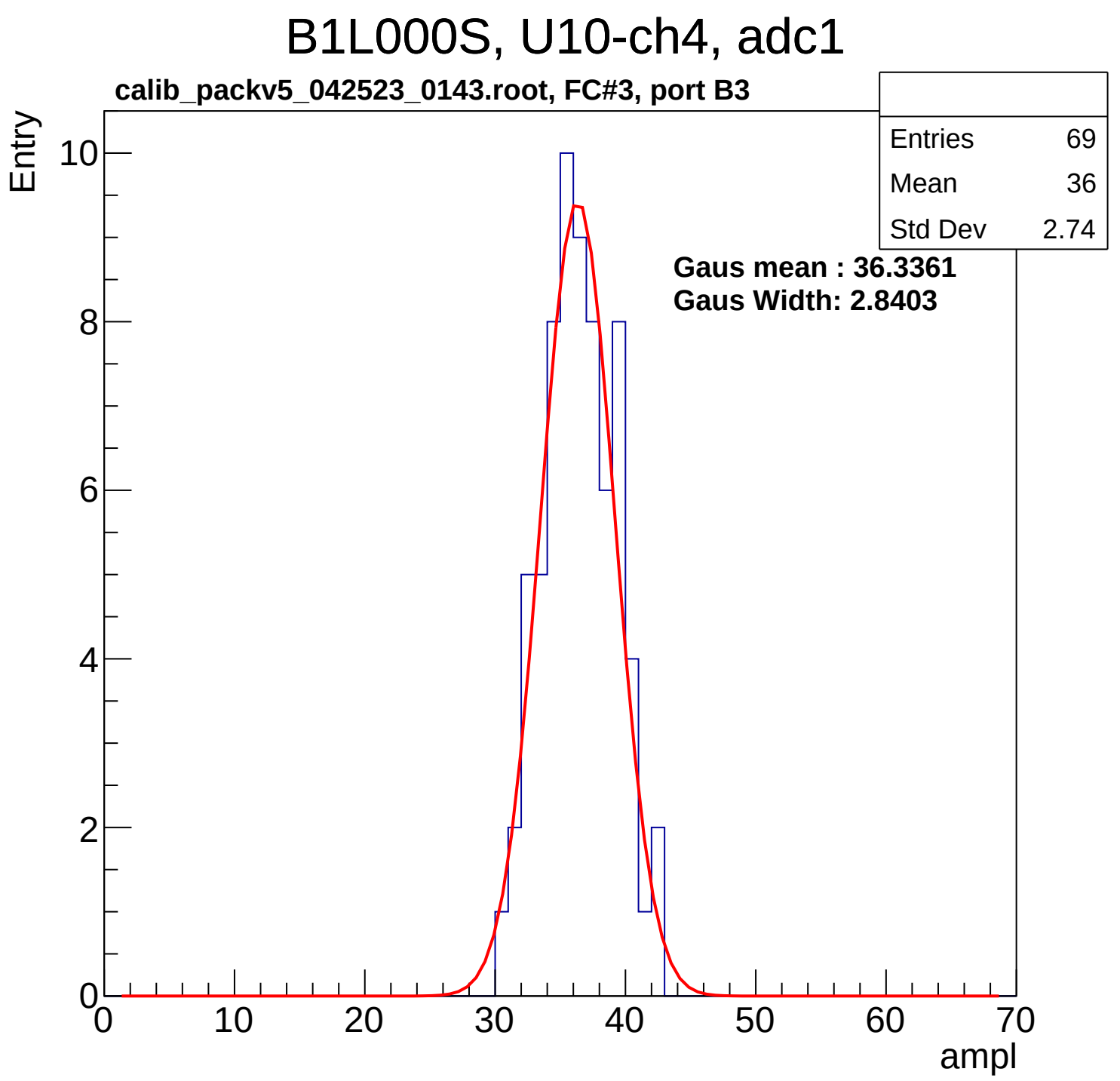
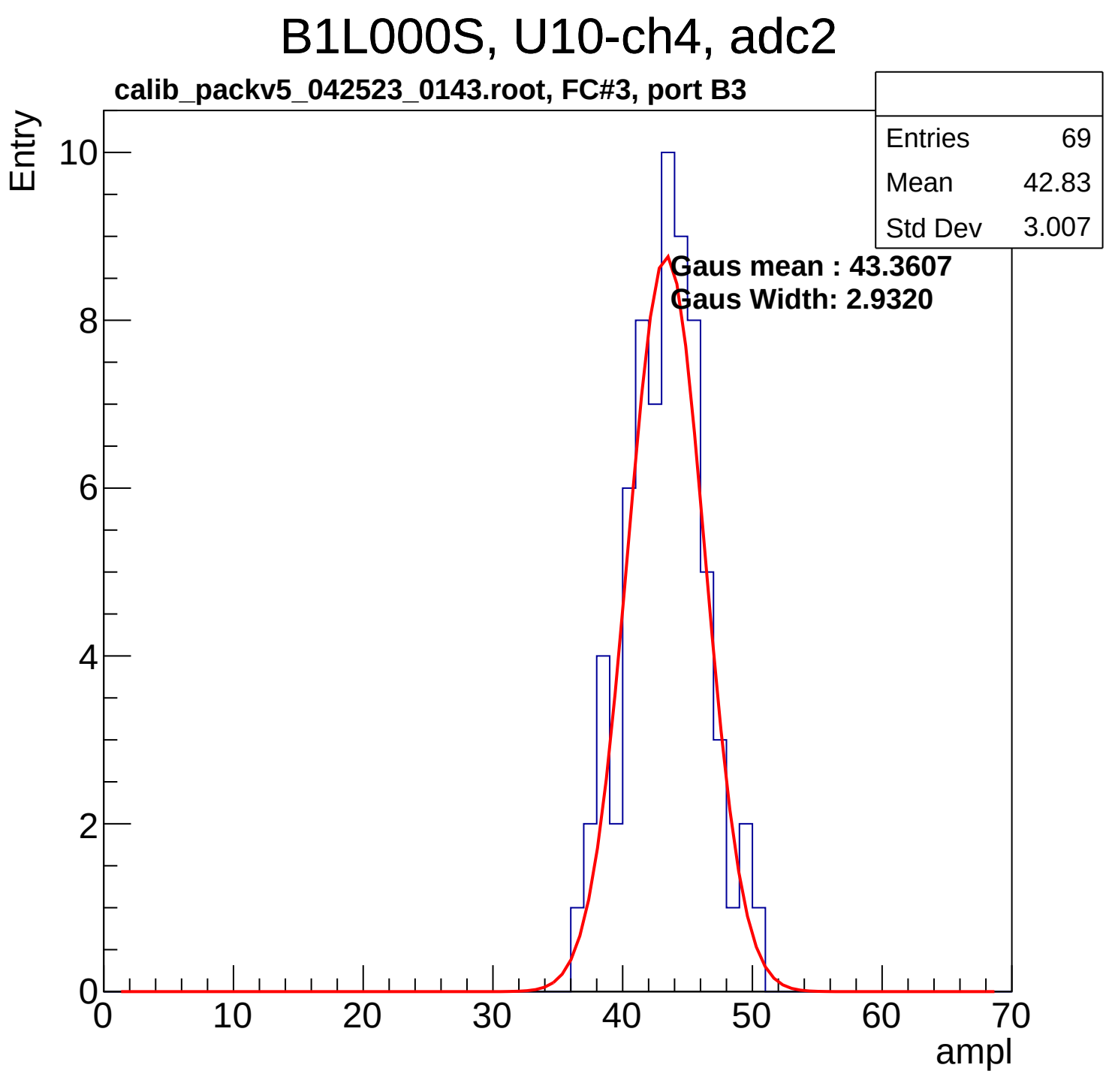
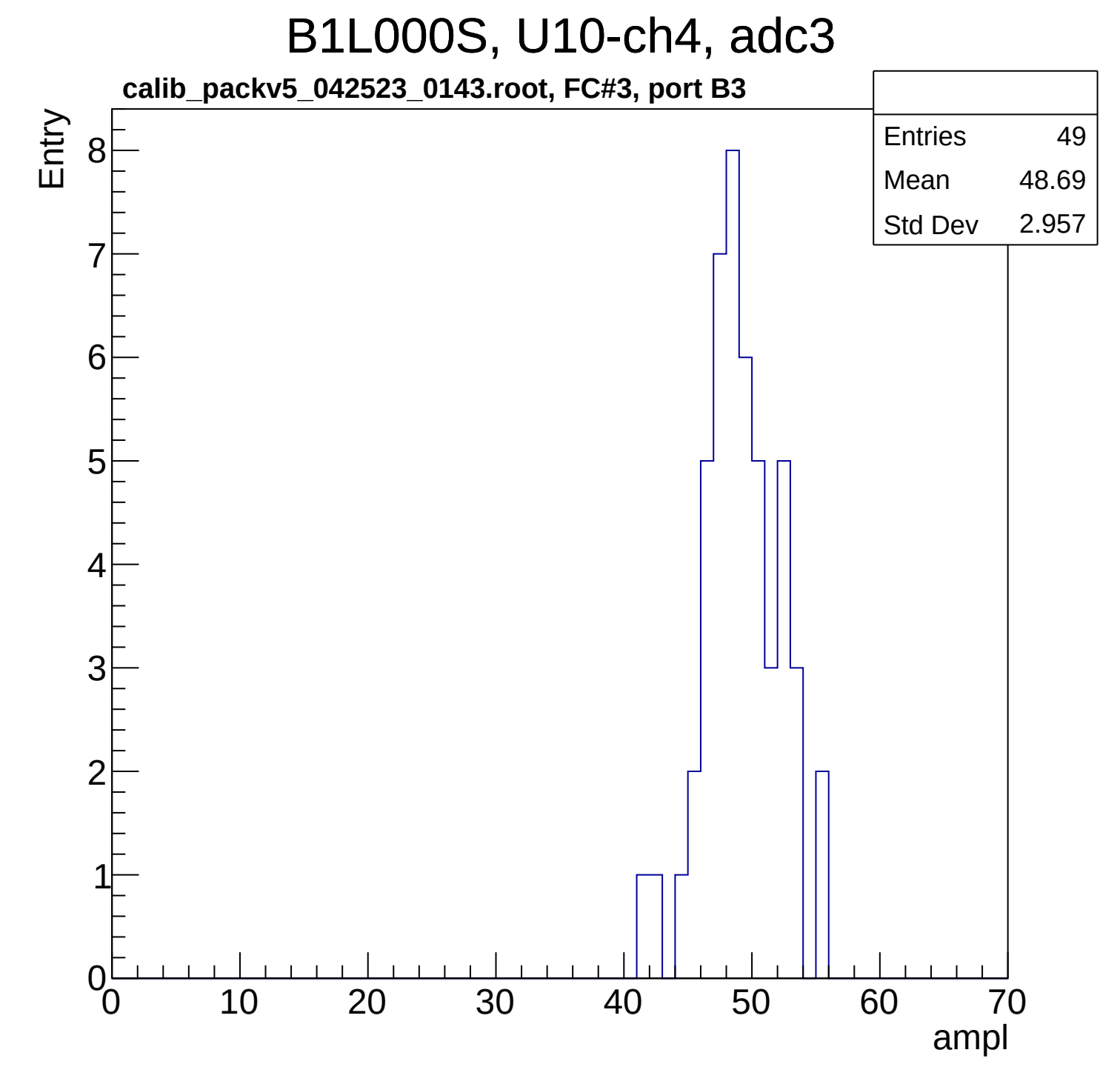
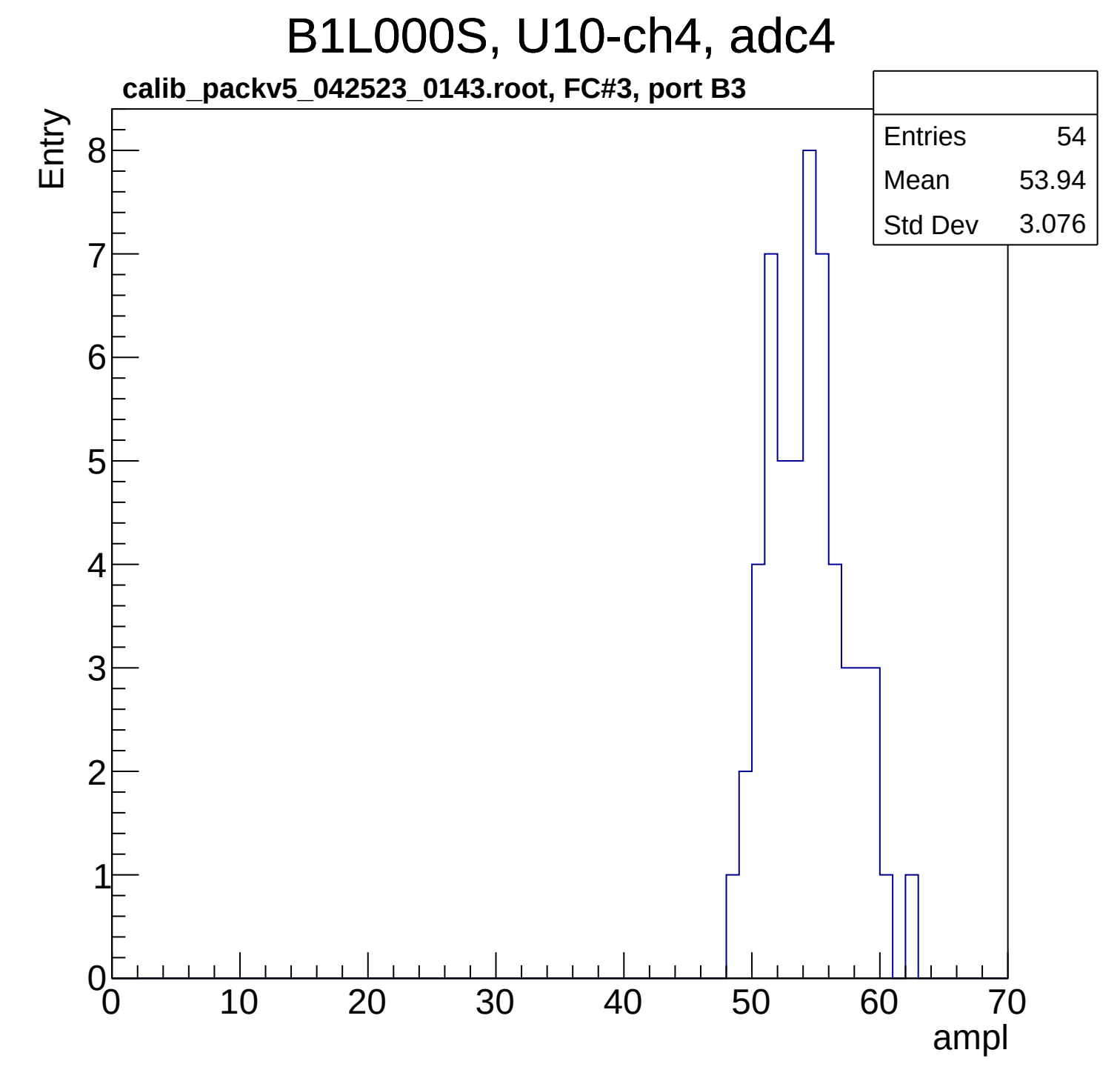
40
50
60
70
ampl


B1L000S, U10-ch4, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 28.38
Std Dev 4.316
Gaus mean : 29.3986
Gaus Width: 3.0513
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch4, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 36
Std Dev 2.74
Gaus mean : 36.3361
Gaus Width: 2.8403
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch4, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 42.83
Std Dev 3.007
Gaus mean : 43.3607
Gaus Width: 2.9320
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch4, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 48.69
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch4, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.94
Std Dev 3.076
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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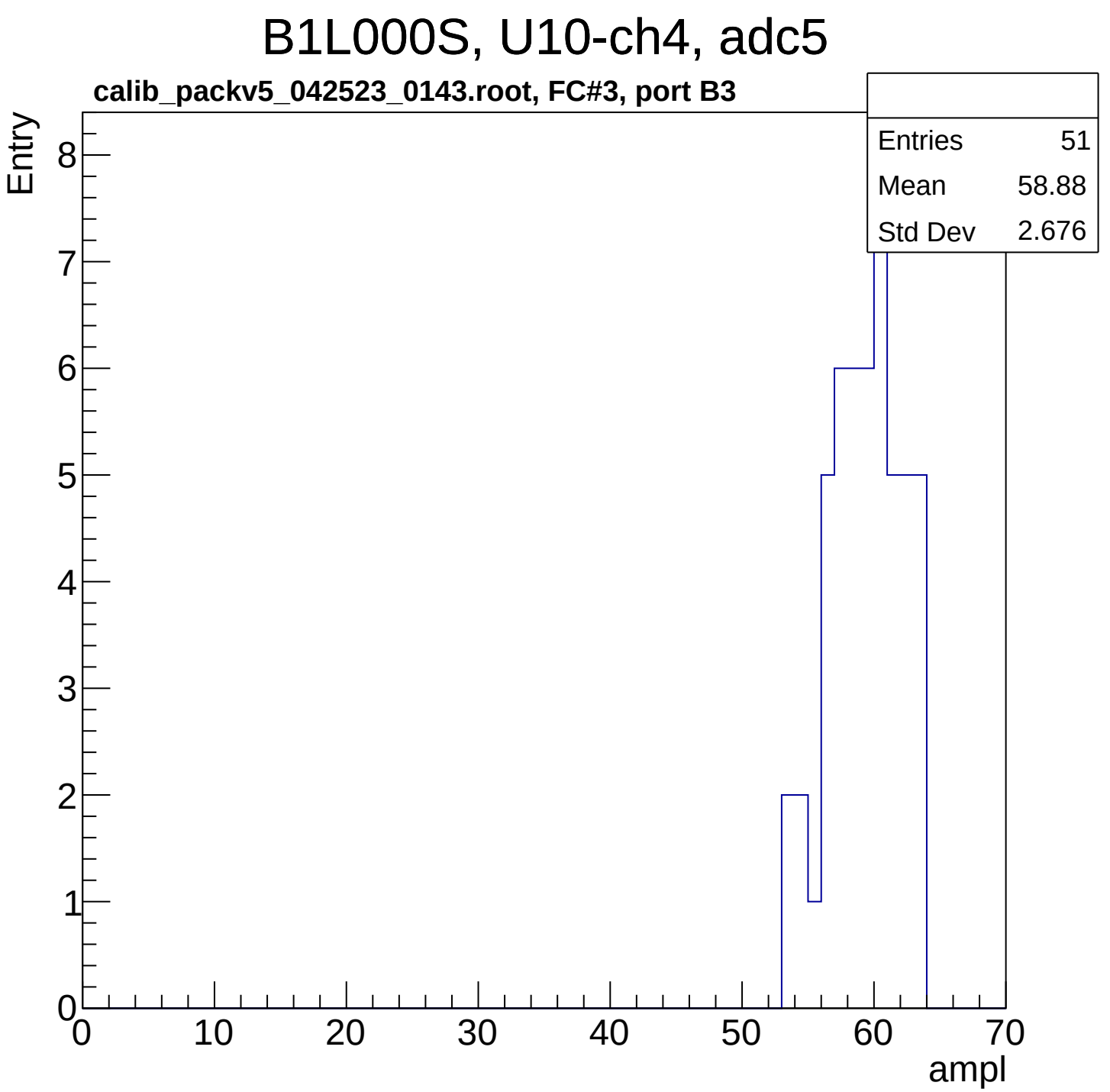
B1L000S, U10-ch4, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 58.88
Std Dev 2.676
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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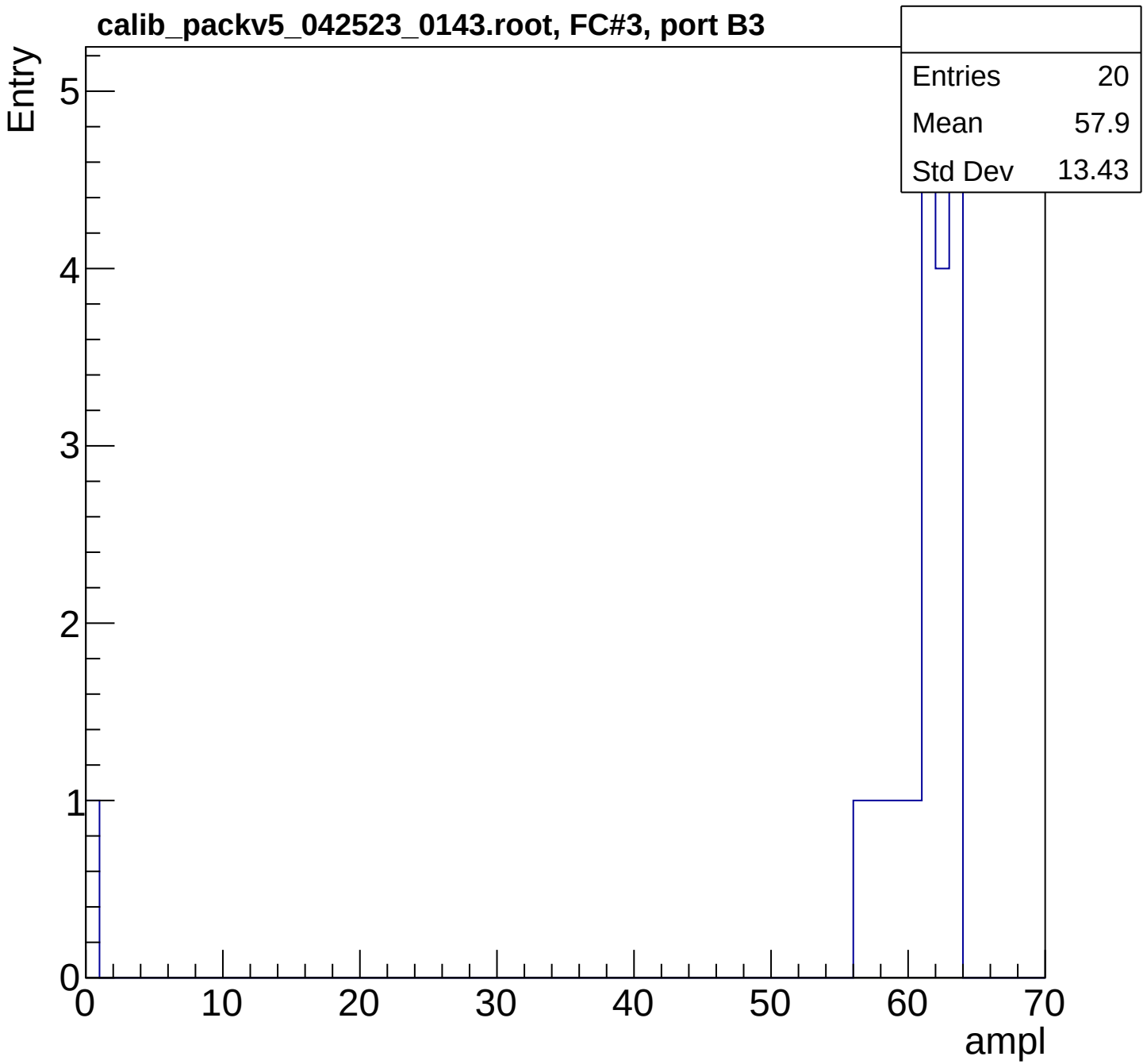
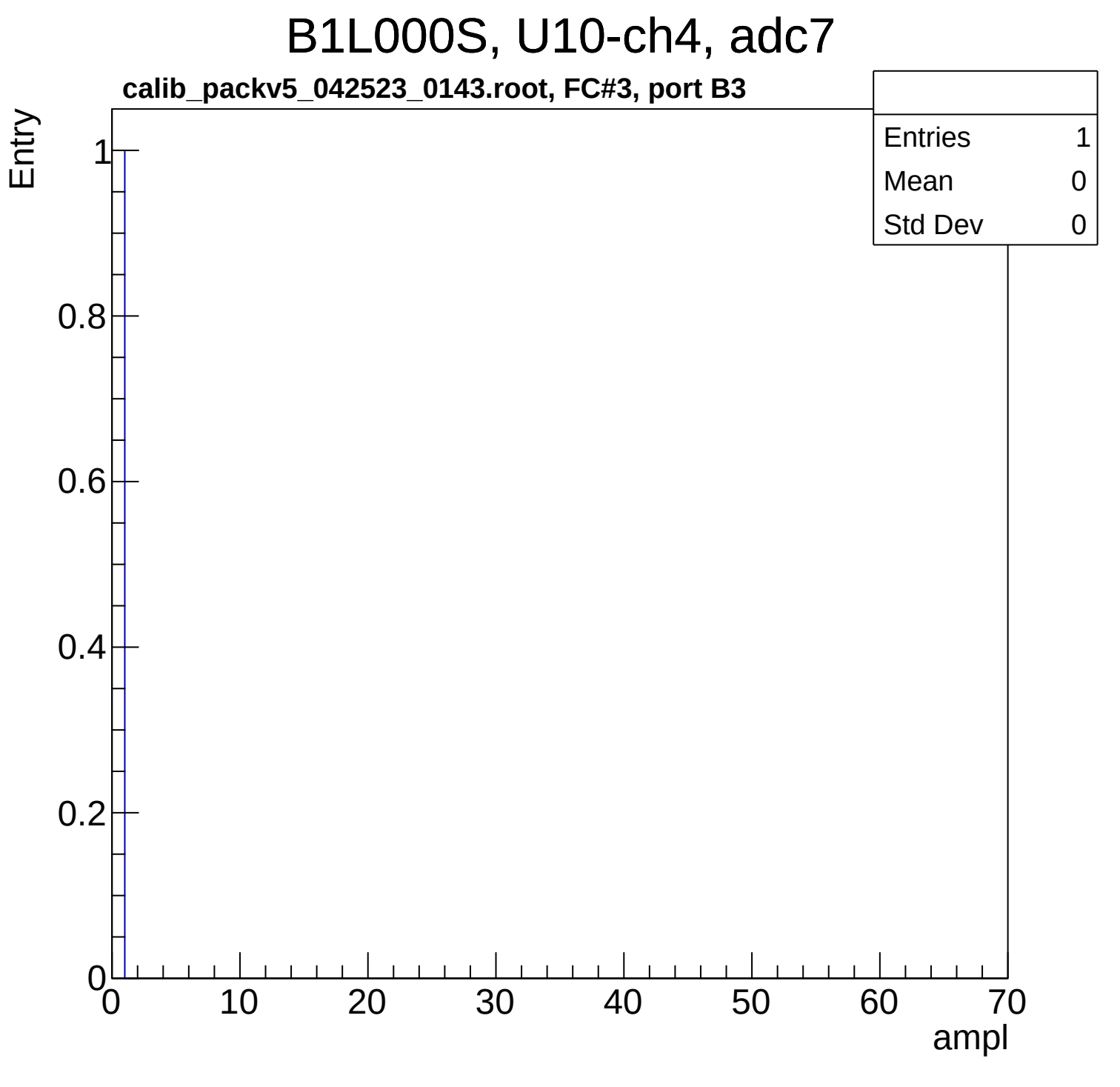
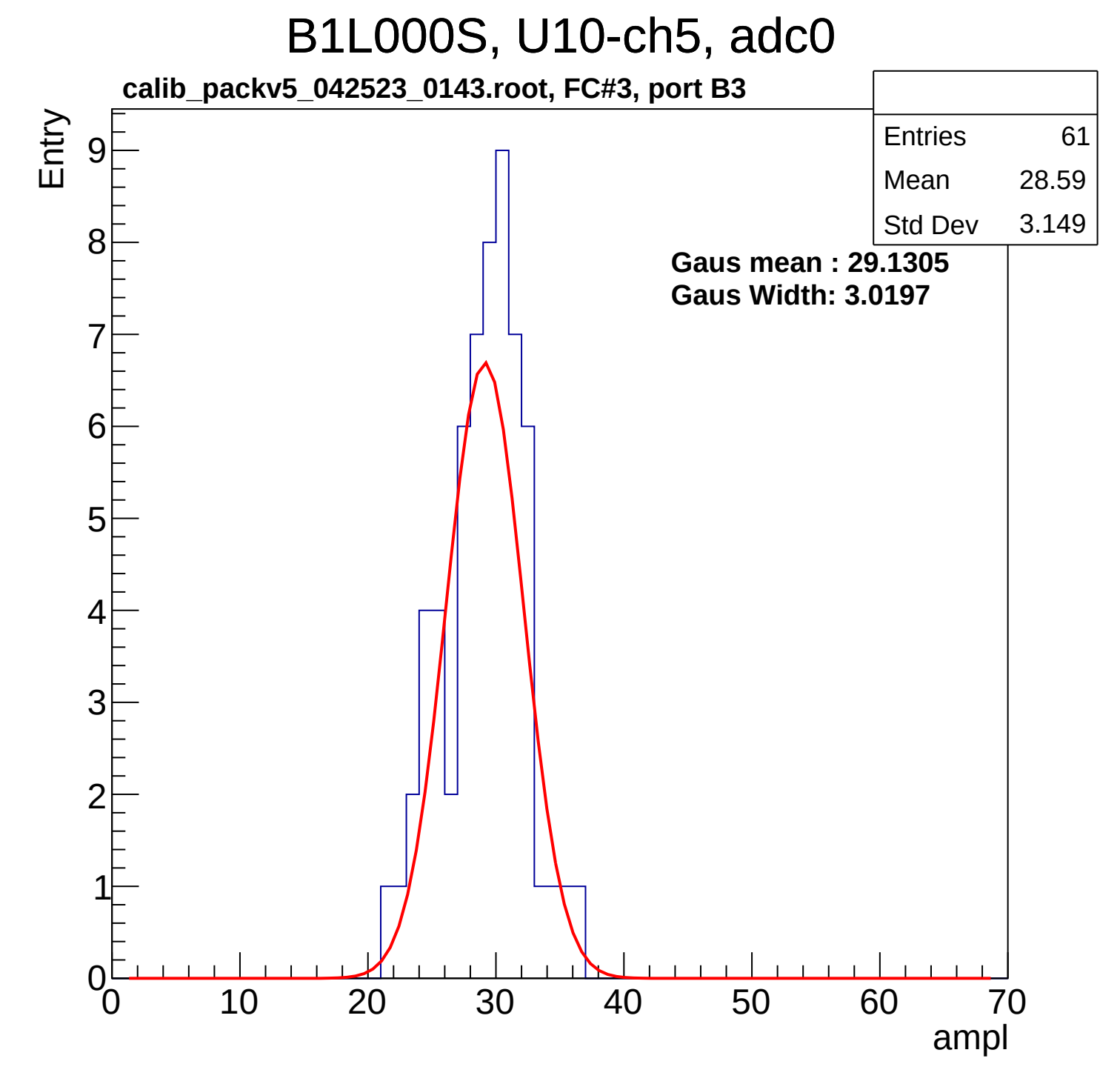
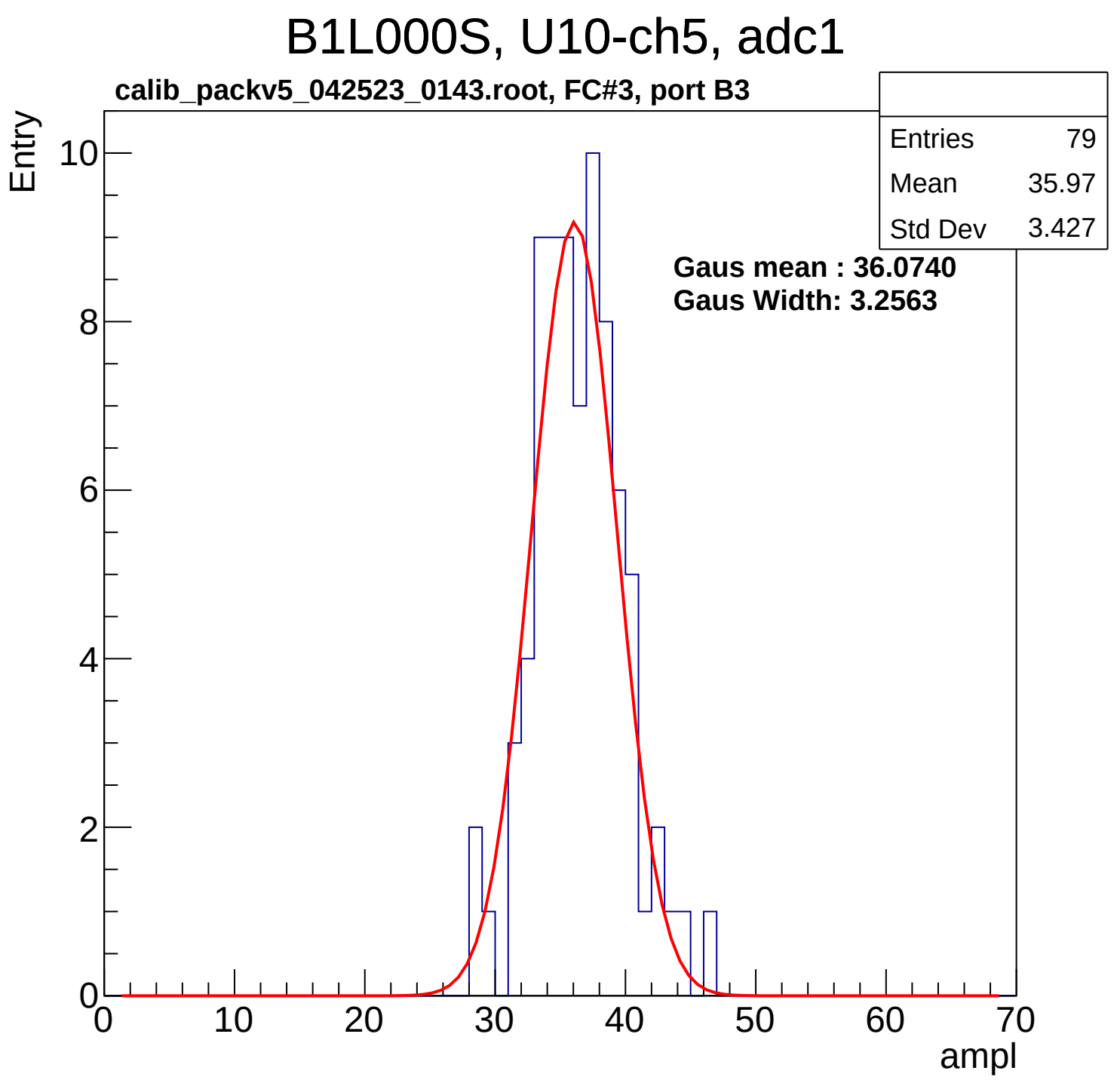
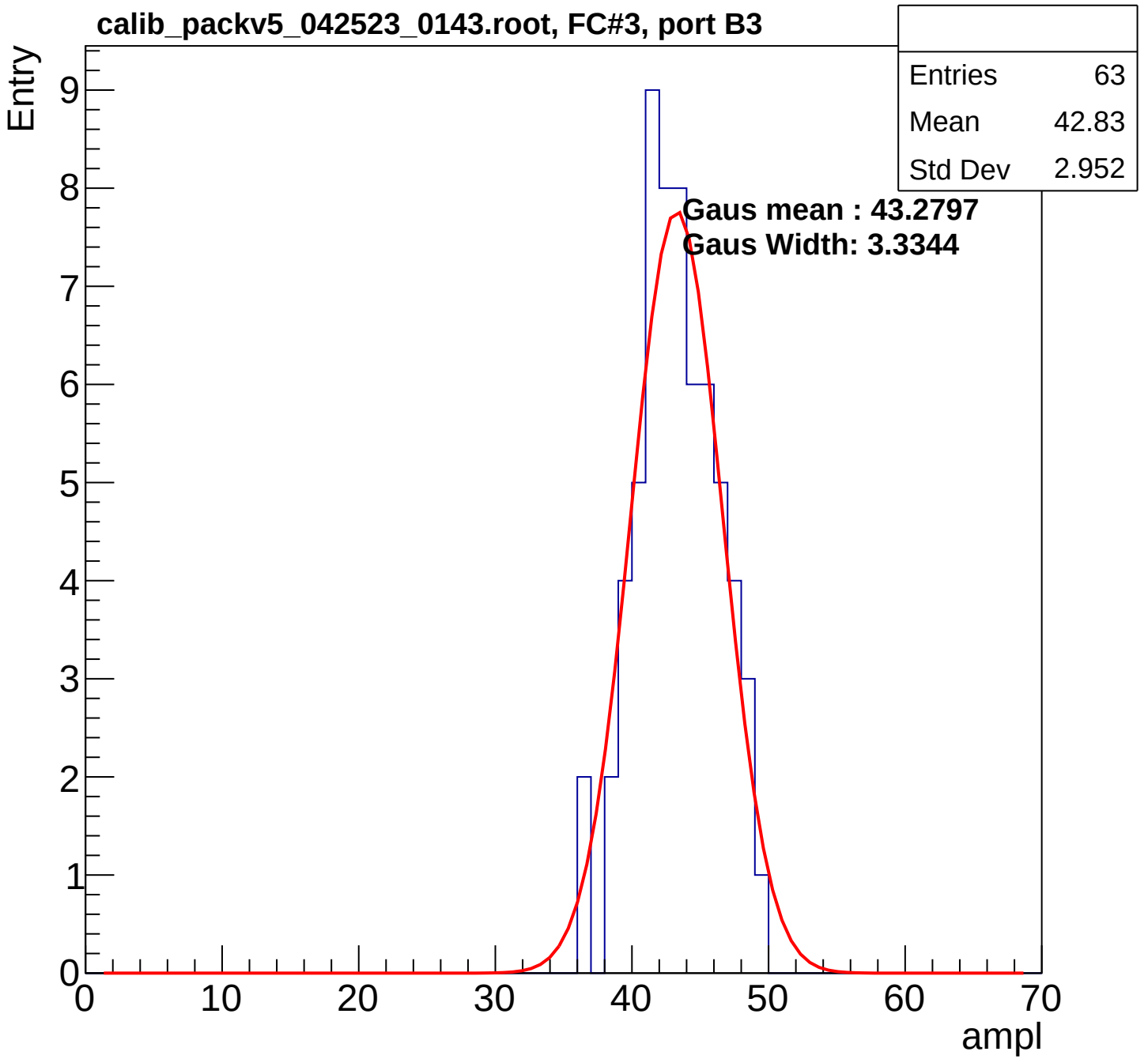

B1L000S, U10-ch4, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 57.9
Std Dev 13.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch4, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch5, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.59
Std Dev 3.149
Gaus mean : 29.1305
Gaus Width: 3.0197
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch5, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 35.97
Std Dev 3.427
Gaus mean : 36.0740
Gaus Width: 3.2563
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch5, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 42.83
Std Dev 2.952
Gaus mean : 43.2797
Gaus Width: 3.3344
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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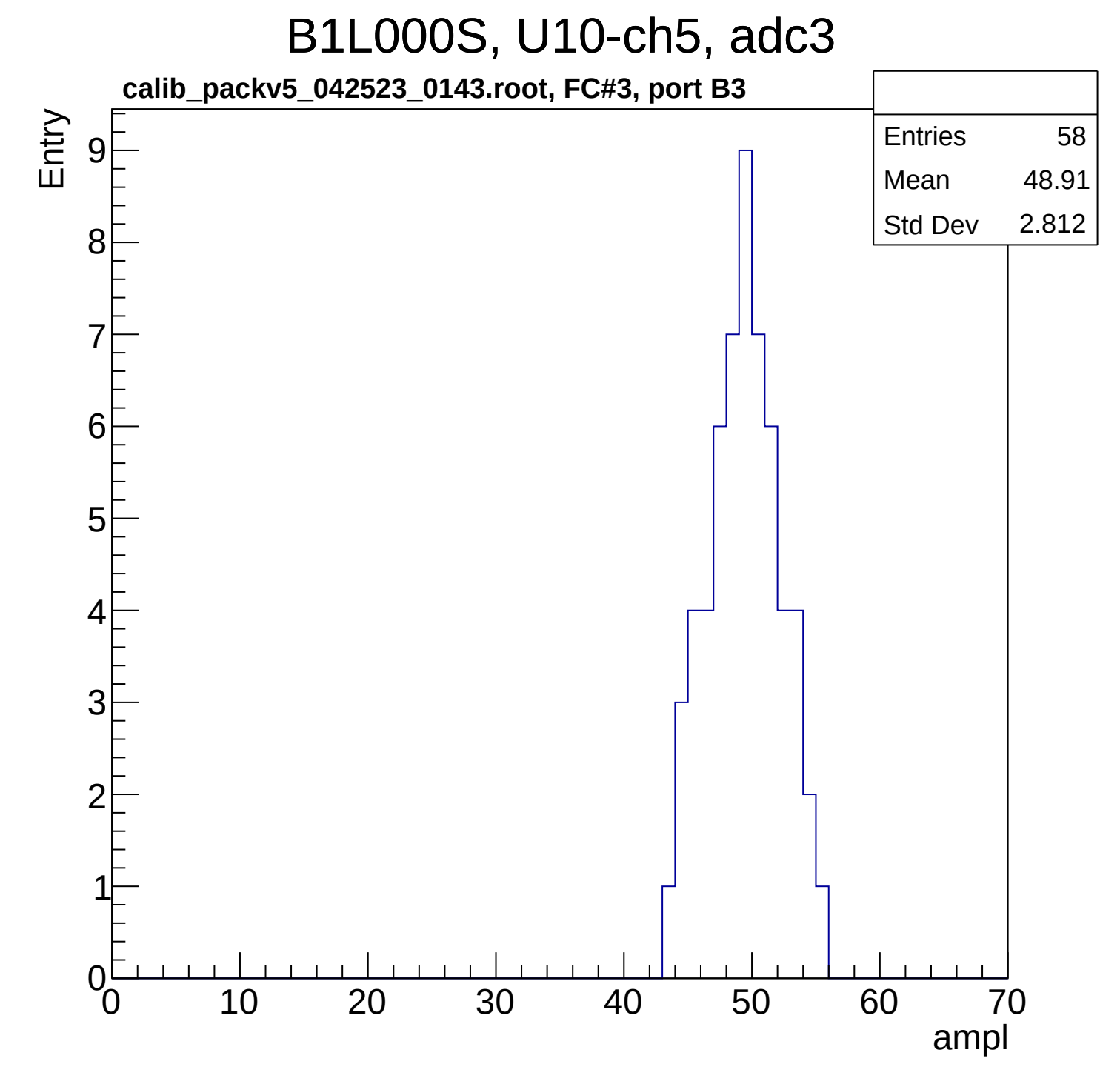
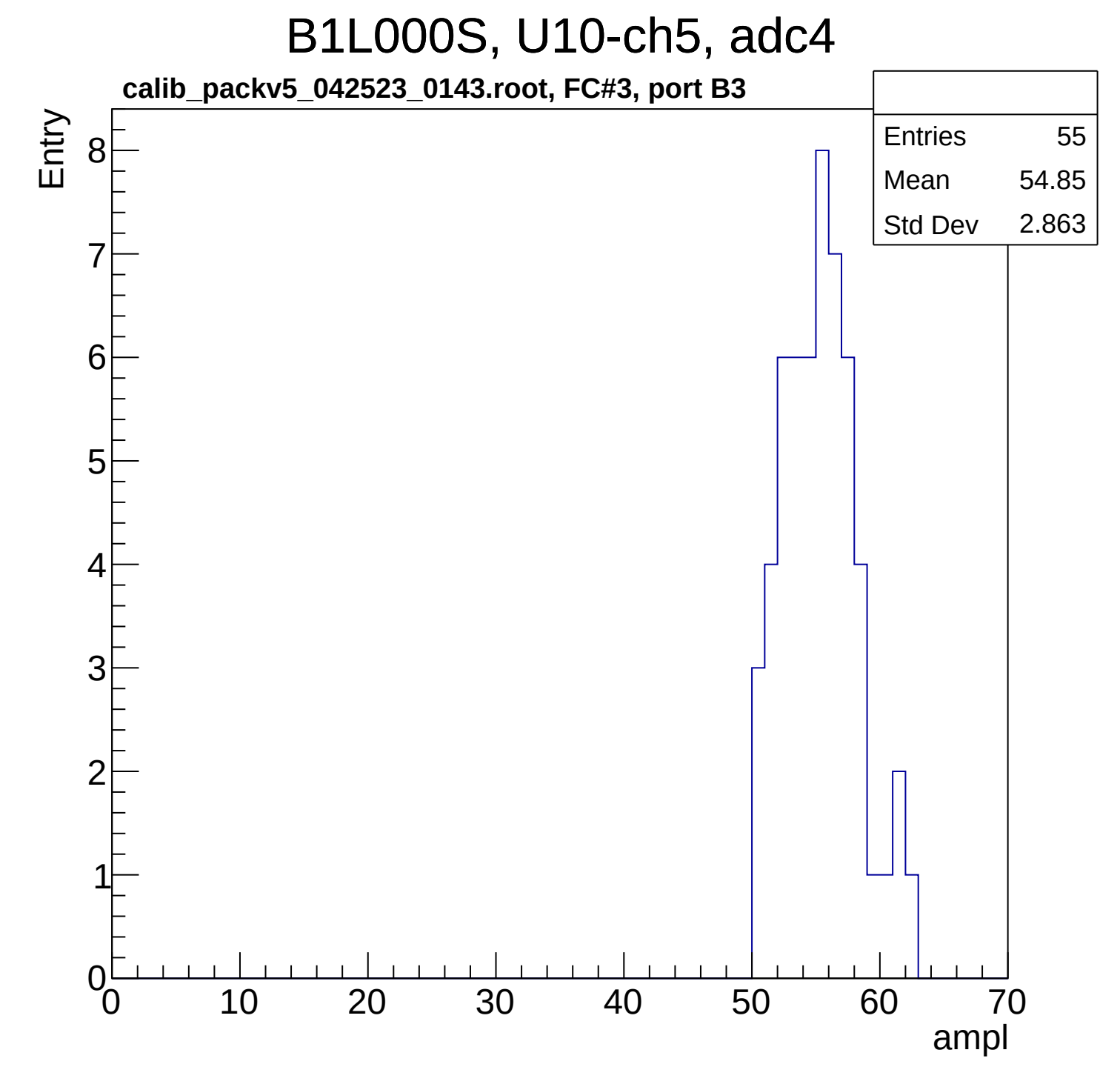

B1L000S, U10-ch5, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 48.91
Std Dev 2.812
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch5, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 54.85
Std Dev 2.863
Entry
ampl
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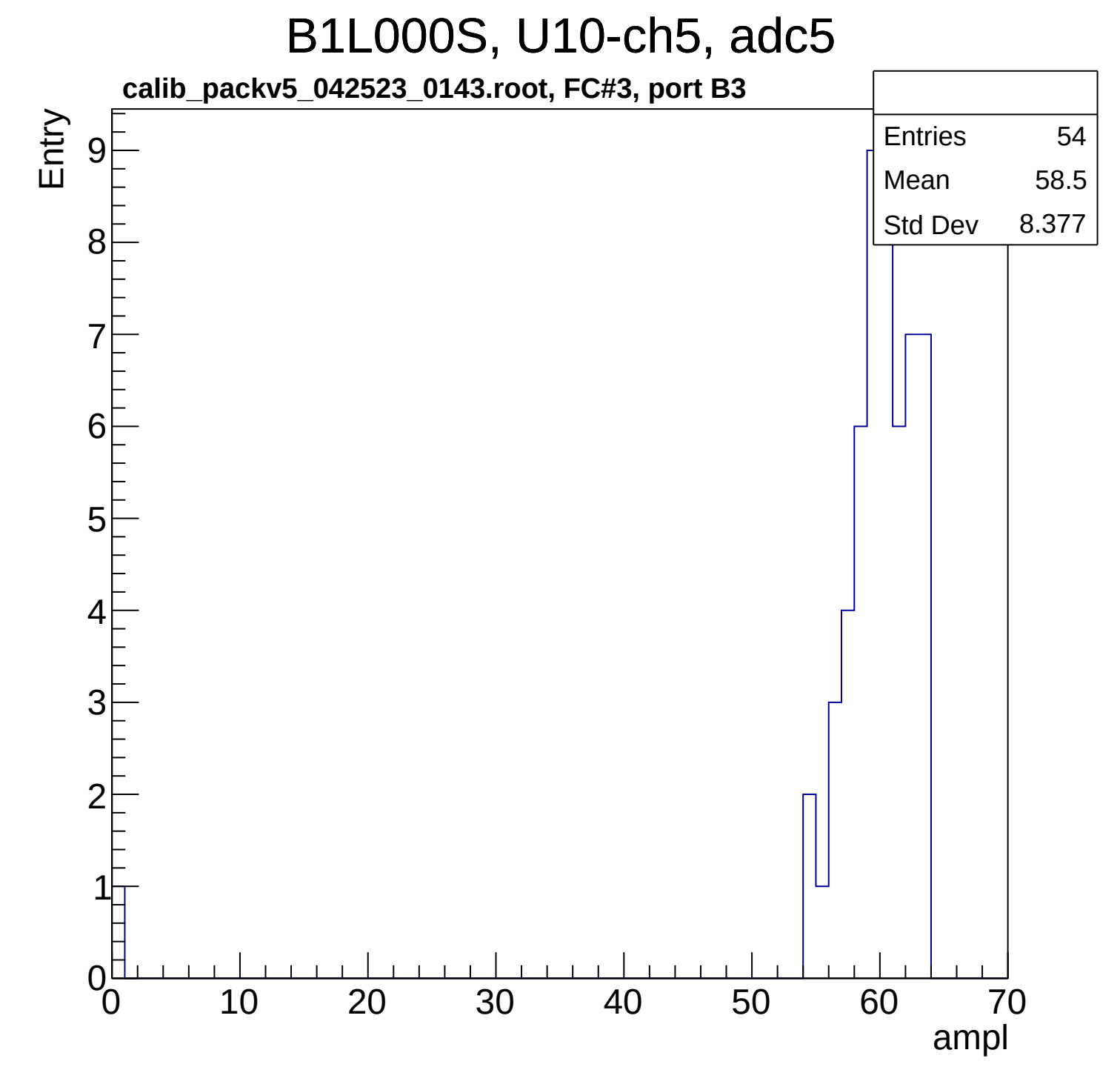
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch5, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.5
Std Dev 8.377
Entry
ampl
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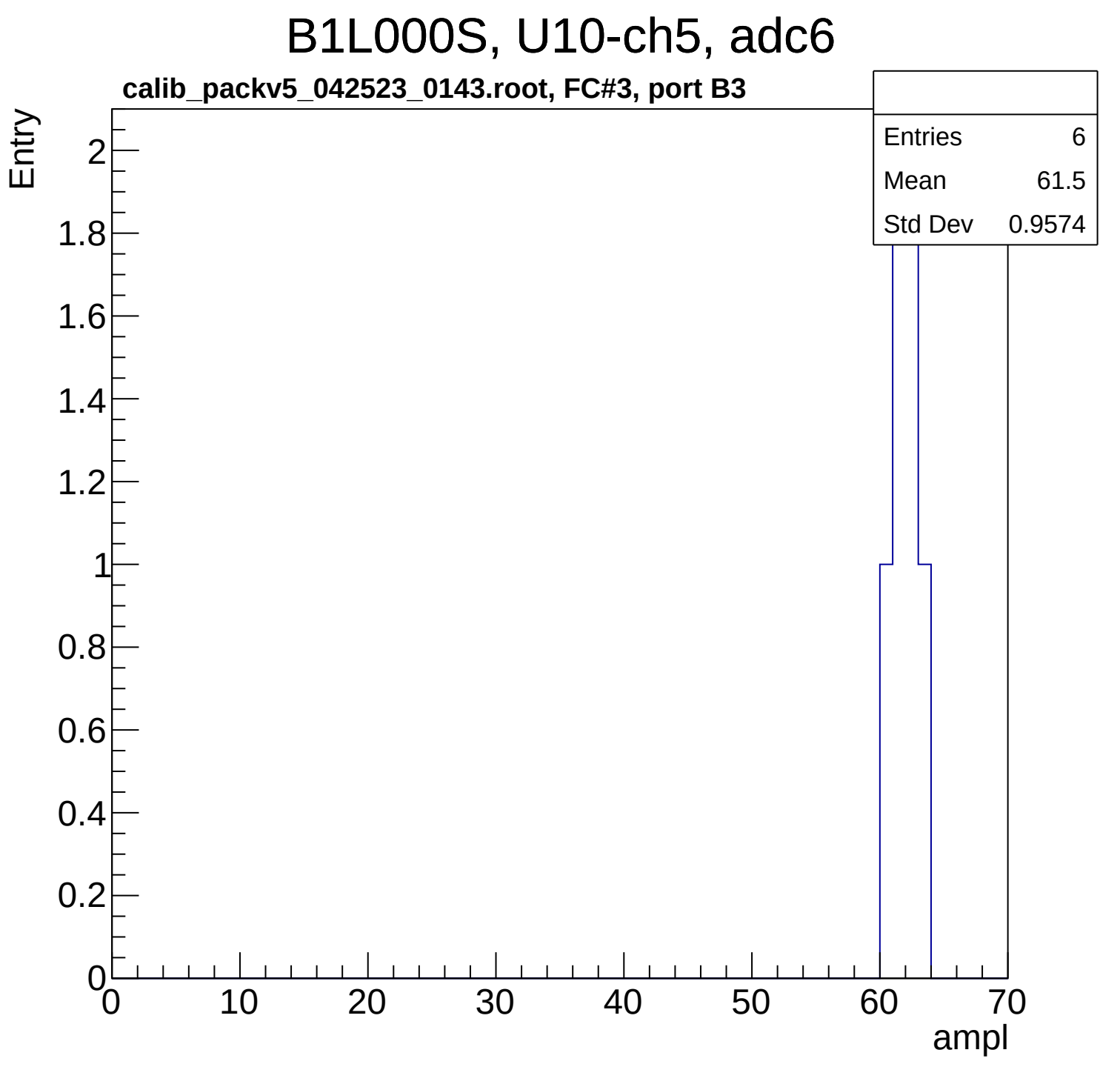
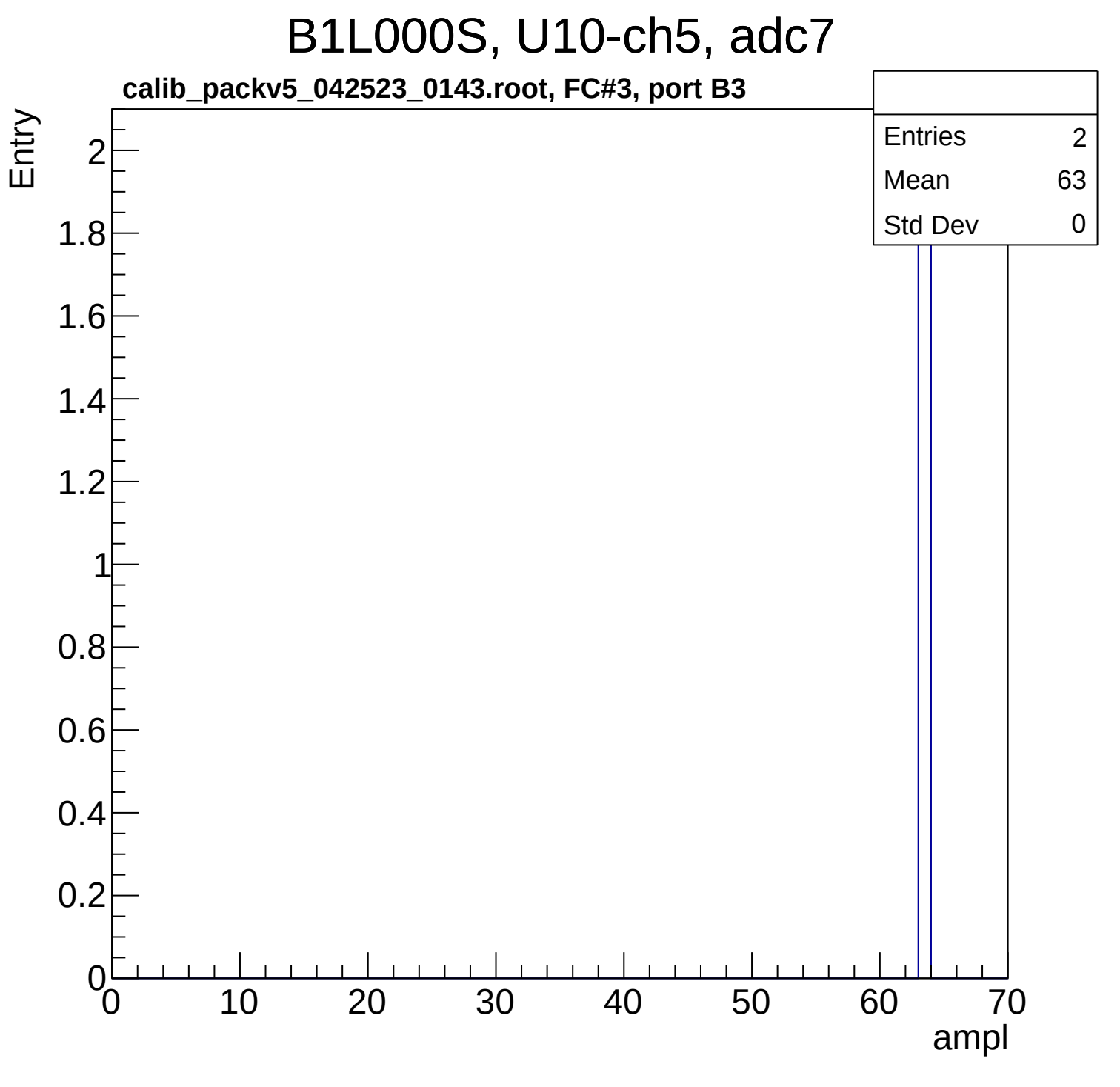
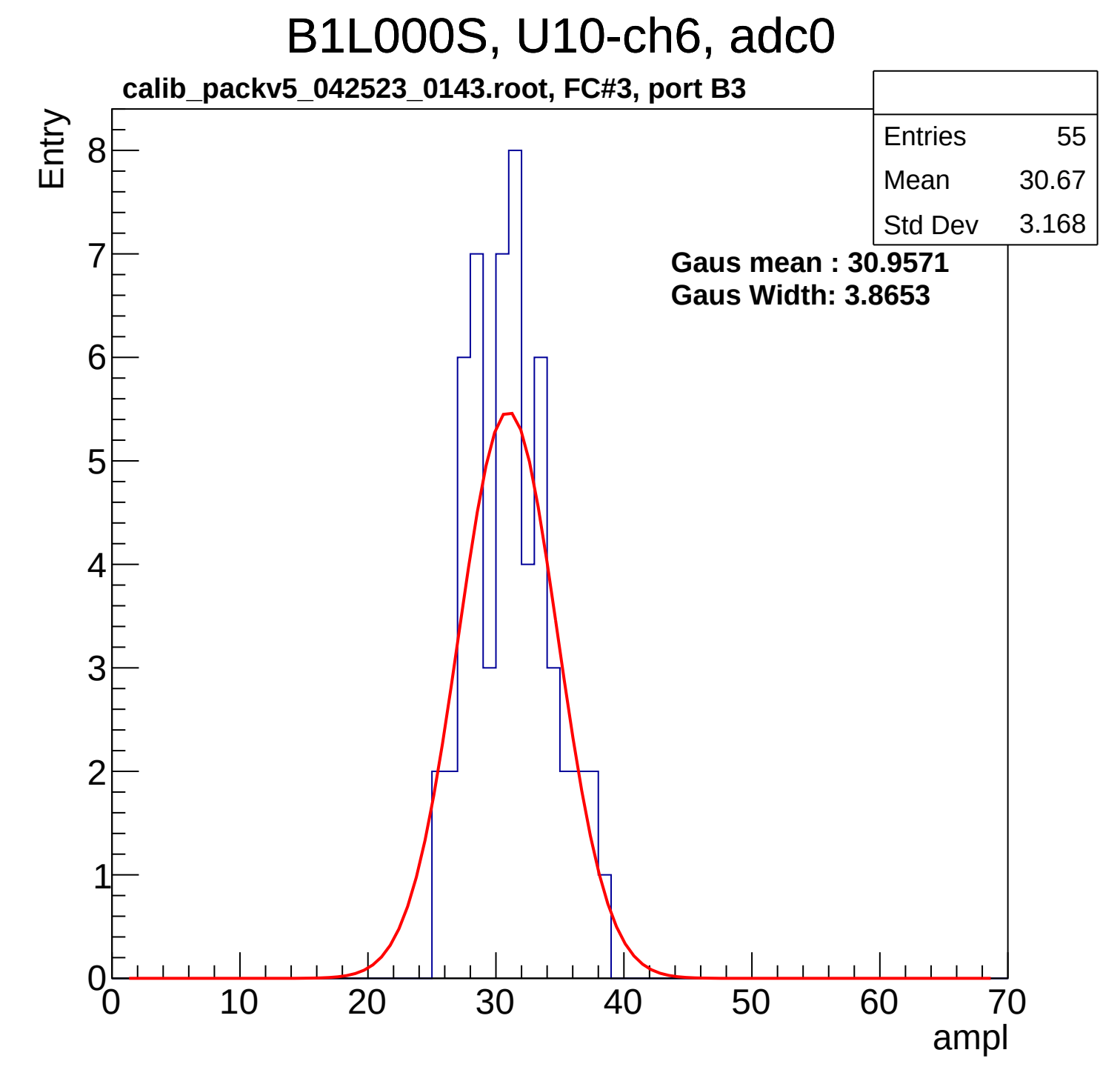
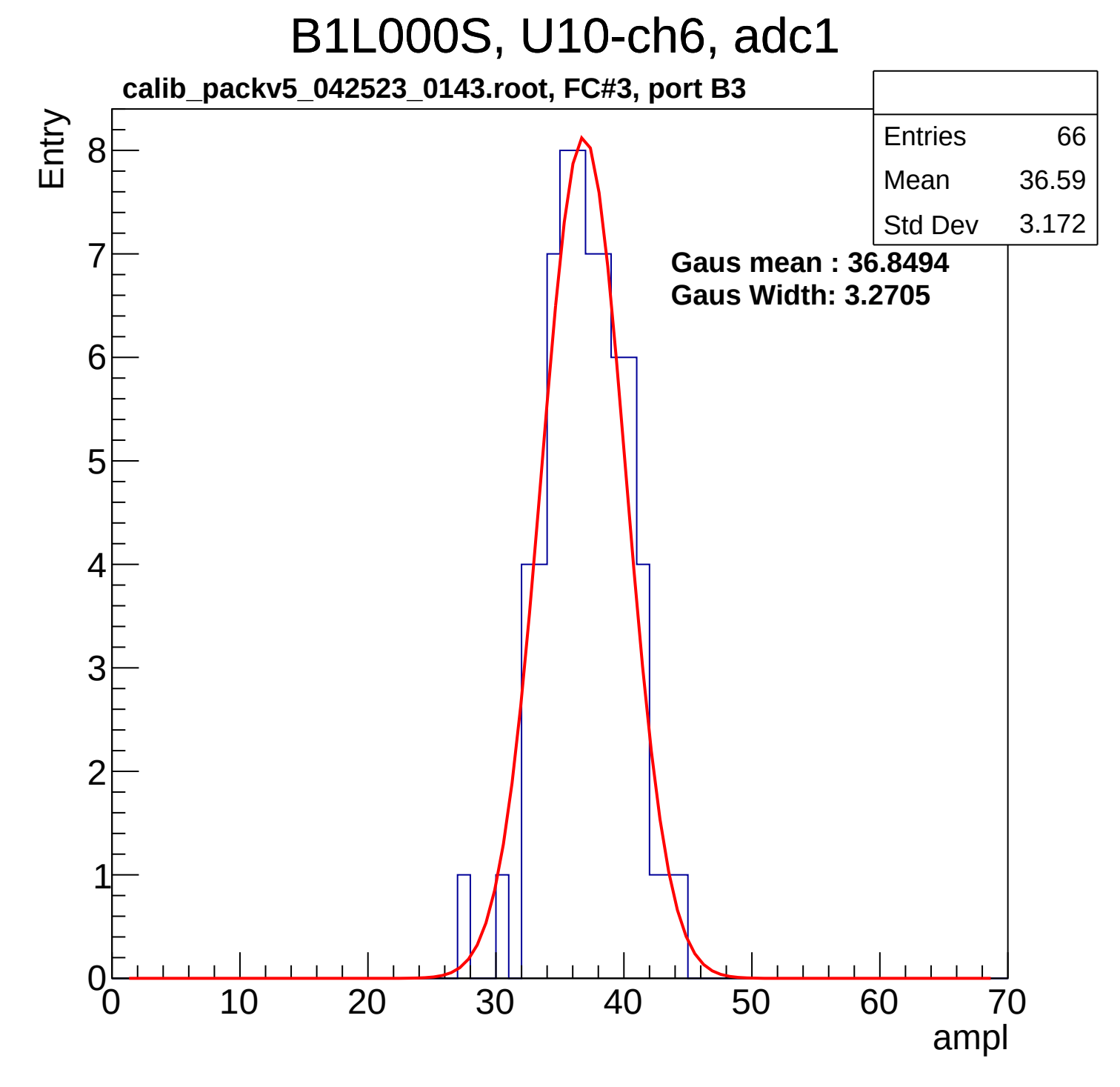
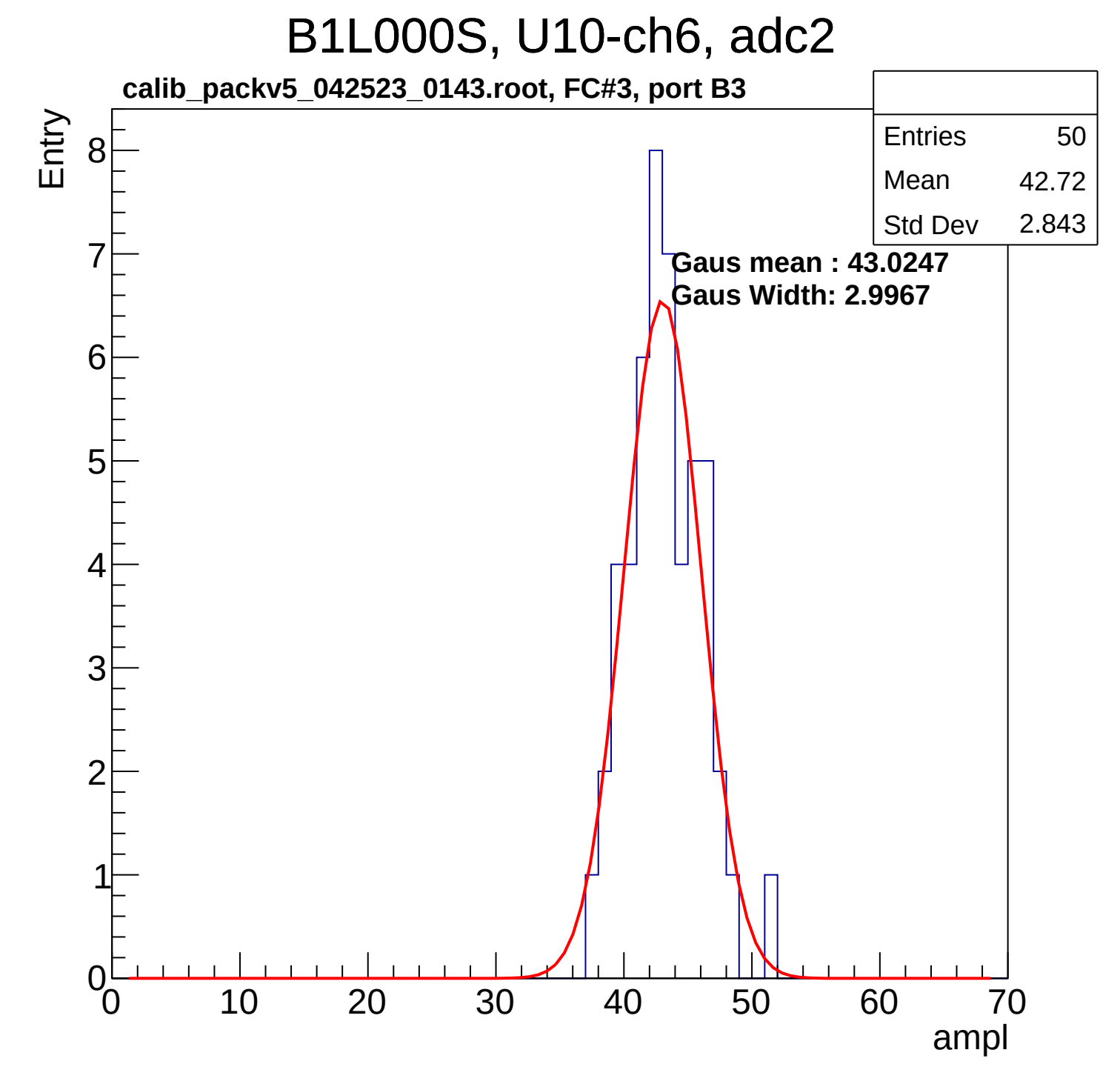
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

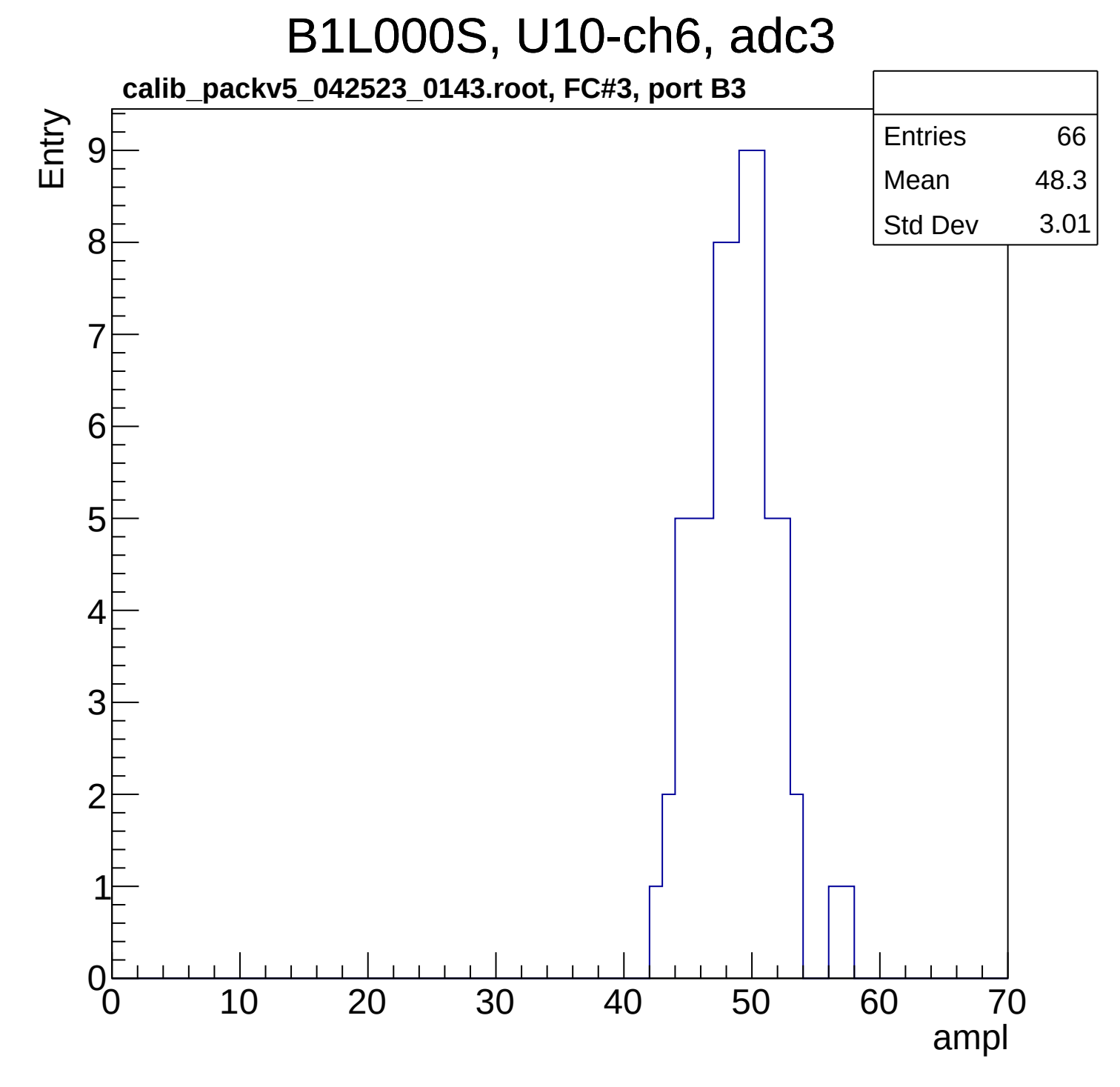

B1L000S, U10-ch5, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 61.5
Std Dev 0.9574
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch5, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch6, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 30.67
Std Dev 3.168
Gaus mean : 30.9571
Gaus Width: 3.8653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch6, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 36.59
Std Dev 3.172
Gaus mean : 36.8494
Gaus Width: 3.2705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch6, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 42.72
Std Dev 2.843
Gaus mean : 43.0247
Gaus Width: 2.9967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch6, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 48.3
Std Dev 3.01
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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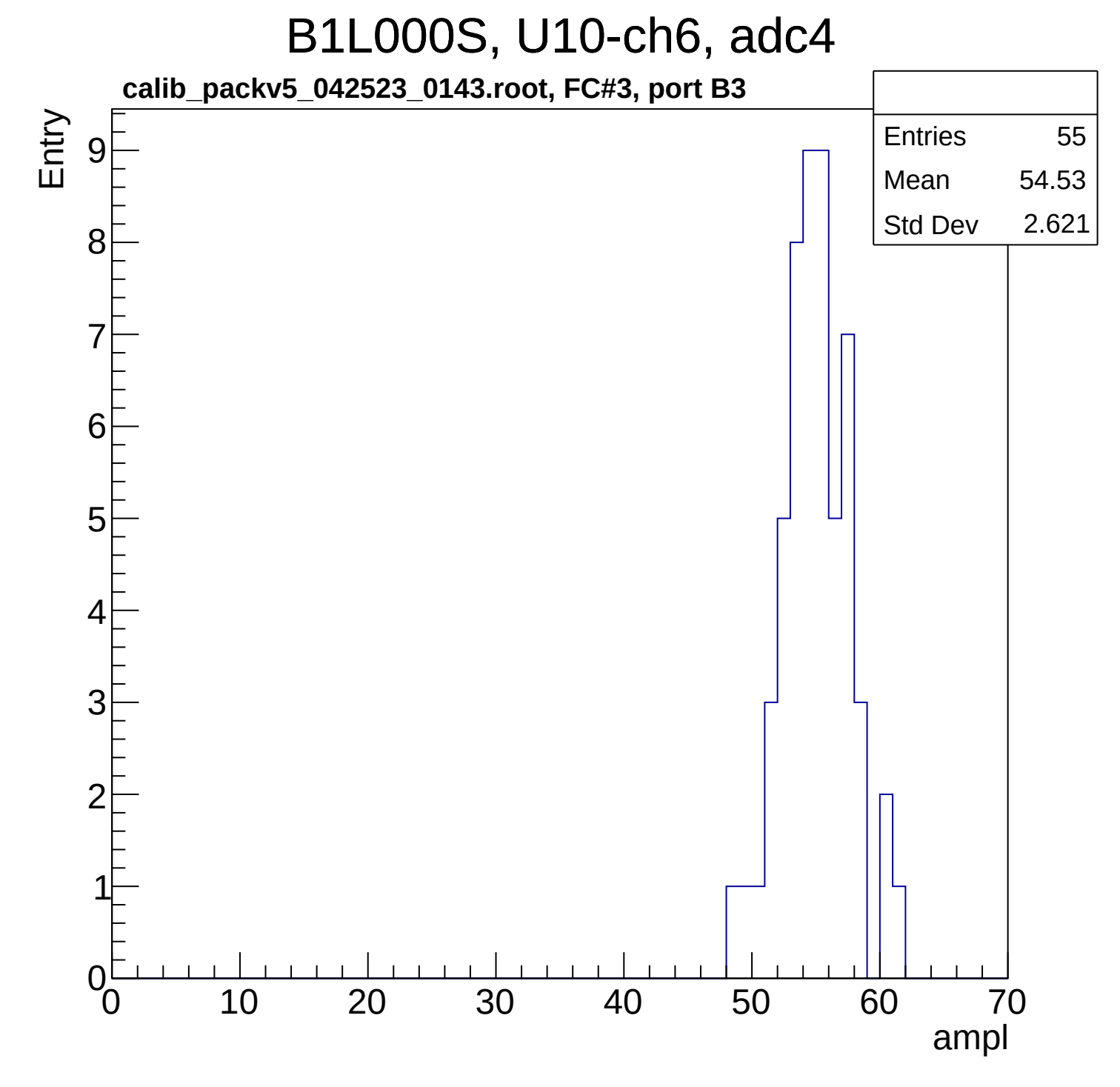
40
50
60
70


B1L000S, U10-ch6, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 54.53
Std Dev 2.621
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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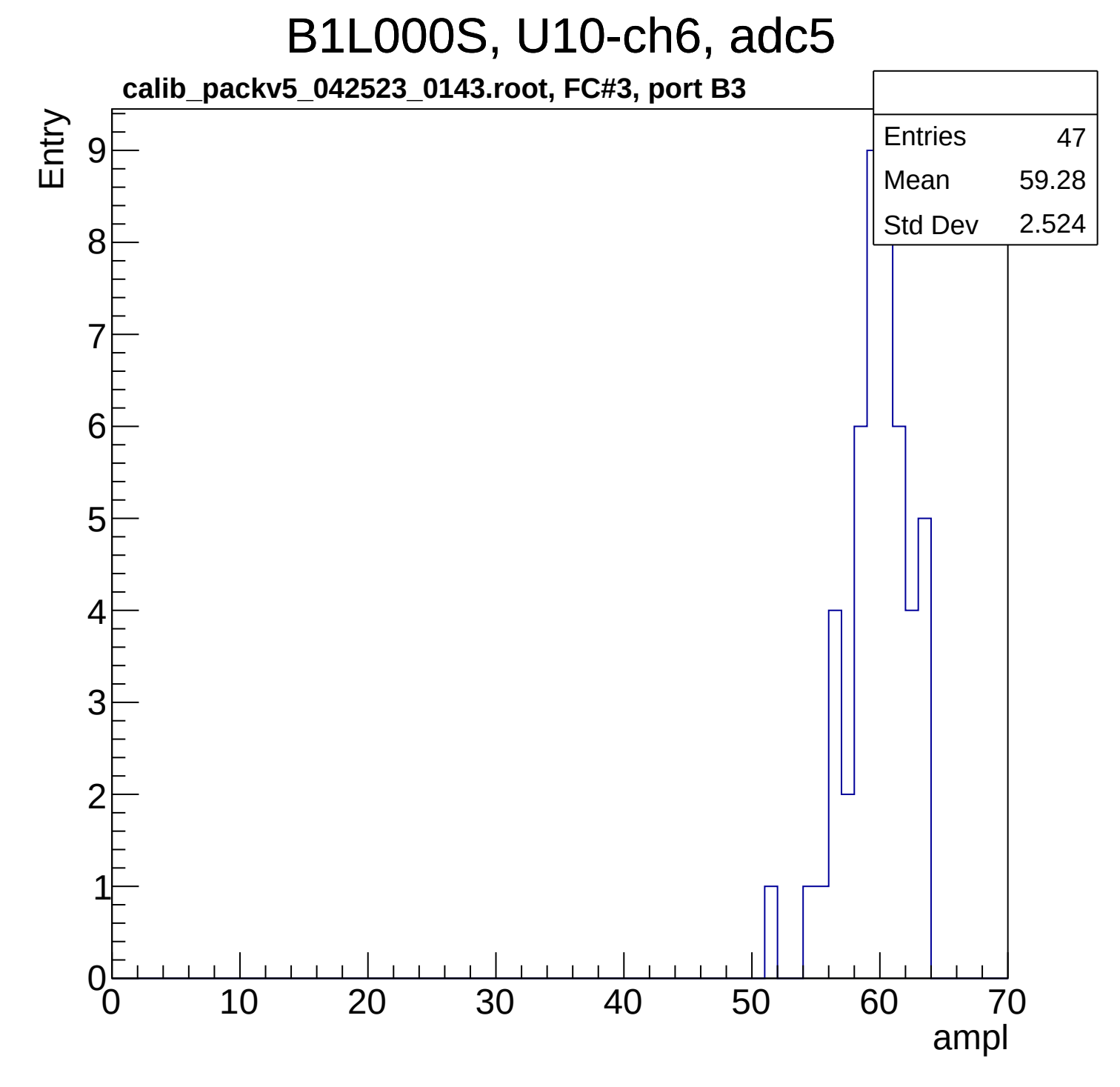
40
50
60
70


B1L000S, U10-ch6, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.28
Std Dev 2.524
Entry
ampl
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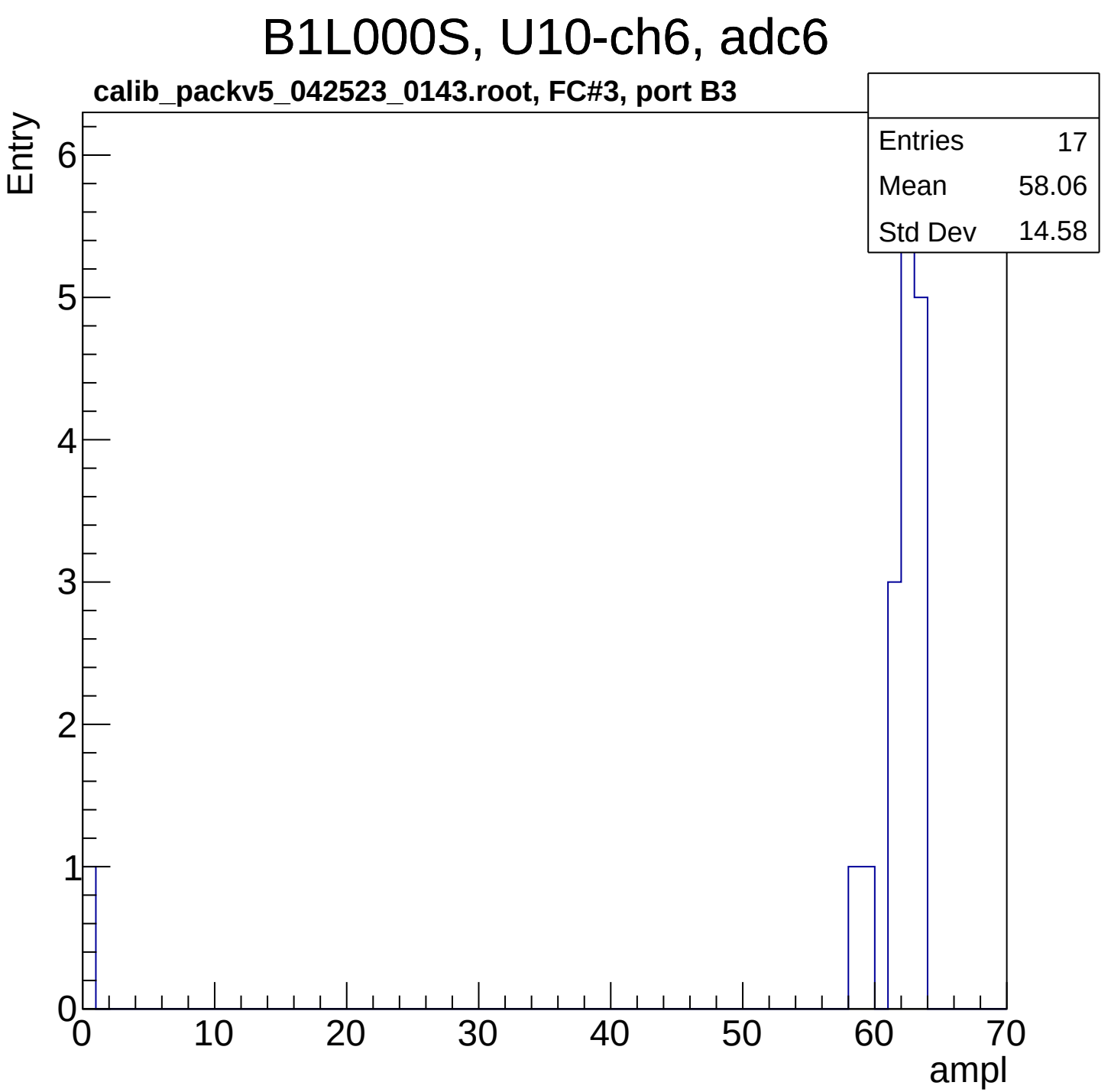
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch6, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 58.06
Std Dev 14.58
Entry
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2
3
4
5
6
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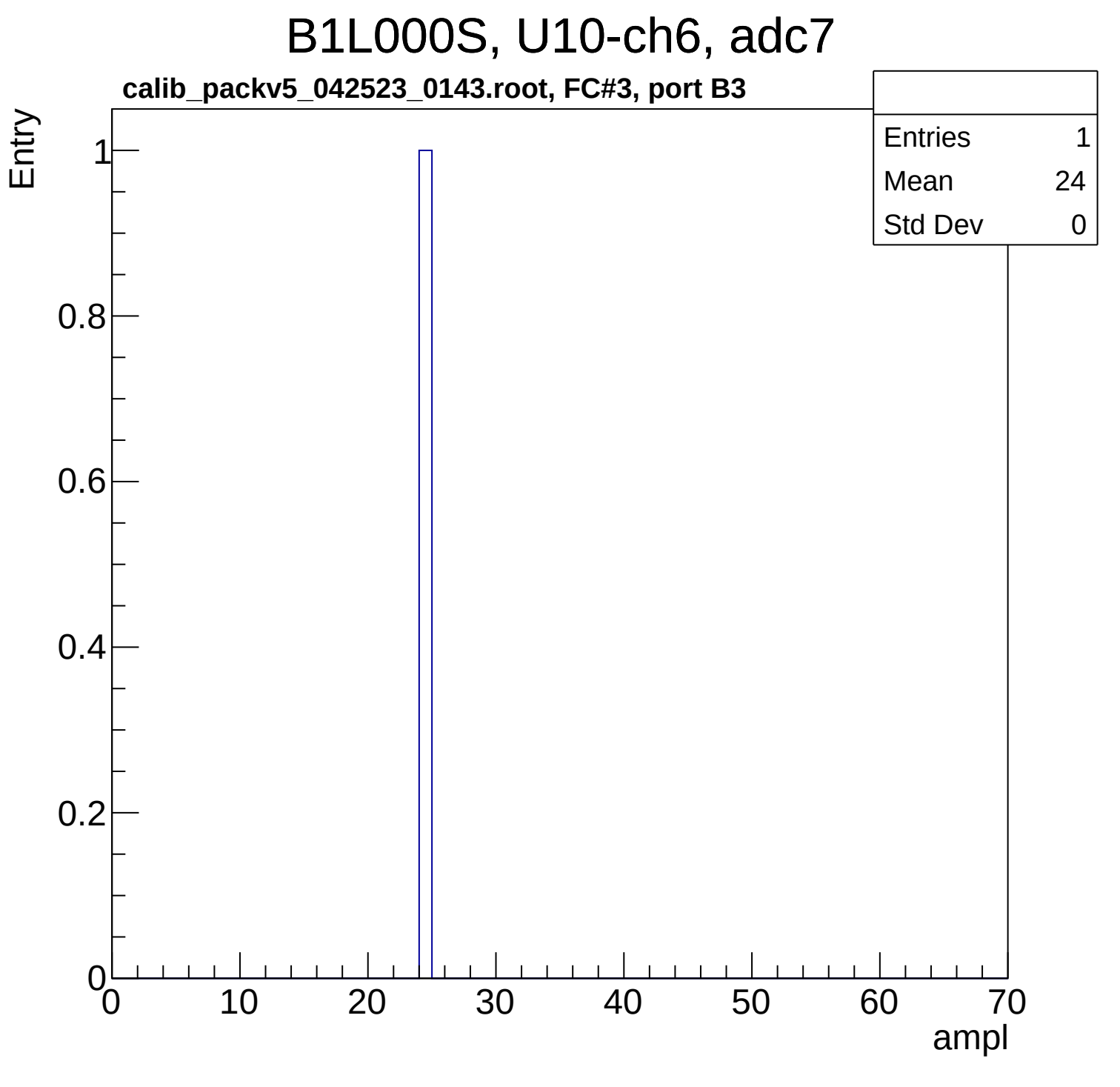
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch6, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 24
Std Dev 0
Entry
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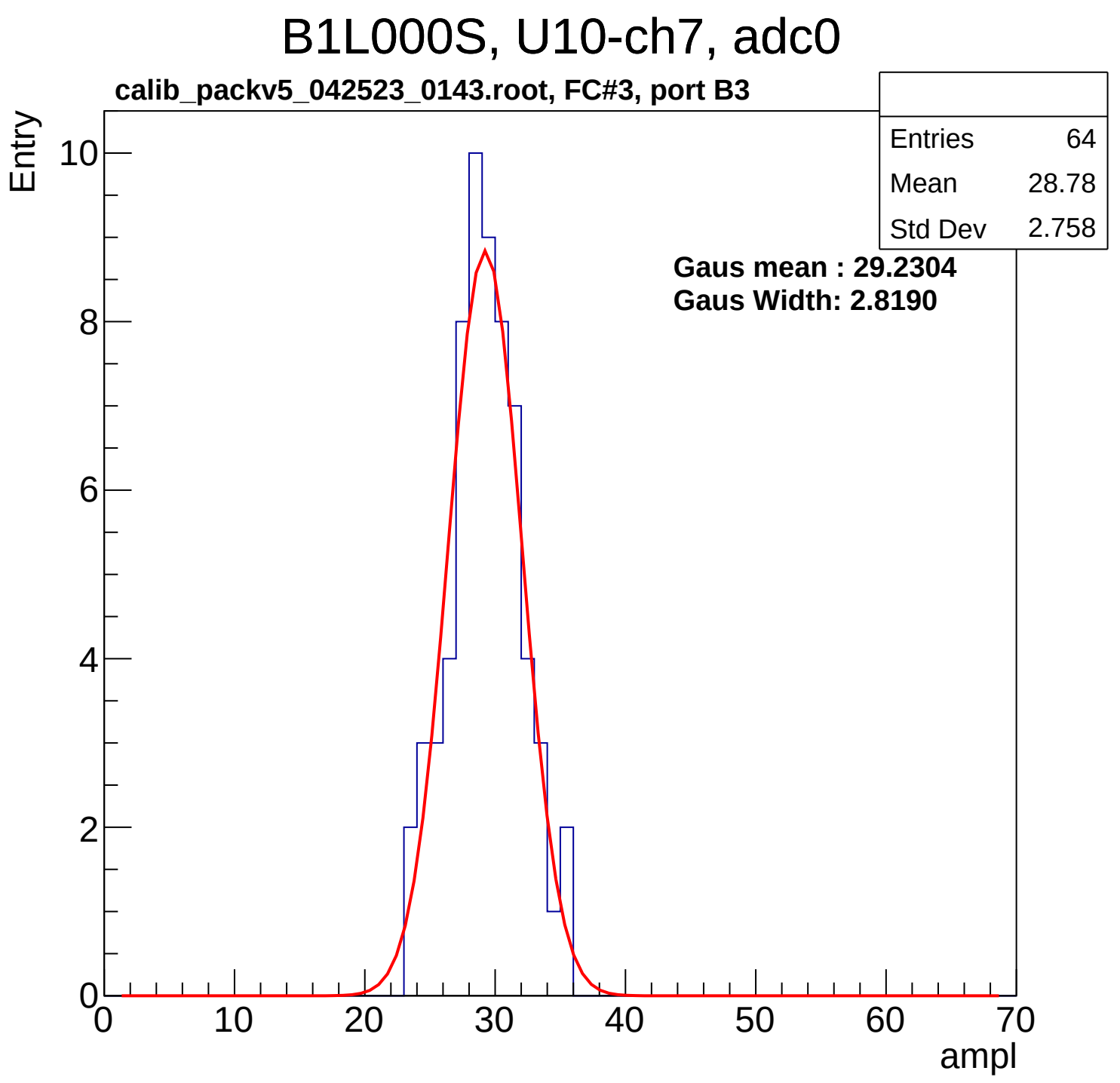
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch7, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.78
Std Dev 2.758
Gaus mean : 29.2304
Gaus Width: 2.8190
Entry
ampl
0
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6
8
10
0
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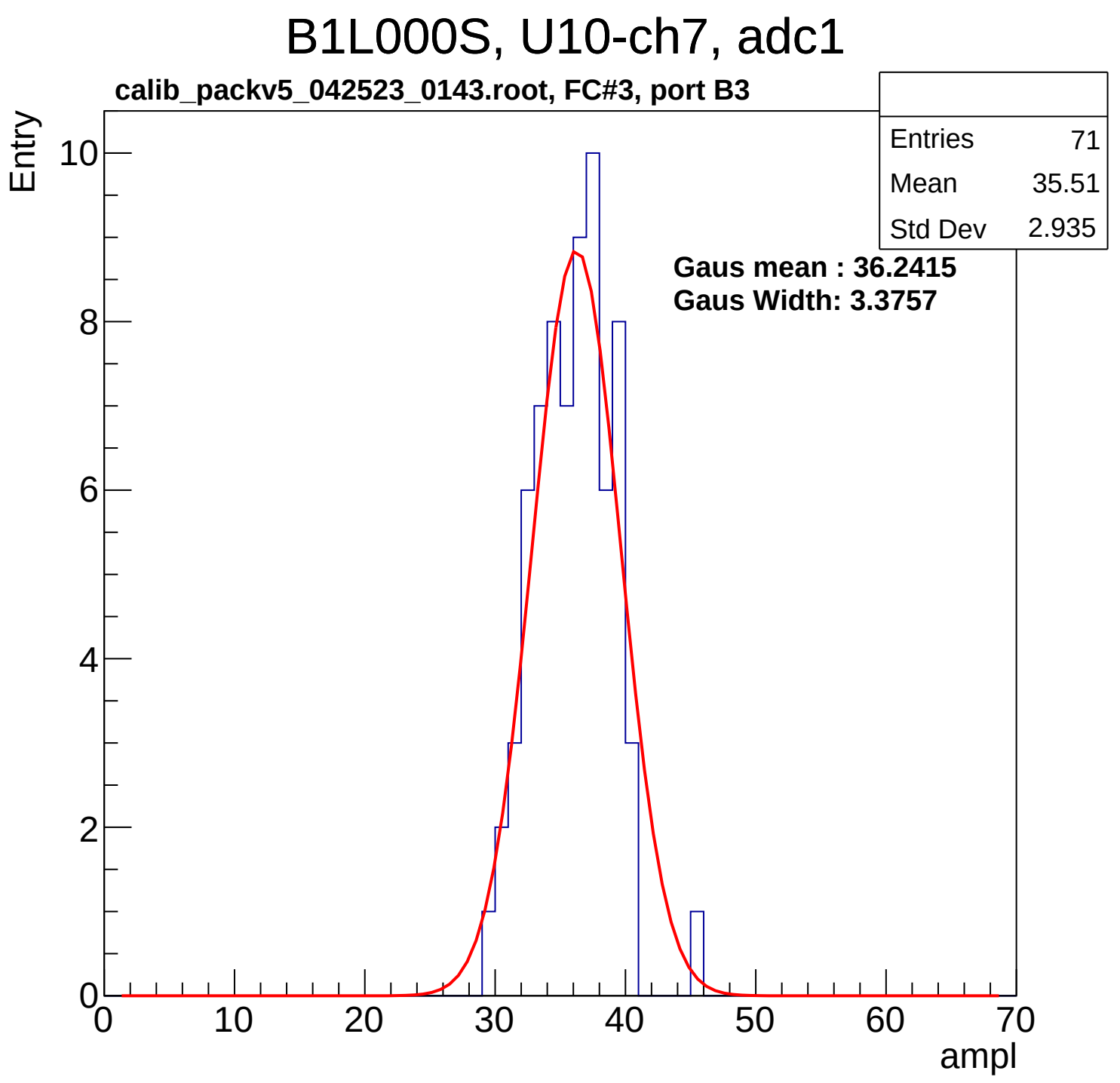
20
30
40
50
60
70


B1L000S, U10-ch7, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.51
Std Dev 2.935
Gaus mean : 36.2415
Gaus Width: 3.3757
Entry
ampl
0
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4
6
8
10
0
10
20
30
40
50
60
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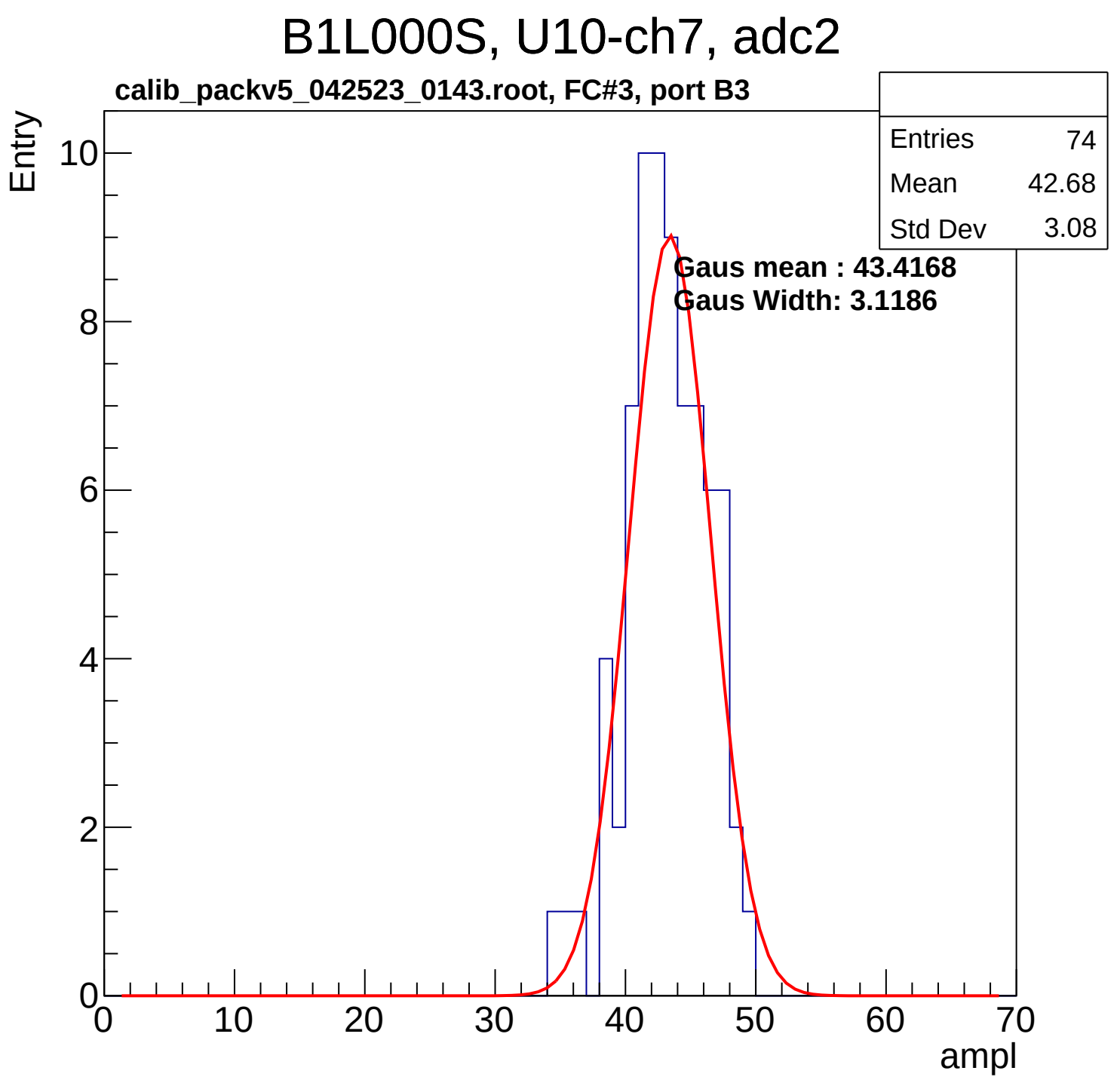
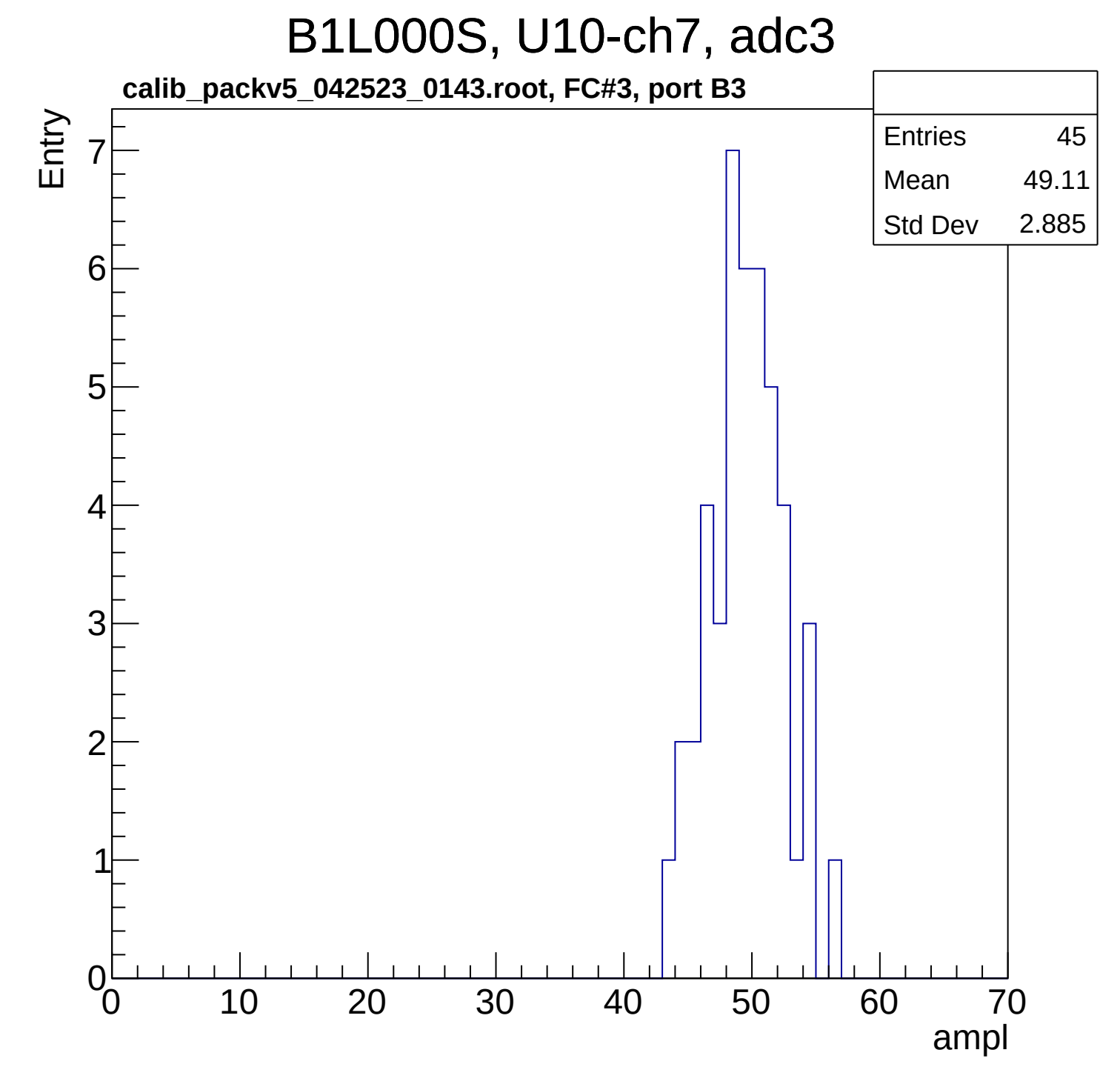

B1L000S, U10-ch7, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 42.68
Std Dev 3.08
Gaus mean : 43.4168
Gaus Width: 3.1186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch7, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 49.11
Std Dev 2.885
Entry
ampl
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2
3
4
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6
7
0
10
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30
40
50
60
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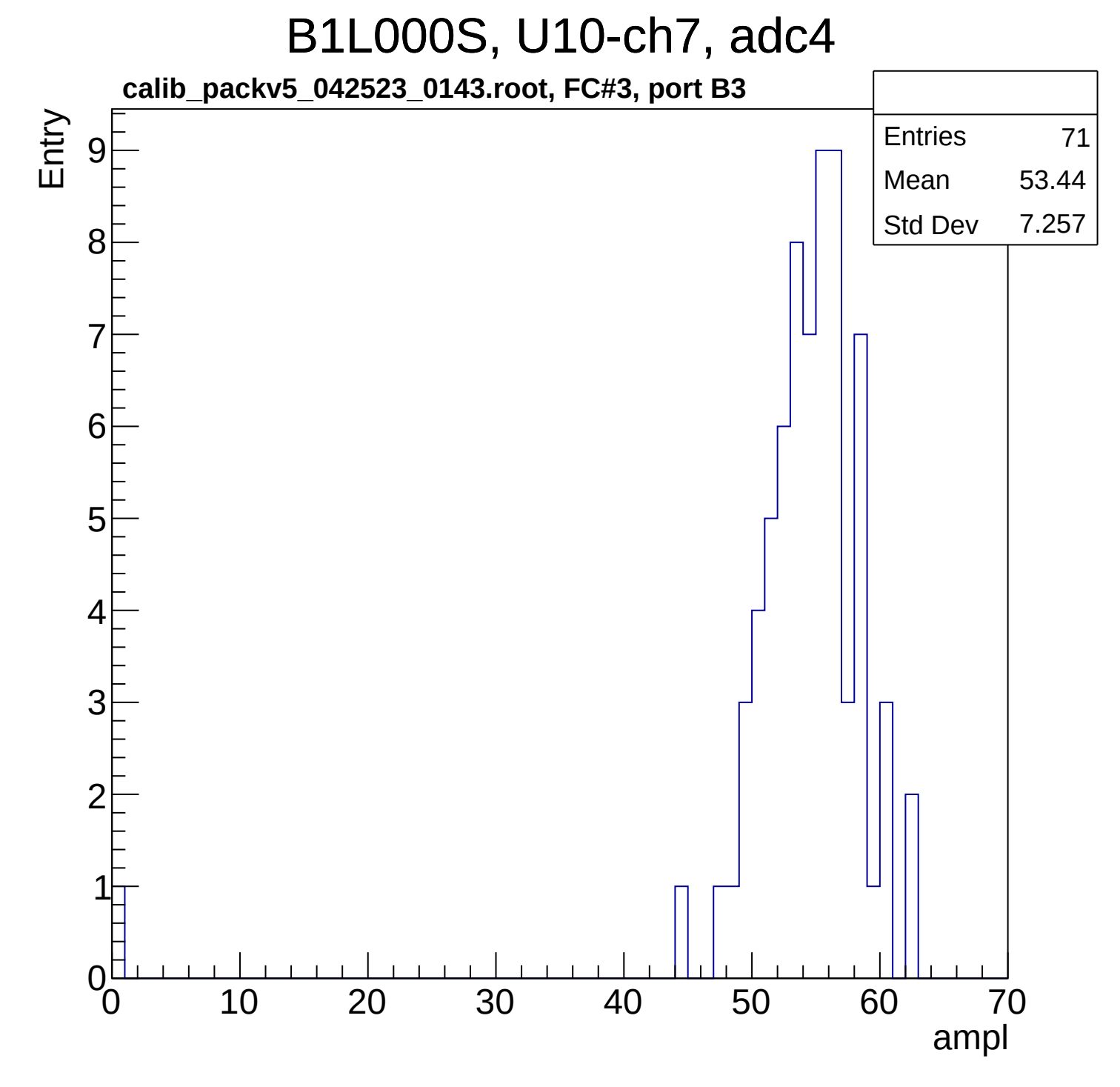
B1L000S, U10-ch7, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 53.44
Std Dev 7.257
Entry
ampl
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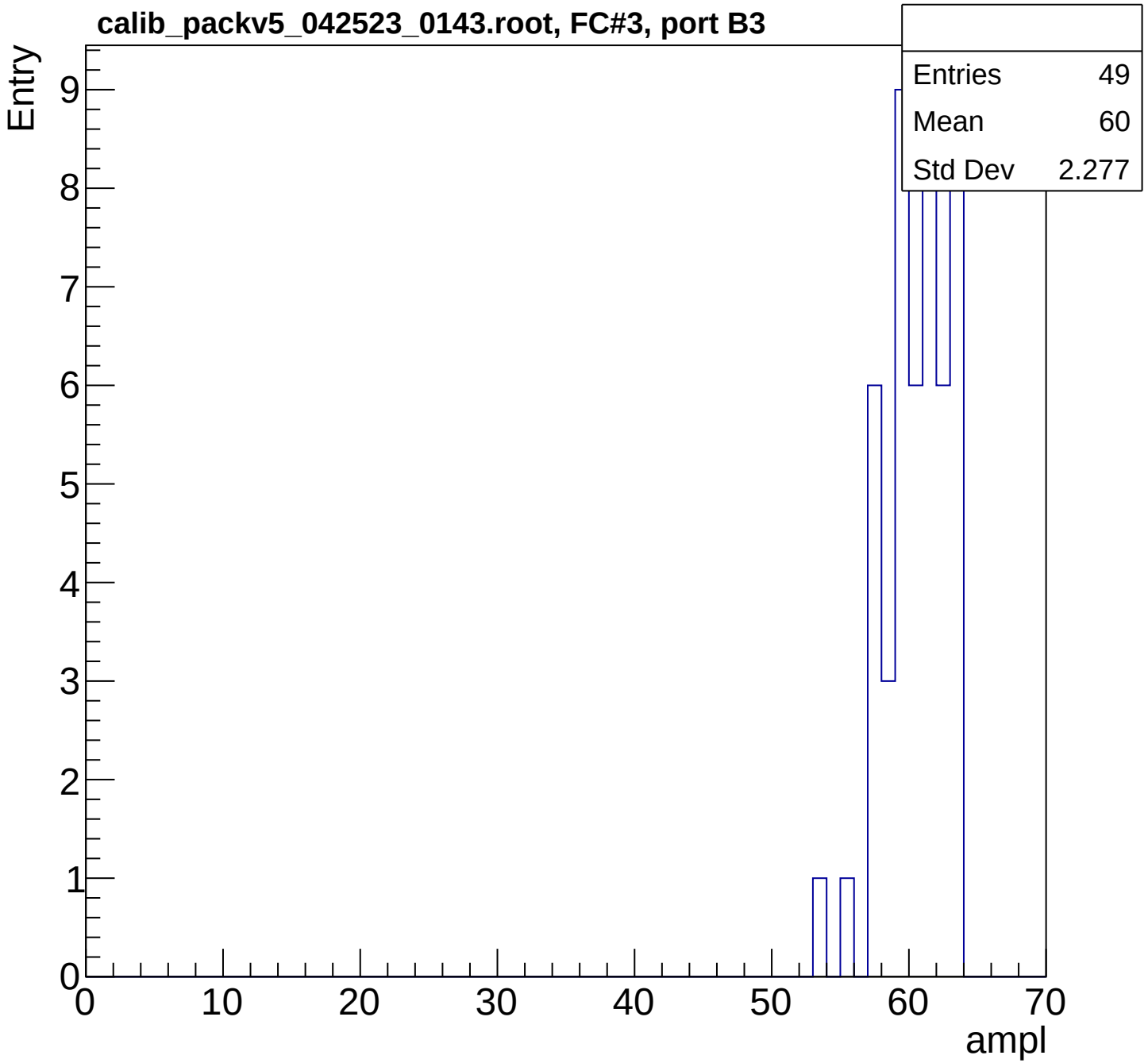
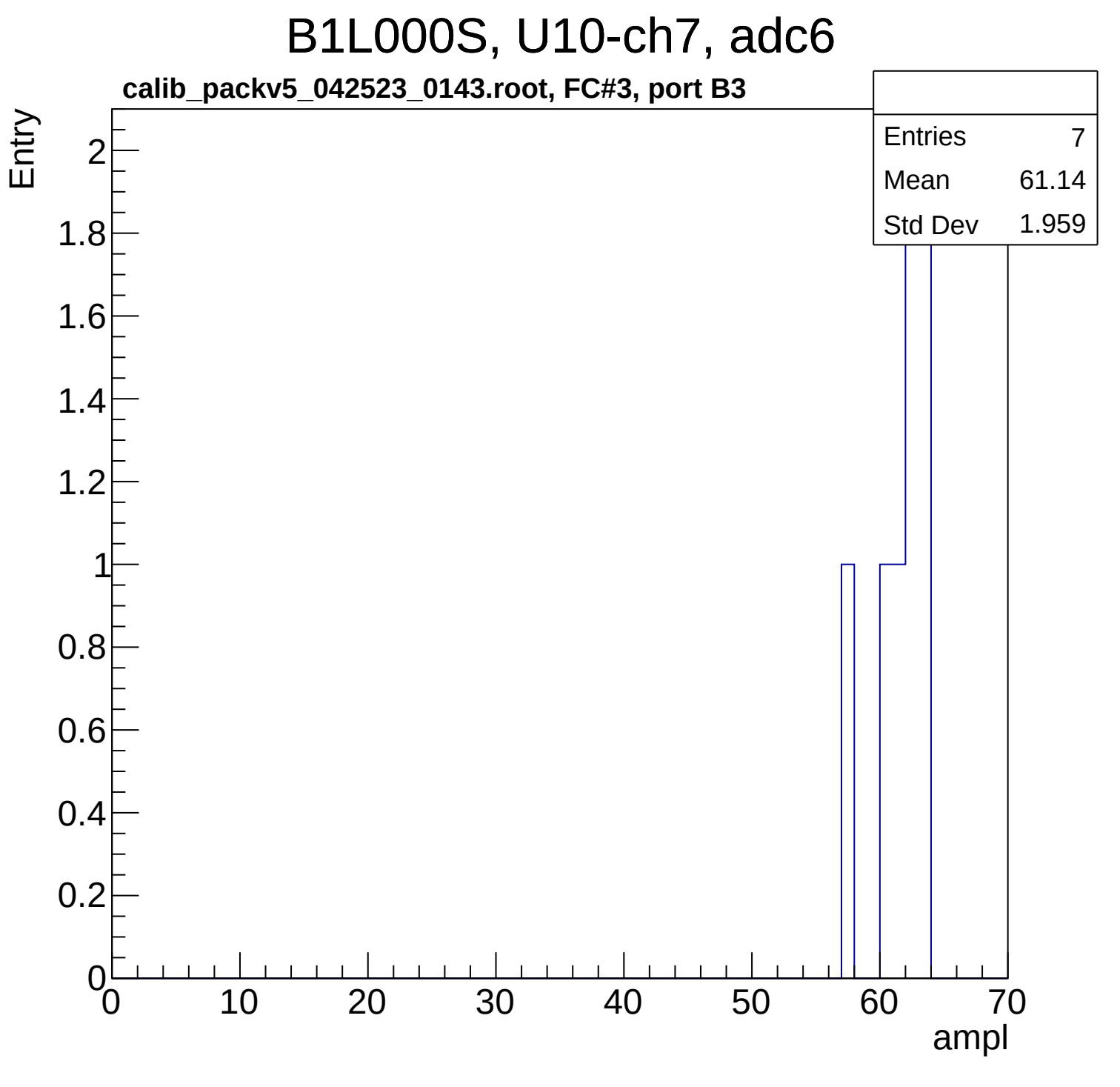

B1L000S, U10-ch7, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 60
Std Dev 2.277
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch7, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 61.14
Std Dev 1.959
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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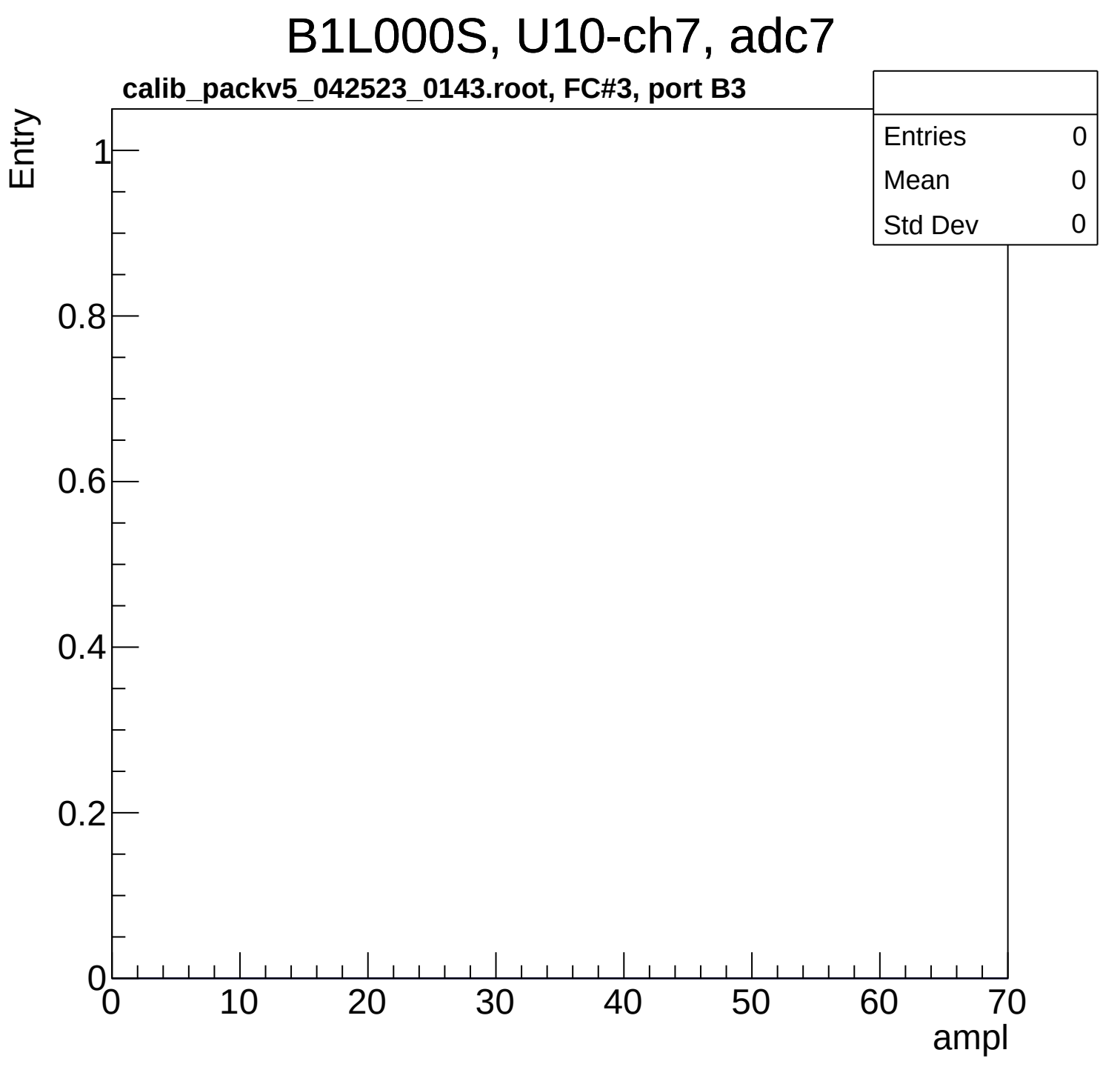
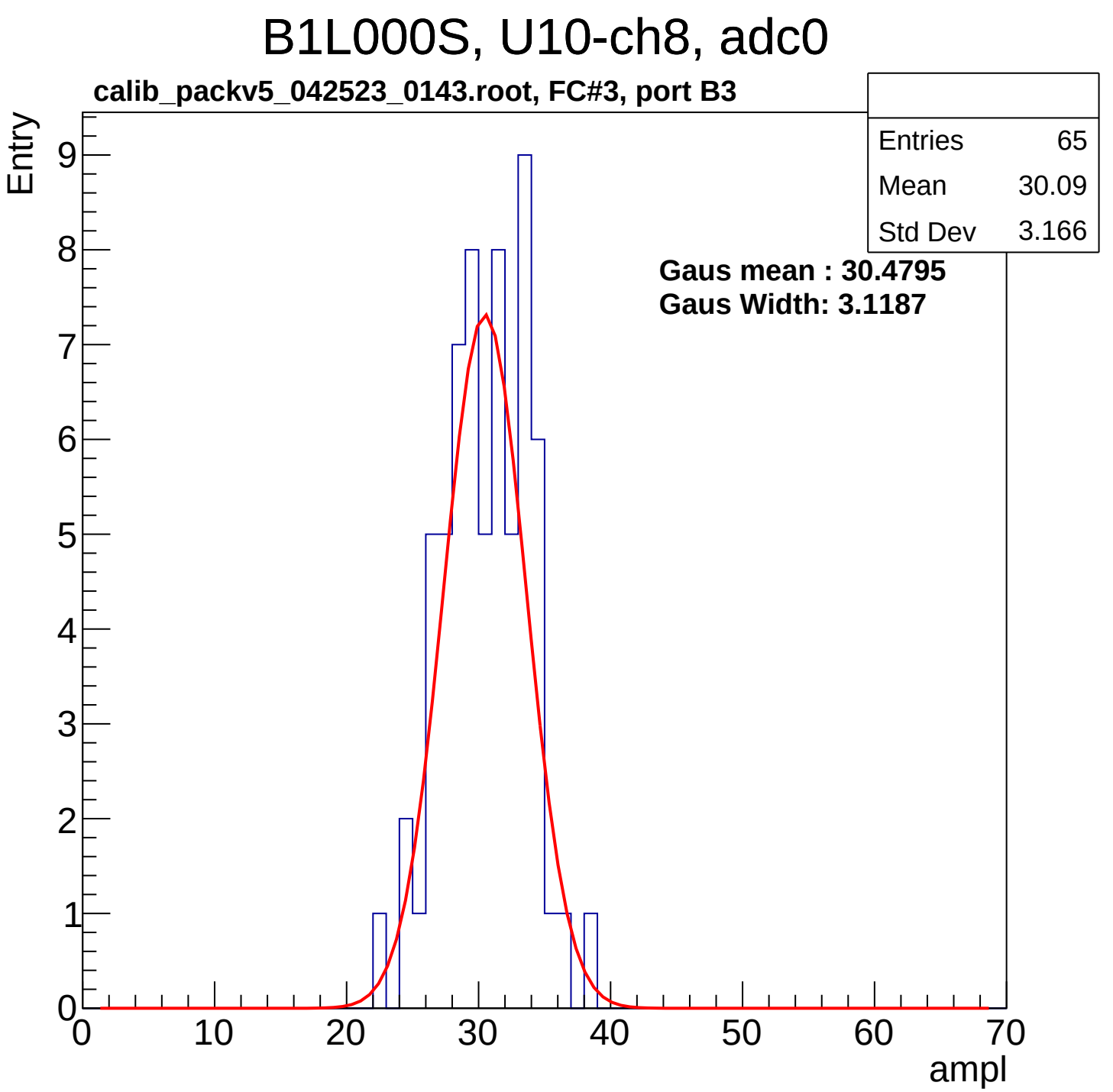

B1L000S, U10-ch7, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch8, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 30.09
Std Dev 3.166
Gaus mean : 30.4795
Gaus Width: 3.1187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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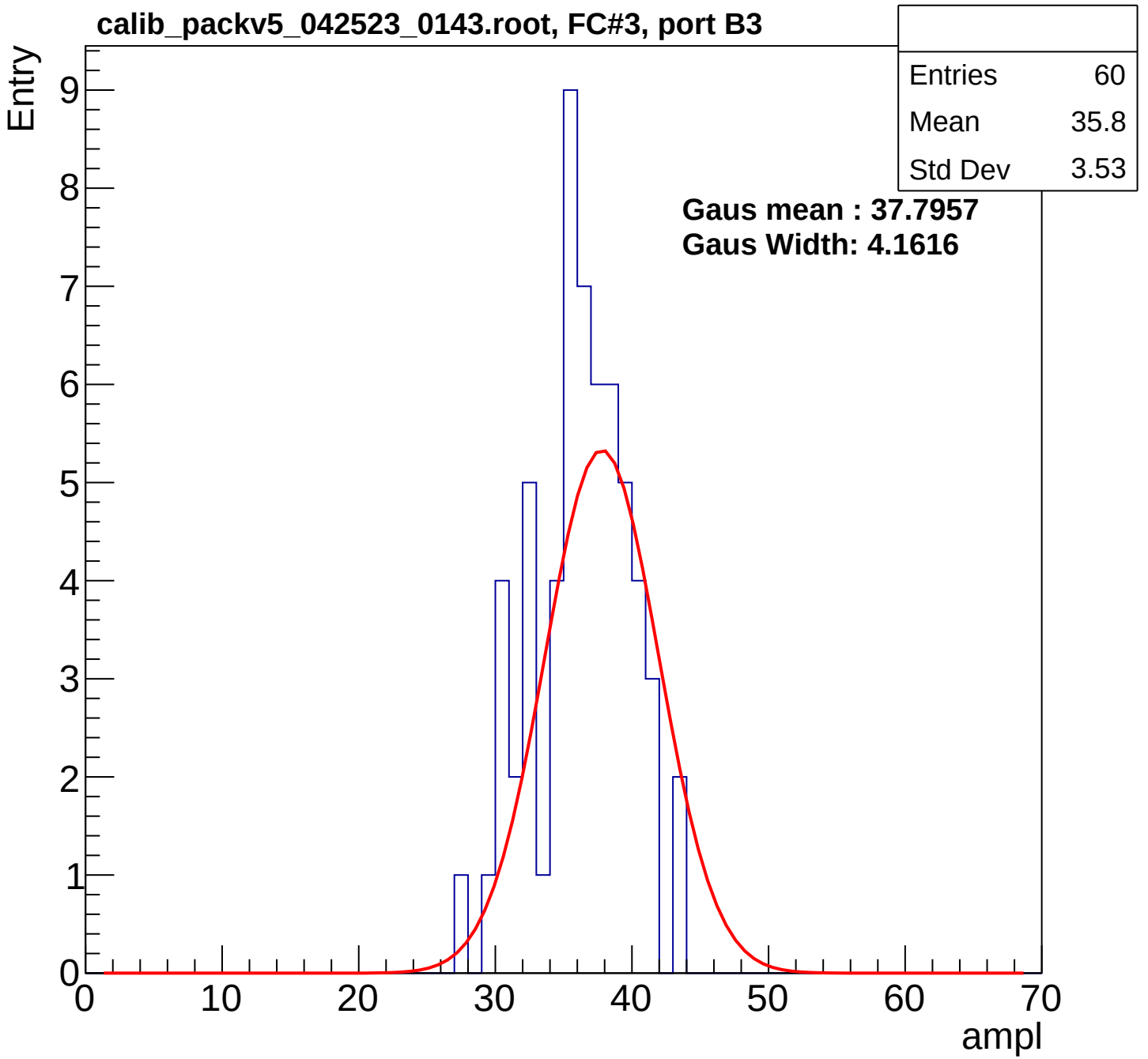
B1L000S, U10-ch8, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 35.8
Std Dev 3.53
Gaus mean : 37.7957
Gaus Width: 4.1616
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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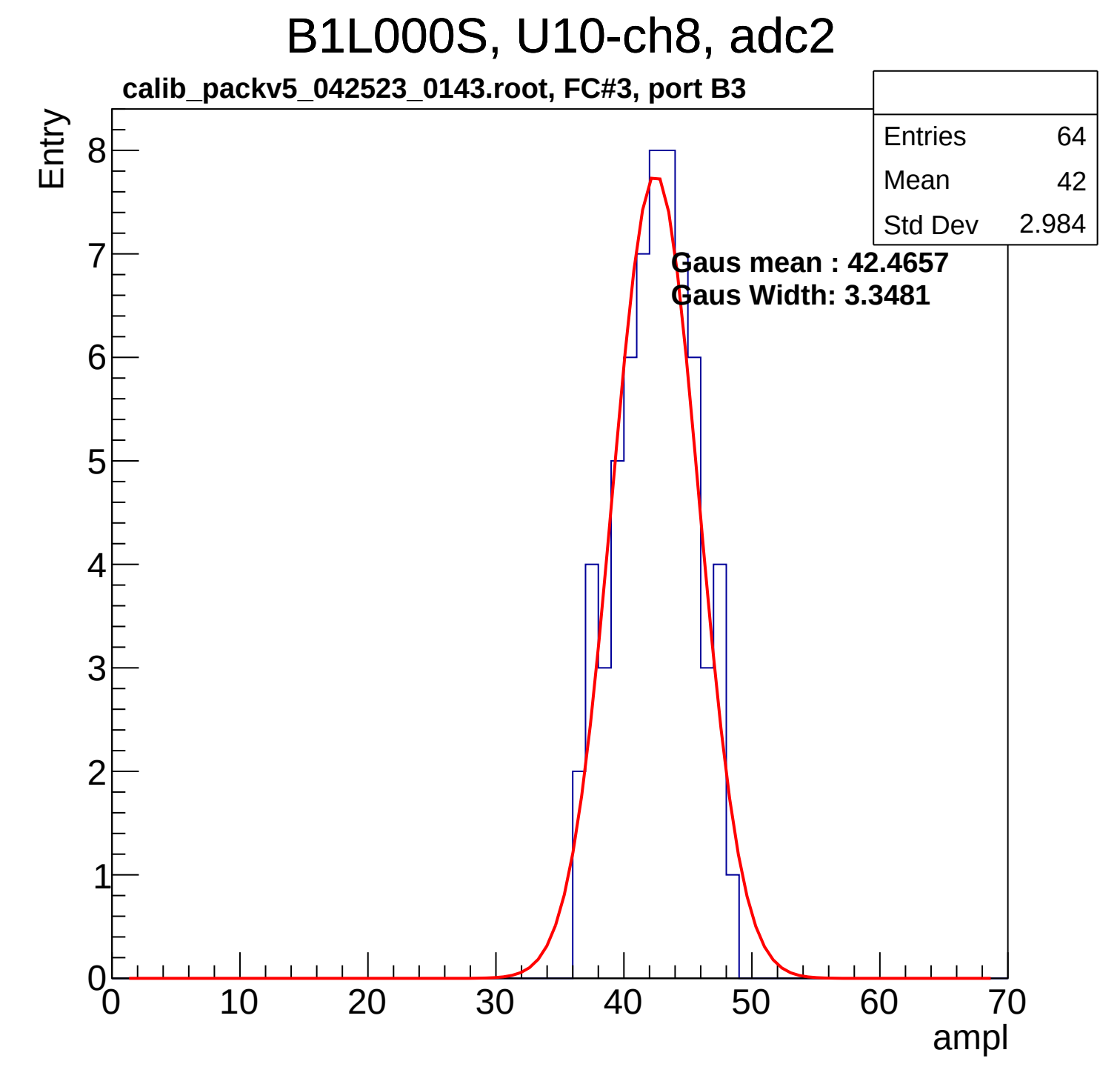
B1L000S, U10-ch8, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42
Std Dev 2.984
Gaus mean : 42.4657
Gaus Width: 3.3481
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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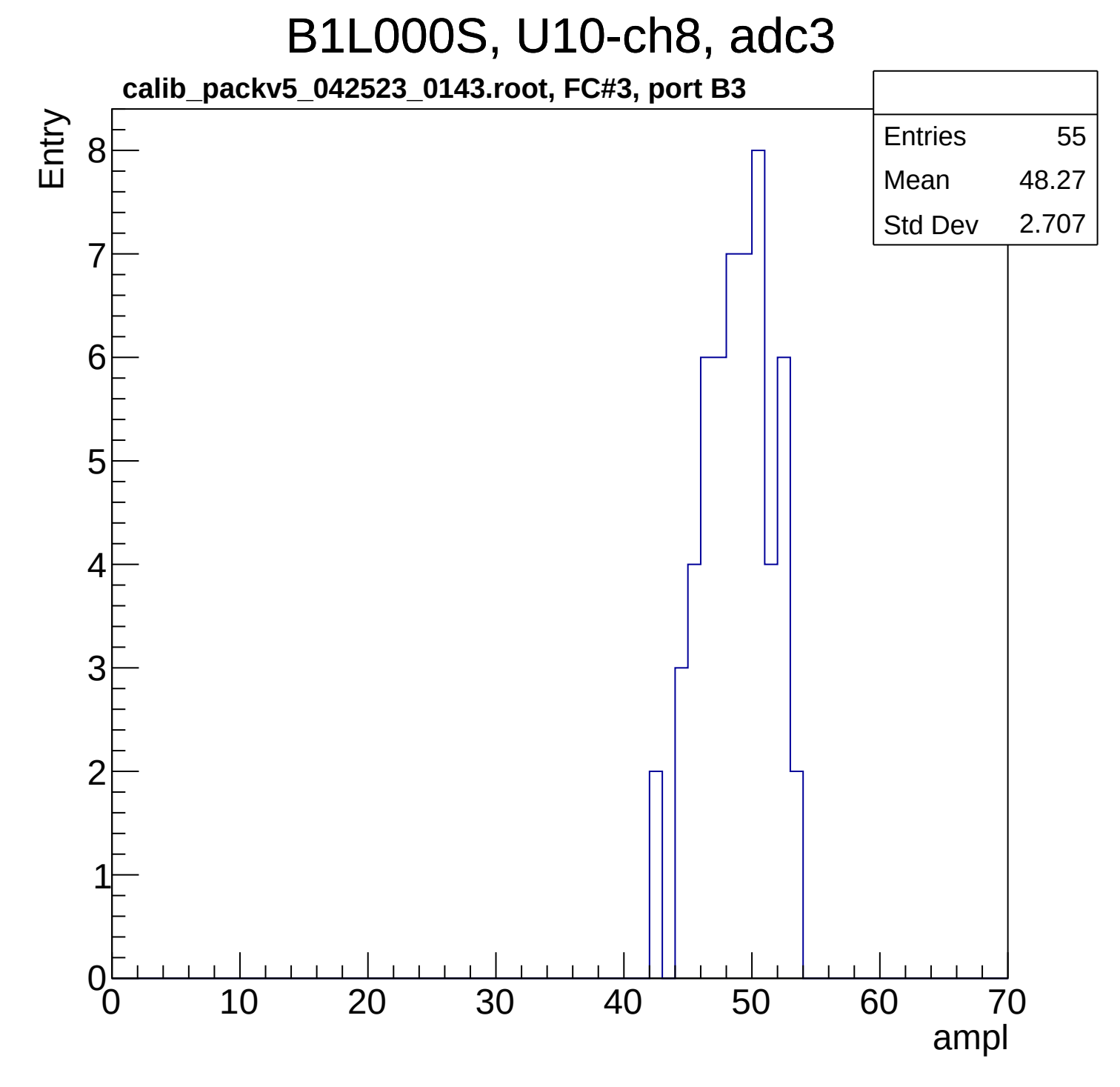
B1L000S, U10-ch8, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 48.27
Std Dev 2.707
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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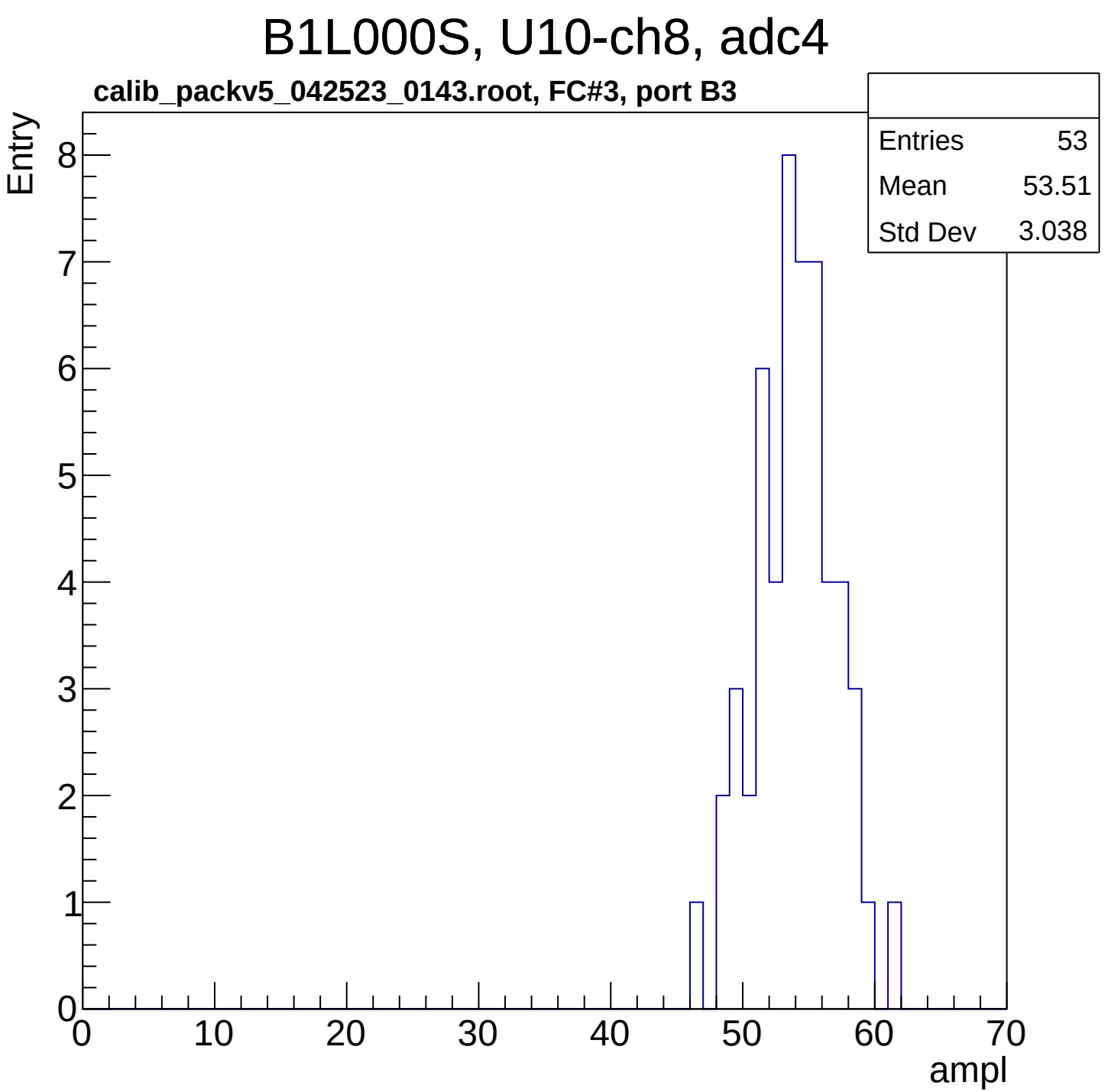
B1L000S, U10-ch8, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 53.51
Std Dev 3.038
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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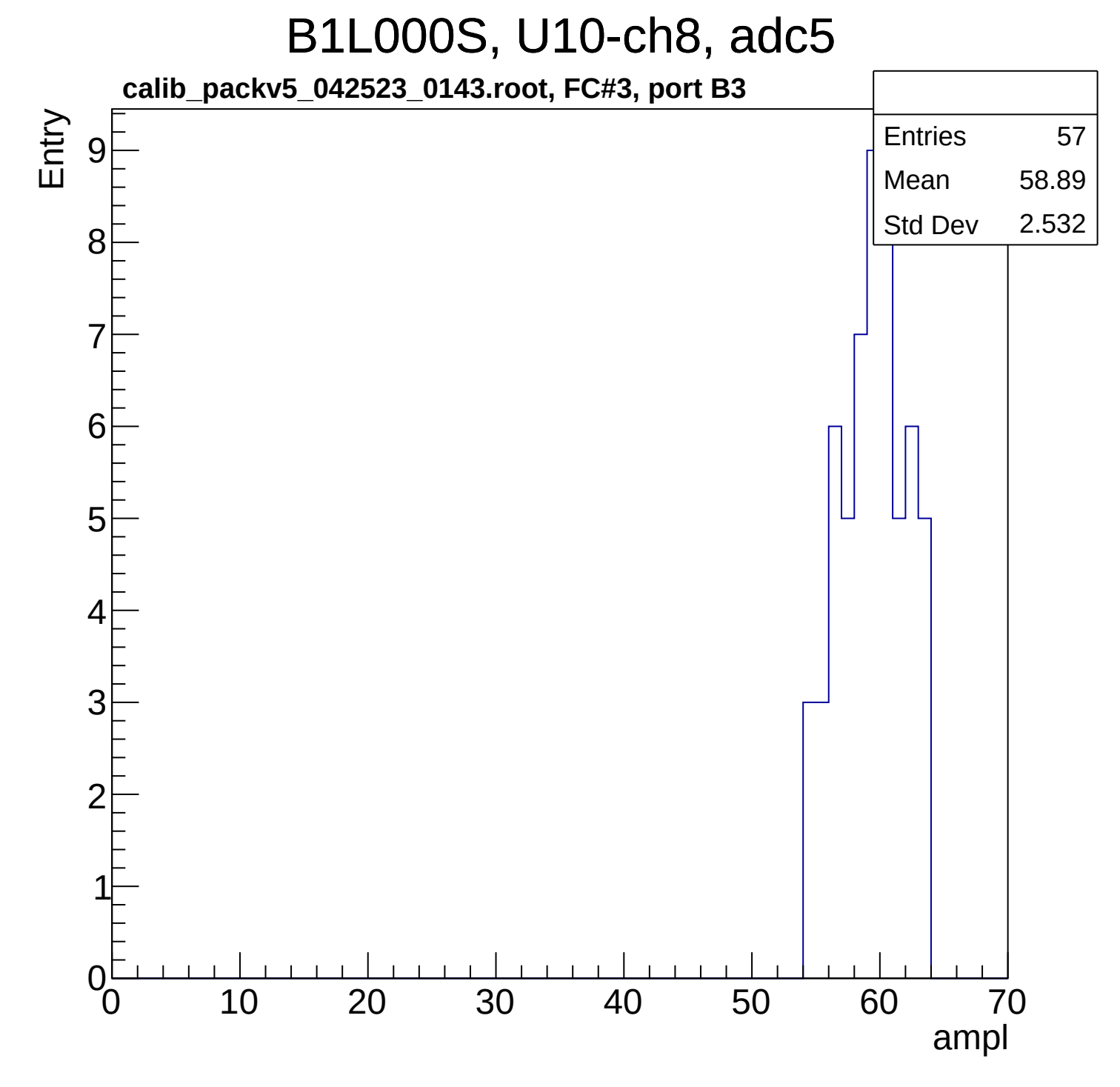
B1L000S, U10-ch8, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.89
Std Dev 2.532
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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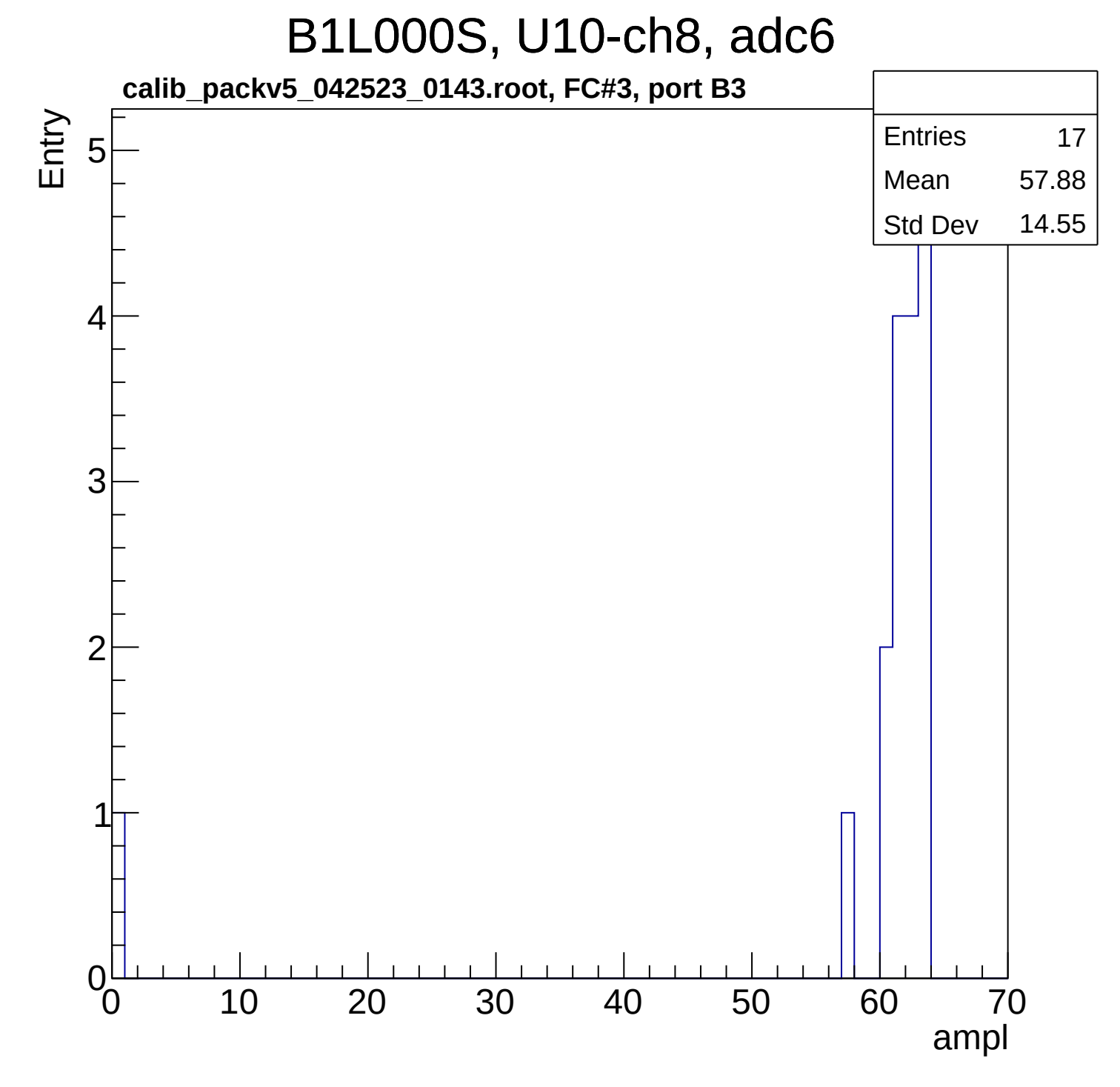
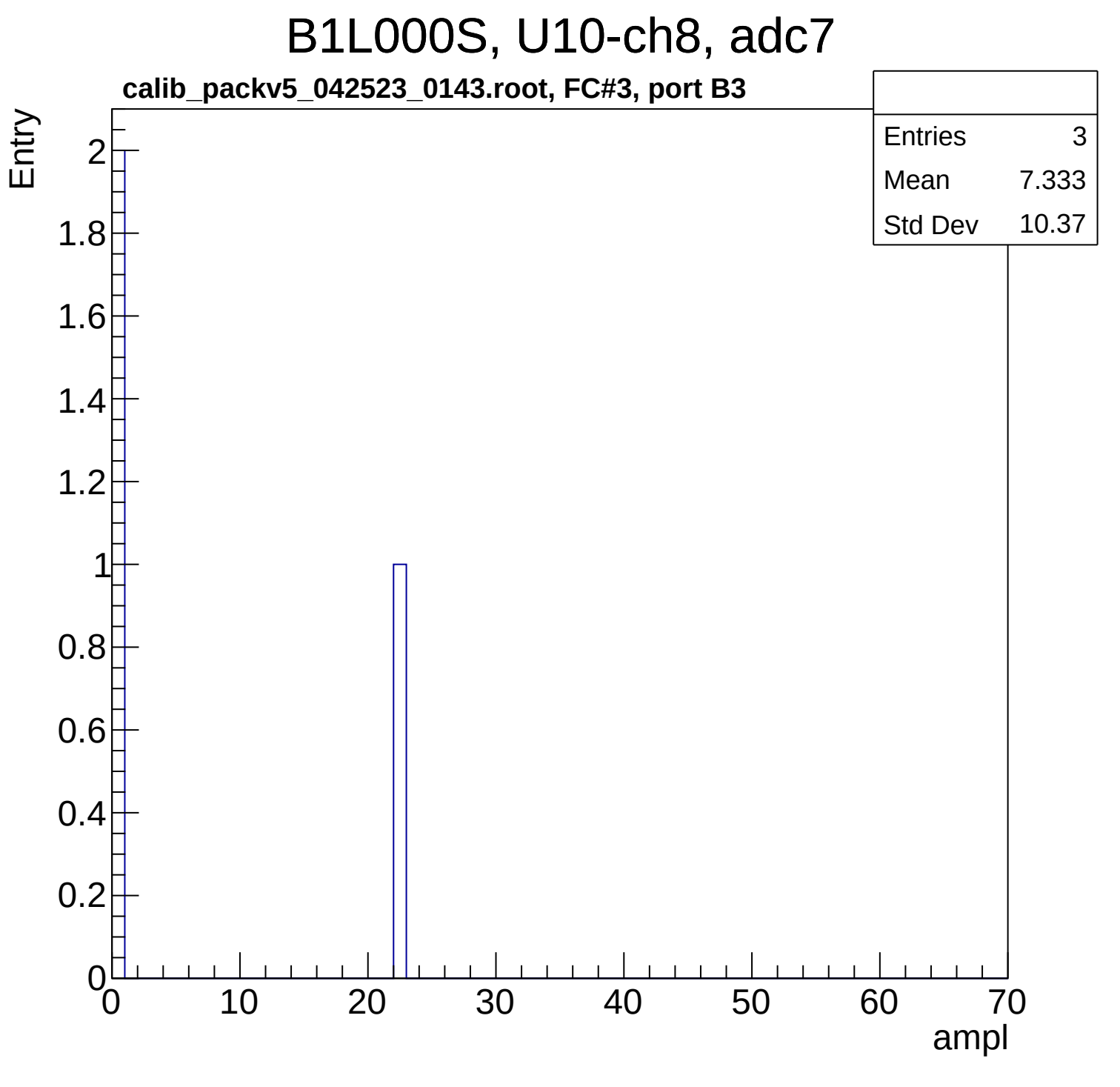
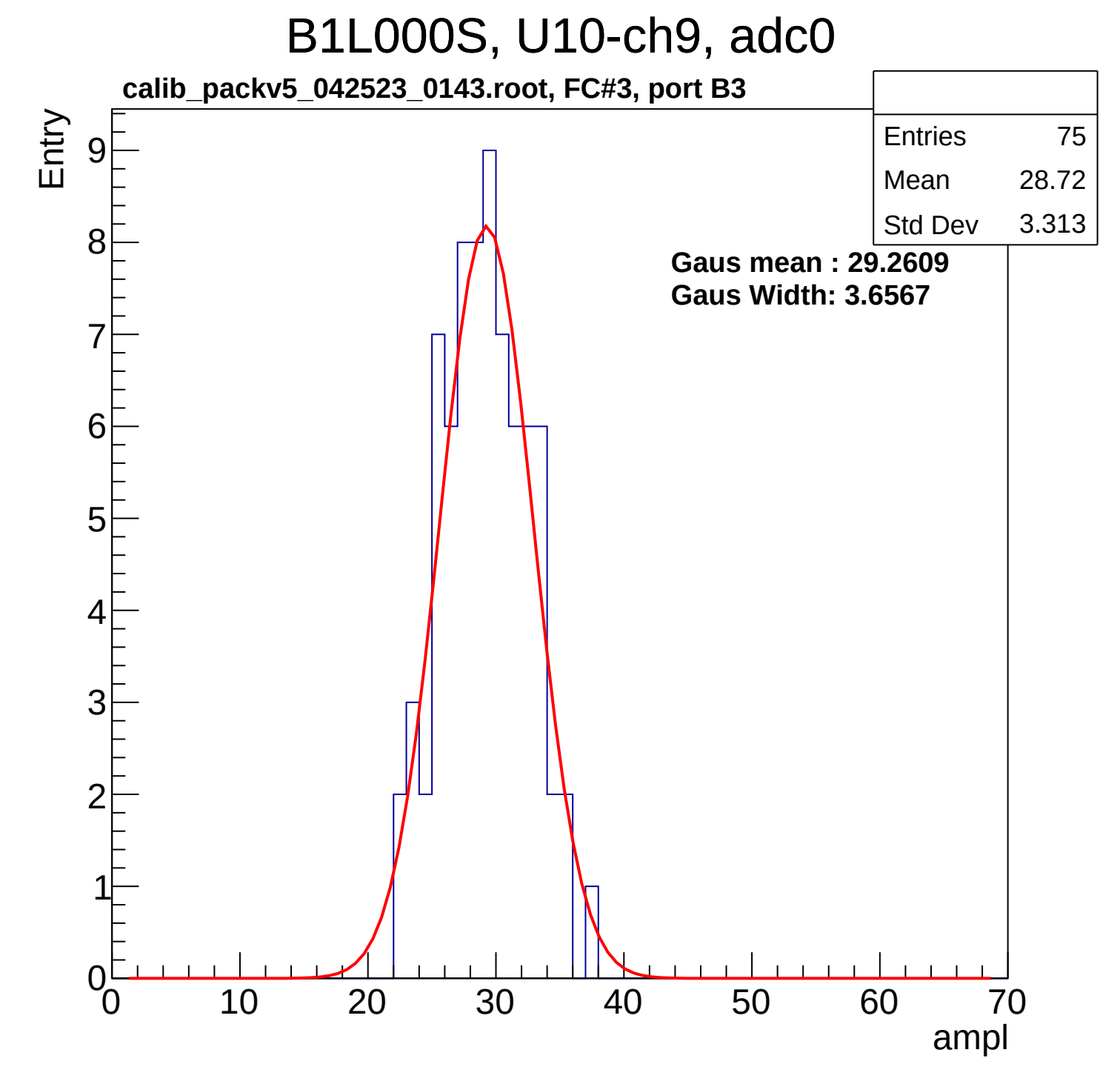

B1L000S, U10-ch8, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 57.88
Std Dev 14.55
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch8, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch9, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.72
Std Dev 3.313
Gaus mean : 29.2609
Gaus Width: 3.6567
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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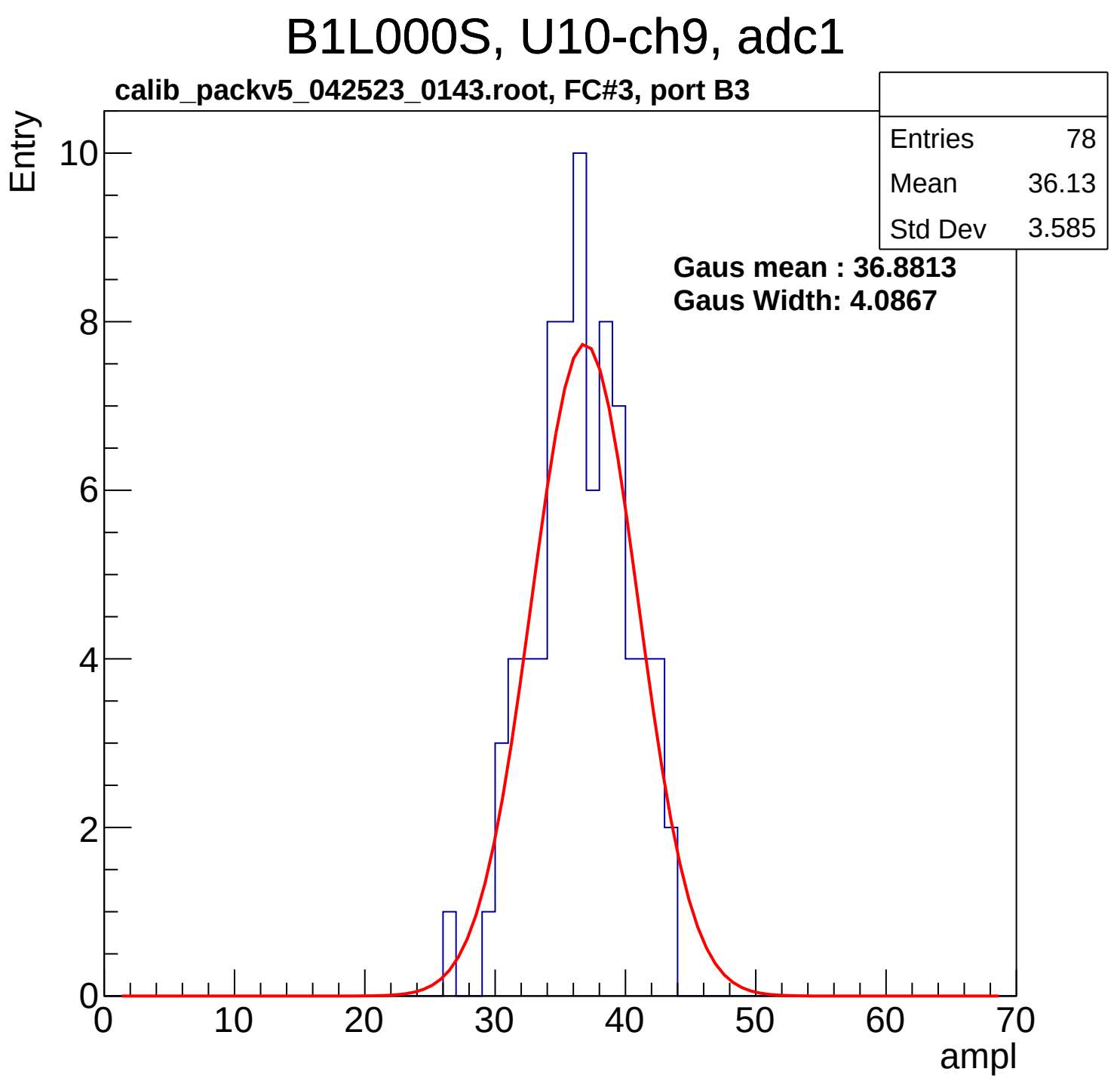
B1L000S, U10-ch9, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 36.13
Std Dev 3.585
Gaus mean : 36.8813
Gaus Width: 4.0867
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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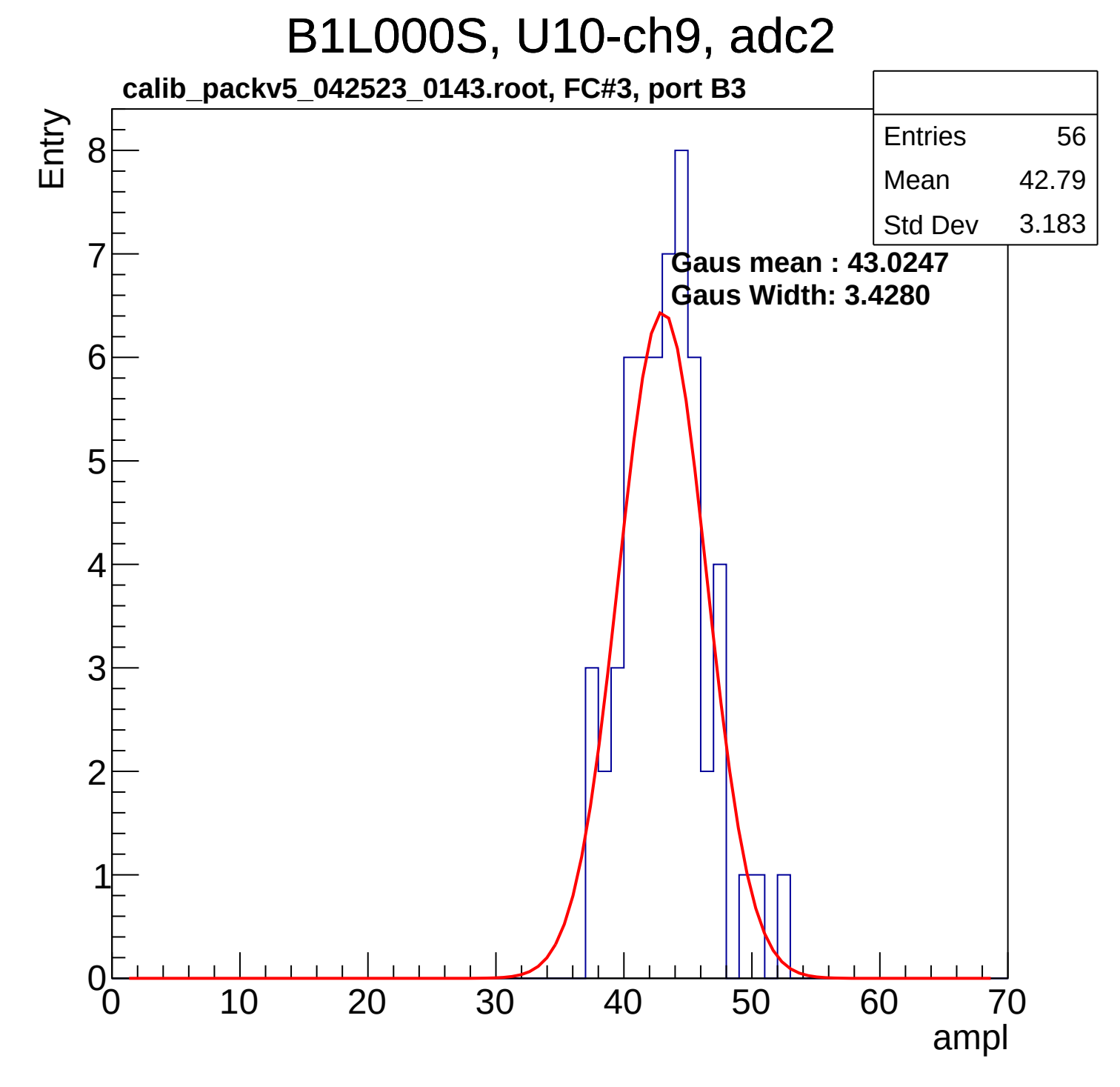
B1L000S, U10-ch9, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.79
Std Dev 3.183
Gaus mean : 43.0247
Gaus Width: 3.4280
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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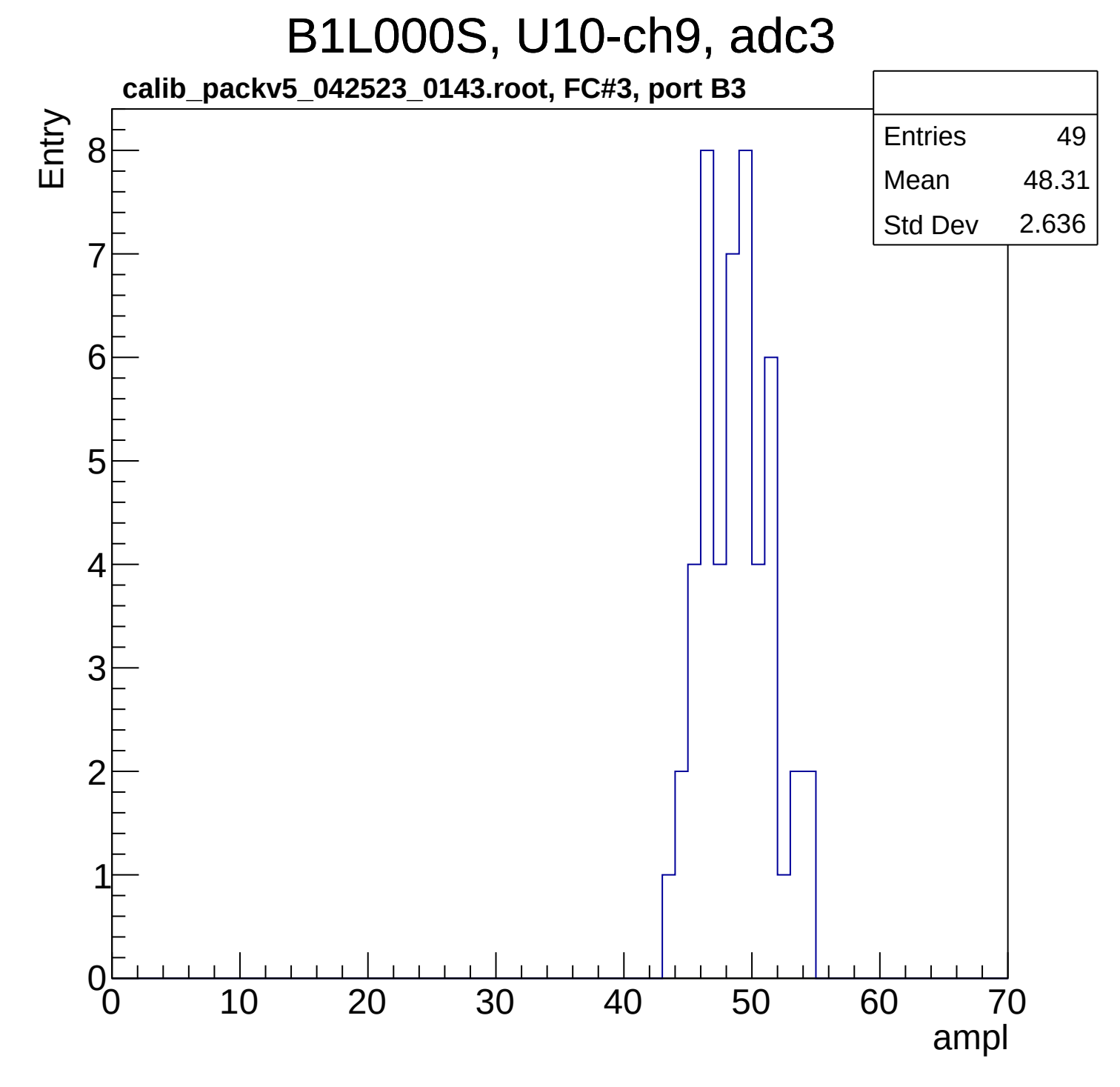
B1L000S, U10-ch9, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 48.31
Std Dev 2.636
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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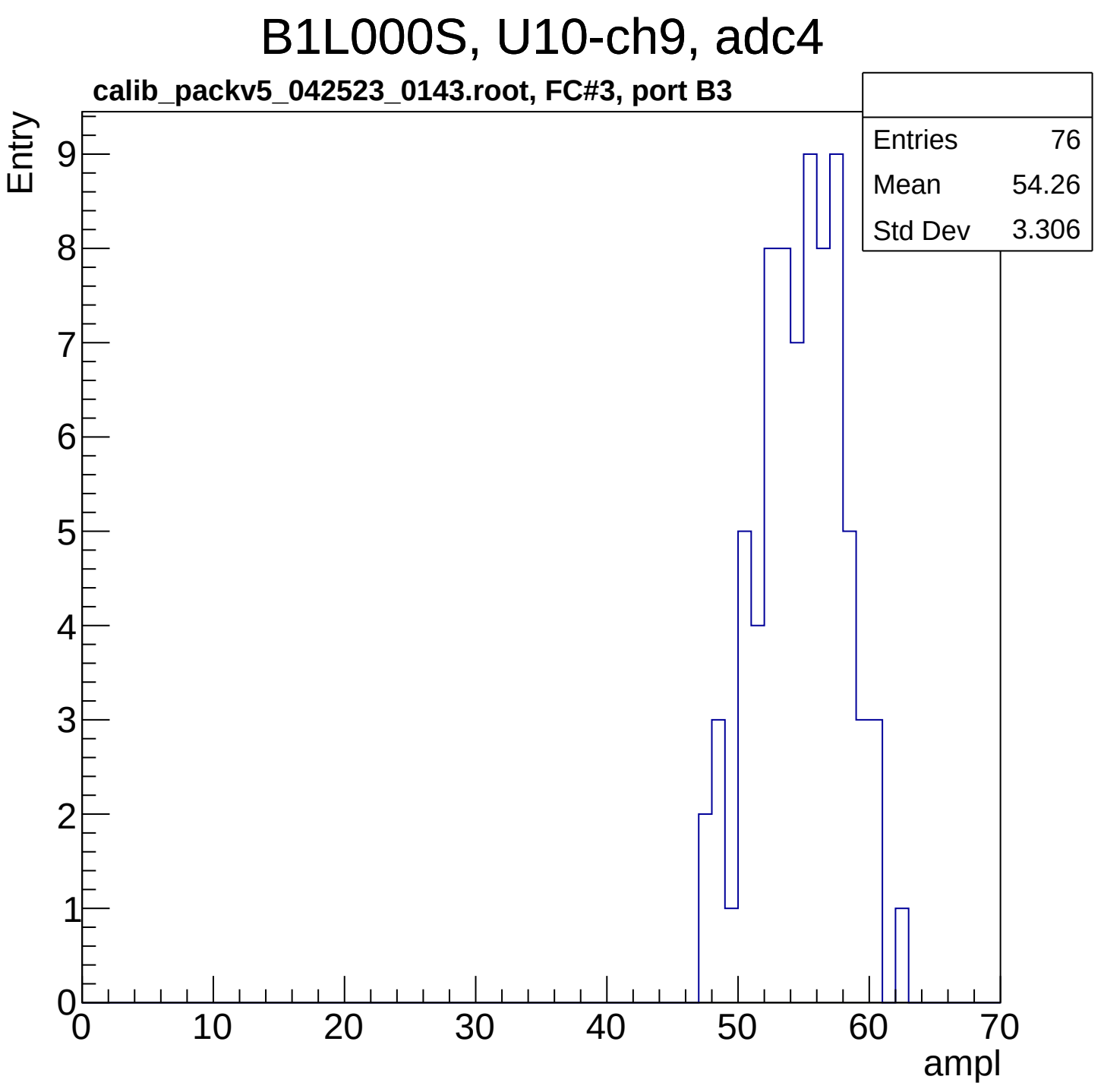
B1L000S, U10-ch9, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 54.26
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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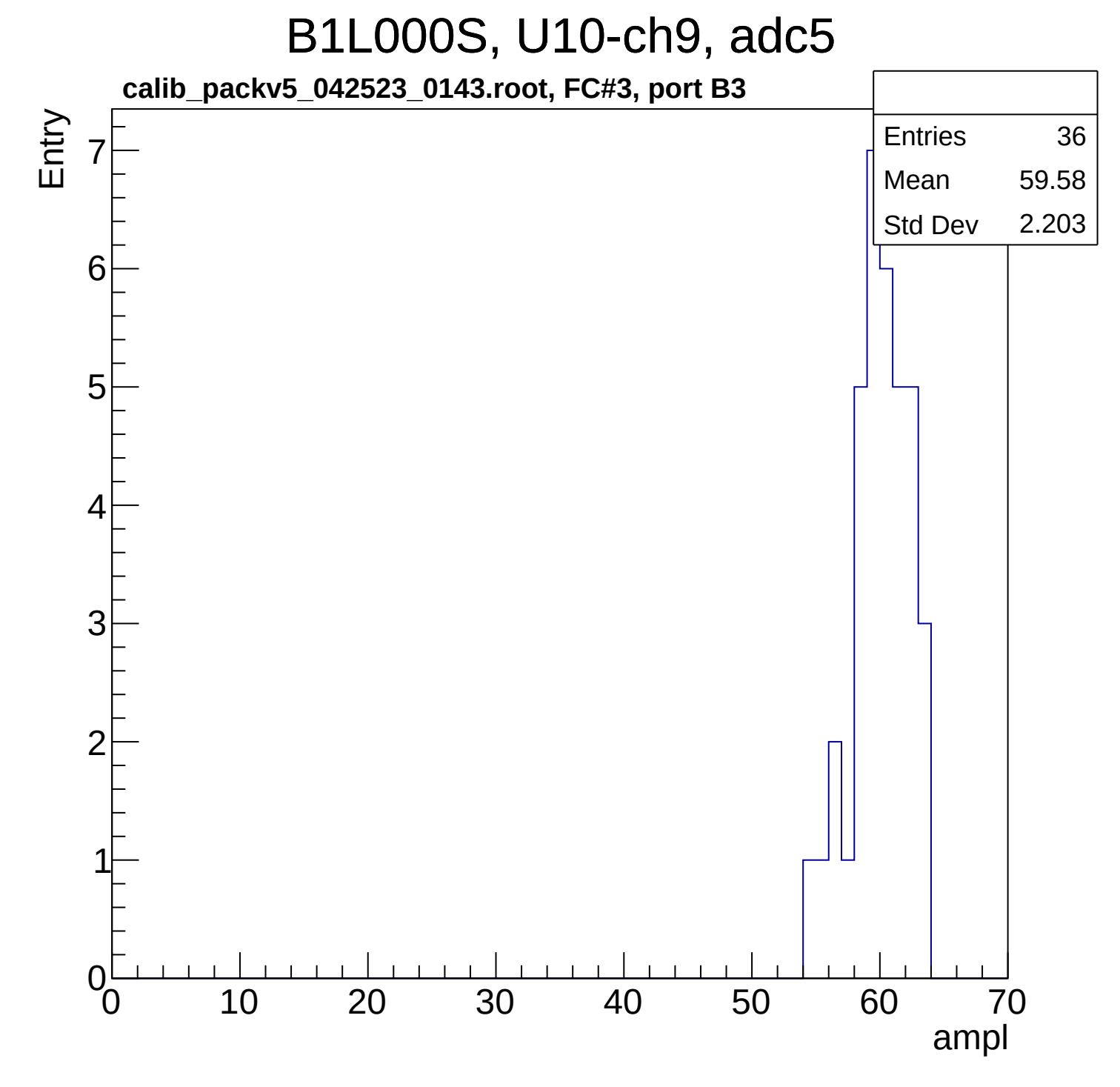
B1L000S, U10-ch9, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 59.58
Std Dev 2.203
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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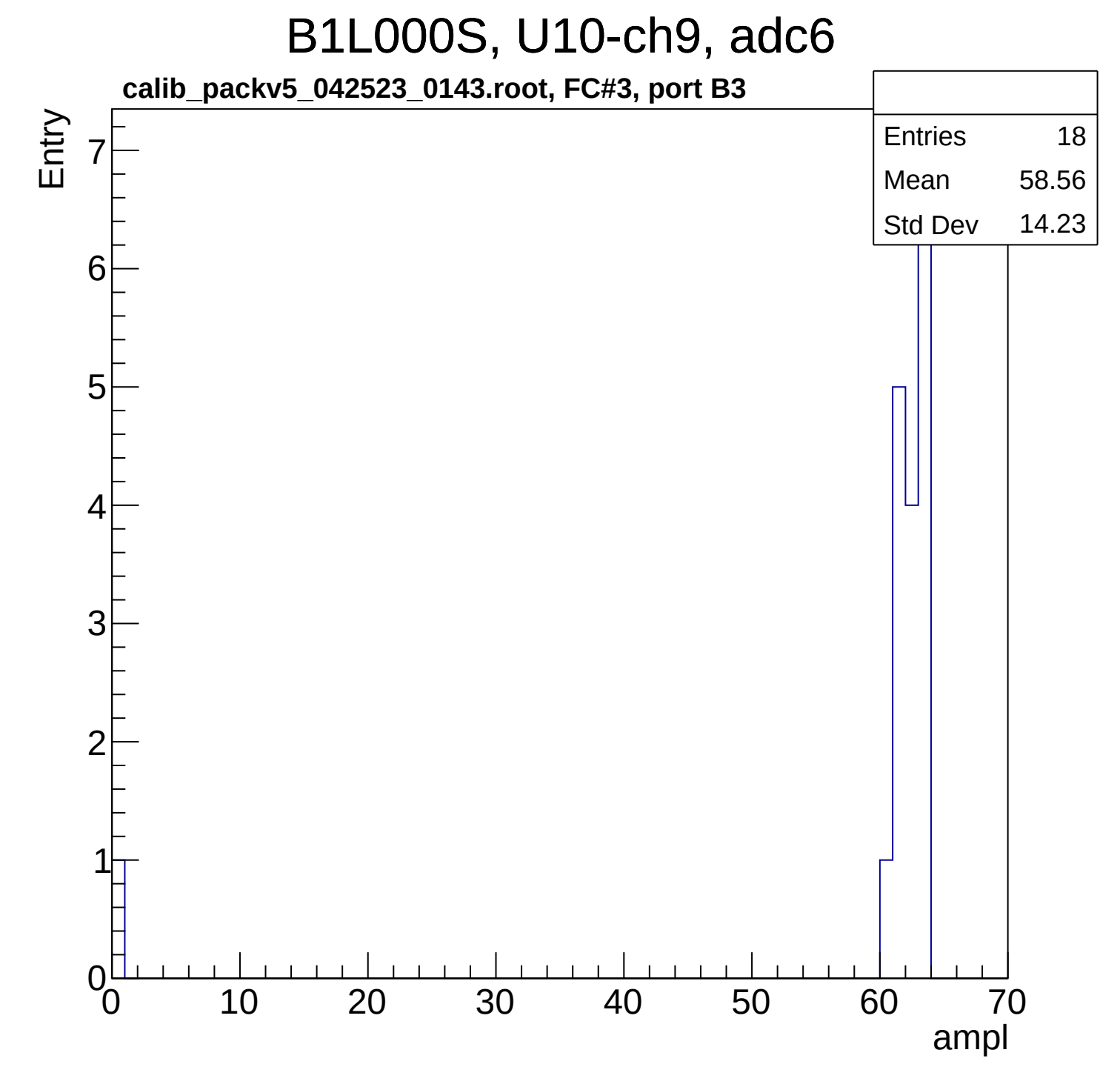
B1L000S, U10-ch9, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 58.56
Std Dev 14.23
Entry
ampl
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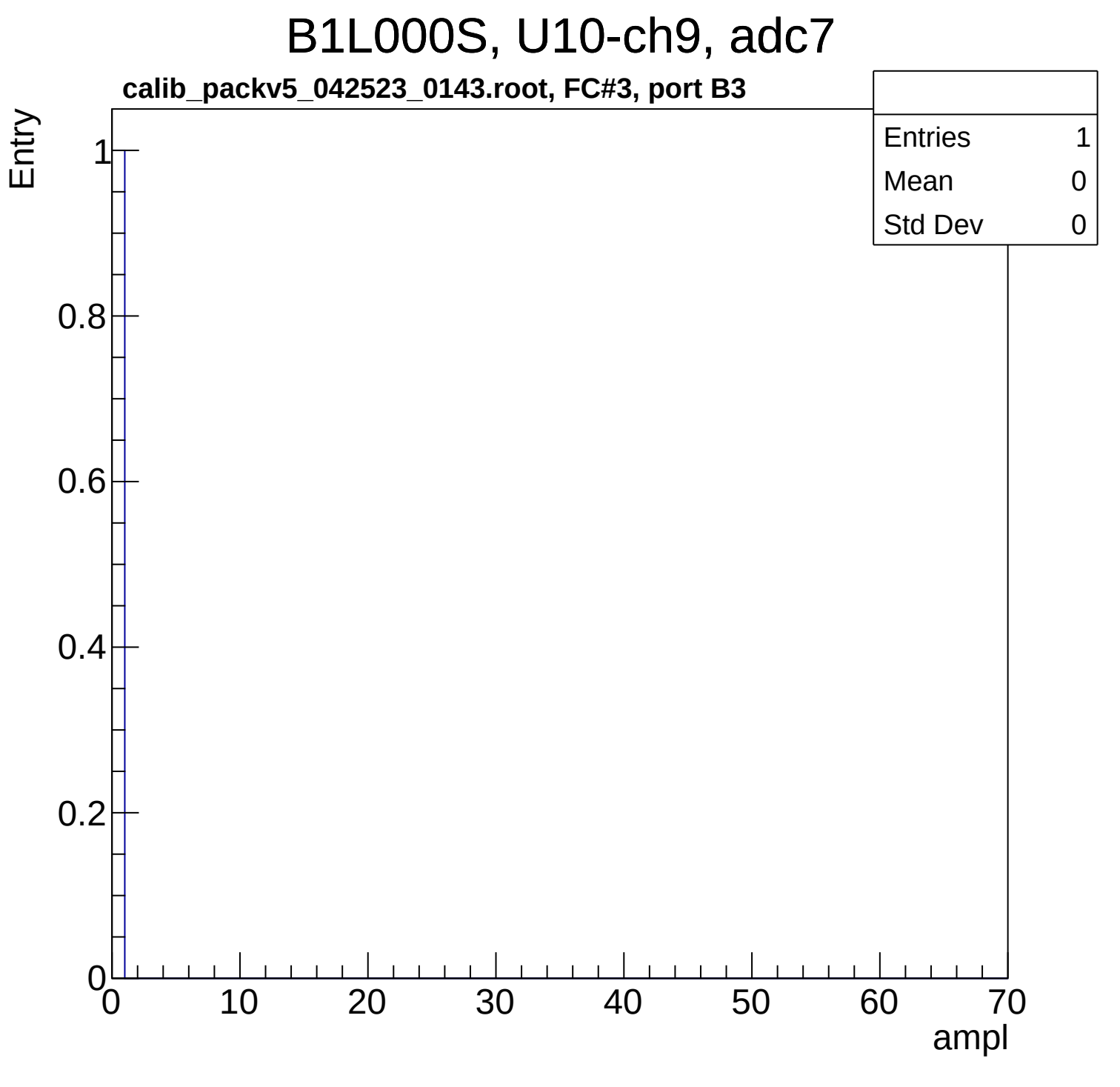
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch9, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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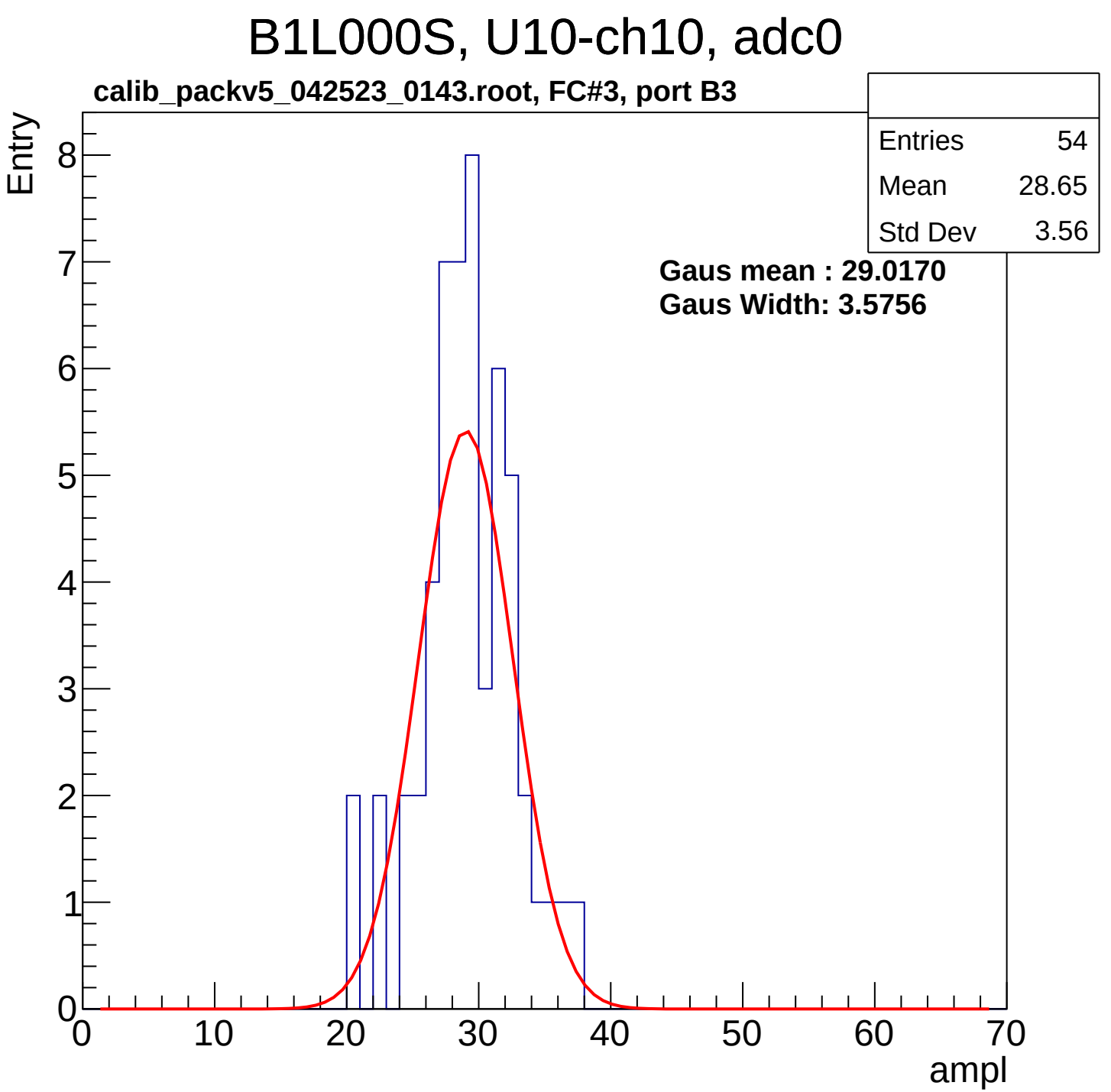
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch10, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.65
Std Dev 3.56
Gaus mean : 29.0170
Gaus Width: 3.5756
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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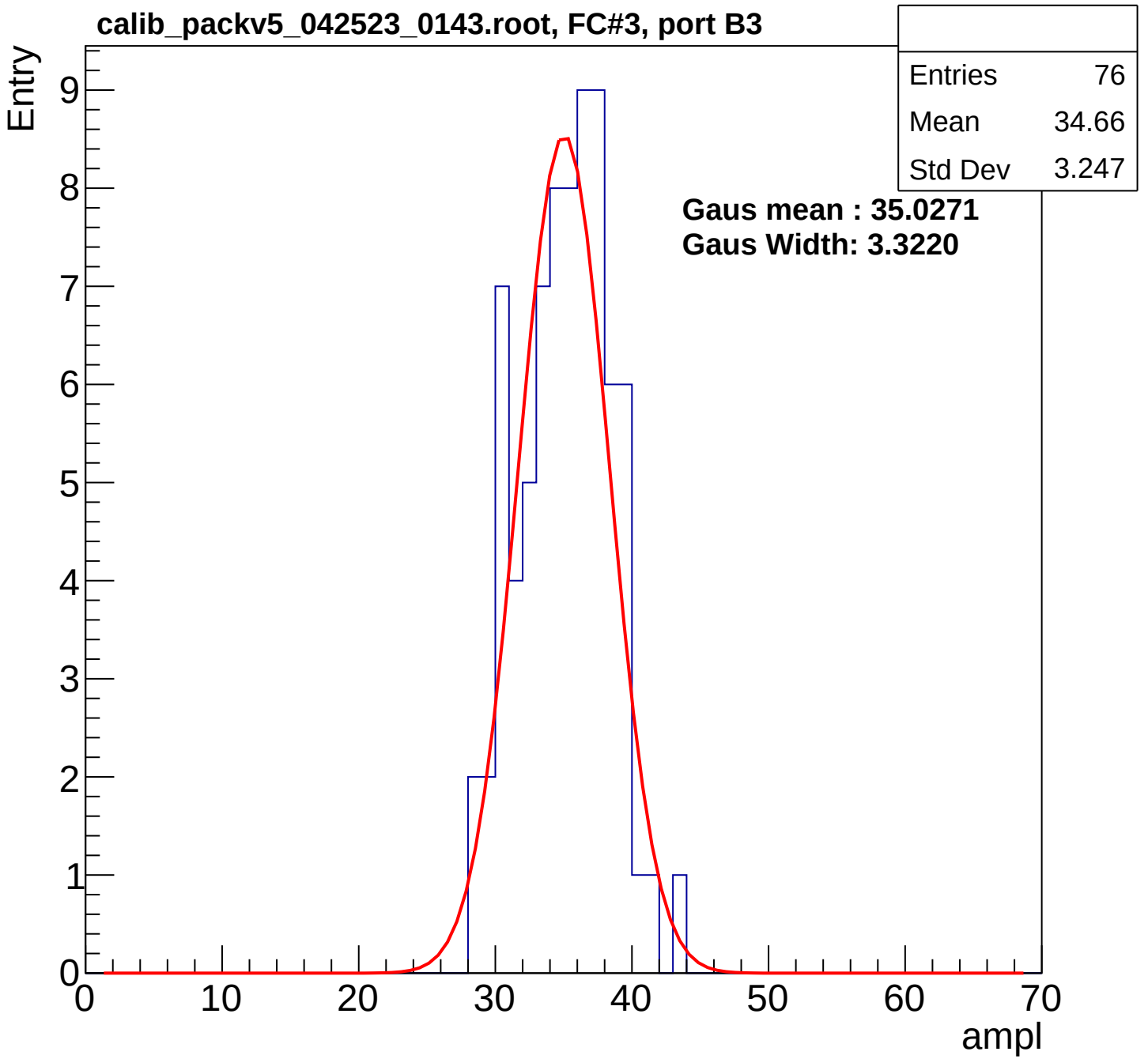
B1L000S, U10-ch10, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 34.66
Std Dev 3.247
Gaus mean : 35.0271
Gaus Width: 3.3220
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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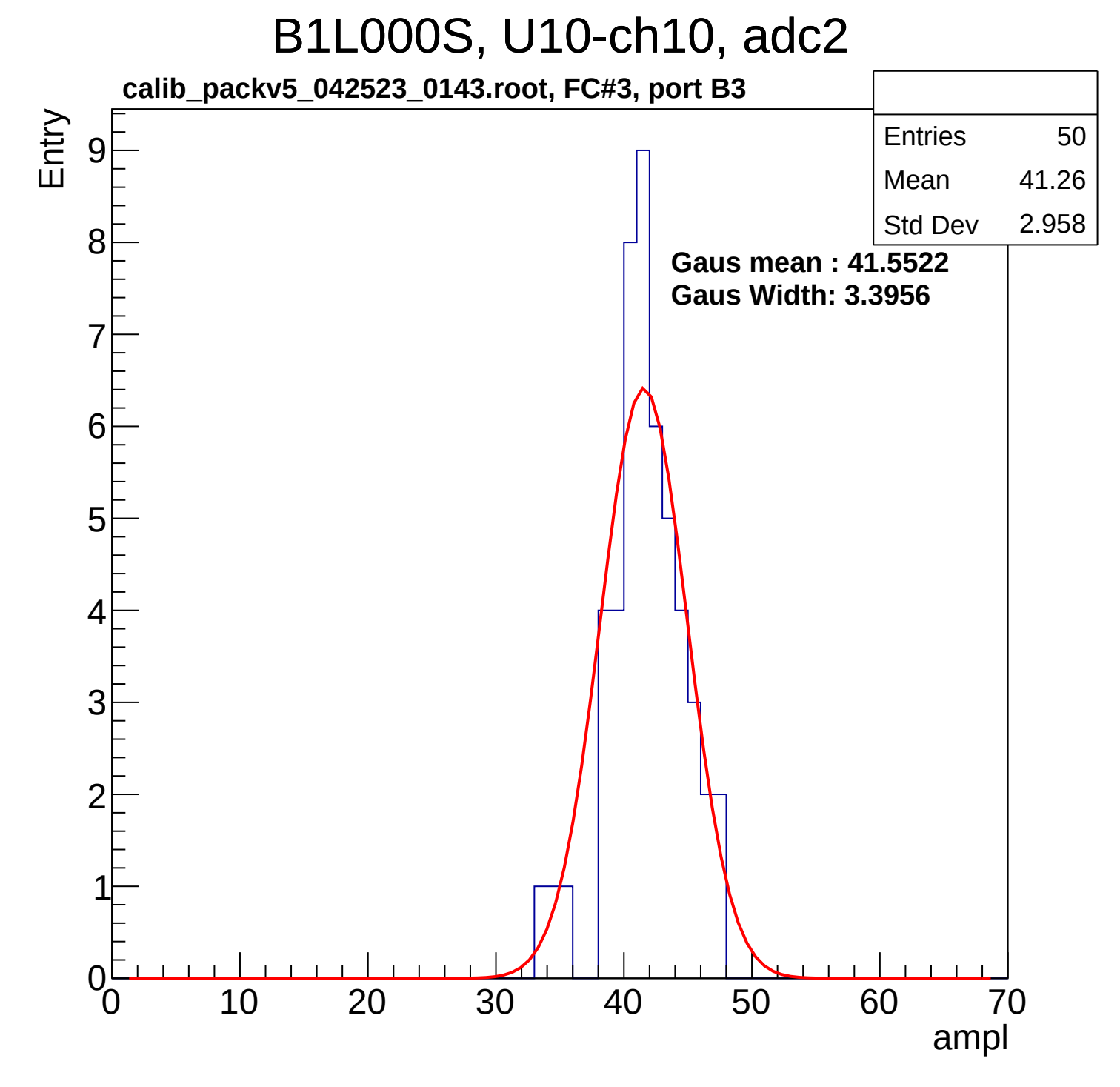
60
70


B1L000S, U10-ch10, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 41.26
Std Dev 2.958
Gaus mean : 41.5522
Gaus Width: 3.3956
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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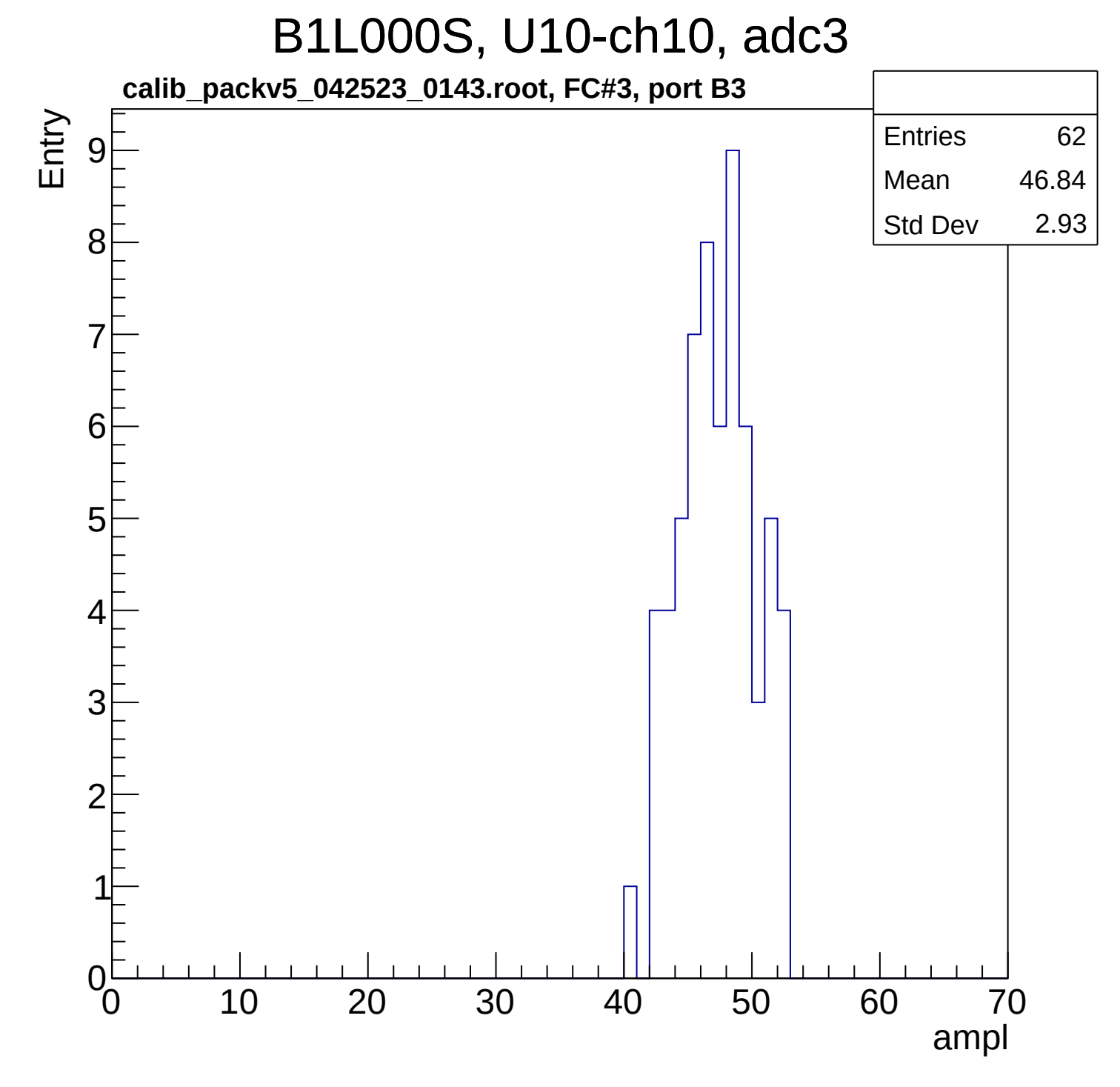
B1L000S, U10-ch10, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.84
Std Dev 2.93
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
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60
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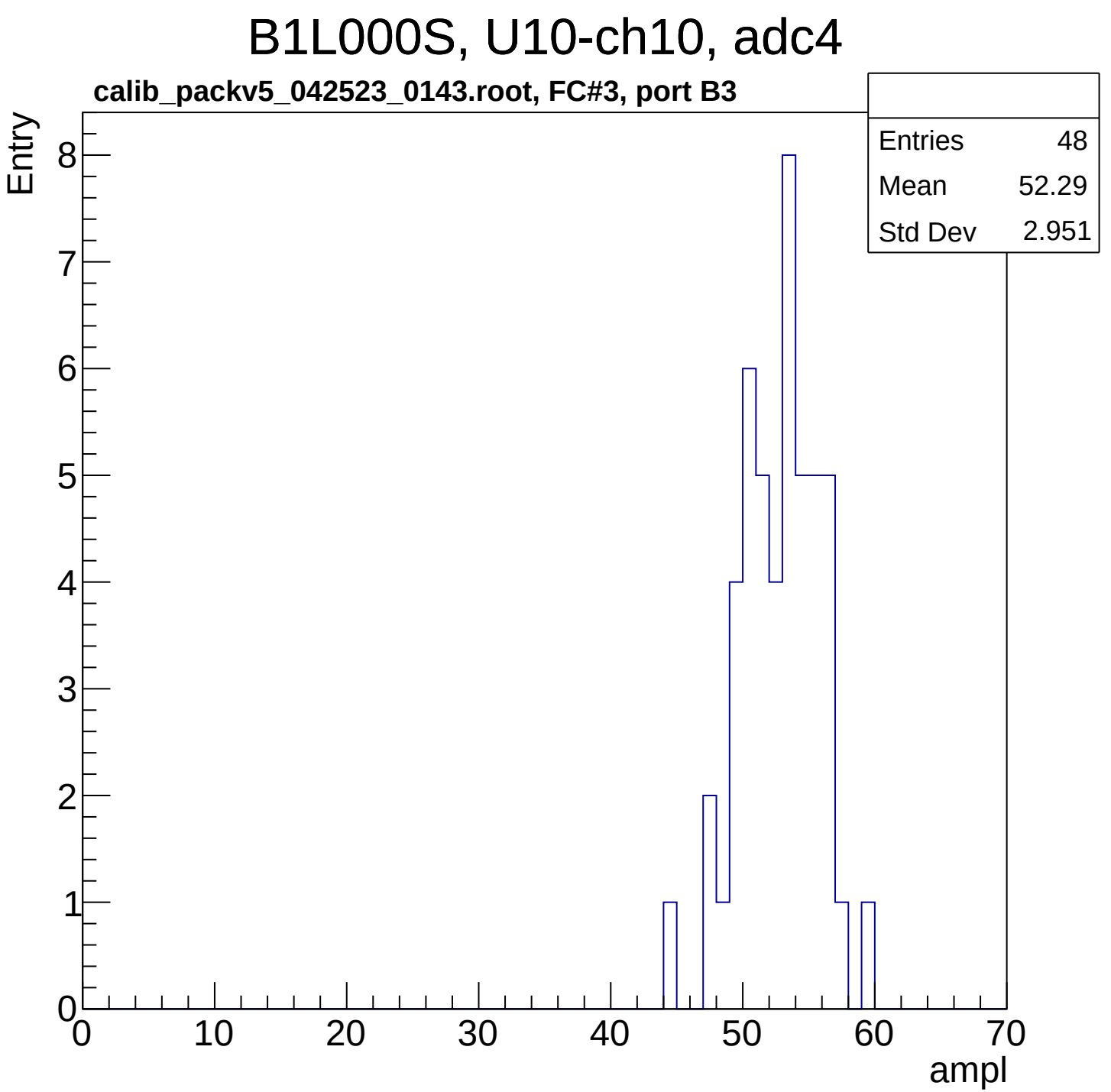
B1L000S, U10-ch10, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 52.29
Std Dev 2.951
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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60
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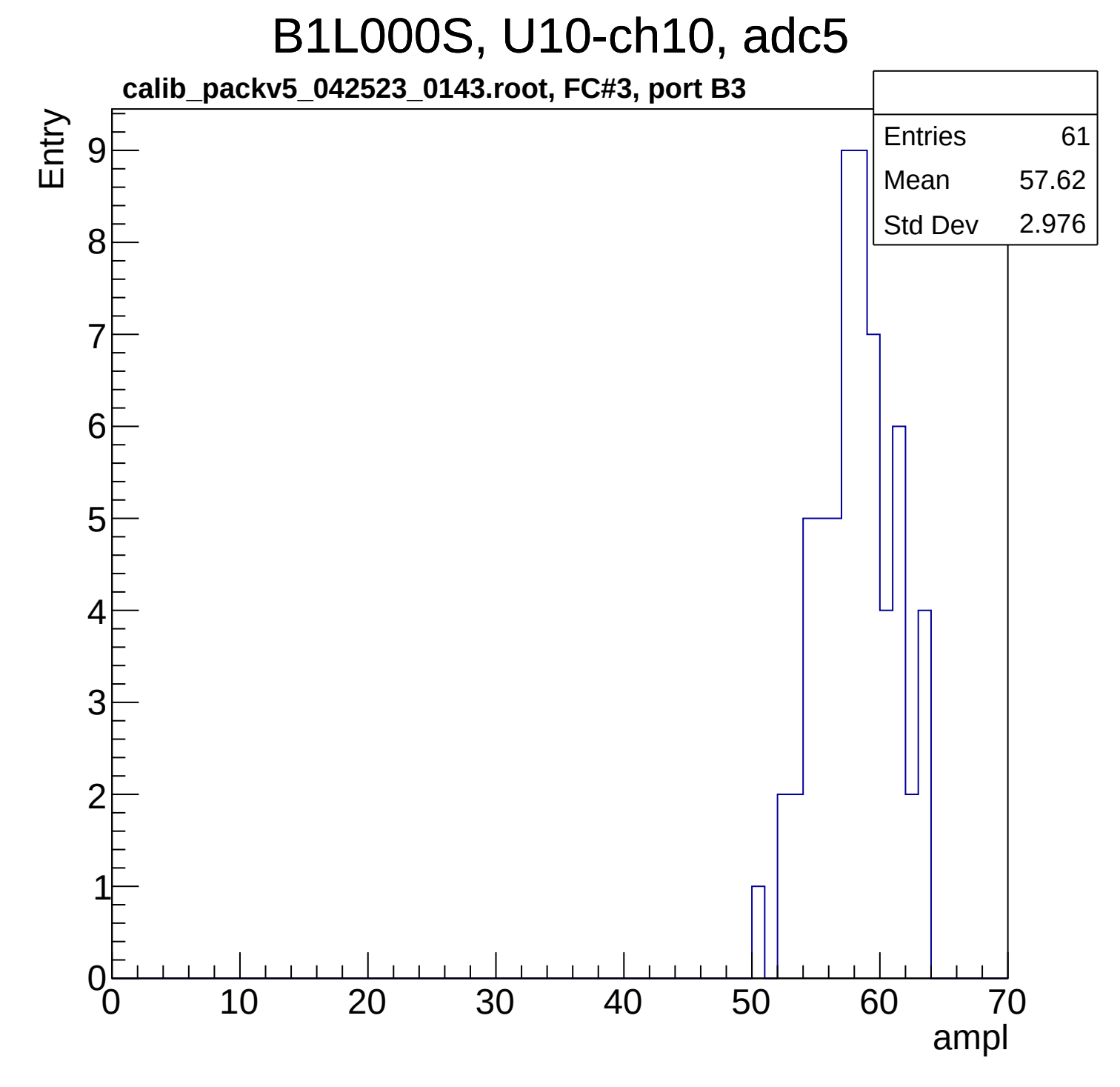
B1L000S, U10-ch10, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.62
Std Dev 2.976
Entry
ampl
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6
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60
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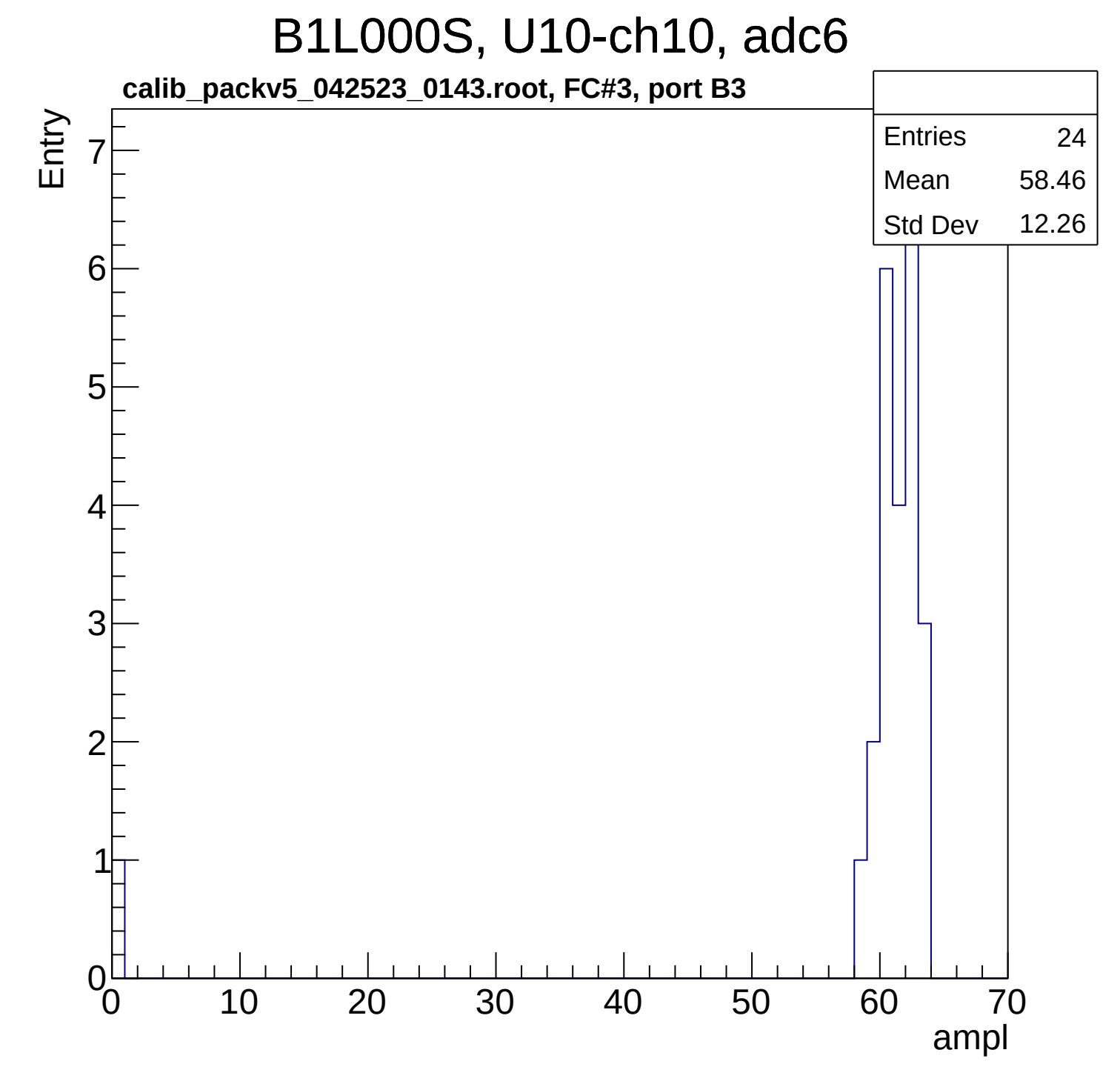
B1L000S, U10-ch10, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 58.46
Std Dev 12.26
Entry
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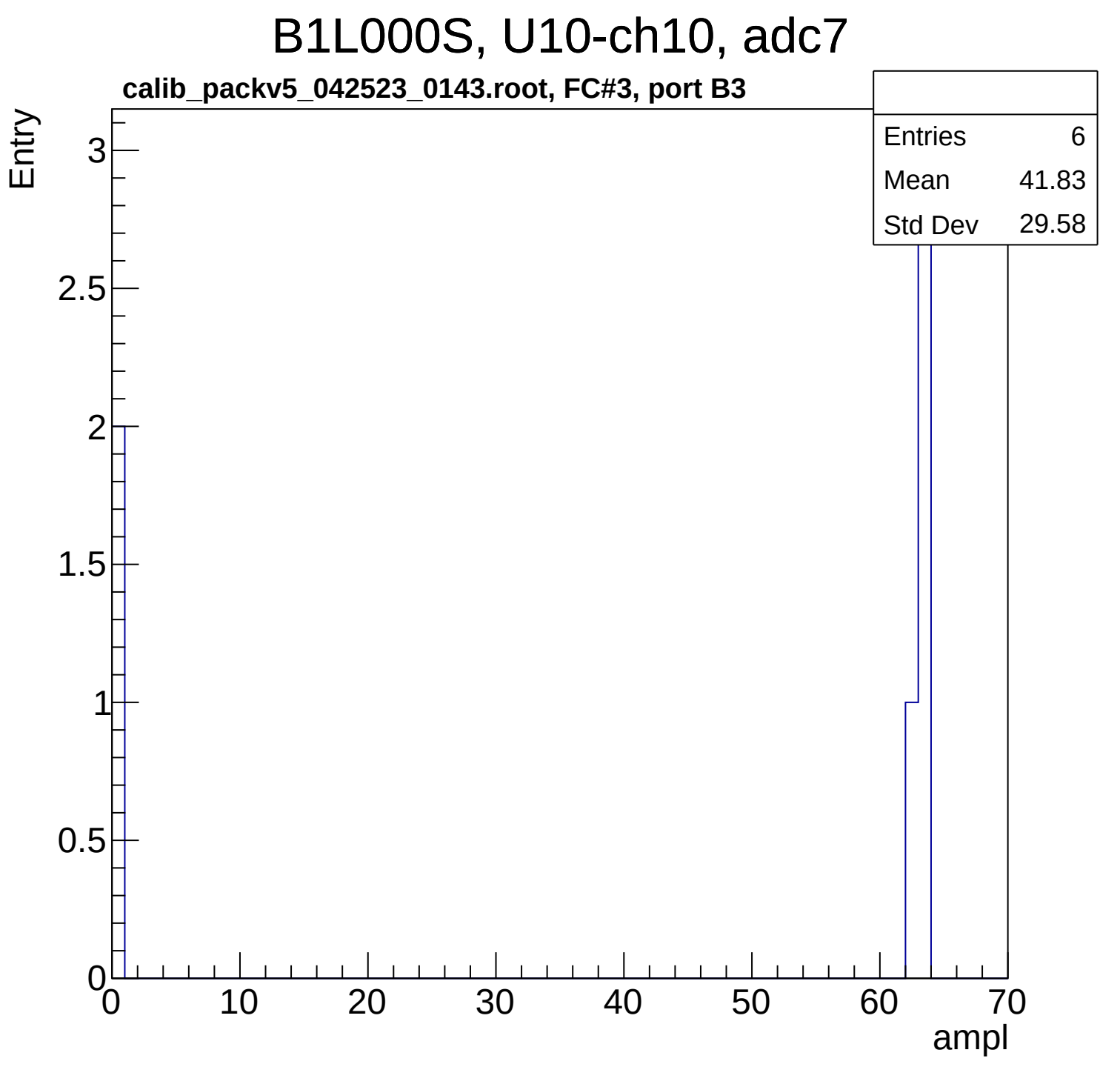
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch10, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 41.83
Std Dev 29.58
Entry
ampl
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1
1.5
2
2.5
3
0
10
20
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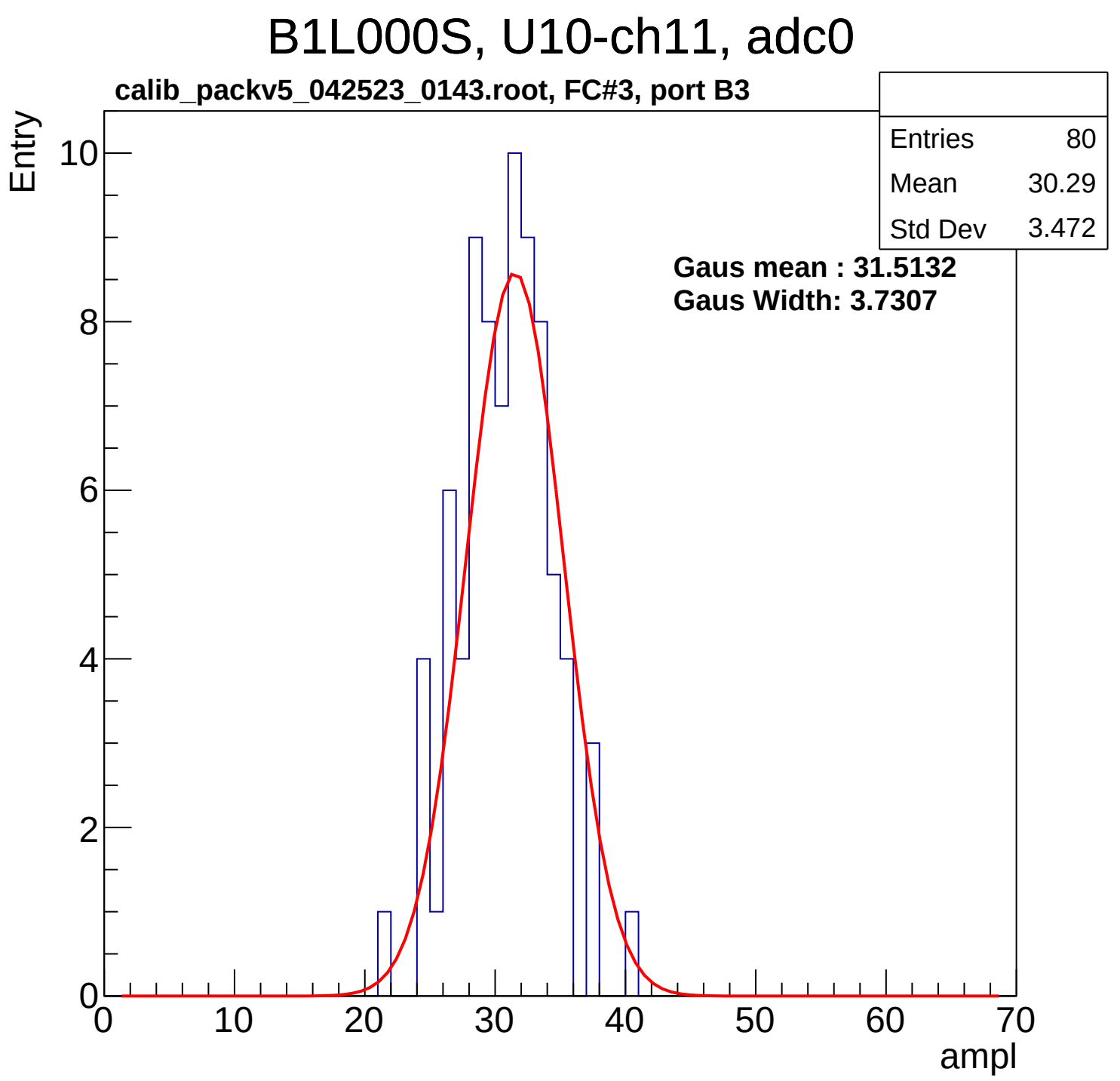
40
50
60
70


B1L000S, U10-ch11, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 30.29
Std Dev 3.472
Gaus mean : 31.5132
Gaus Width: 3.7307
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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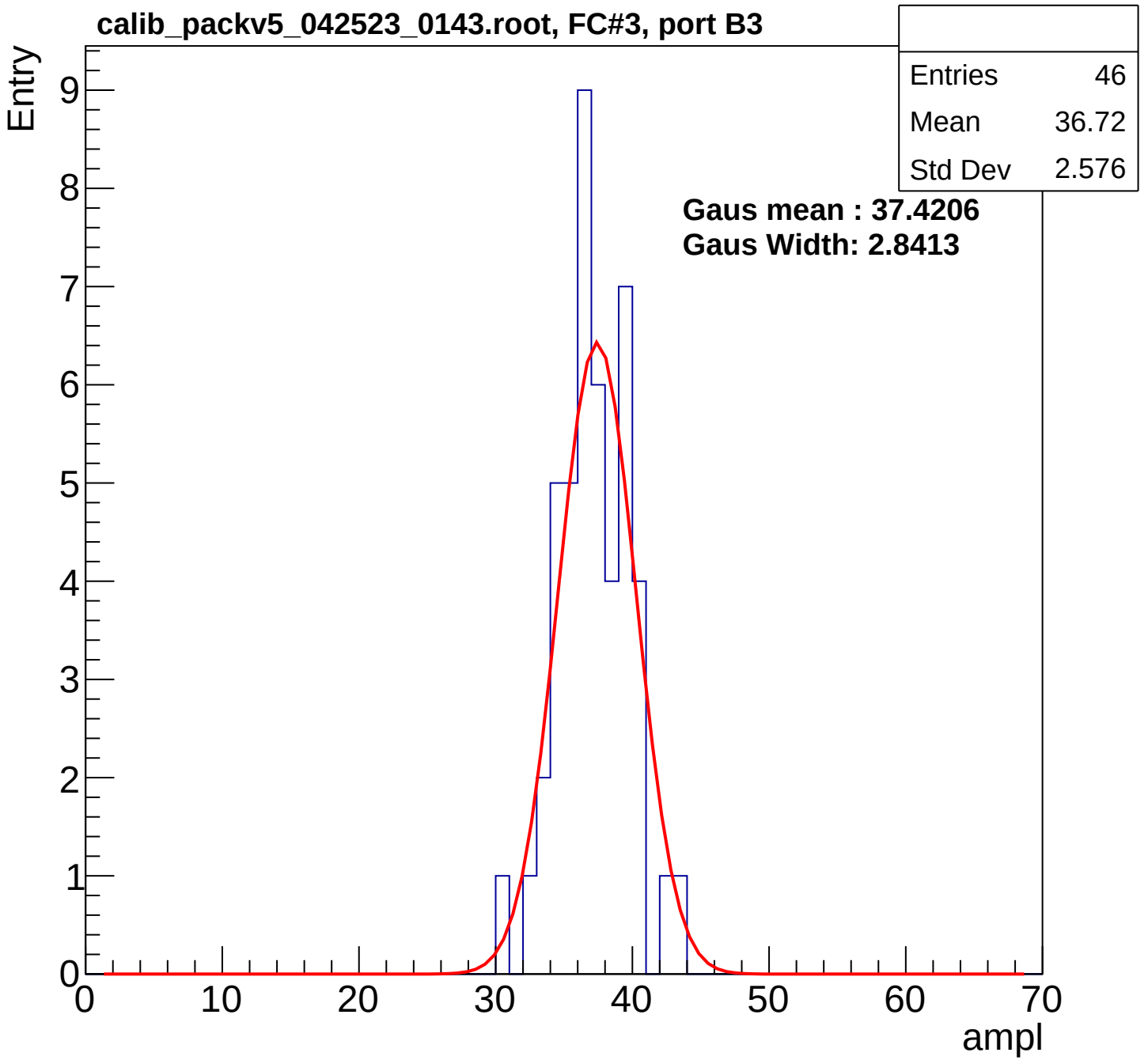
B1L000S, U10-ch11, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 36.72
Std Dev 2.576
Gaus mean : 37.4206
Gaus Width: 2.8413
Entry
ampl
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2
3
4
5
6
7
8
9
0
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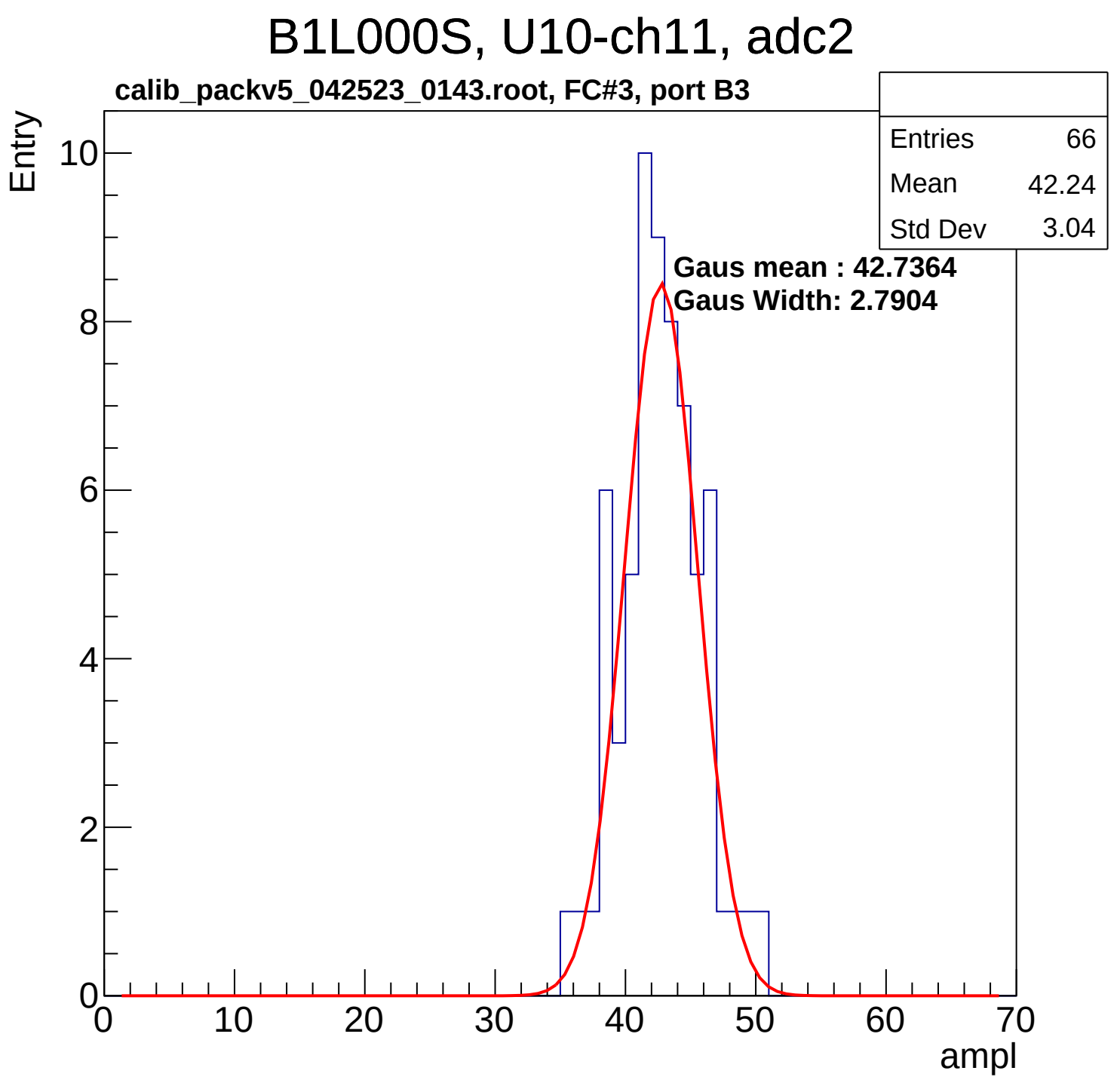
B1L000S, U10-ch11, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 42.24
Std Dev 3.04
Gaus mean : 42.7364
Gaus Width: 2.7904
Entry
ampl
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2
4
6
8
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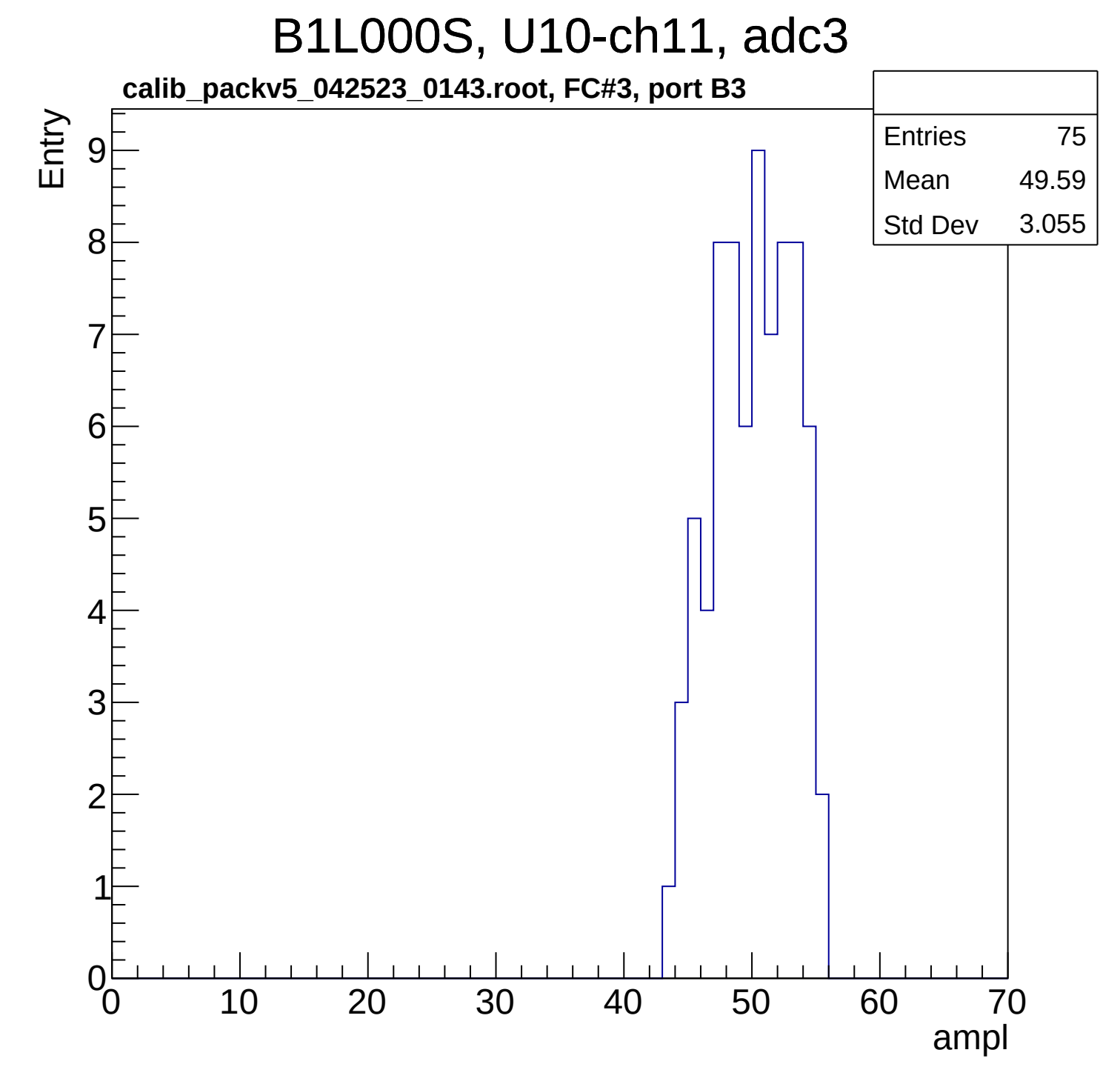
B1L000S, U10-ch11, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 49.59
Std Dev 3.055
Entry
ampl
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2
3
4
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6
7
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0
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60
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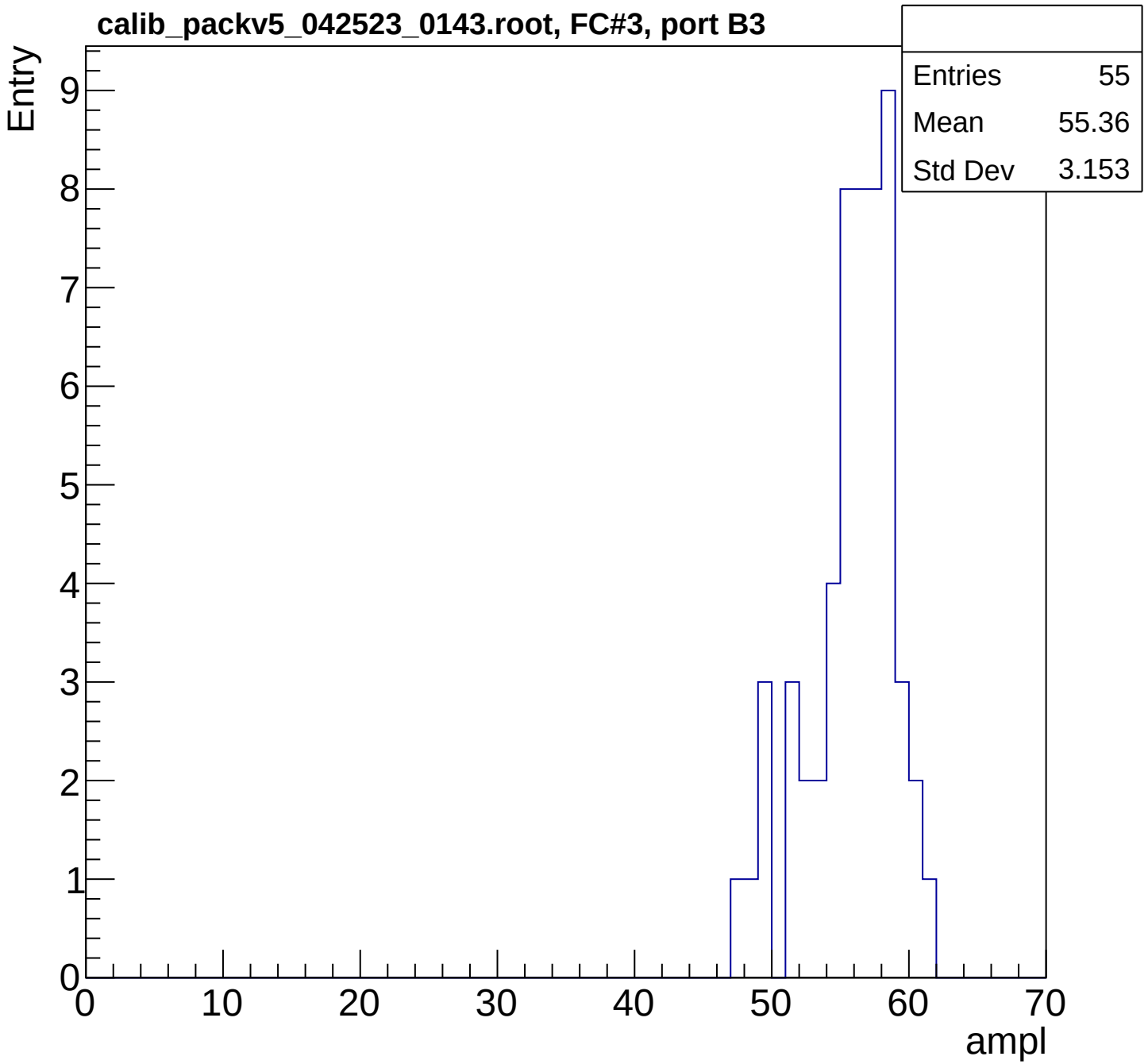
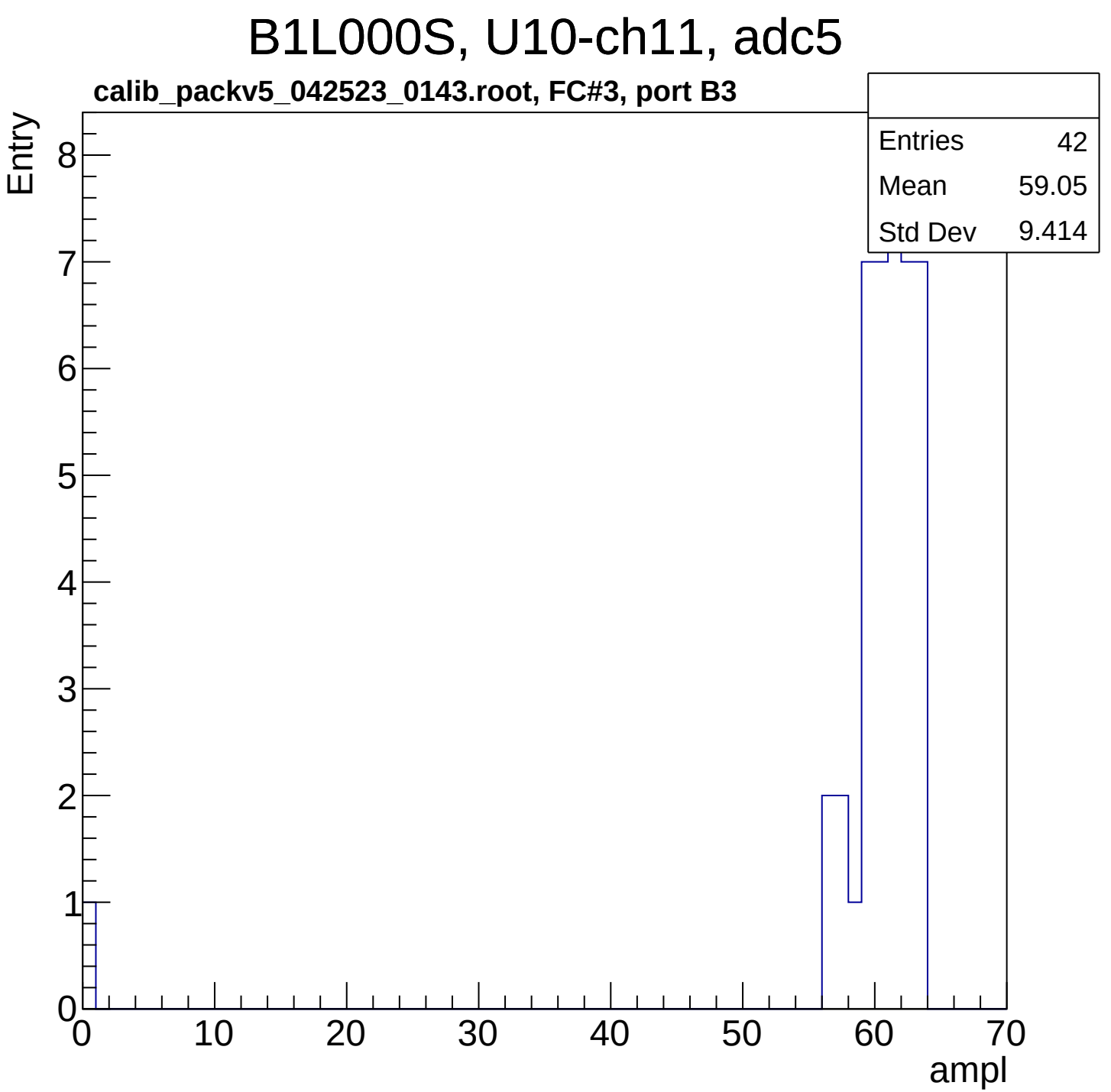

B1L000S, U10-ch11, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 55.36
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch11, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 59.05
Std Dev 9.414
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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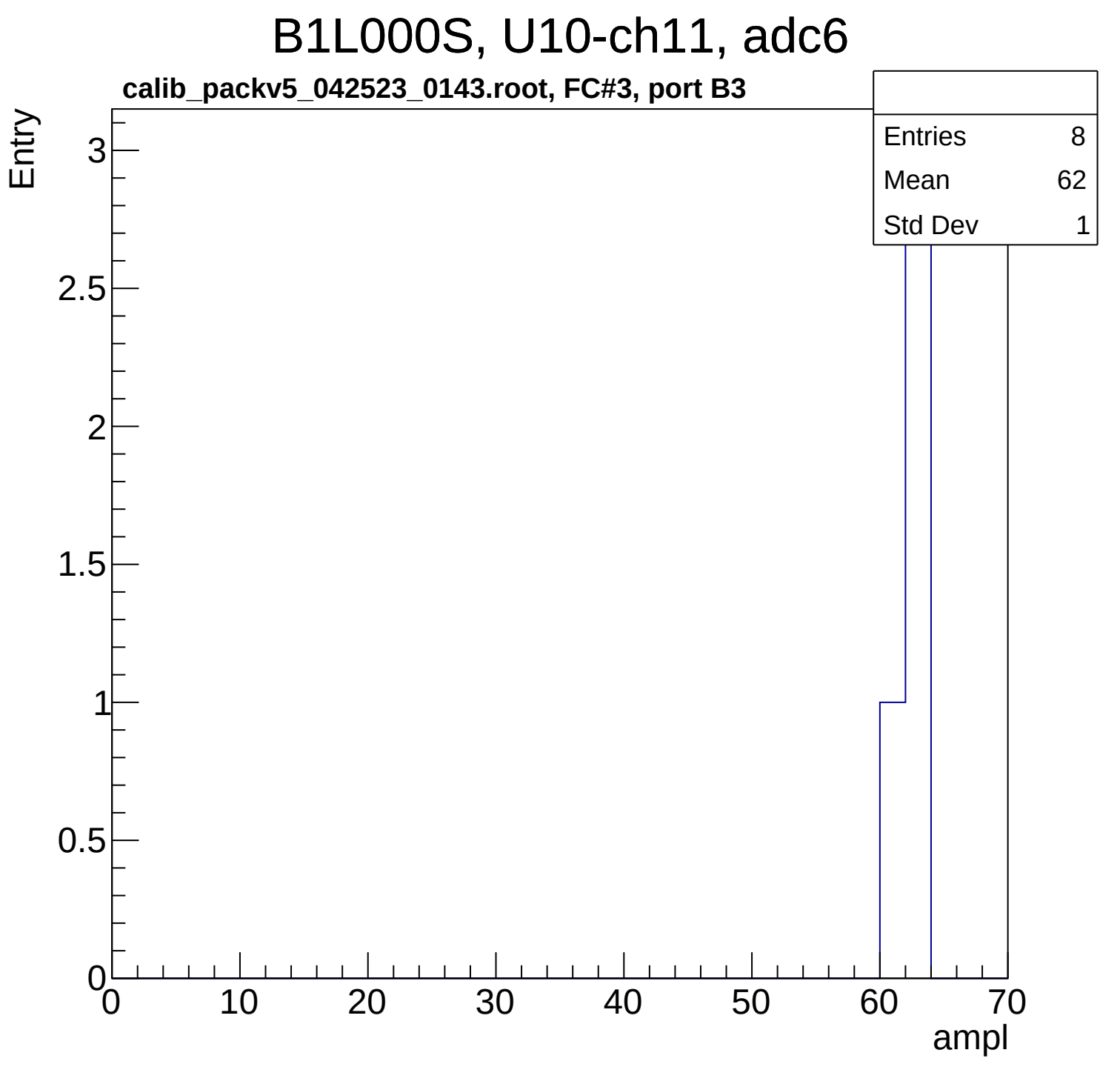
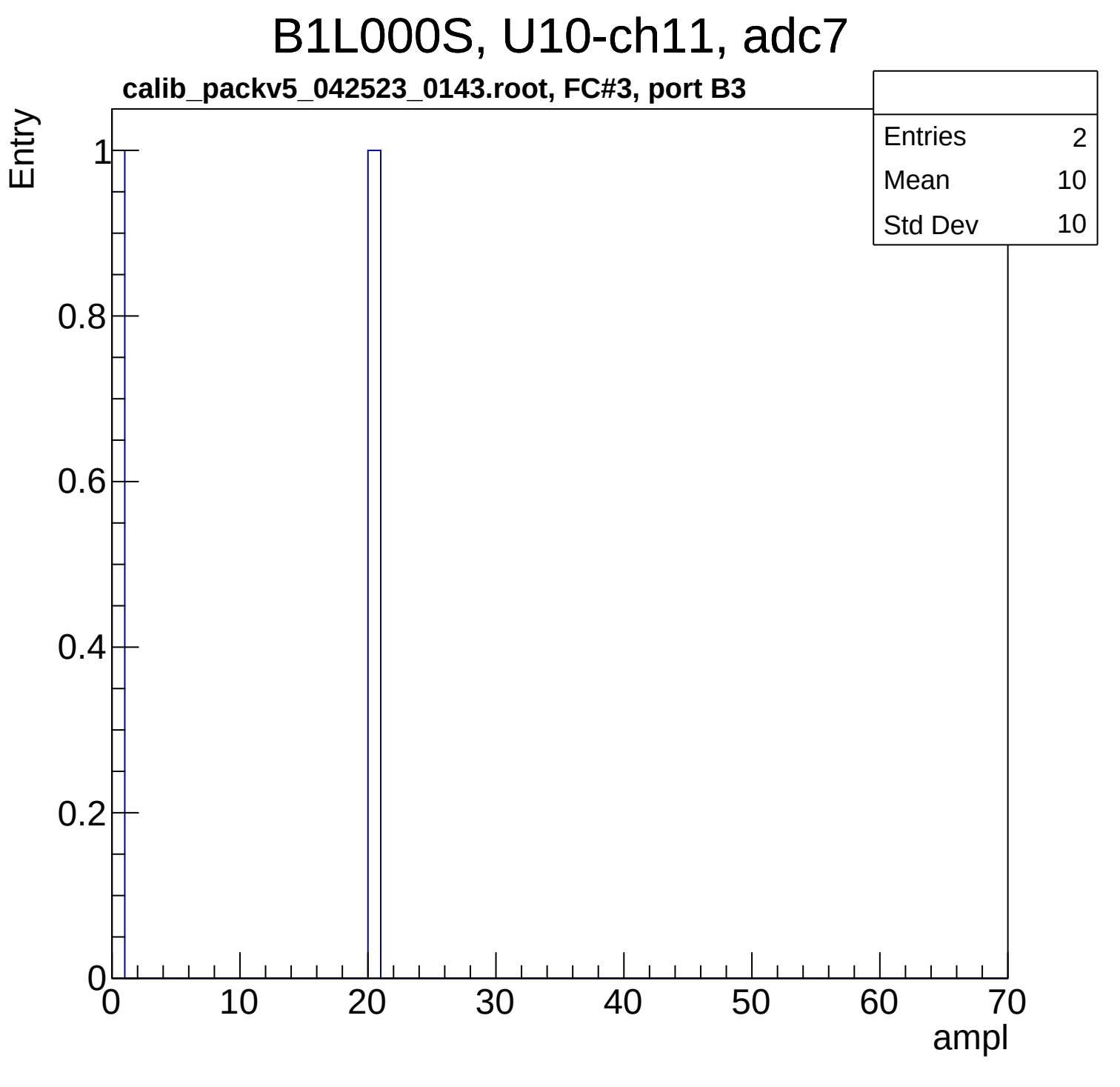
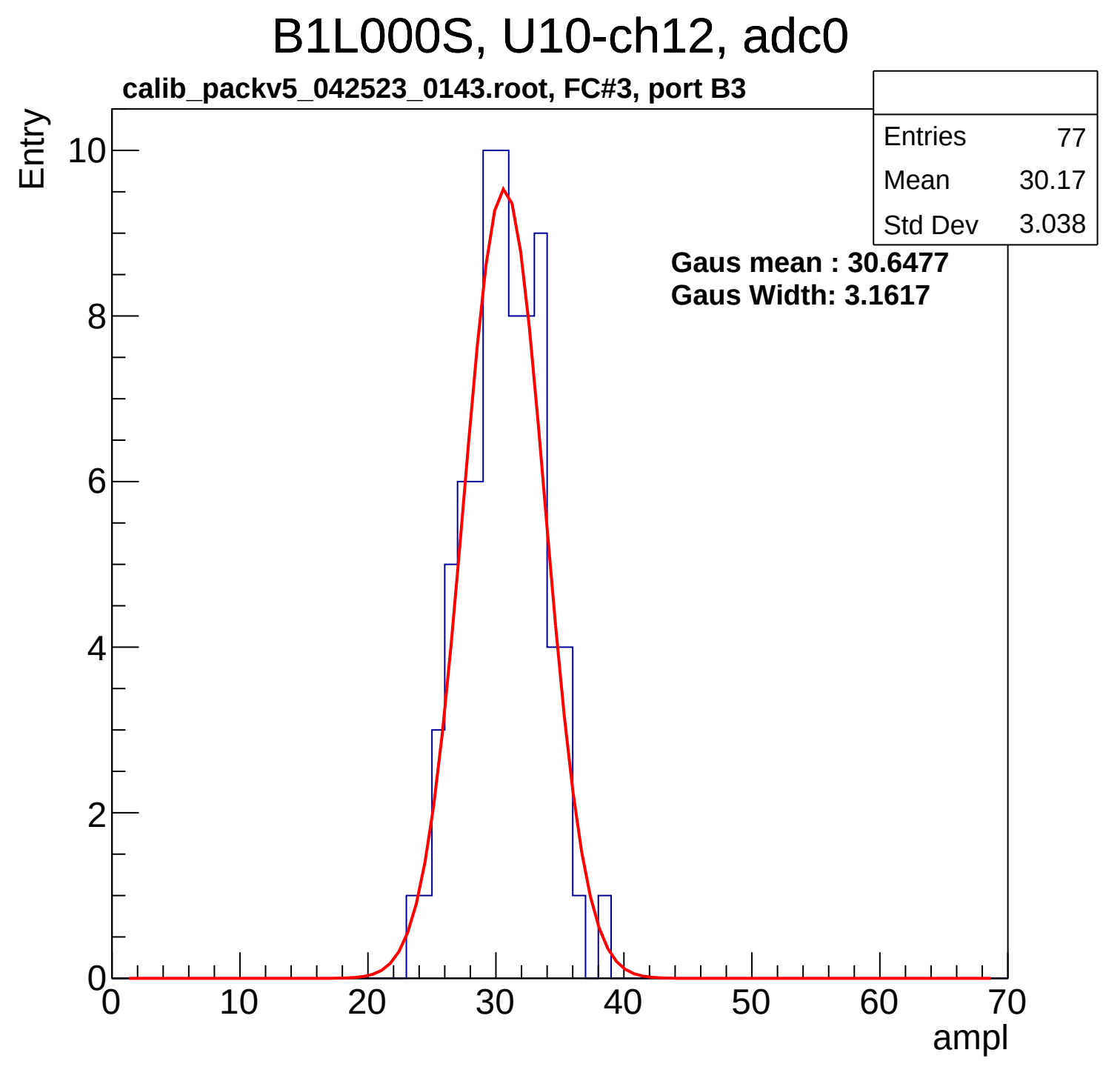
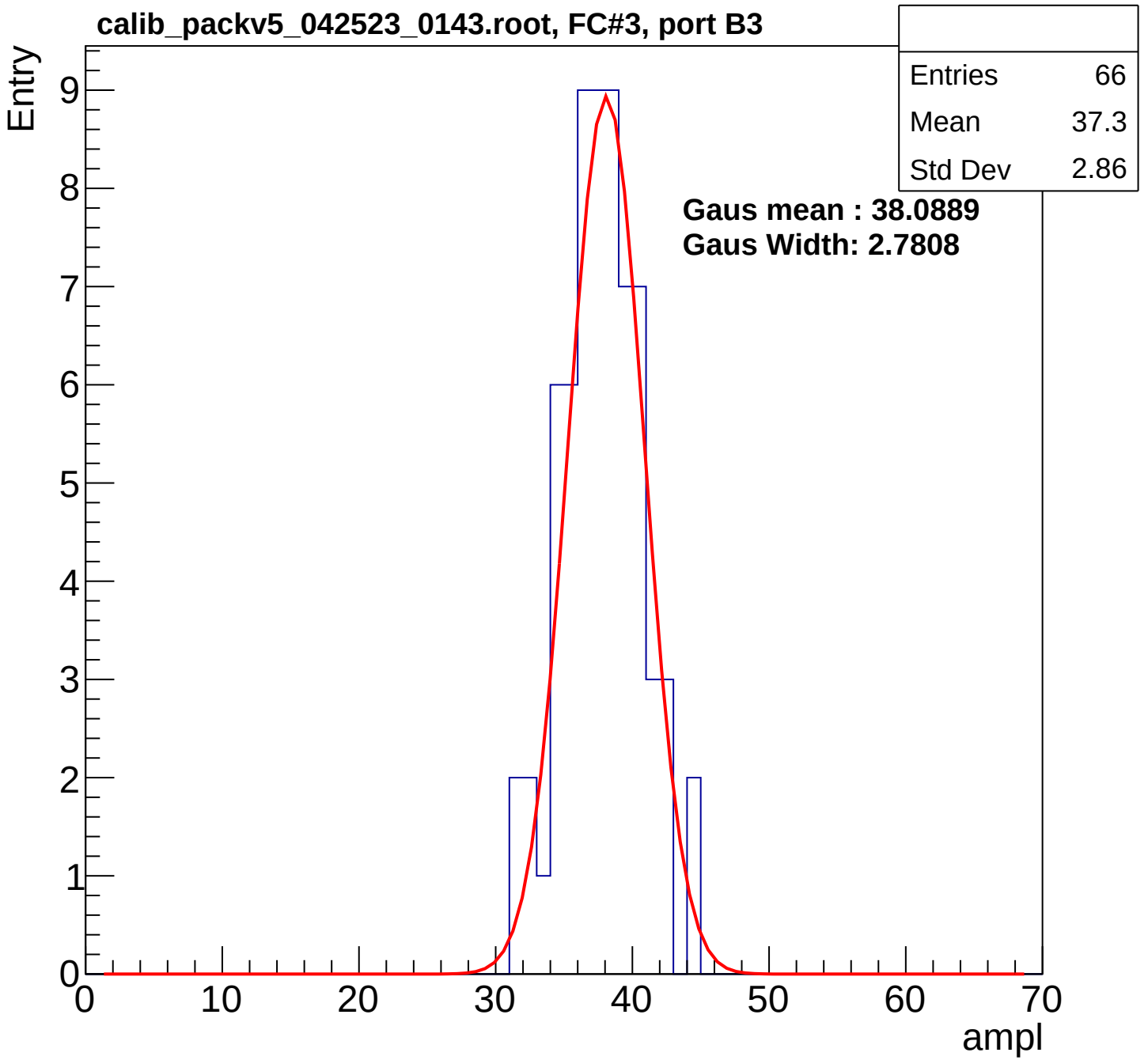
60
70


B1L000S, U10-ch11, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch11, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch12, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 30.17
Std Dev 3.038
Gaus mean : 30.6477
Gaus Width: 3.1617
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch12, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 37.3
Std Dev 2.86
Gaus mean : 38.0889
Gaus Width: 2.7808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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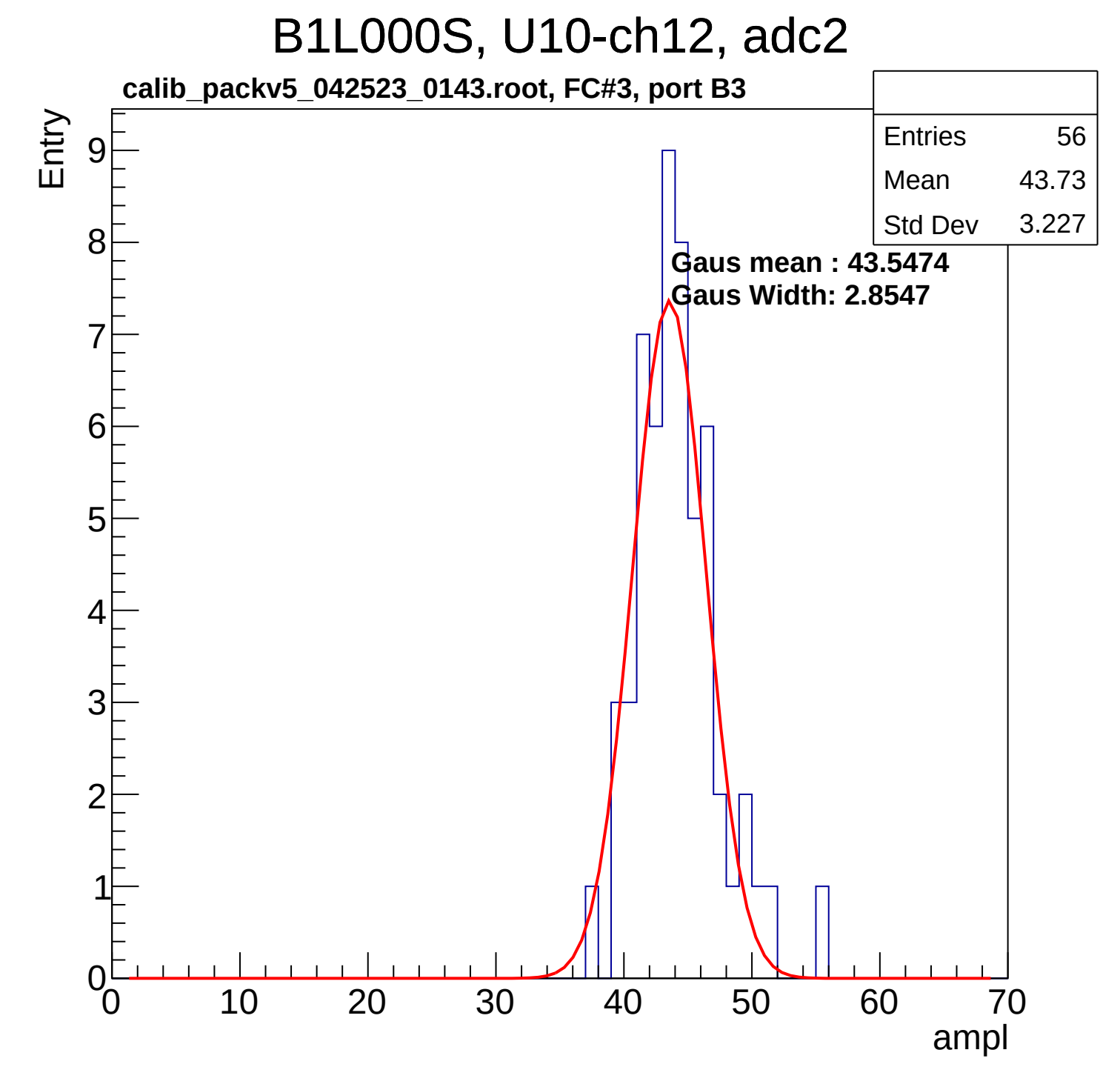
B1L000S, U10-ch12, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 43.73
Std Dev 3.227
Gaus mean : 43.5474
Gaus Width: 2.8547
Entry
ampl
0
1
2
3
4
5
6
7
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0
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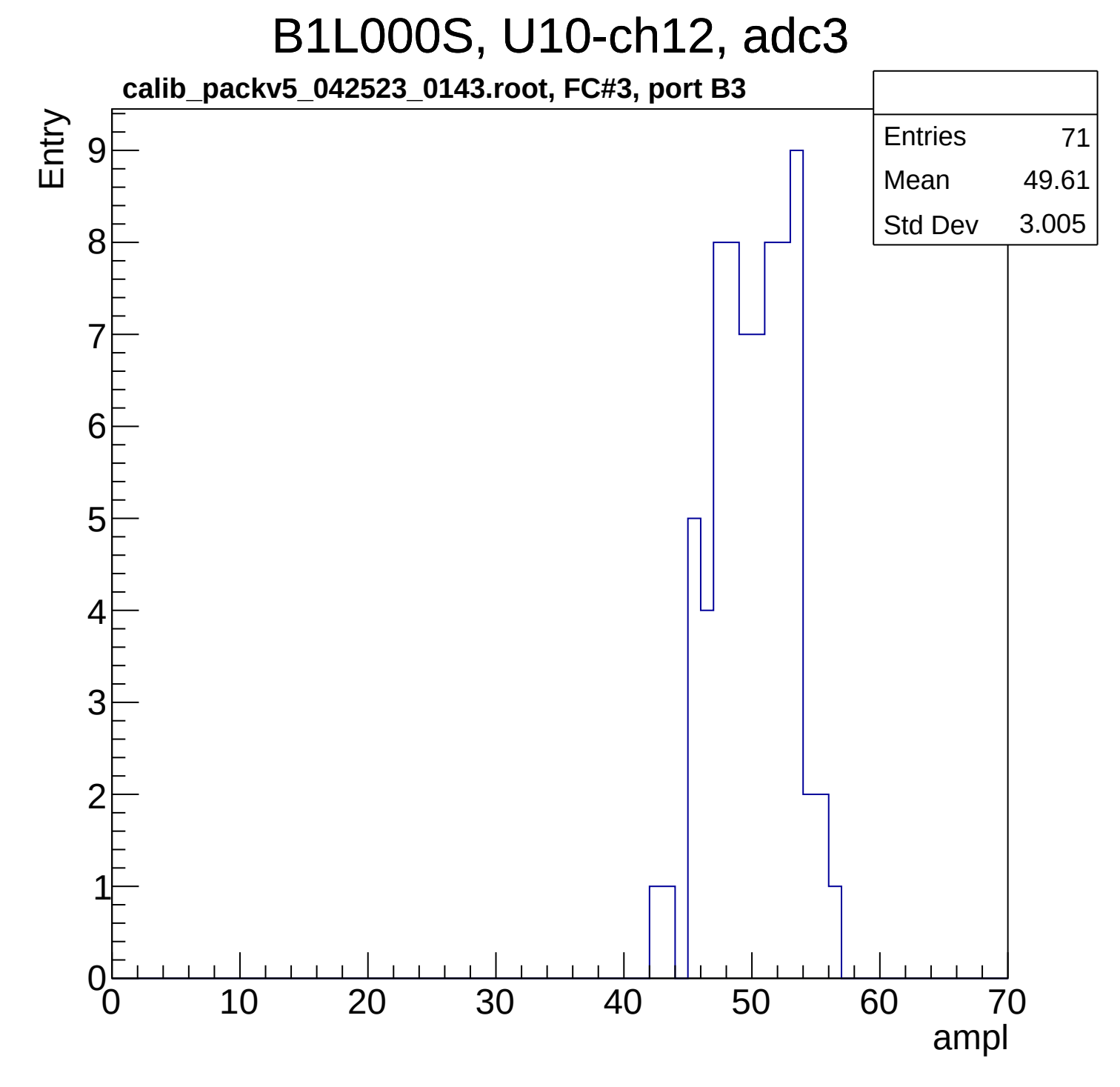
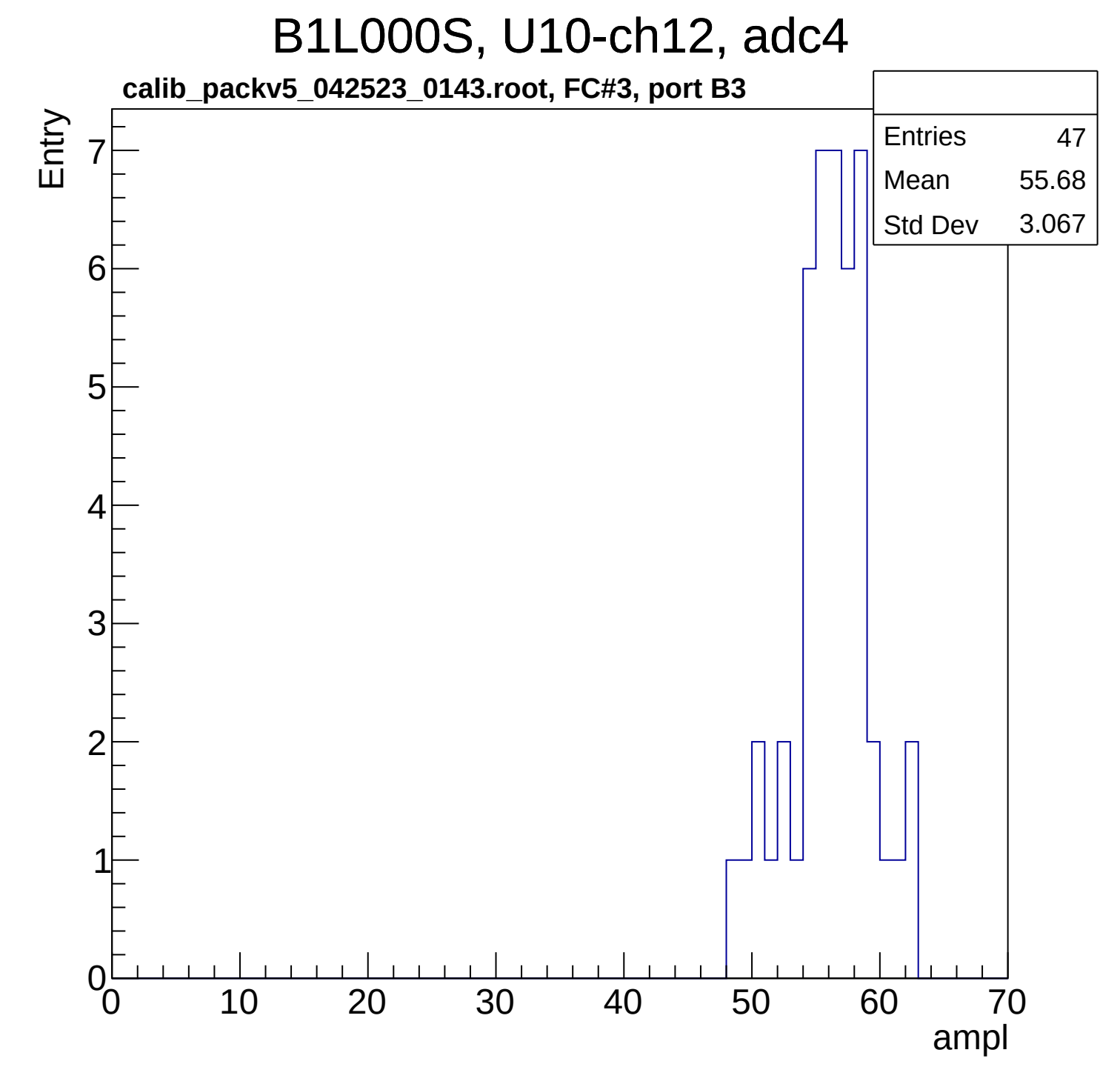

B1L000S, U10-ch12, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 49.61
Std Dev 3.005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch12, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 55.68
Std Dev 3.067
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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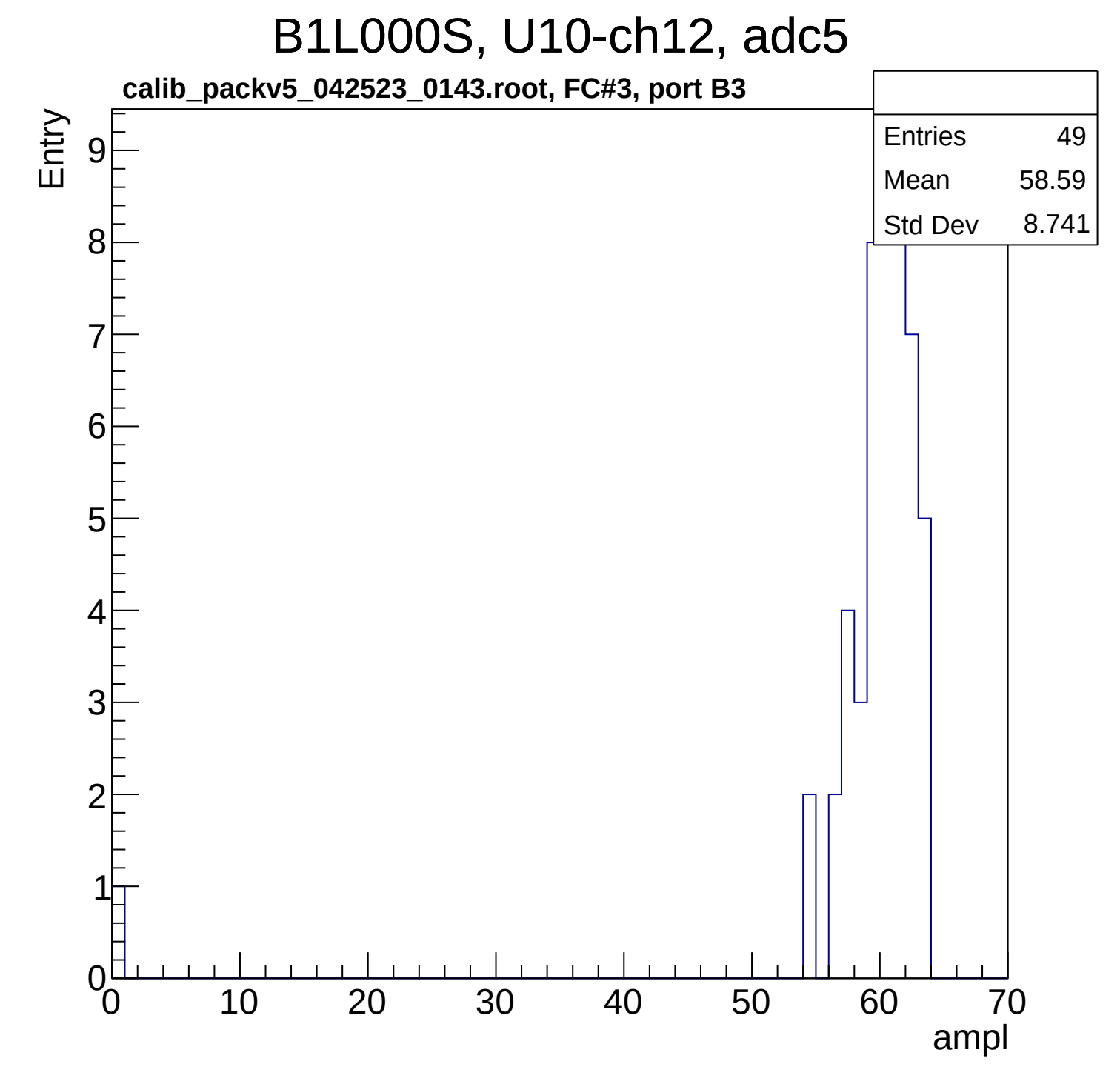
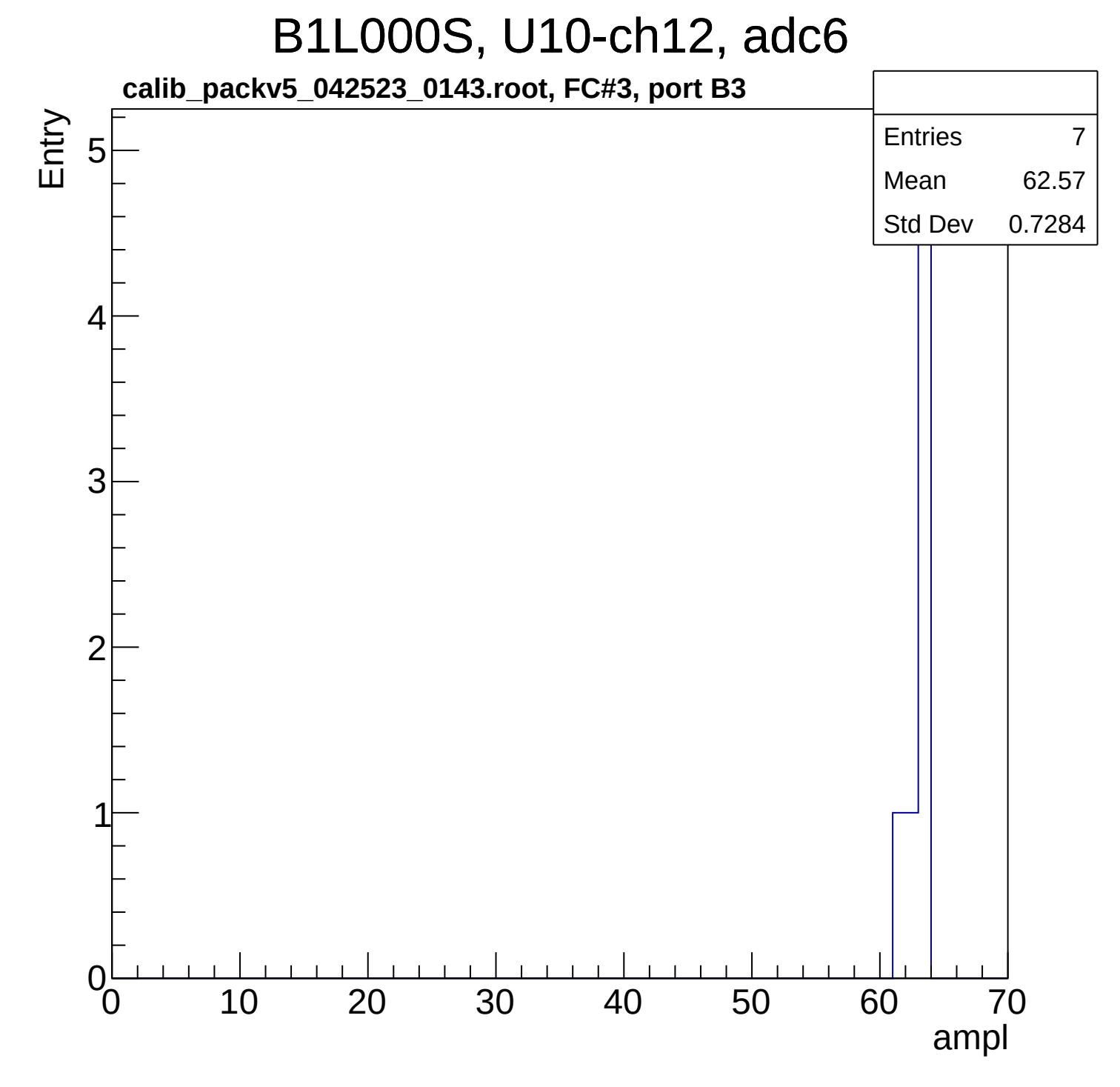
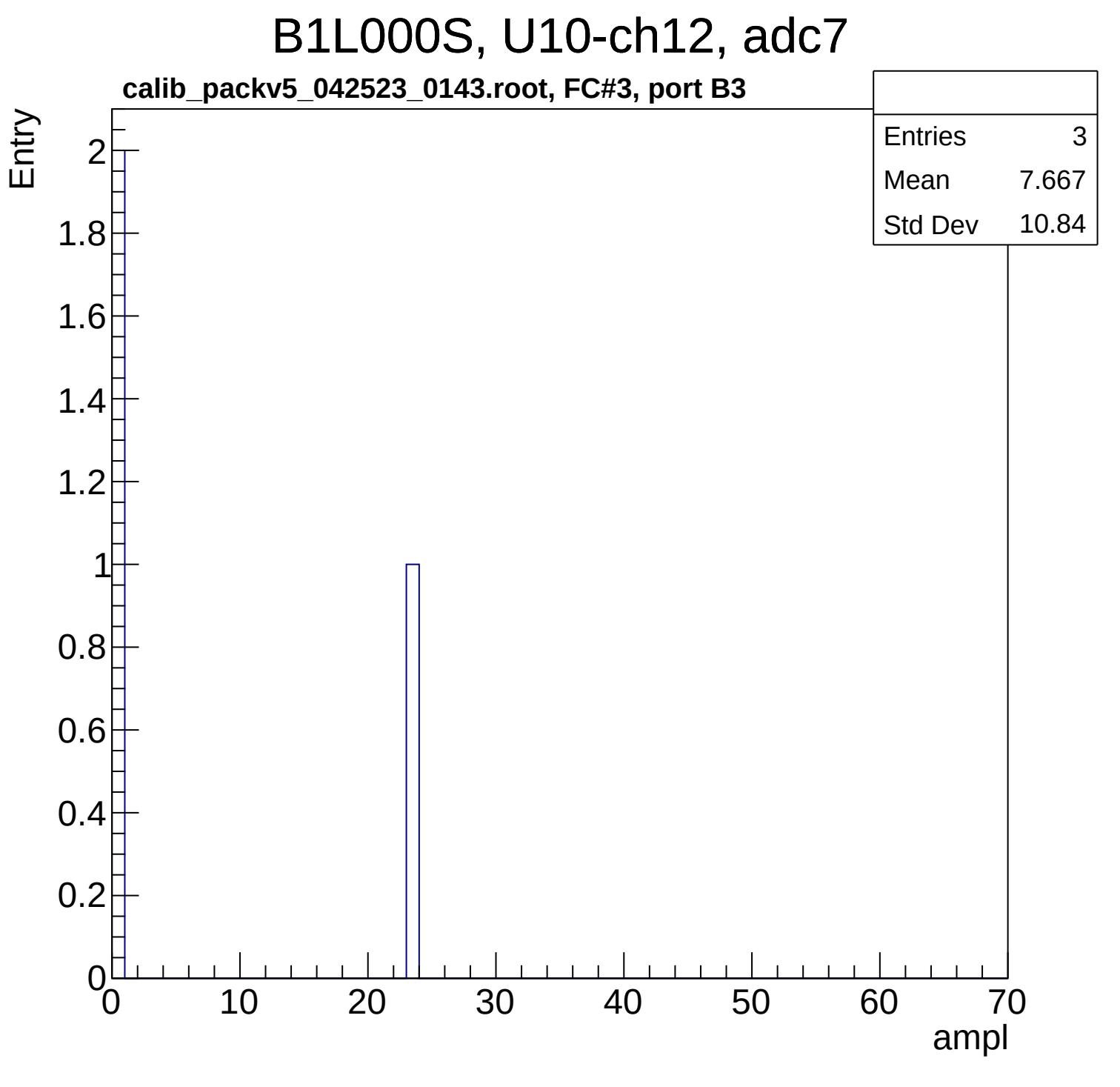
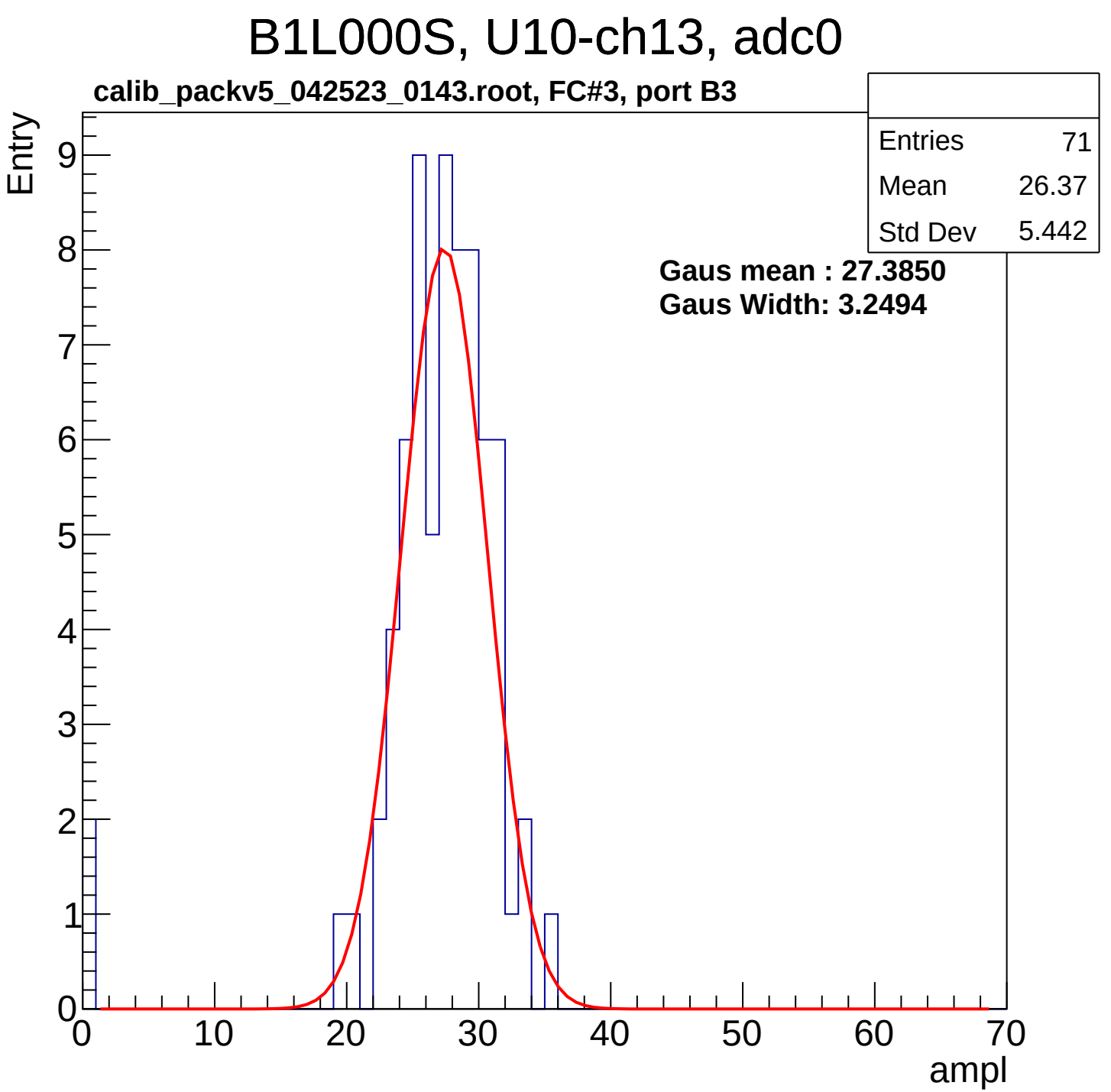
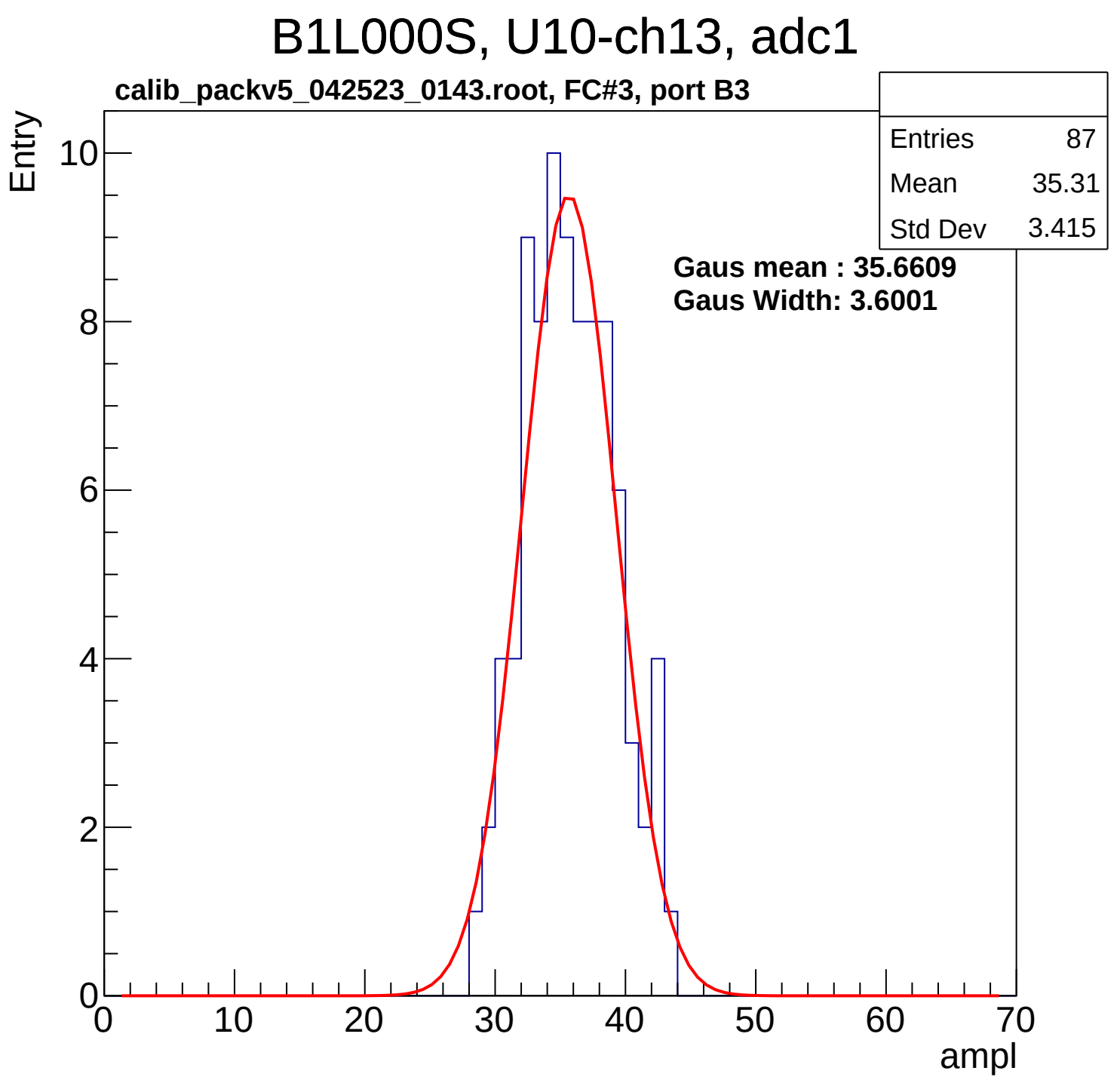
40
50
60
70

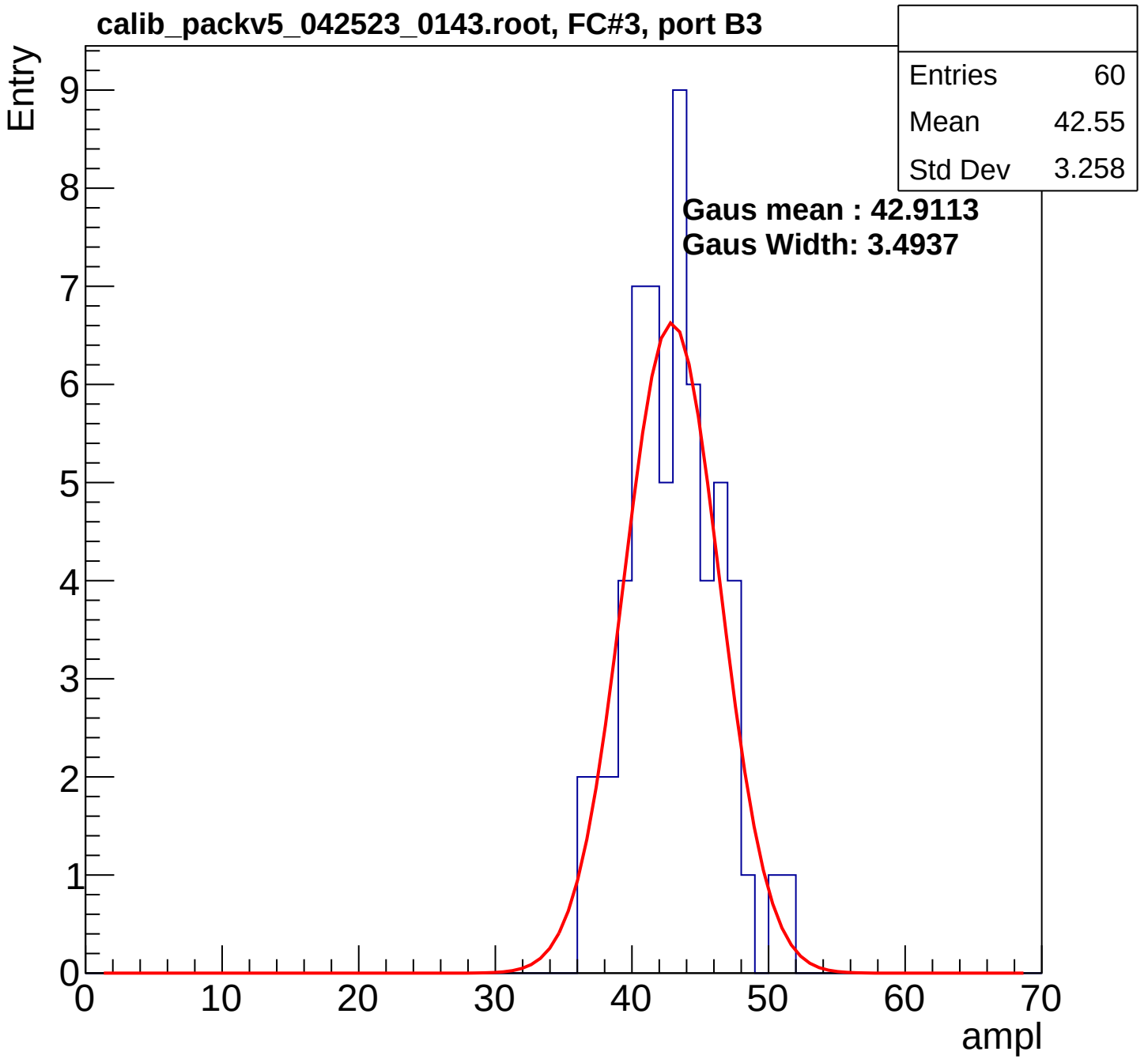

B1L000S, U10-ch12, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.59
Std Dev 8.741
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

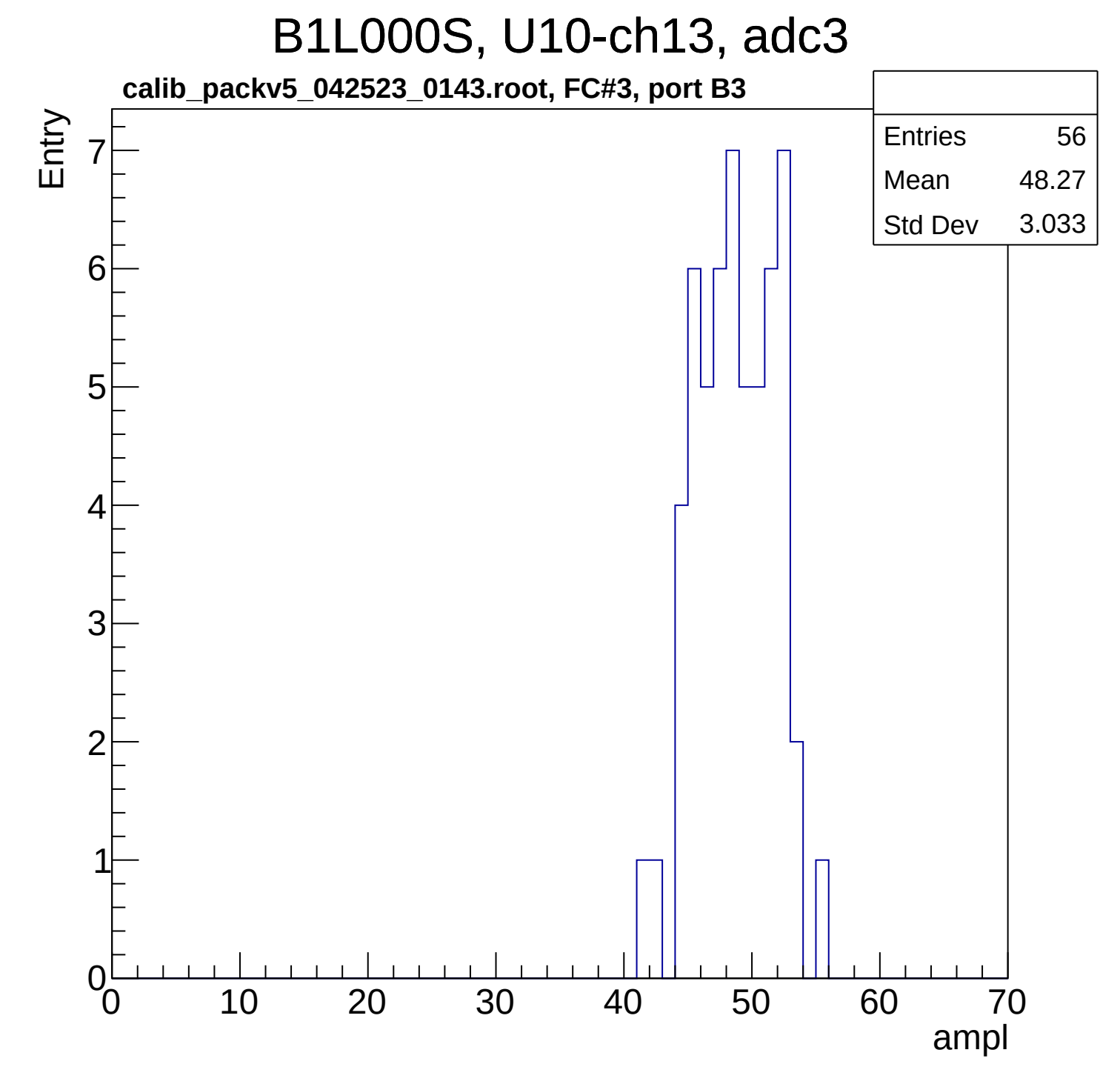

B1L000S, U10-ch12, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.57
Std Dev 0.7284
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

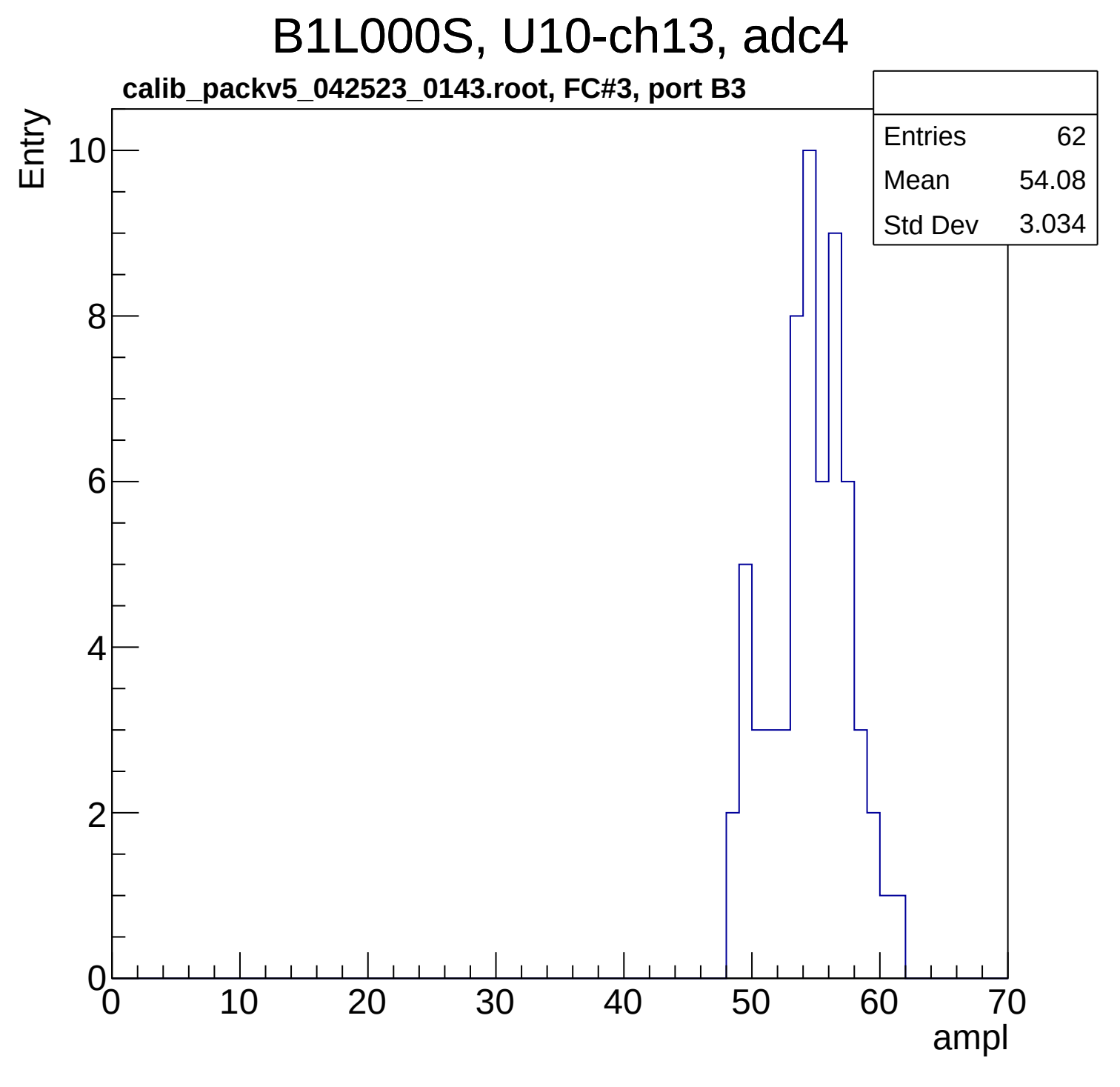

B1L000S, U10-ch12, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

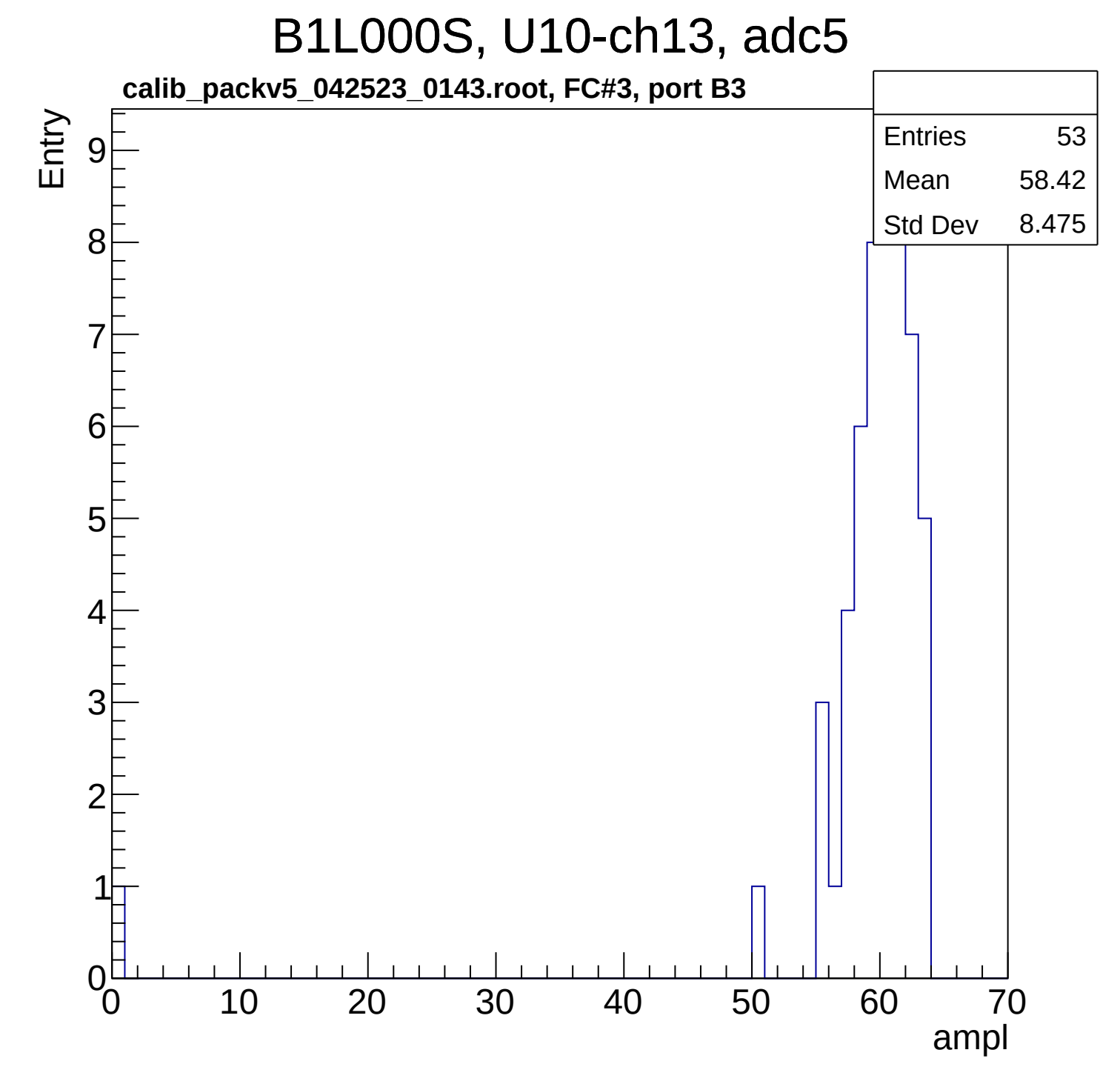

B1L000S, U10-ch13, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 26.37
Std Dev 5.442
Gaus mean : 27.3850
Gaus Width: 3.2494
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

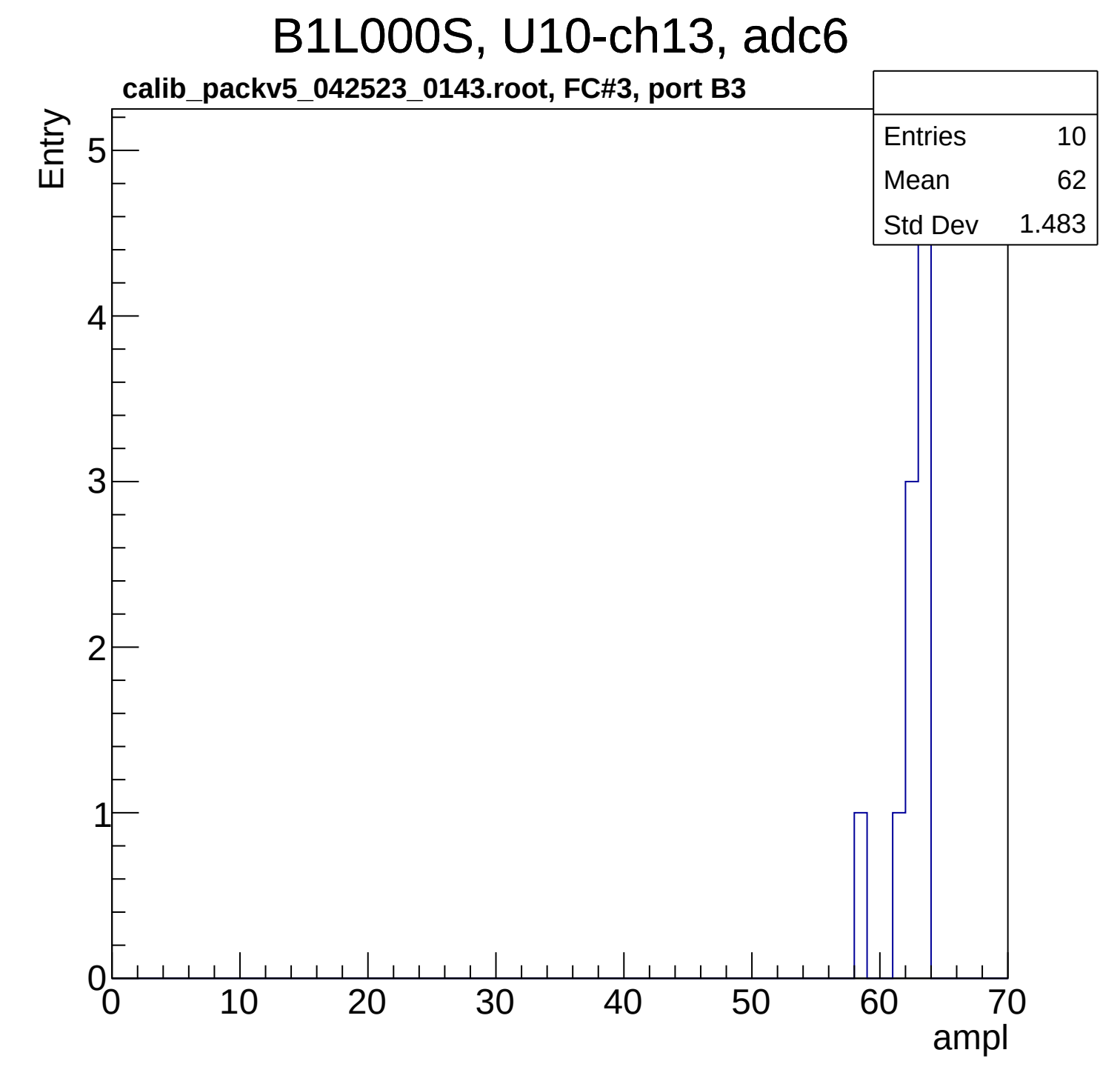

B1L000S, U10-ch13, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 87
Mean 35.31
Std Dev 3.415
Gaus mean : 35.6609
Gaus Width: 3.6001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

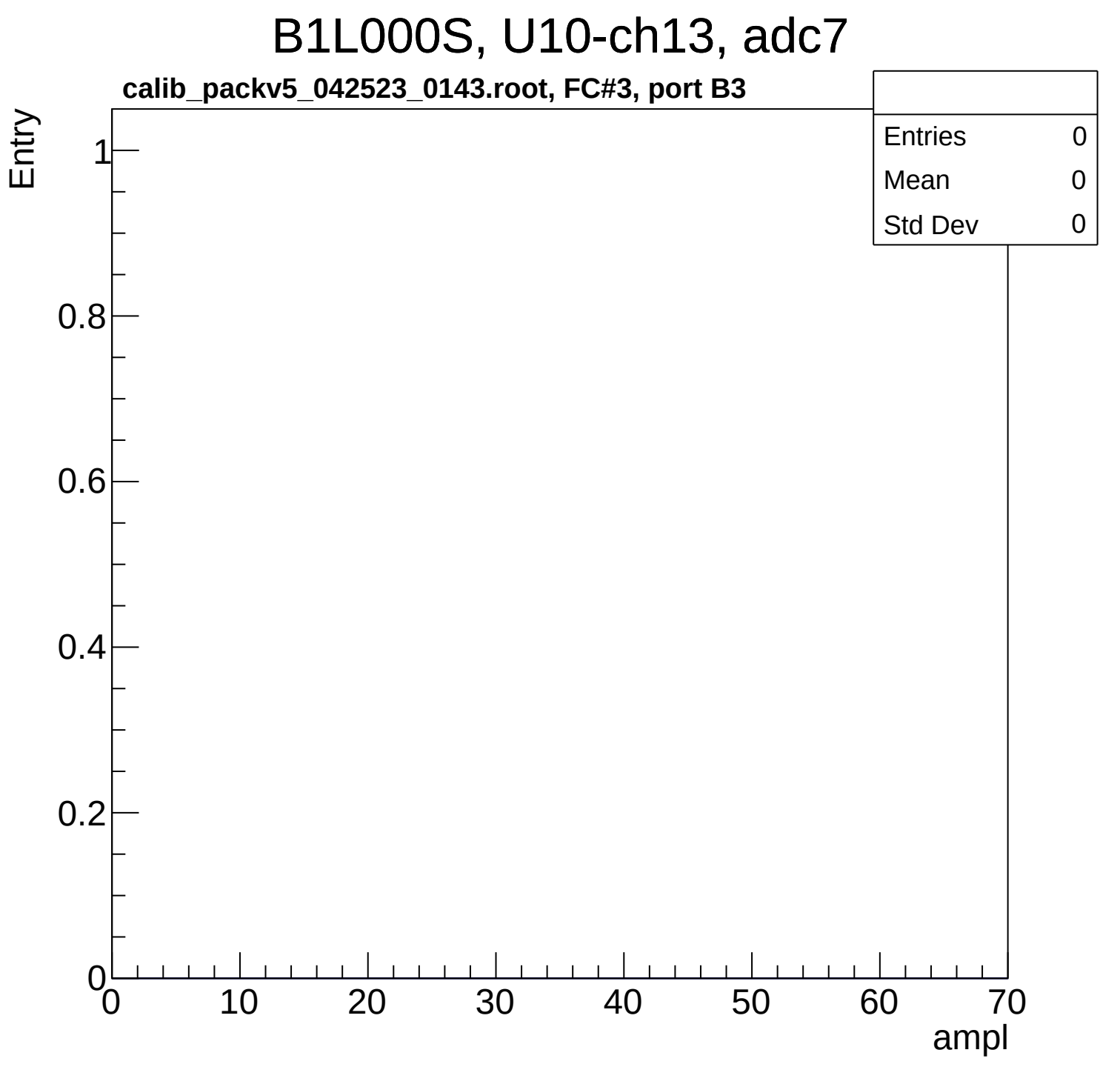

B1L000S, U10-ch13, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 42.55
Std Dev 3.258
Gaus mean : 42.9113
Gaus Width: 3.4937
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

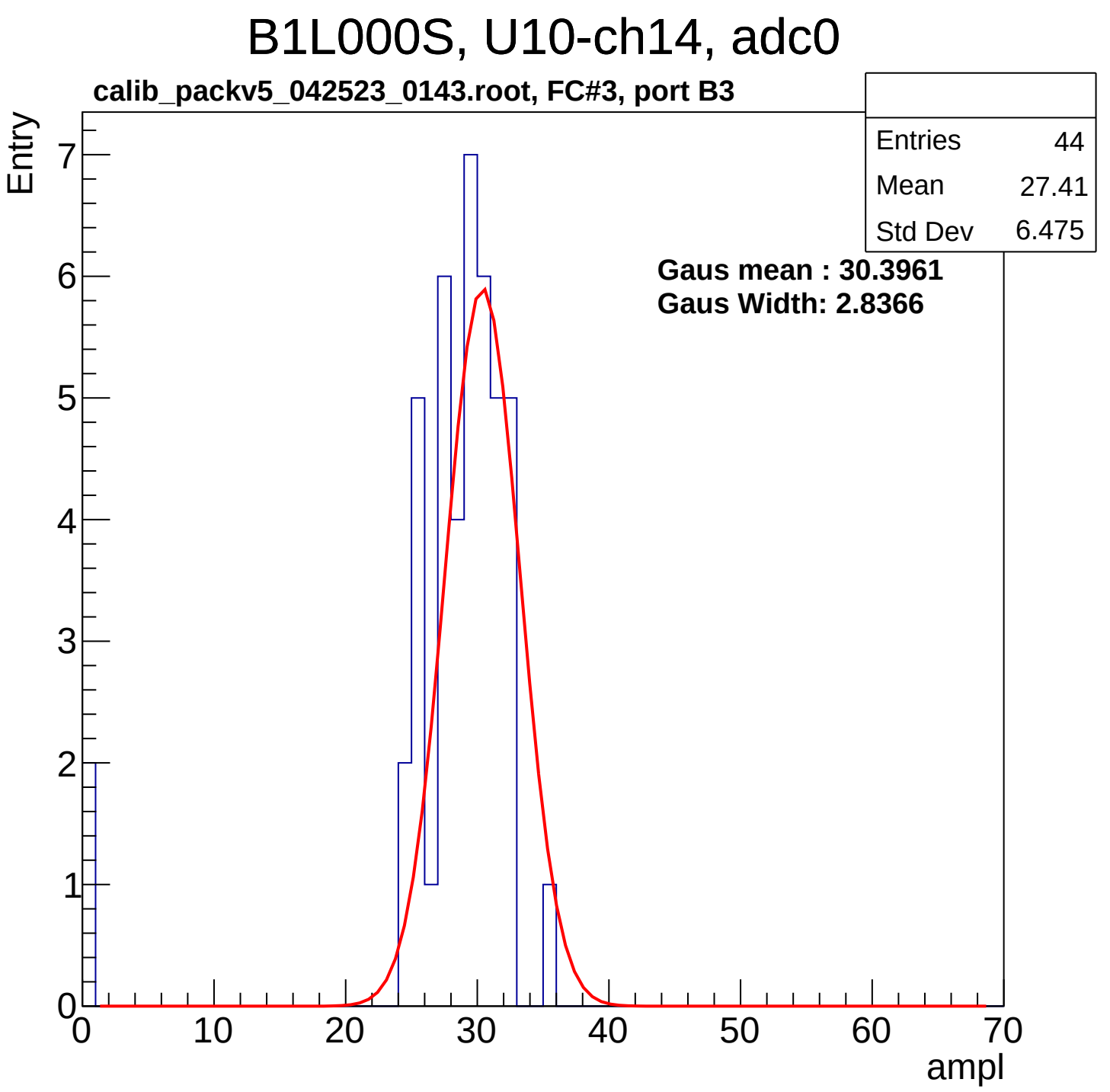

B1L000S, U10-ch13, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 48.27
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

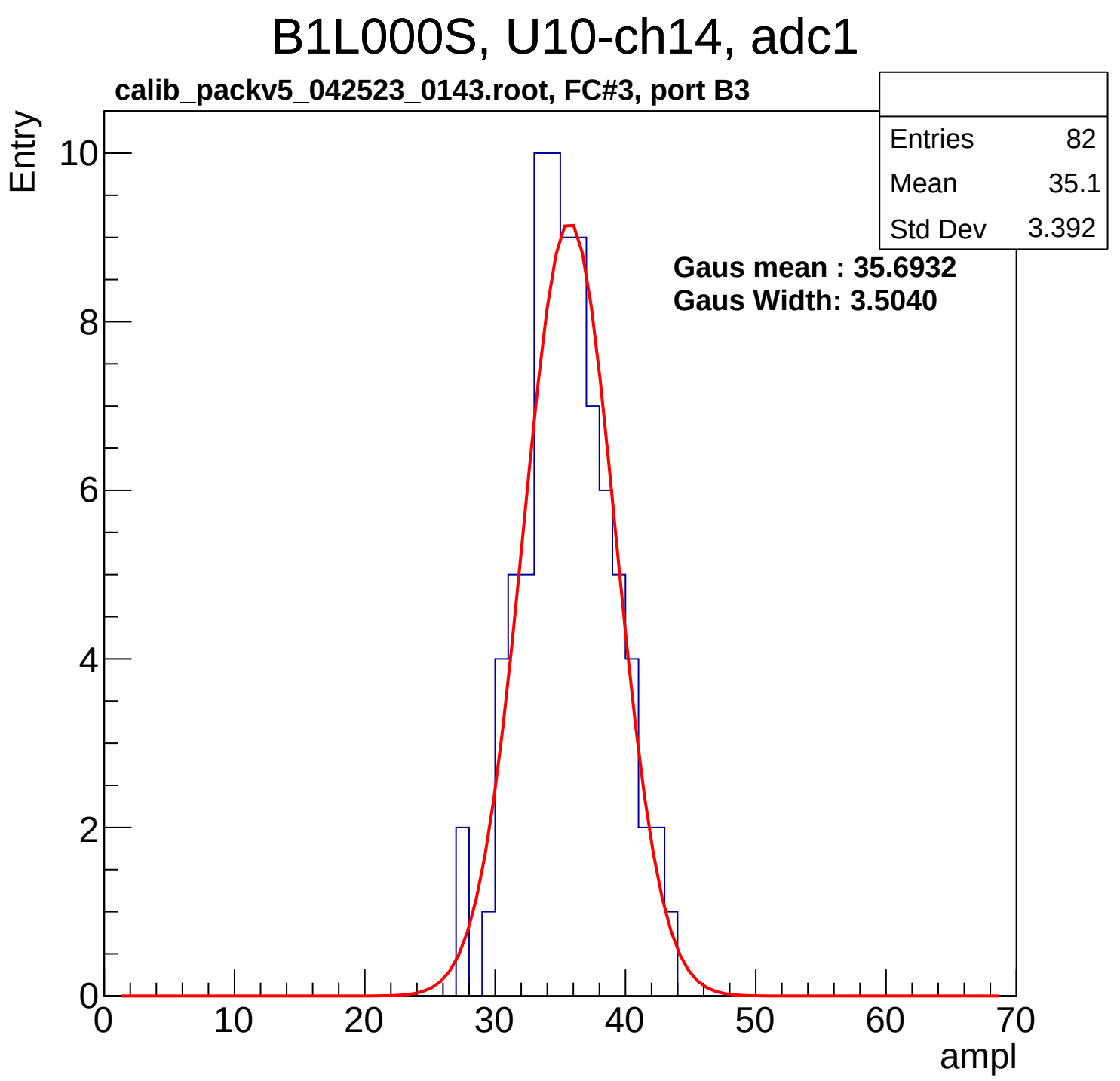

B1L000S, U10-ch13, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 54.08
Std Dev 3.034
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

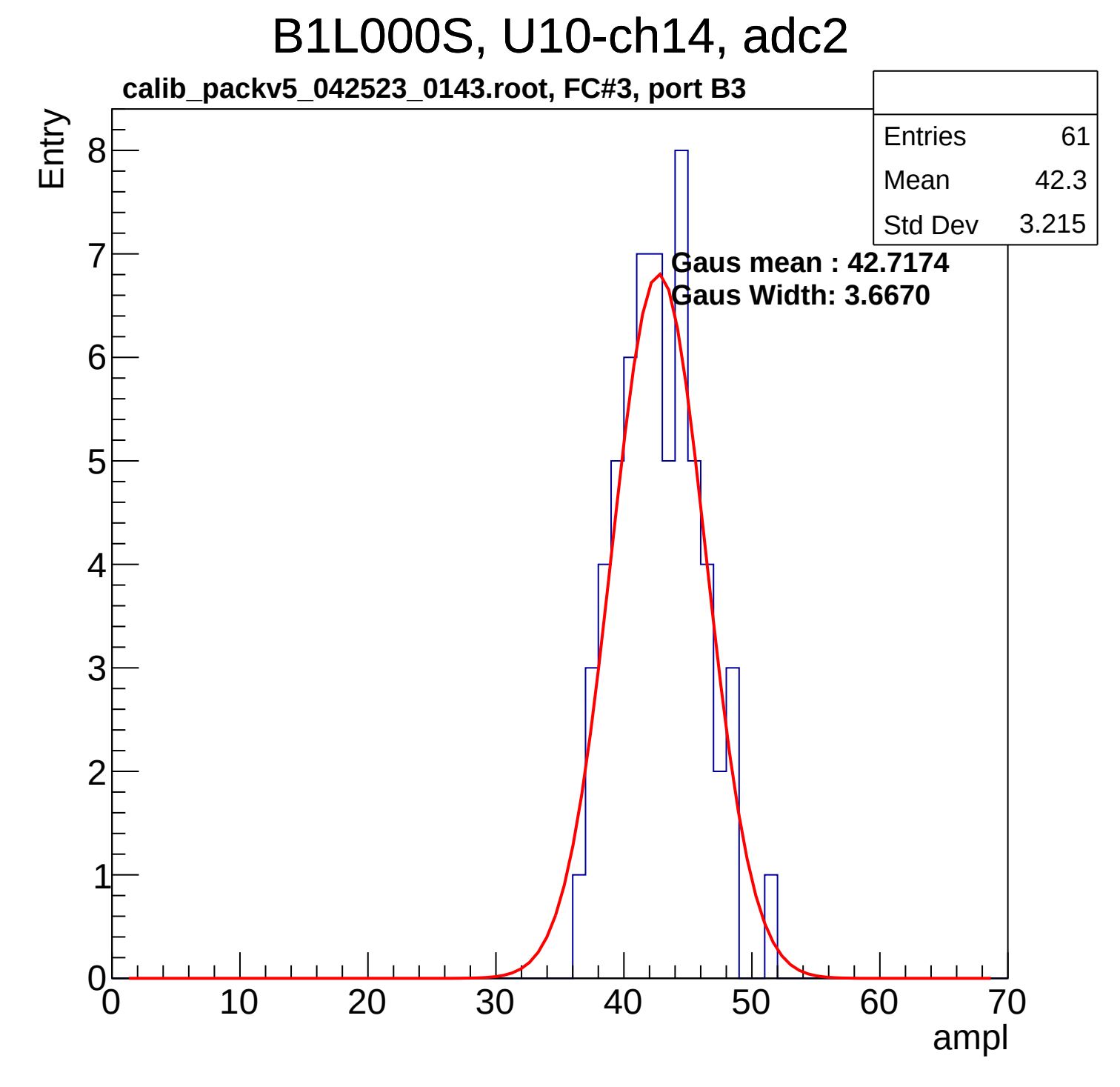

B1L000S, U10-ch13, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.42
Std Dev 8.475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

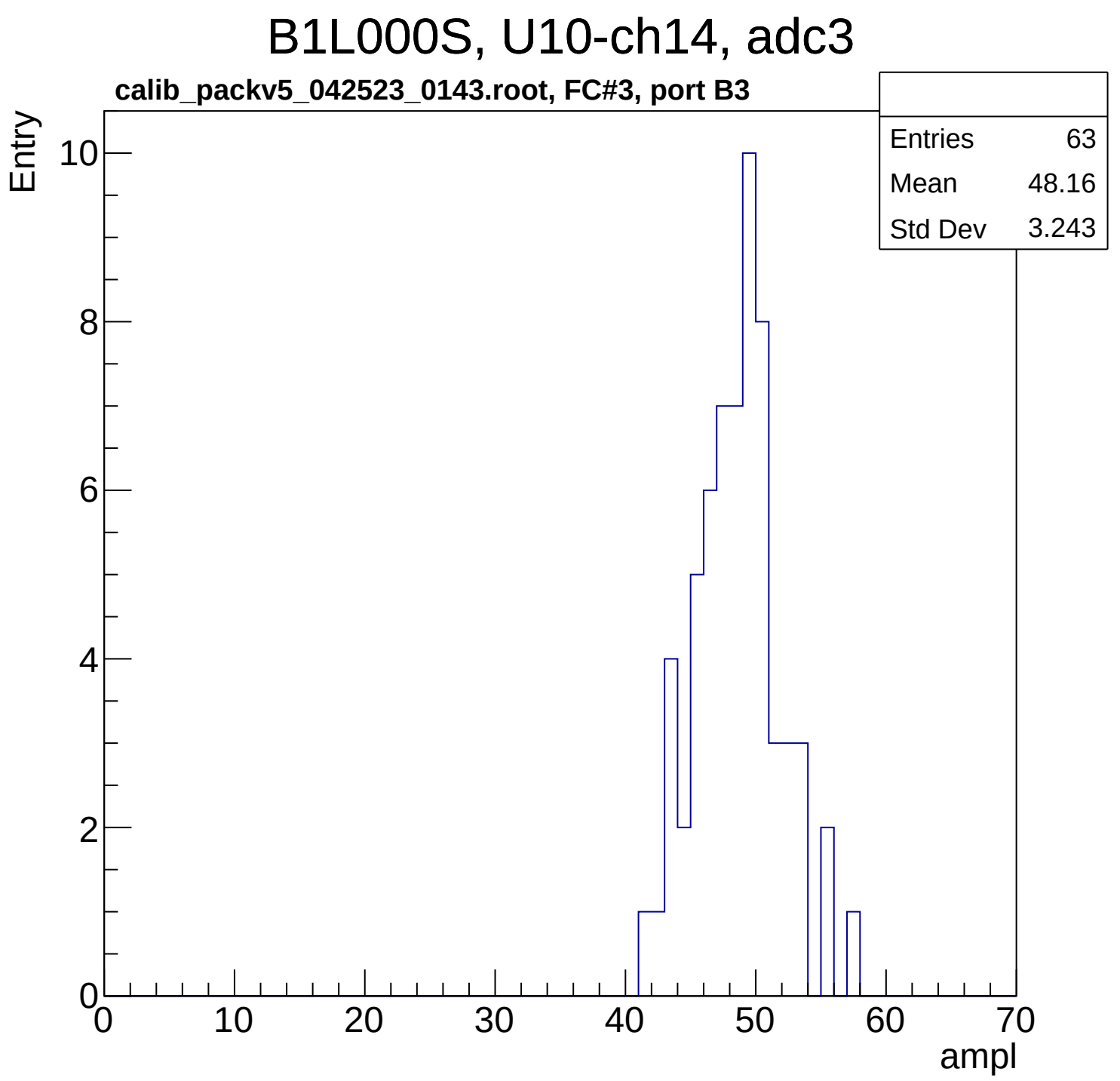

B1L000S, U10-ch13, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62
Std Dev 1.483
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

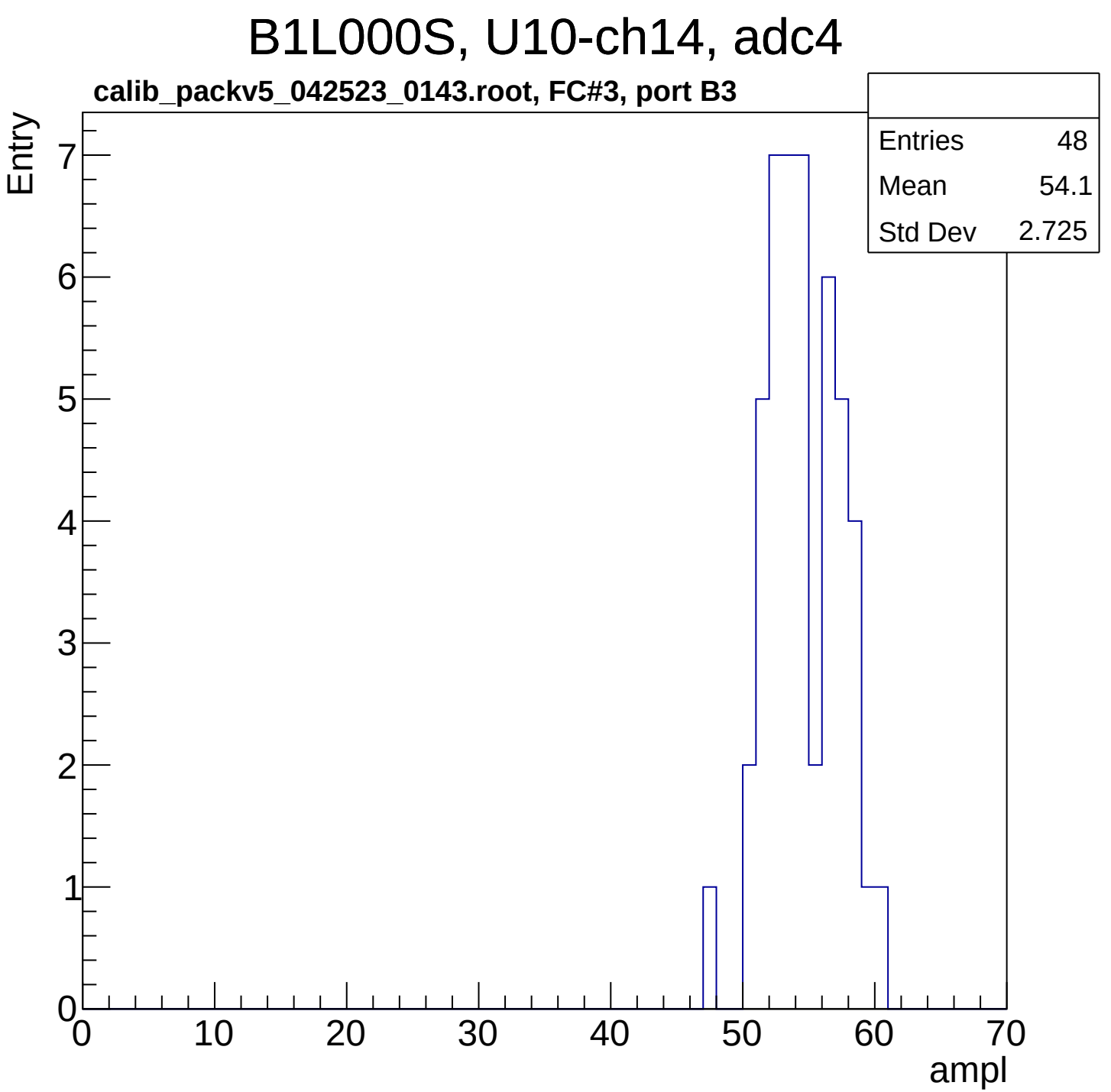

B1L000S, U10-ch13, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch14, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 27.41
Std Dev 6.475
Gaus mean : 30.3961
Gaus Width: 2.8366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch14, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 35.1
Std Dev 3.392
Gaus mean : 35.6932
Gaus Width: 3.5040
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch14, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 42.3
Std Dev 3.215
Gaus mean : 42.7174
Gaus Width: 3.6670
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch14, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 48.16
Std Dev 3.243
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch14, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 54.1
Std Dev 2.725
Entry
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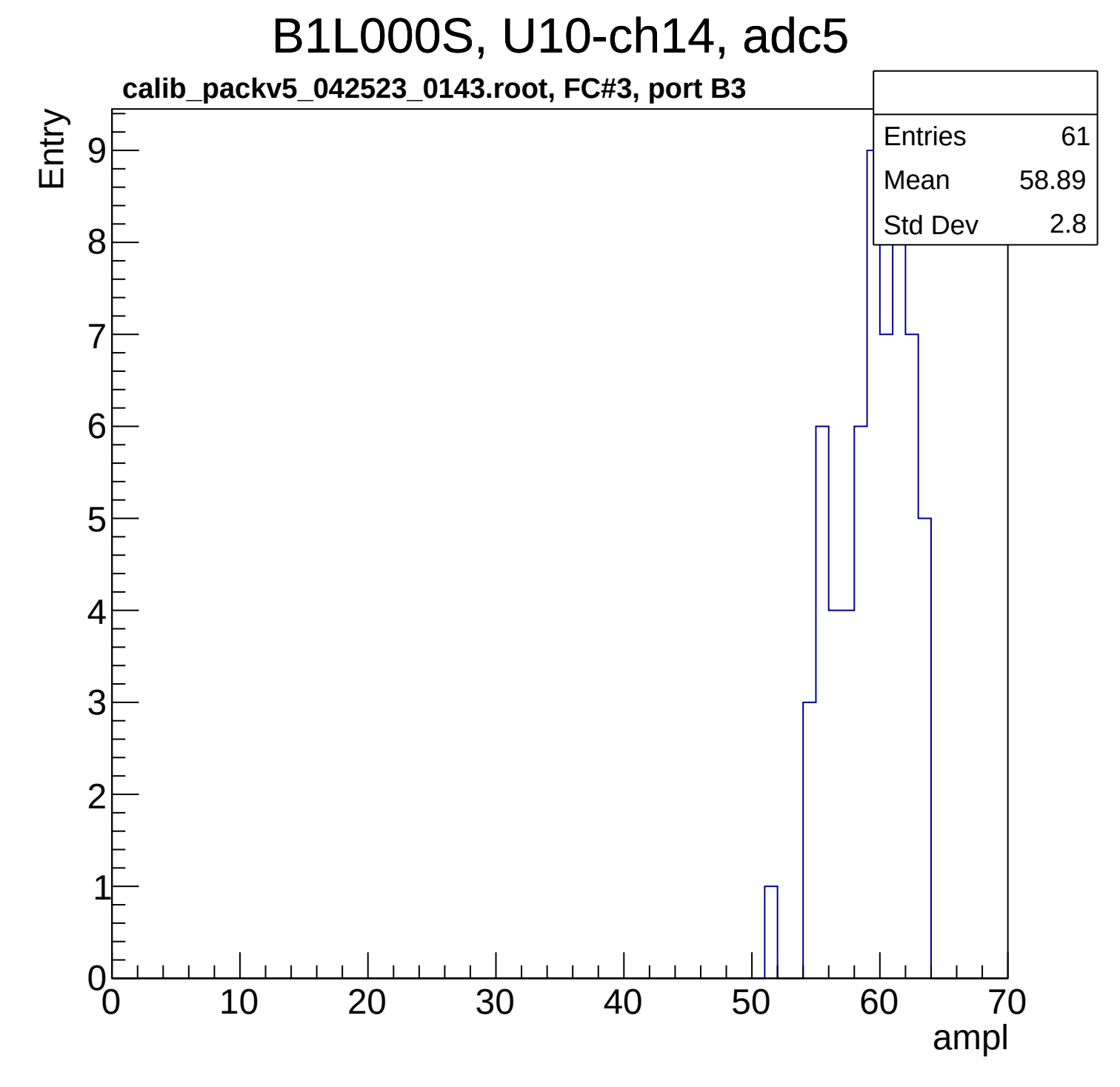
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch14, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.89
Std Dev 2.8
Entry
ampl
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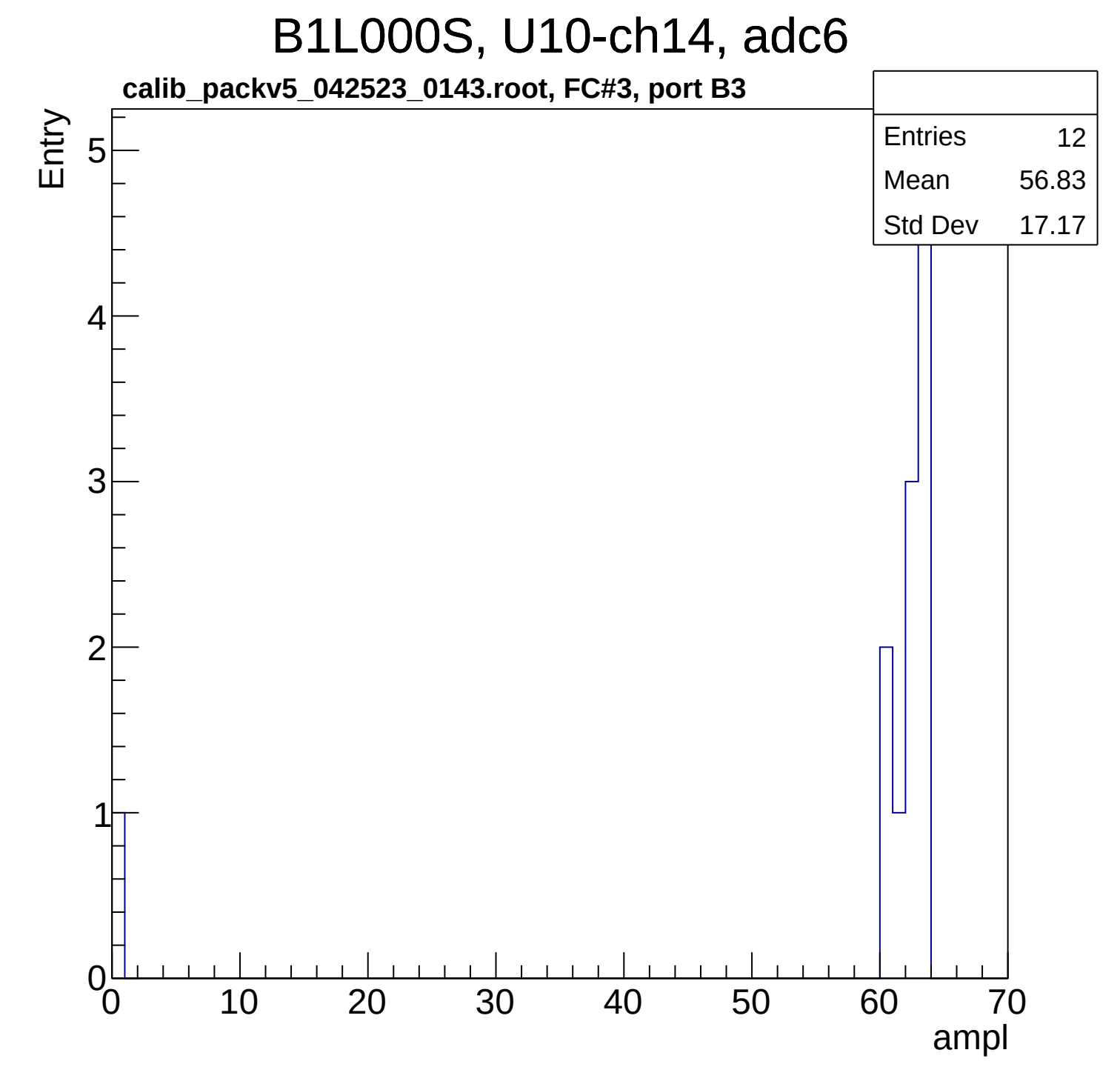
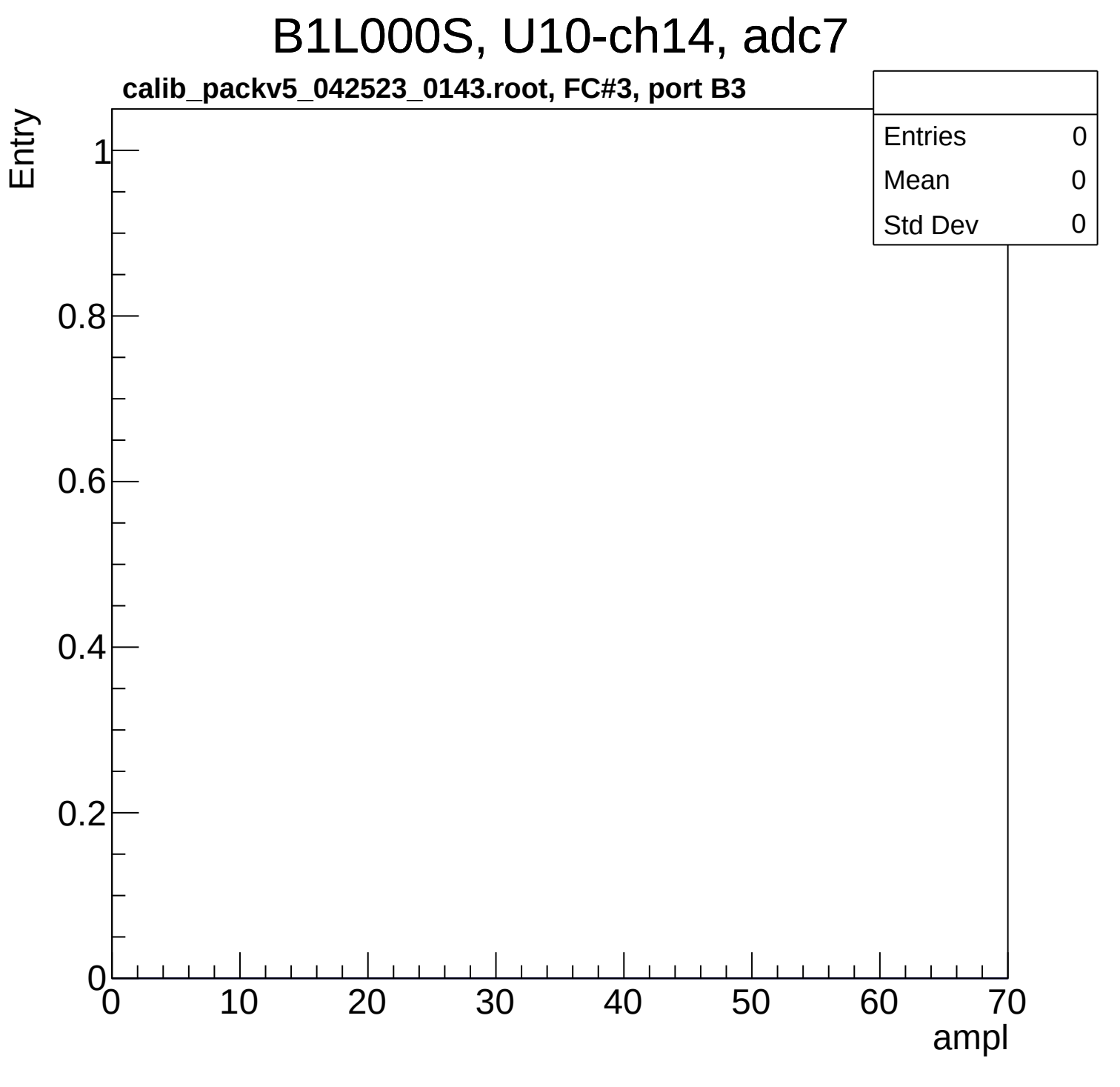
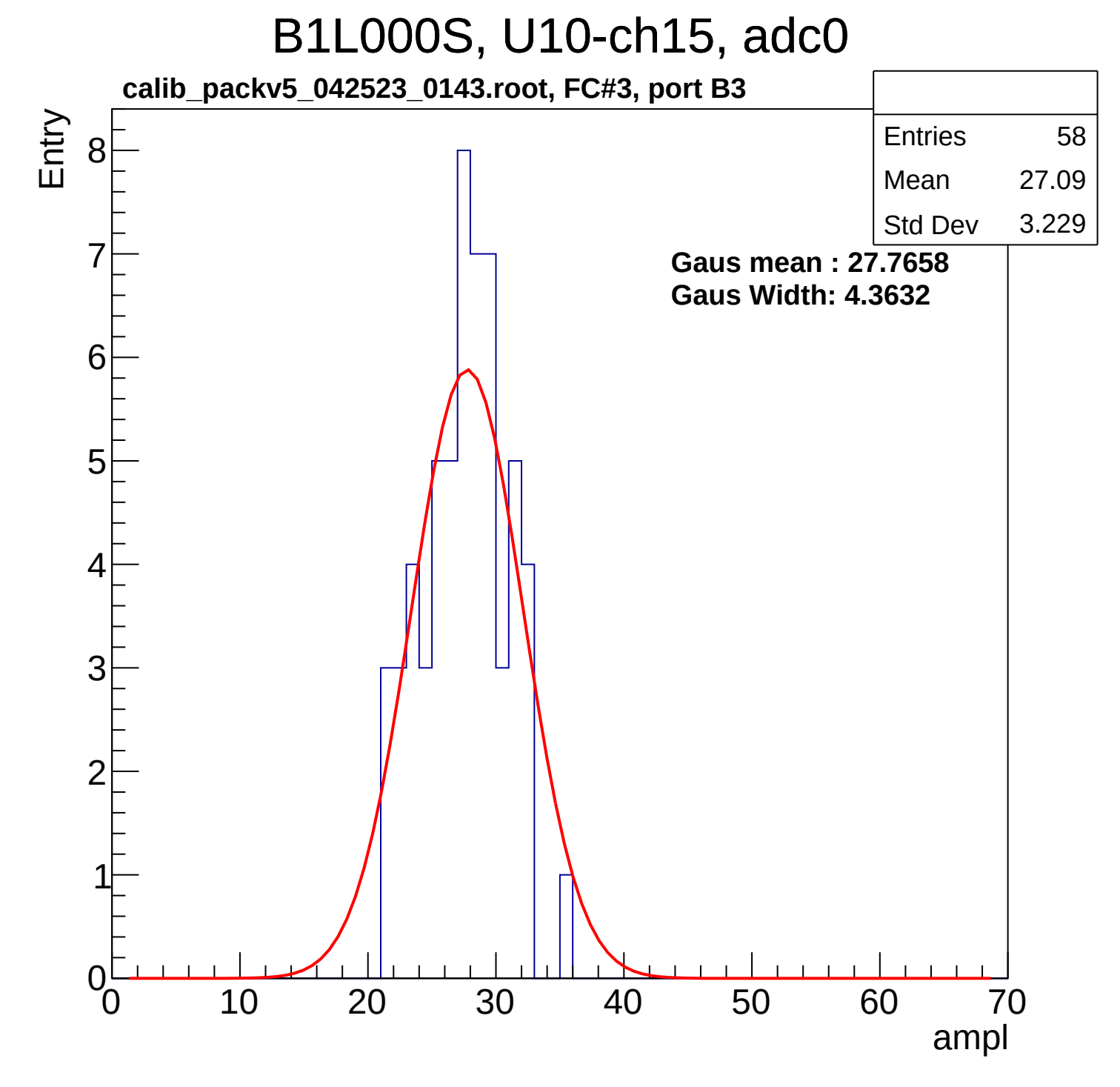
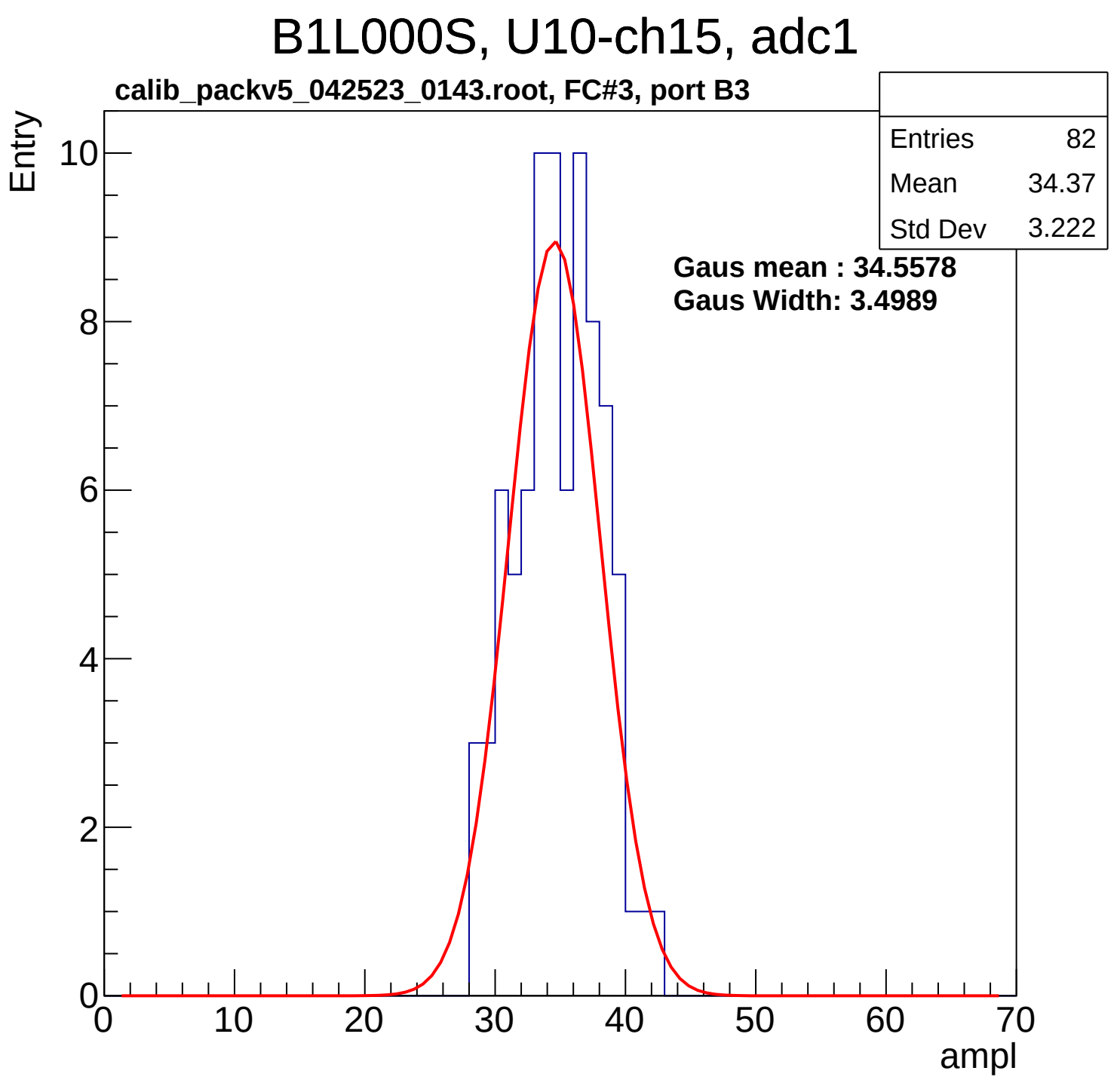
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch14, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.83
Std Dev 17.17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch14, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch15, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 27.09
Std Dev 3.229
Gaus mean : 27.7658
Gaus Width: 4.3632
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch15, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 34.37
Std Dev 3.222
Gaus mean : 34.5578
Gaus Width: 3.4989
Entry
ampl
0
2
4
6
8
10
0
10
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30
40
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60
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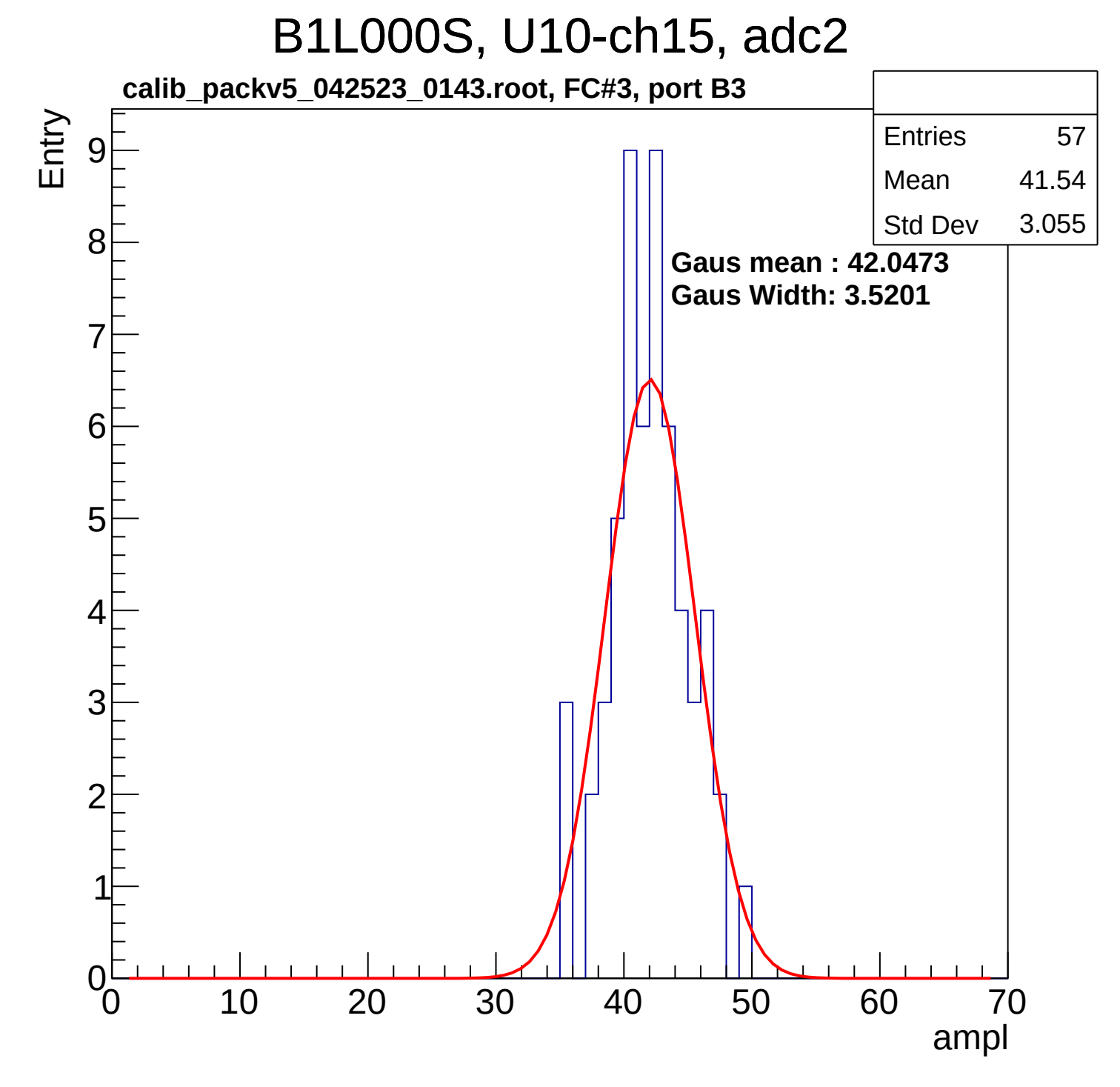
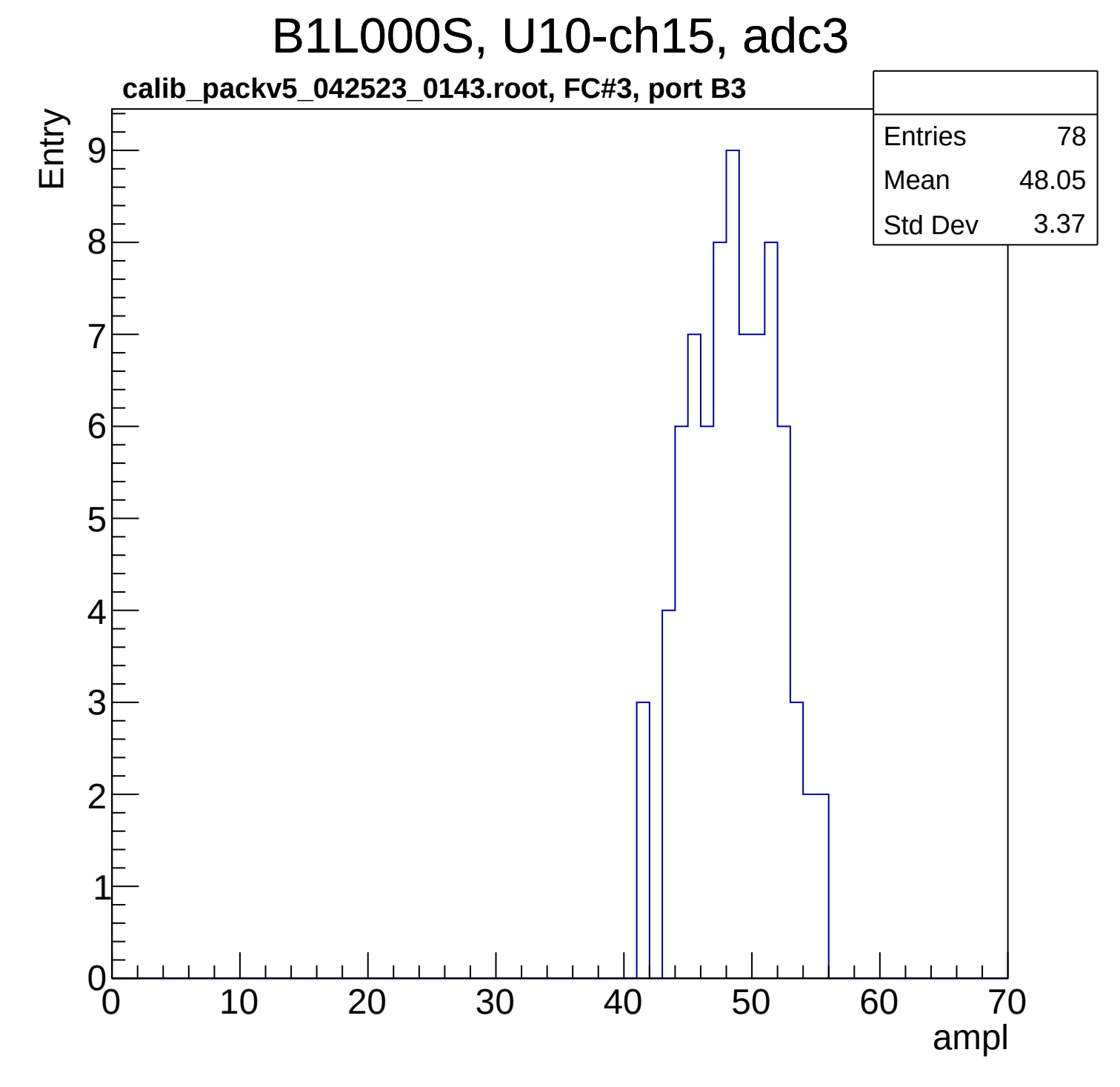

B1L000S, U10-ch15, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.54
Std Dev 3.055
Gaus mean : 42.0473
Gaus Width: 3.5201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch15, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 48.05
Std Dev 3.37
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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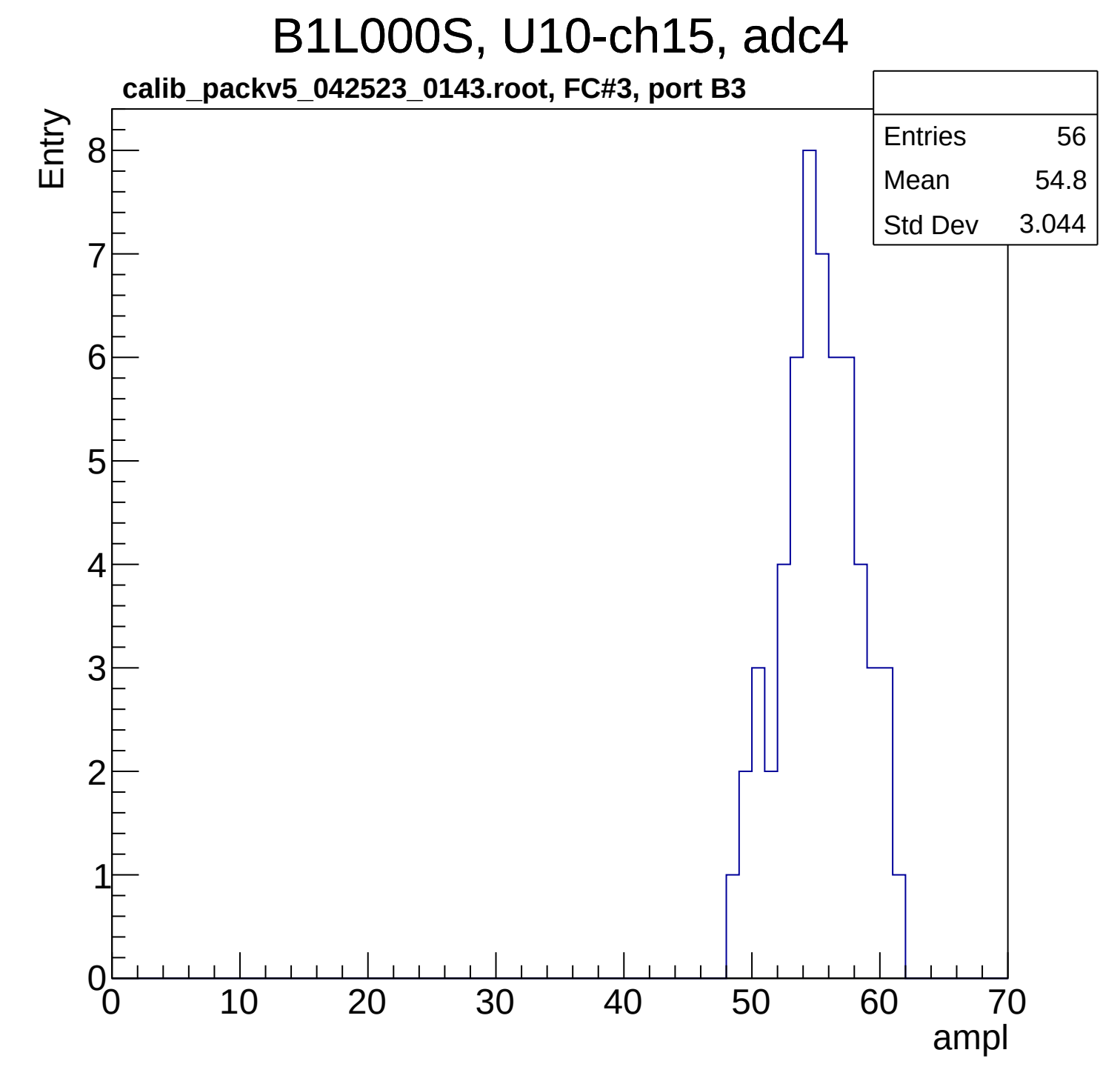
B1L000S, U10-ch15, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 54.8
Std Dev 3.044
Entry
ampl
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2
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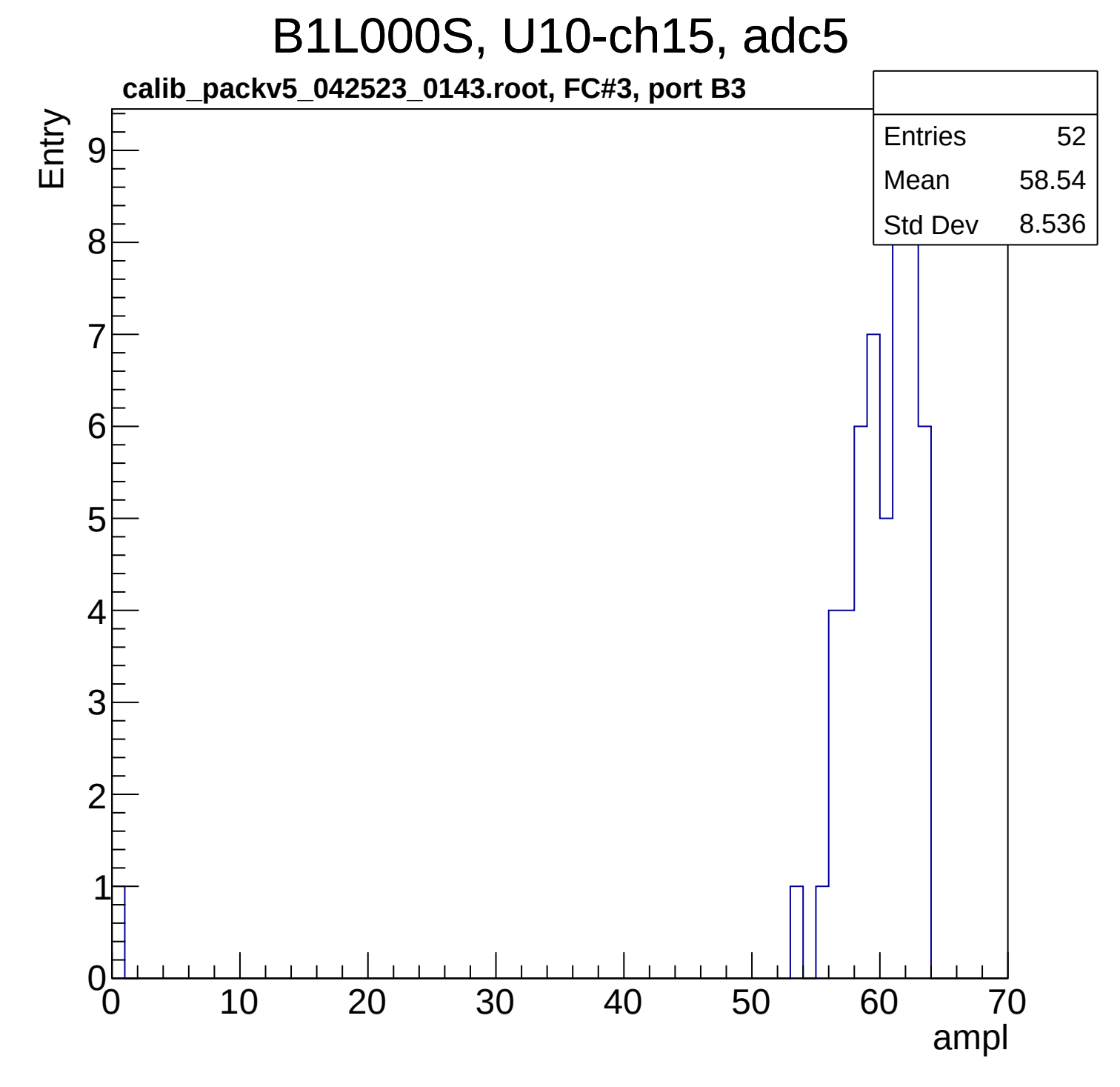
B1L000S, U10-ch15, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.54
Std Dev 8.536
Entry
ampl
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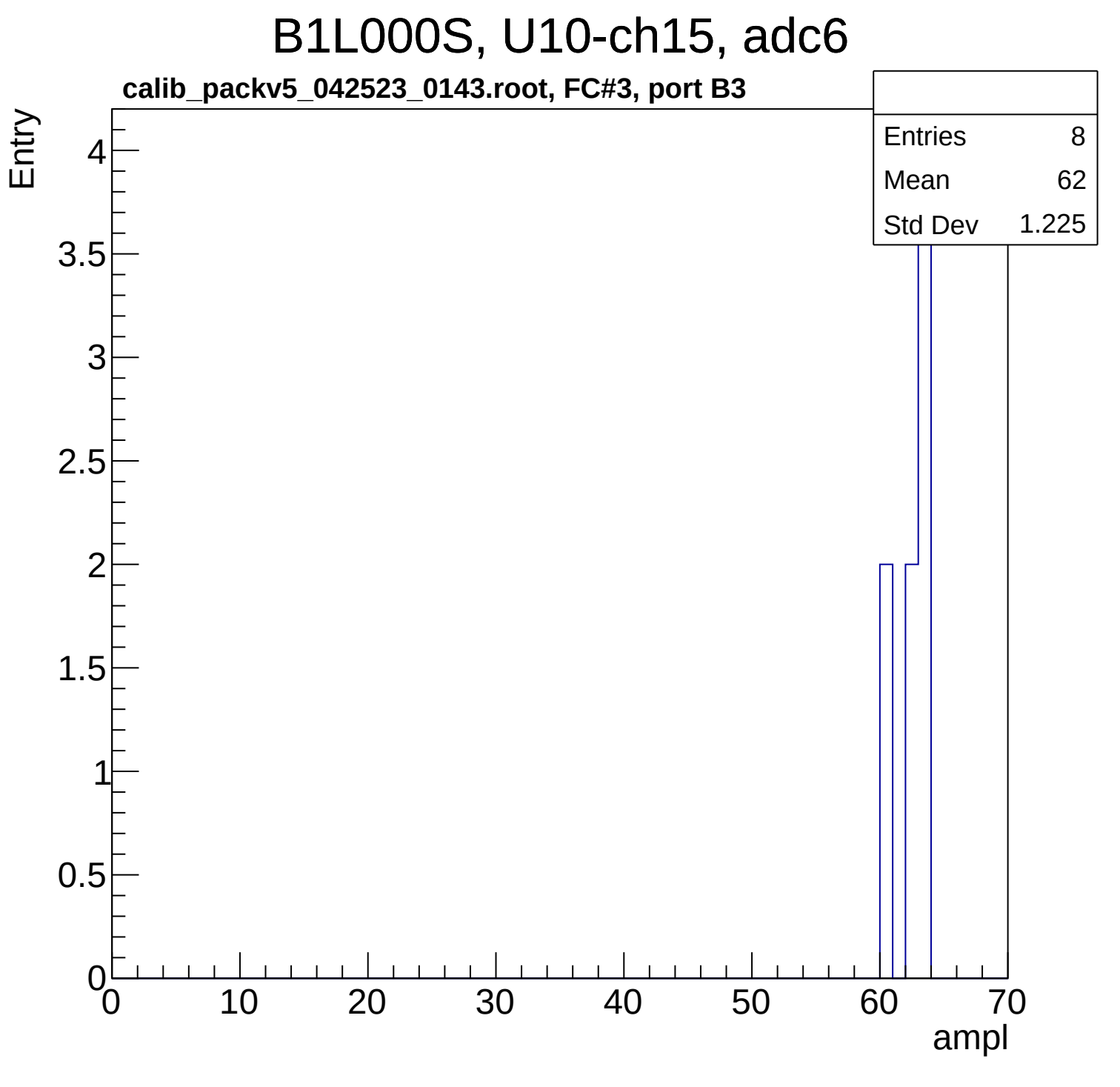
B1L000S, U10-ch15, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62
Std Dev 1.225
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
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0
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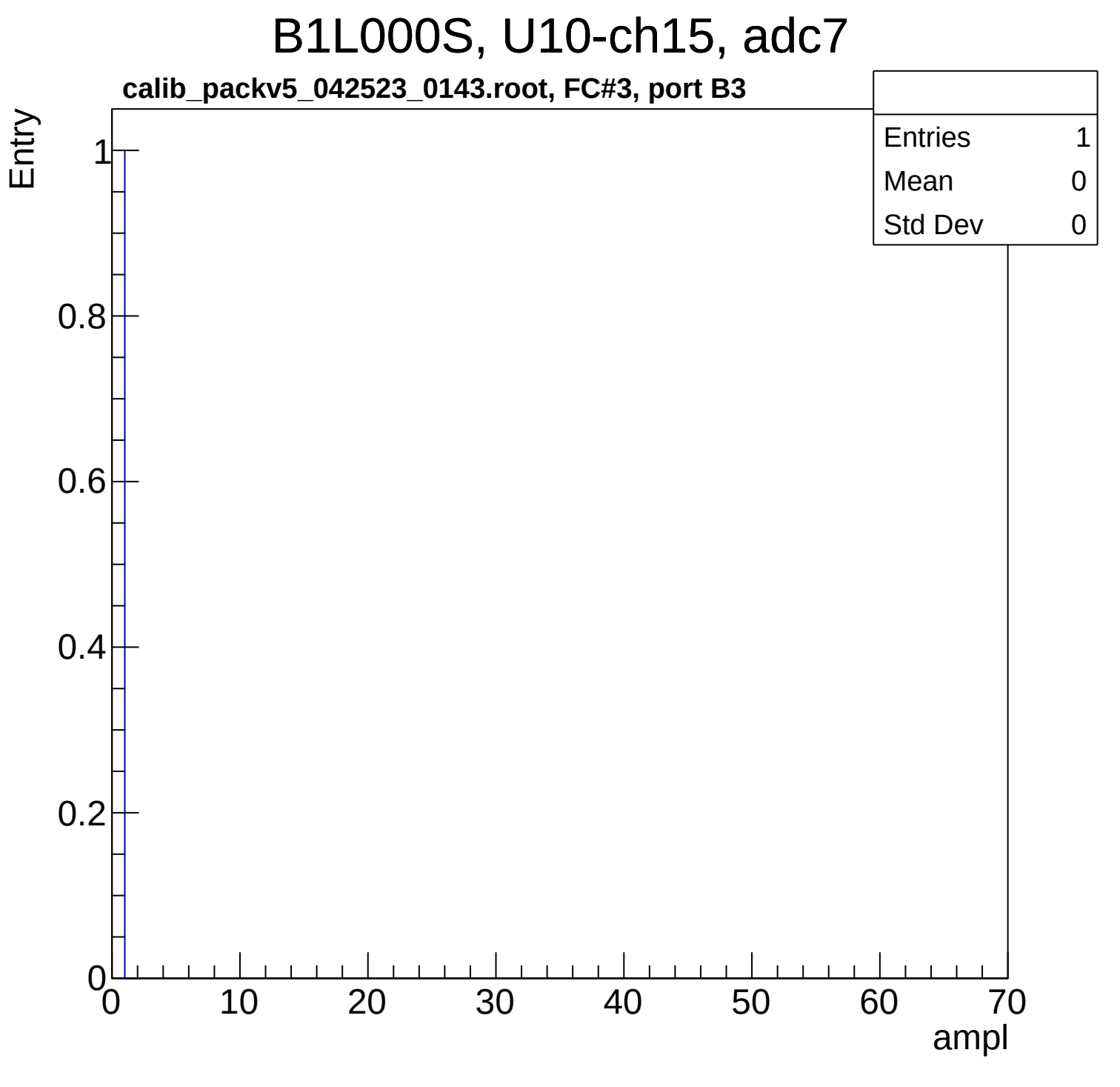
B1L000S, U10-ch15, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
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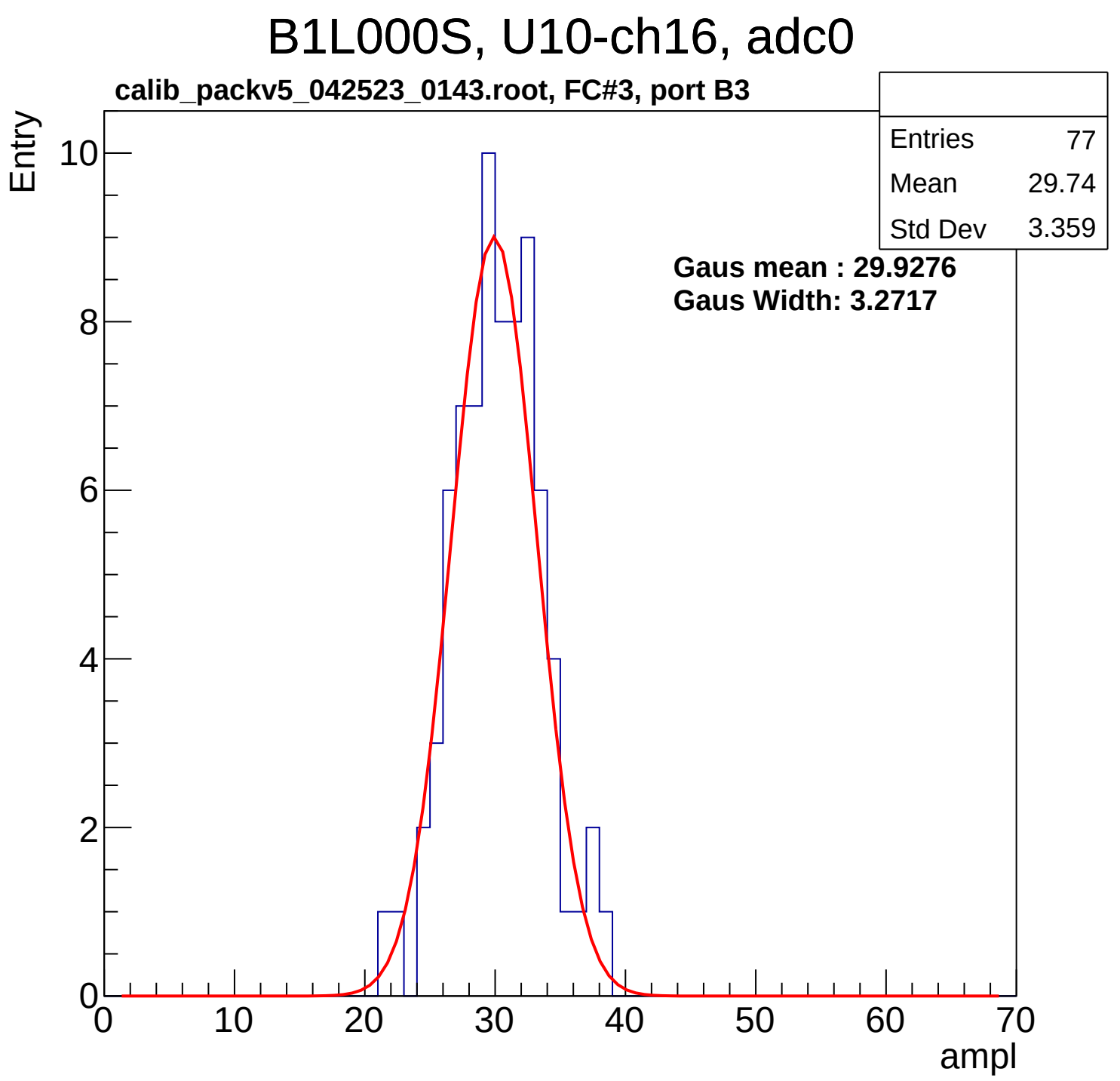
50
60
70
ampl


B1L000S, U10-ch16, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 29.74
Std Dev 3.359
Gaus mean : 29.9276
Gaus Width: 3.2717
Entry
ampl
0
2
4
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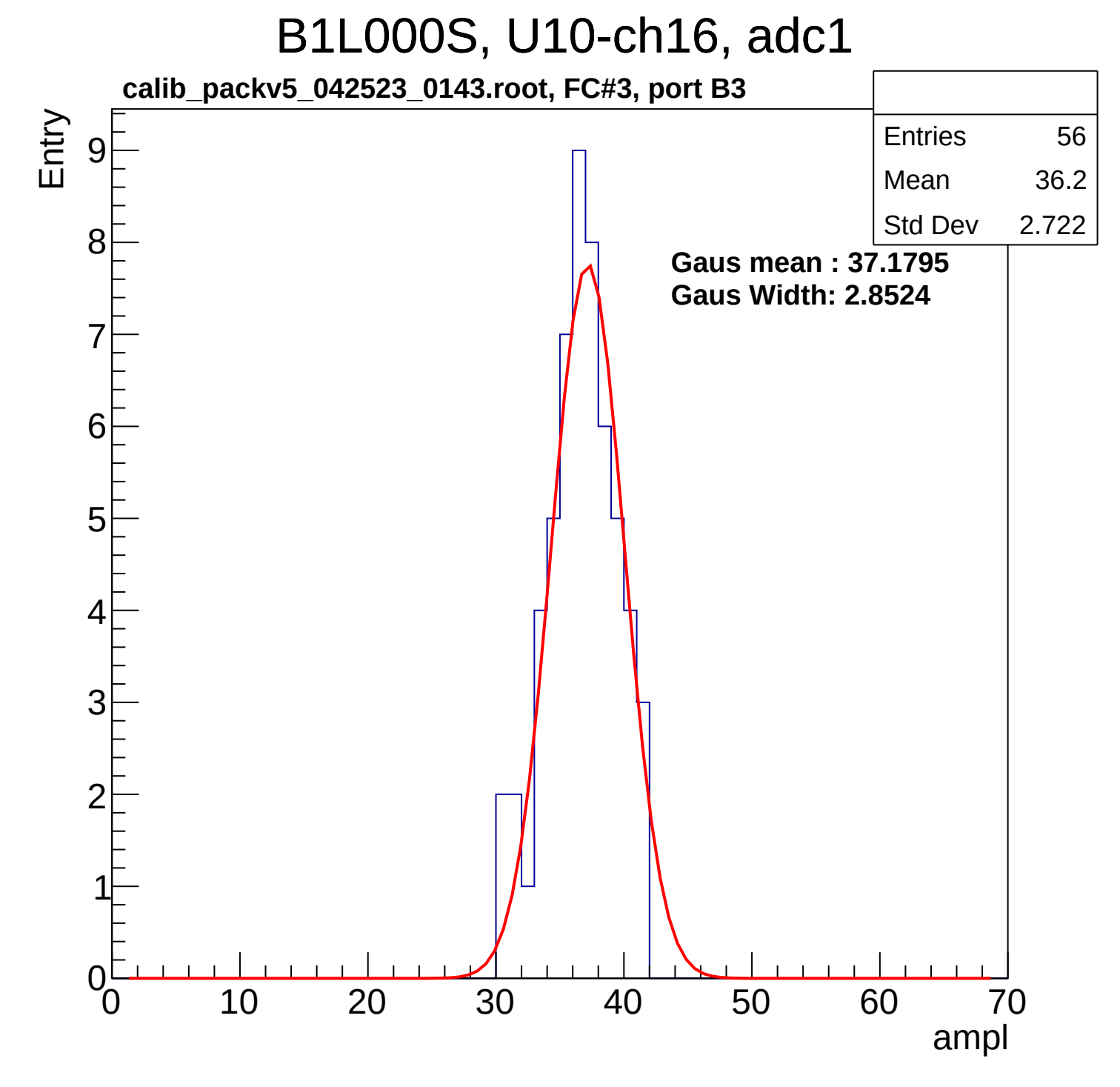
B1L000S, U10-ch16, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 36.2
Std Dev 2.722
Gaus mean : 37.1795
Gaus Width: 2.8524
Entry
ampl
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3
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5
6
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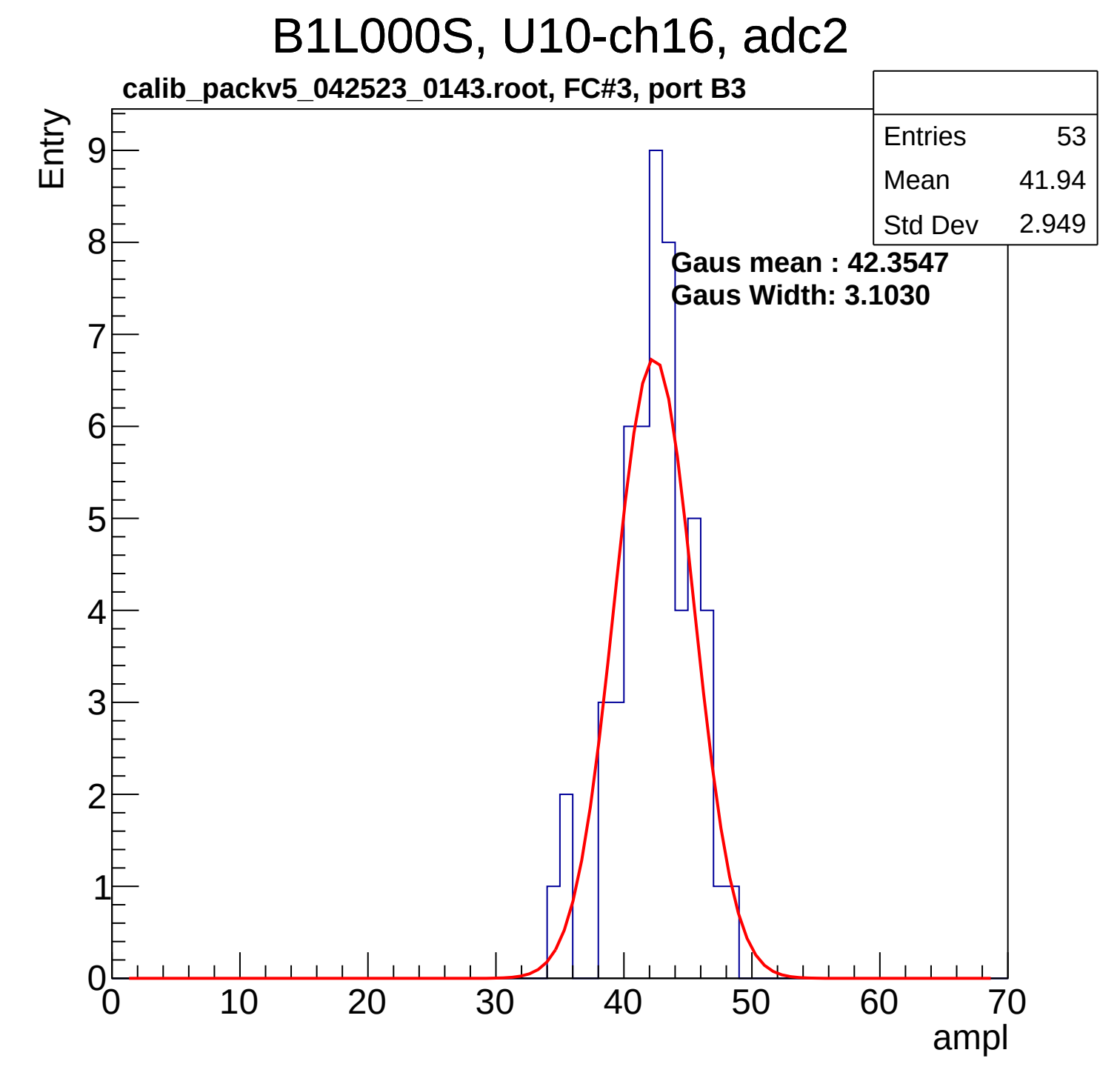
B1L000S, U10-ch16, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 41.94
Std Dev 2.949
Gaus mean : 42.3547
Gaus Width: 3.1030
Entry
ampl
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2
3
4
5
6
7
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9
0
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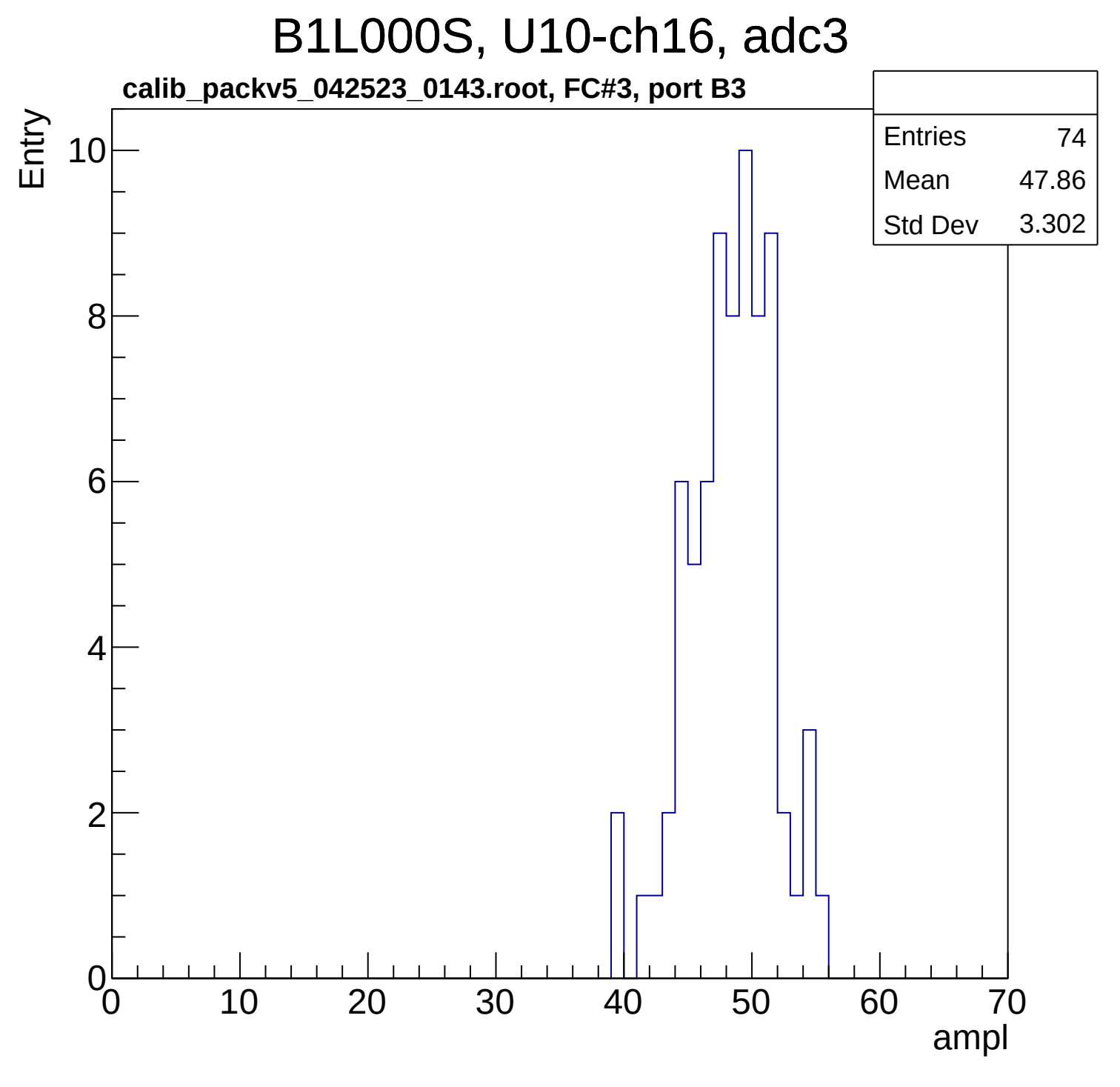
B1L000S, U10-ch16, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 47.86
Std Dev 3.302
Entry
ampl
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0
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40
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60
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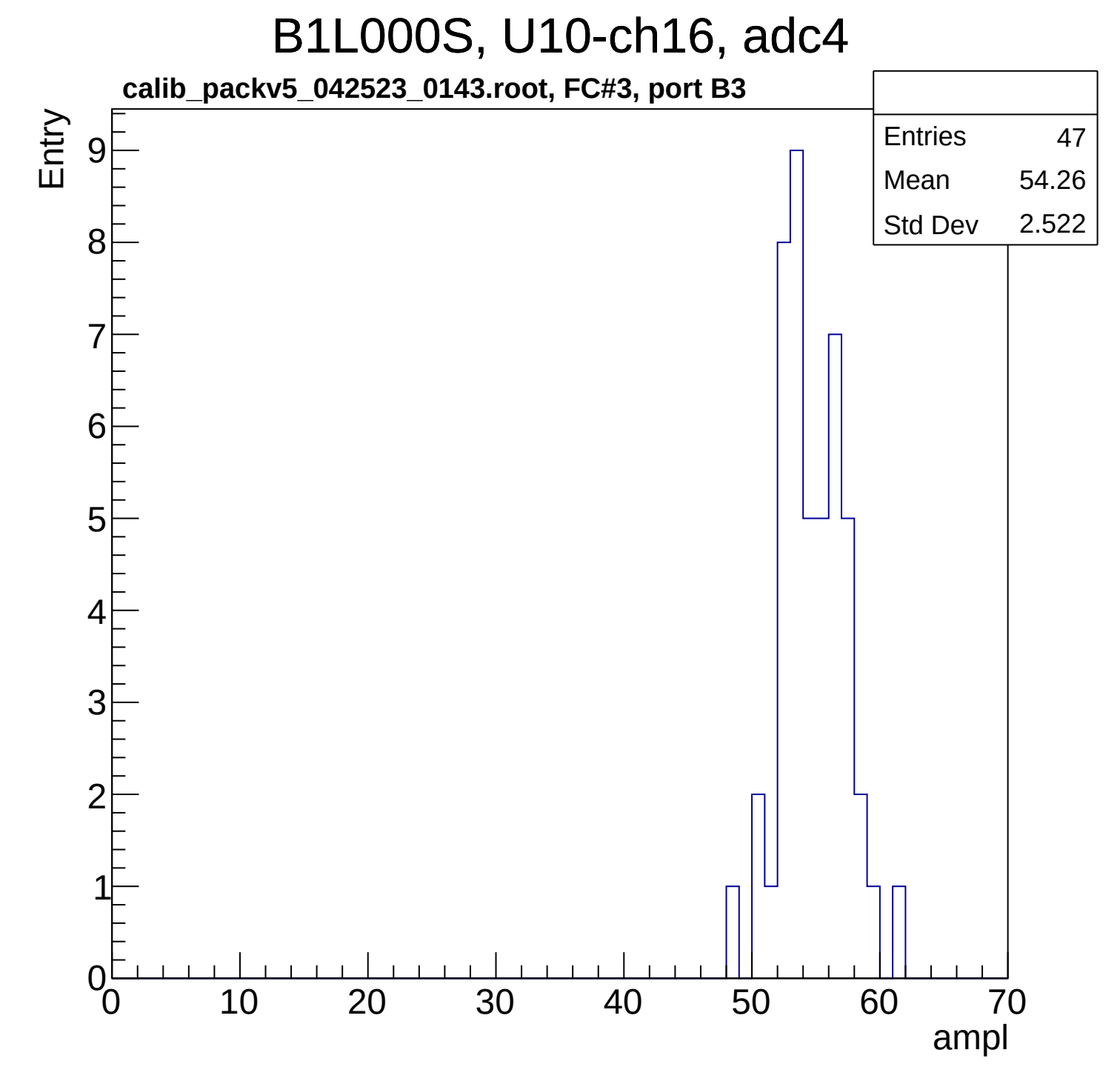
B1L000S, U10-ch16, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 54.26
Std Dev 2.522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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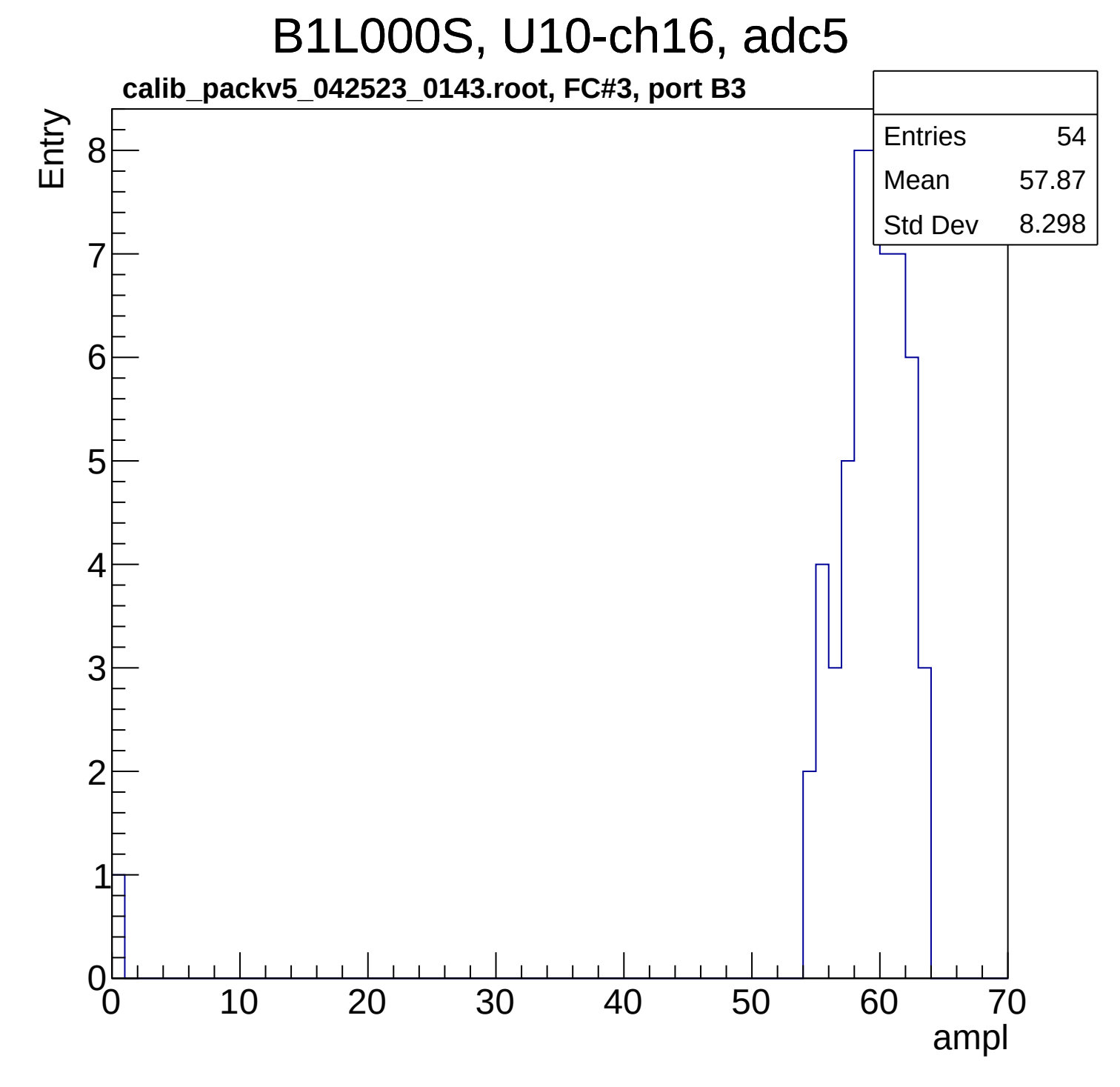
B1L000S, U10-ch16, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.87
Std Dev 8.298
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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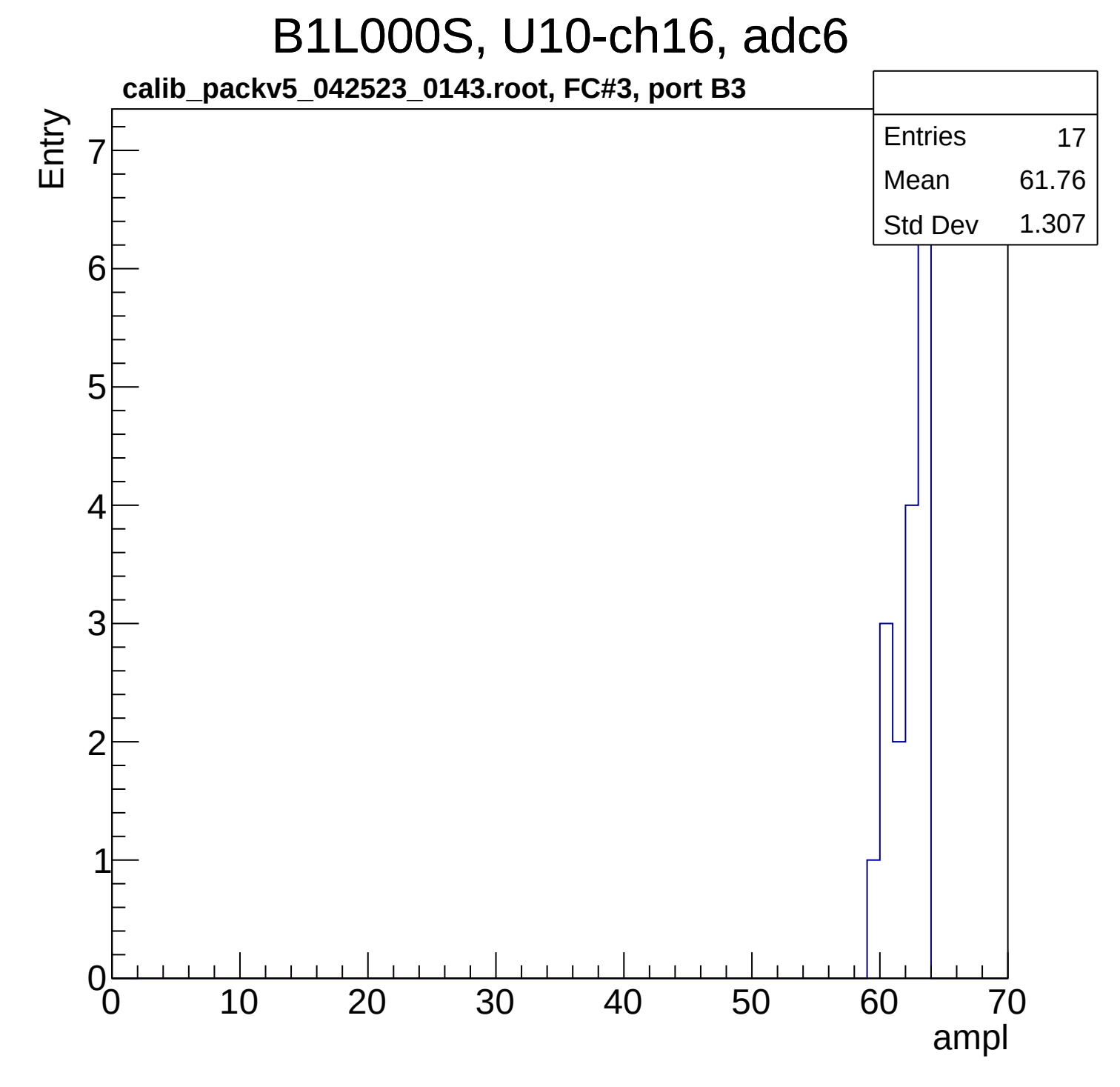
B1L000S, U10-ch16, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.76
Std Dev 1.307
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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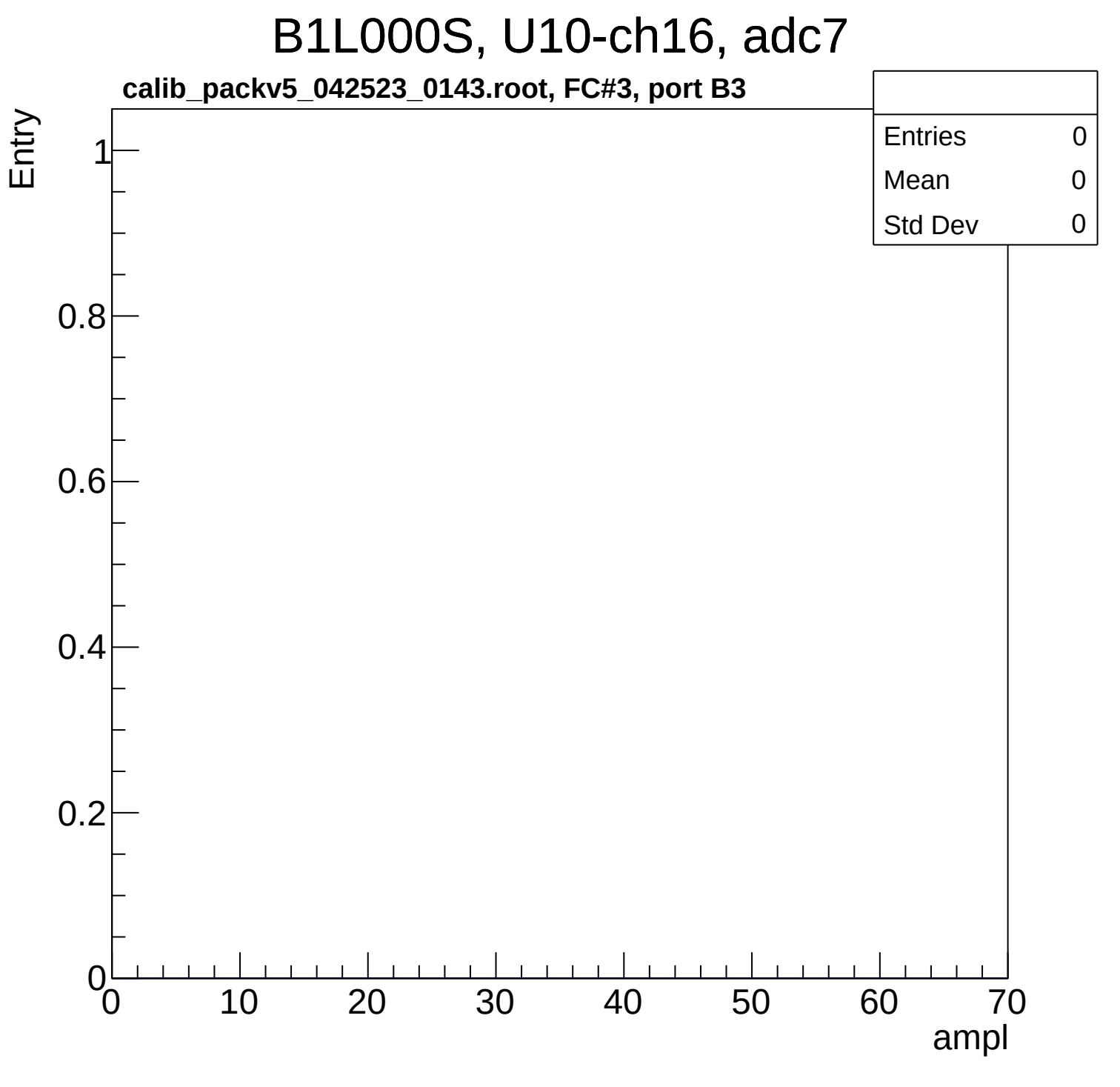
B1L000S, U10-ch16, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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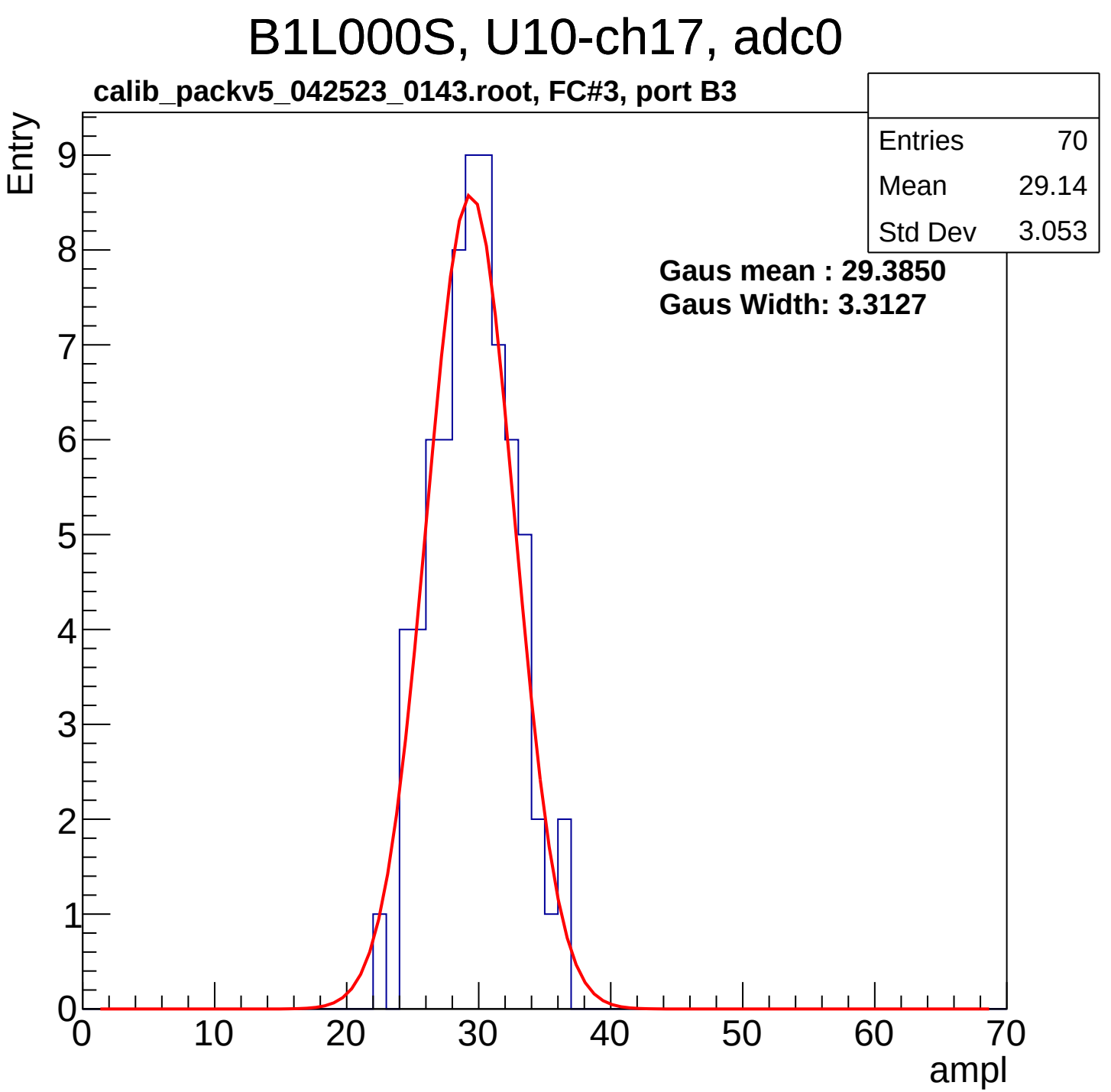
50
60
70
ampl


B1L000S, U10-ch17, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 29.14
Std Dev 3.053
Gaus mean : 29.3850
Gaus Width: 3.3127
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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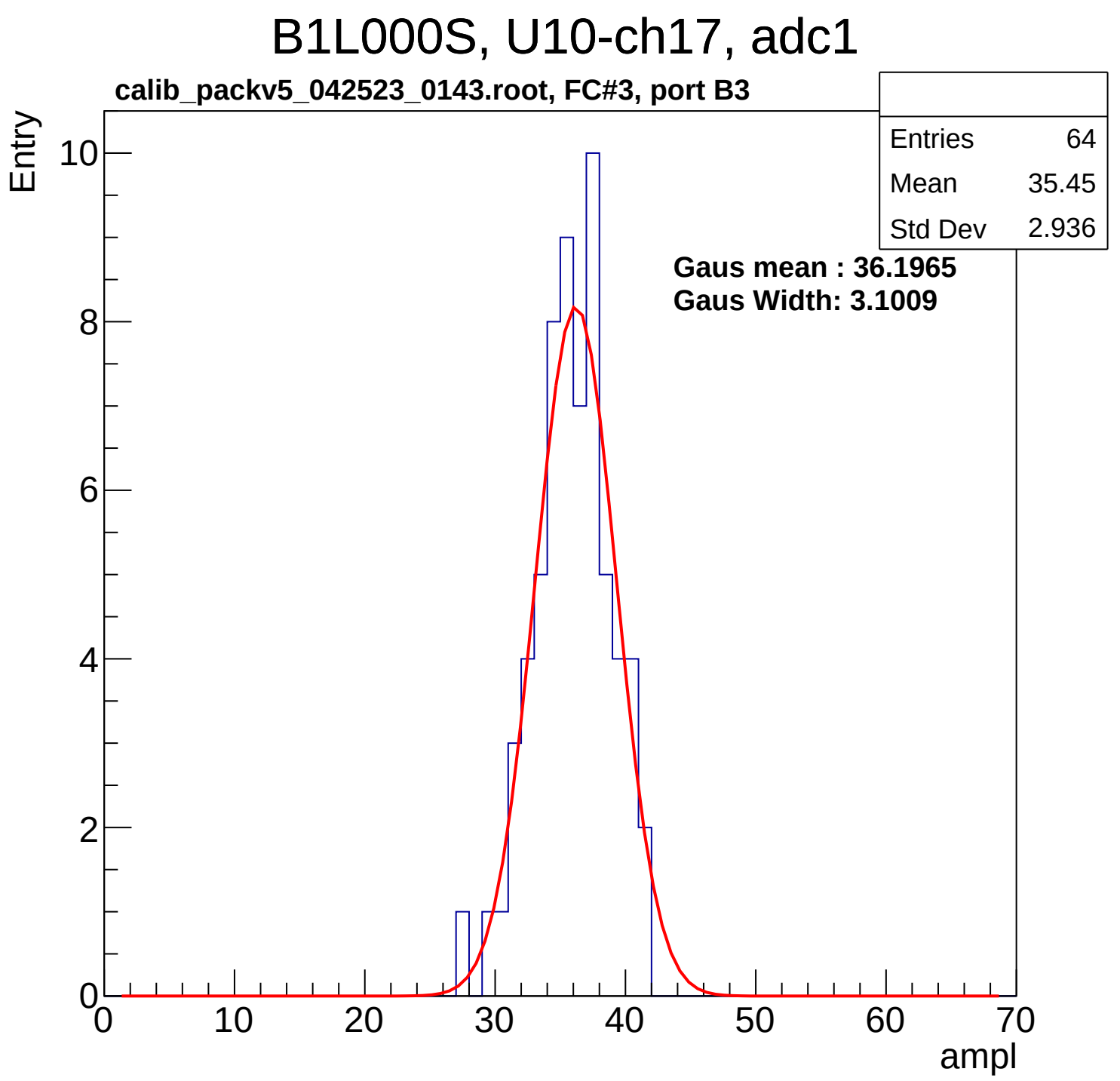
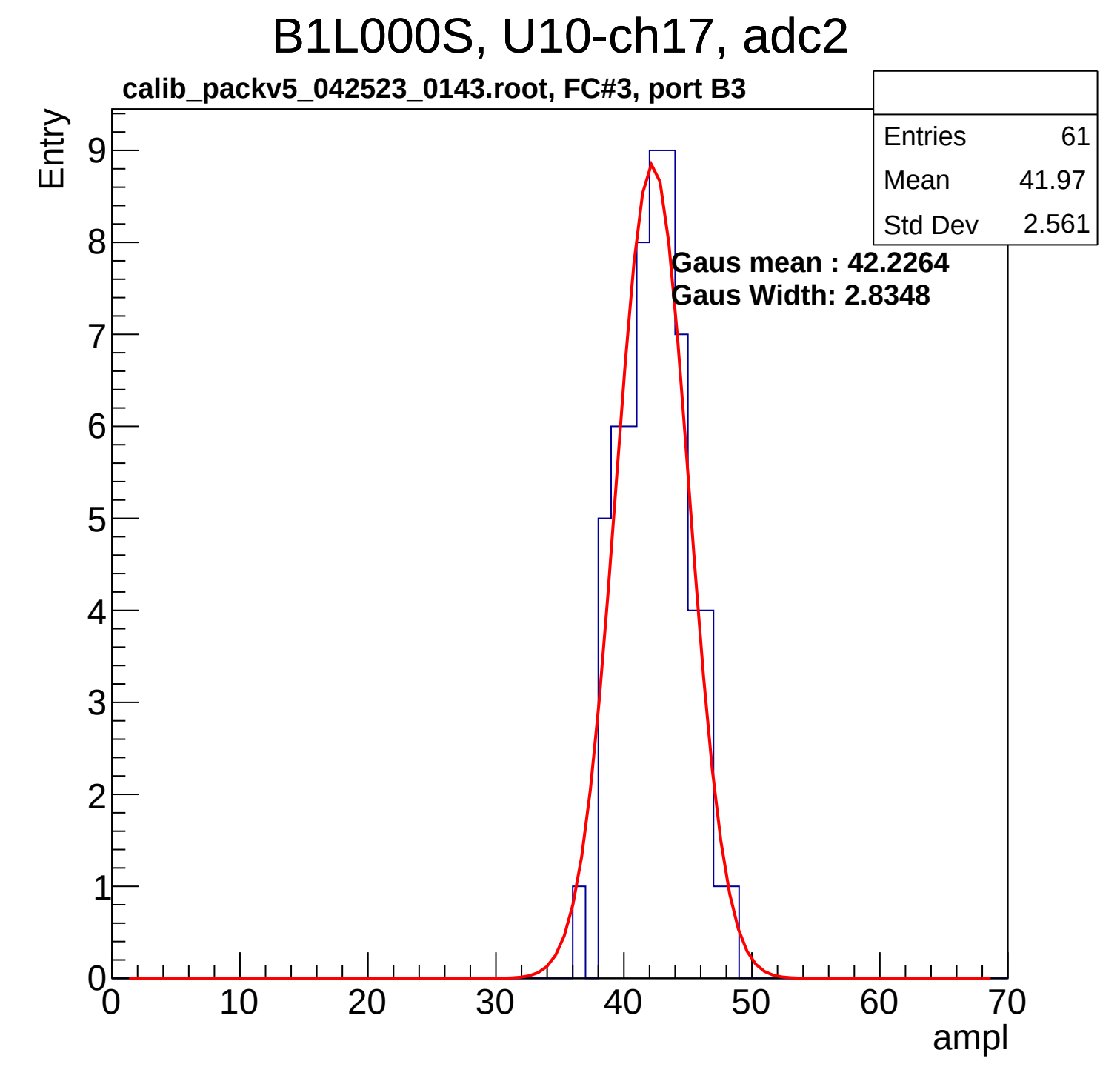

B1L000S, U10-ch17, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.45
Std Dev 2.936
Gaus mean : 36.1965
Gaus Width: 3.1009
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch17, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.97
Std Dev 2.561
Gaus mean : 42.2264
Gaus Width: 2.8348
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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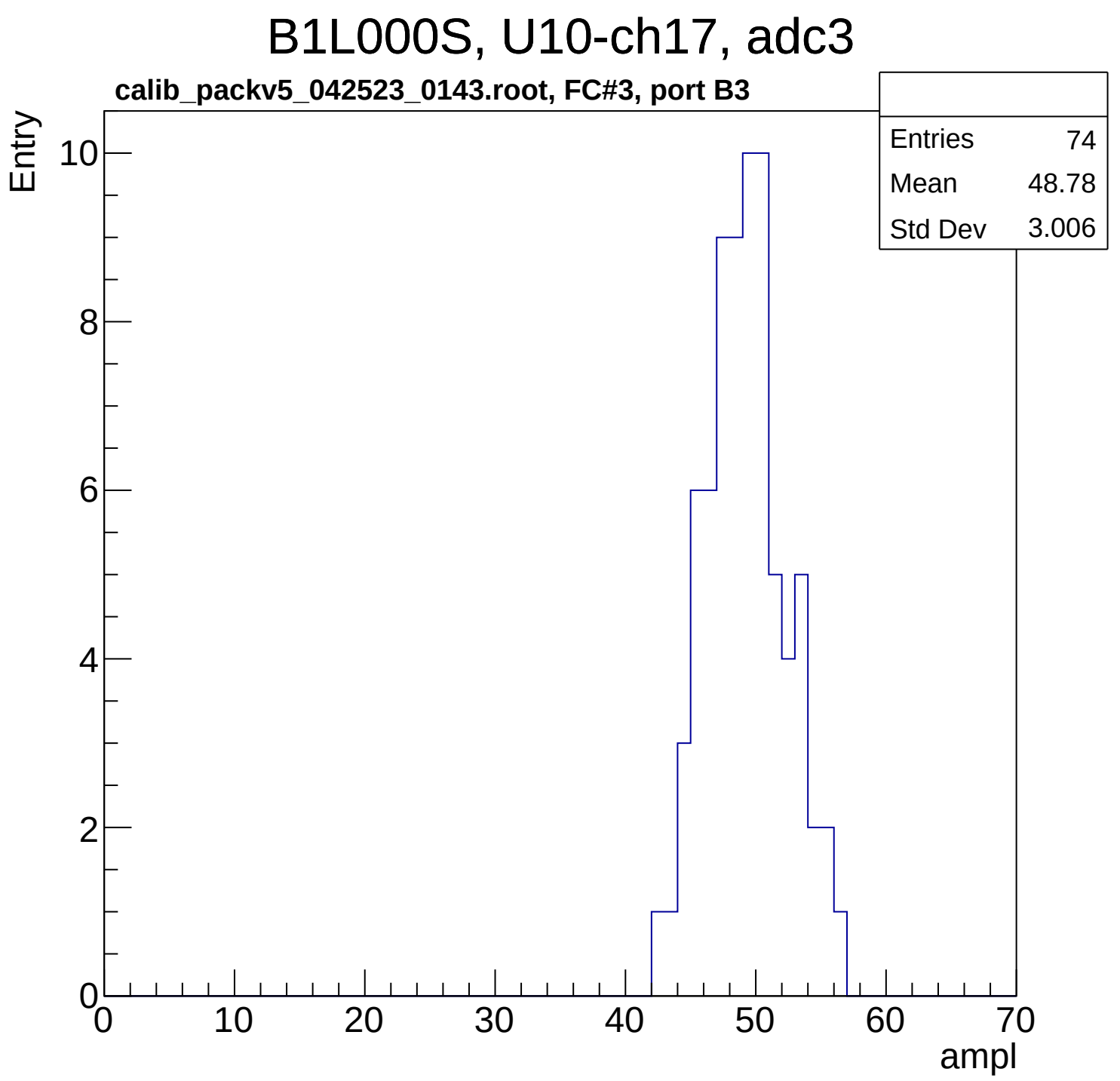
B1L000S, U10-ch17, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 48.78
Std Dev 3.006
Entry
0
2
4
6
8
10
0
10
20
30
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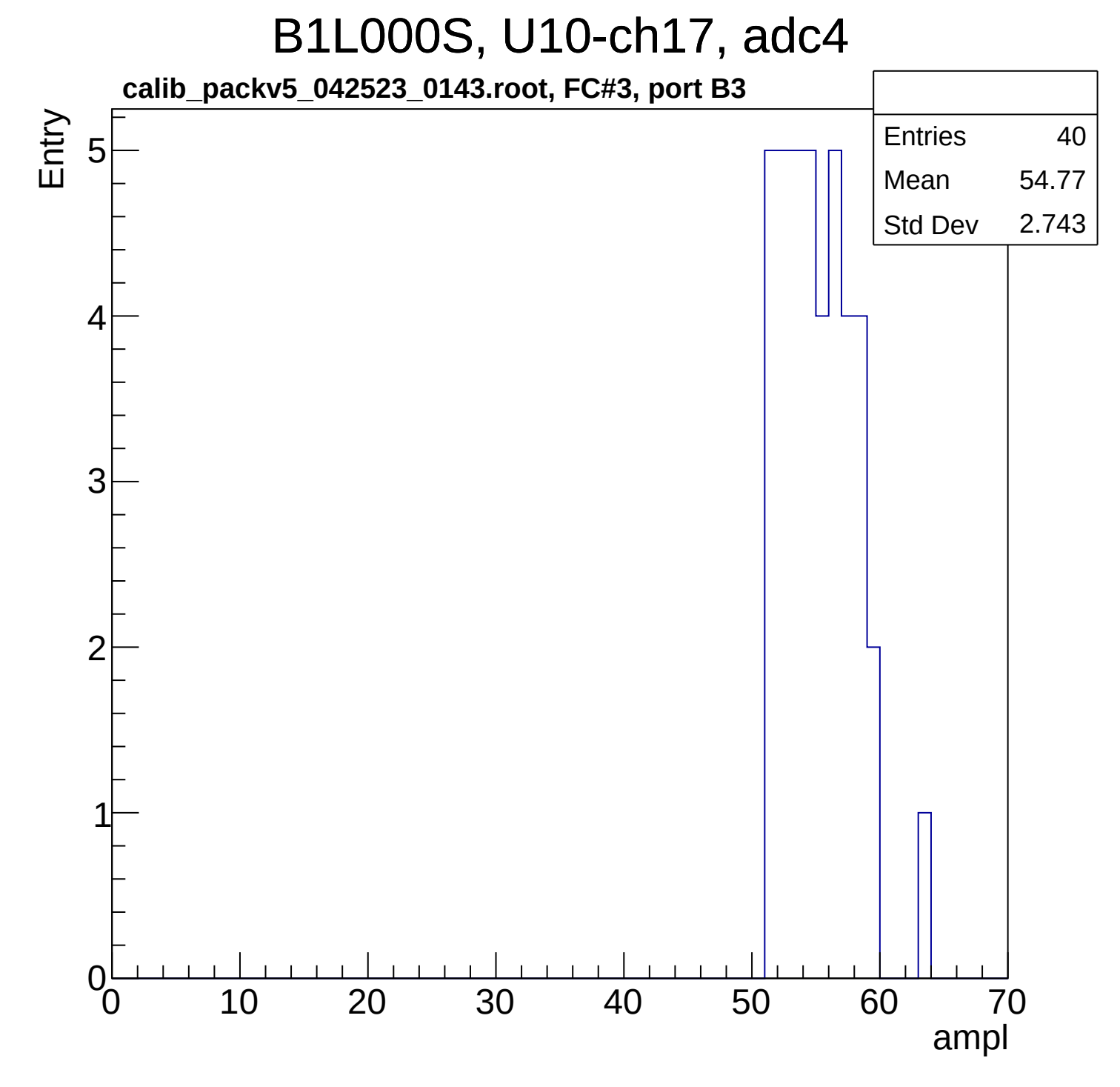
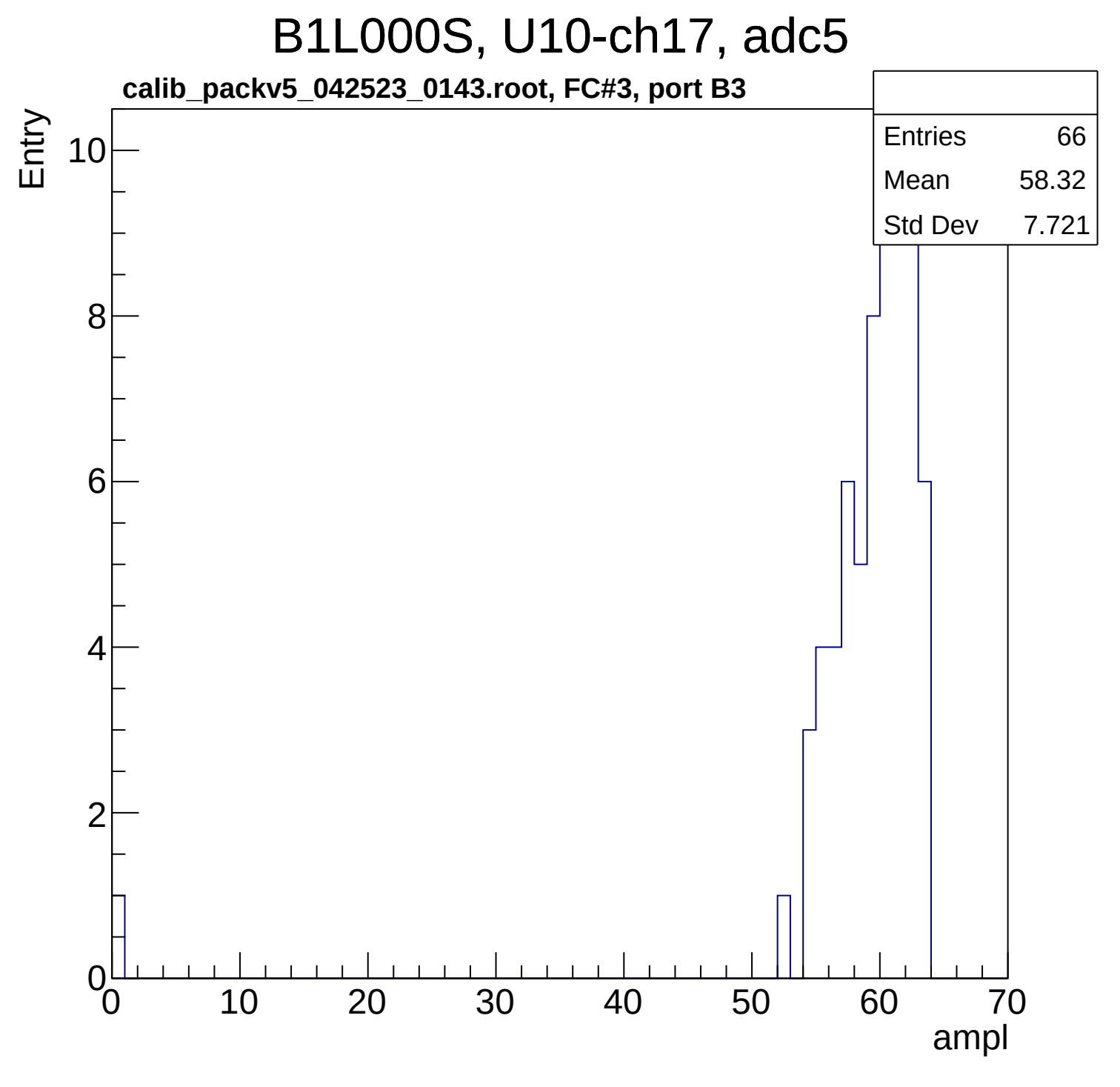
50
60
70
ampl


B1L000S, U10-ch17, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 54.77
Std Dev 2.743
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch17, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 58.32
Std Dev 7.721
Entry
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6
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10
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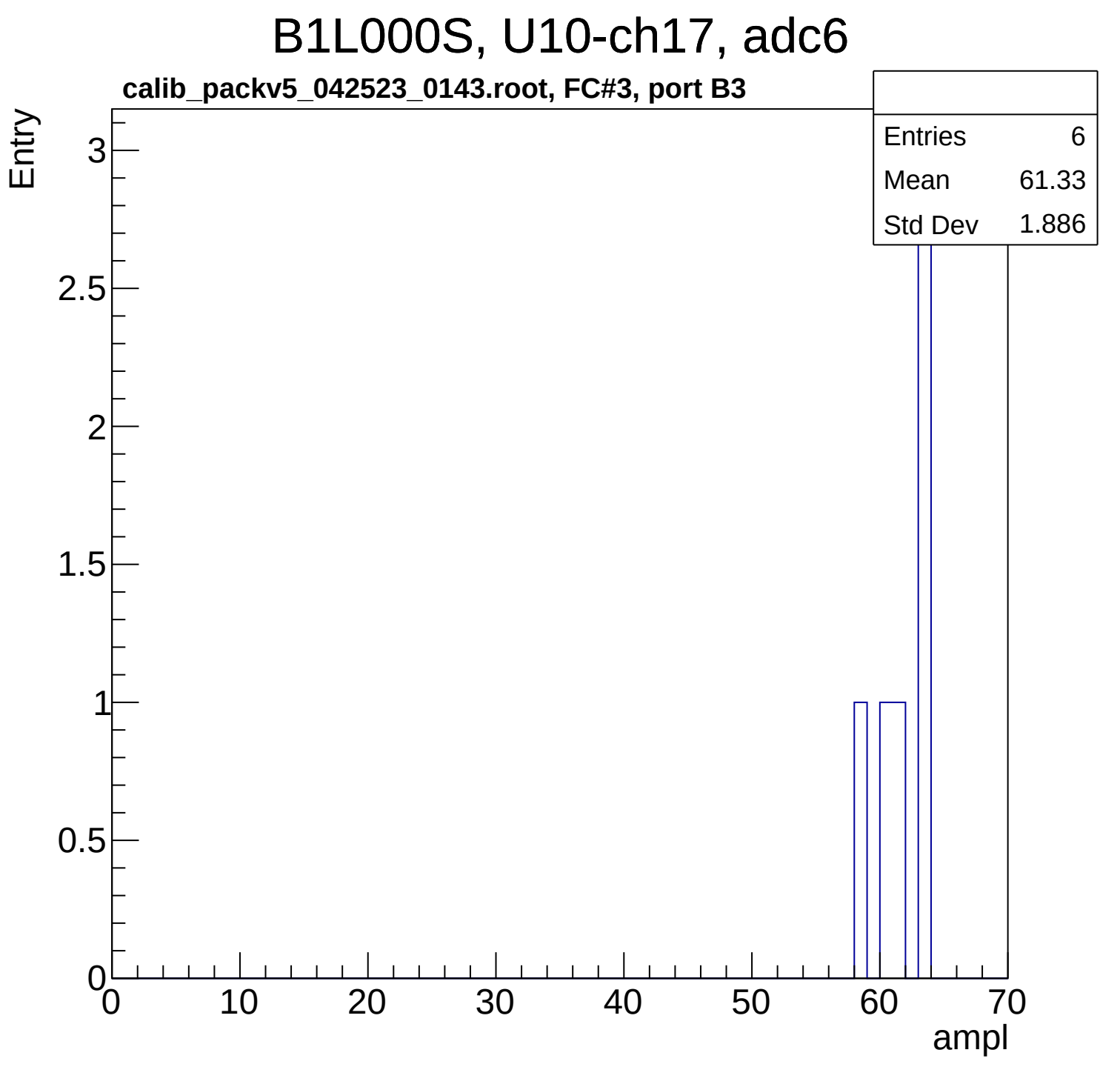
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch17, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 61.33
Std Dev 1.886
Entry
ampl
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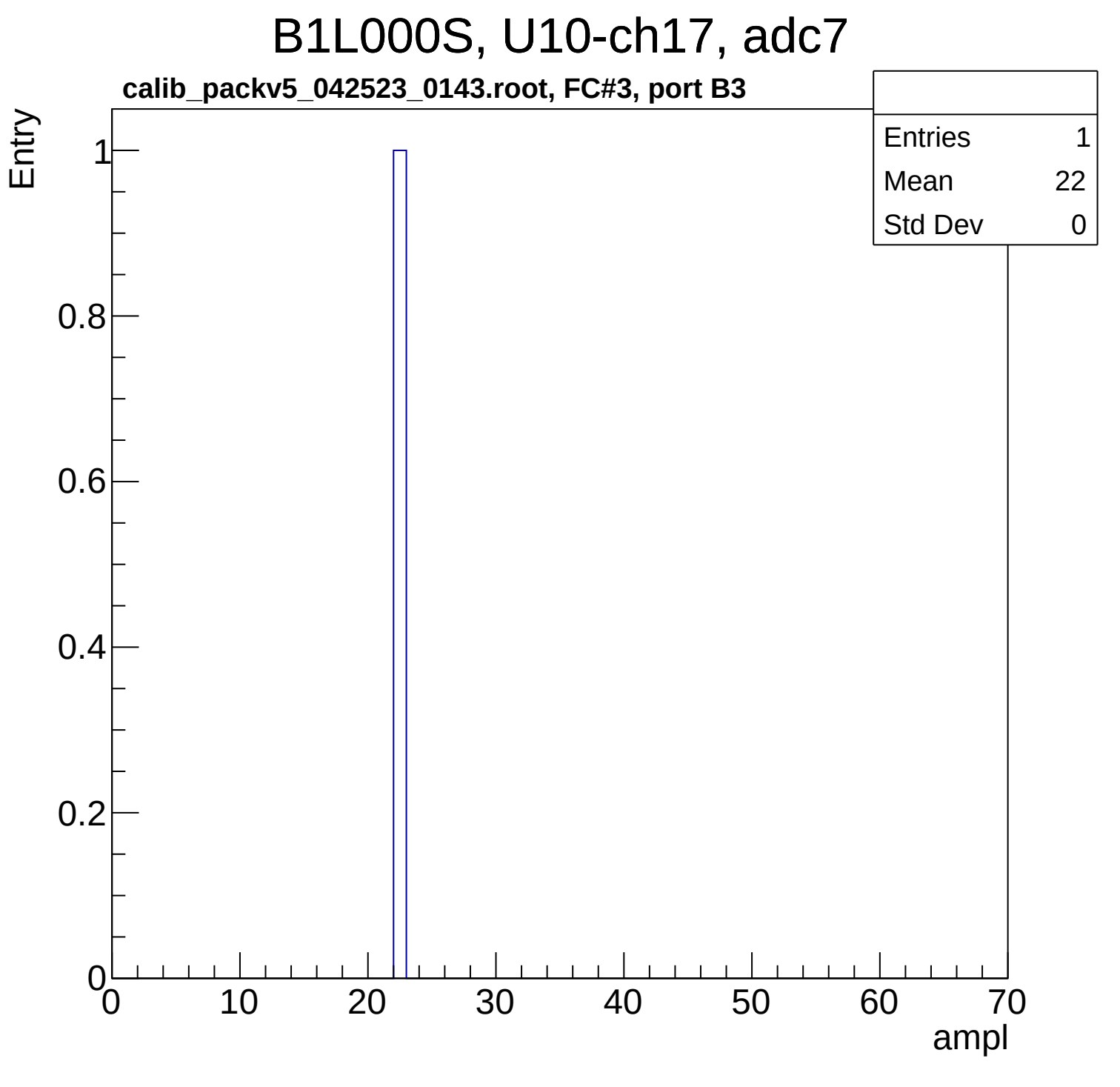
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch17, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
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10
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30
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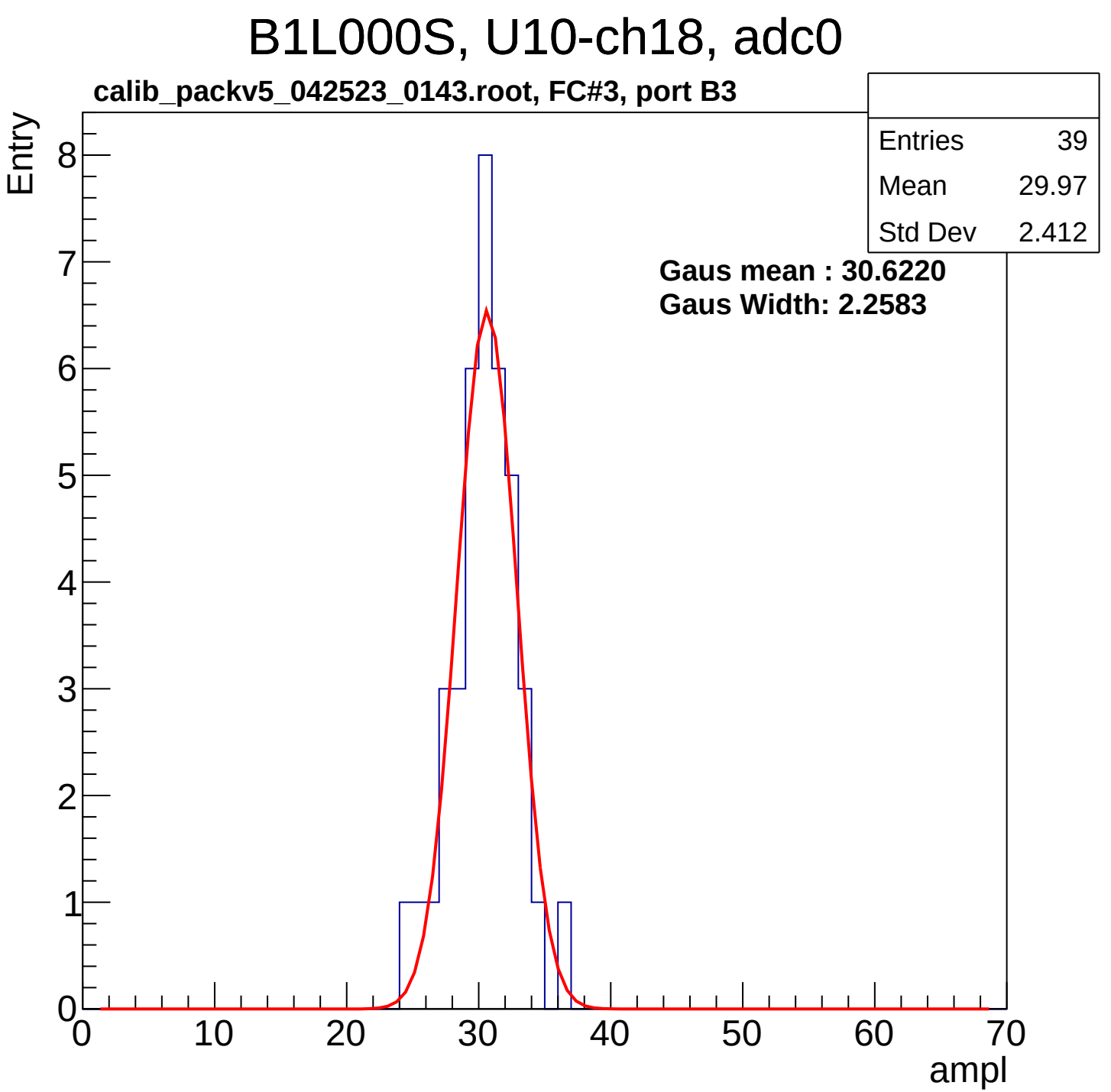
50
60
70
ampl


B1L000S, U10-ch18, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 29.97
Std Dev 2.412
Gaus mean : 30.6220
Gaus Width: 2.2583
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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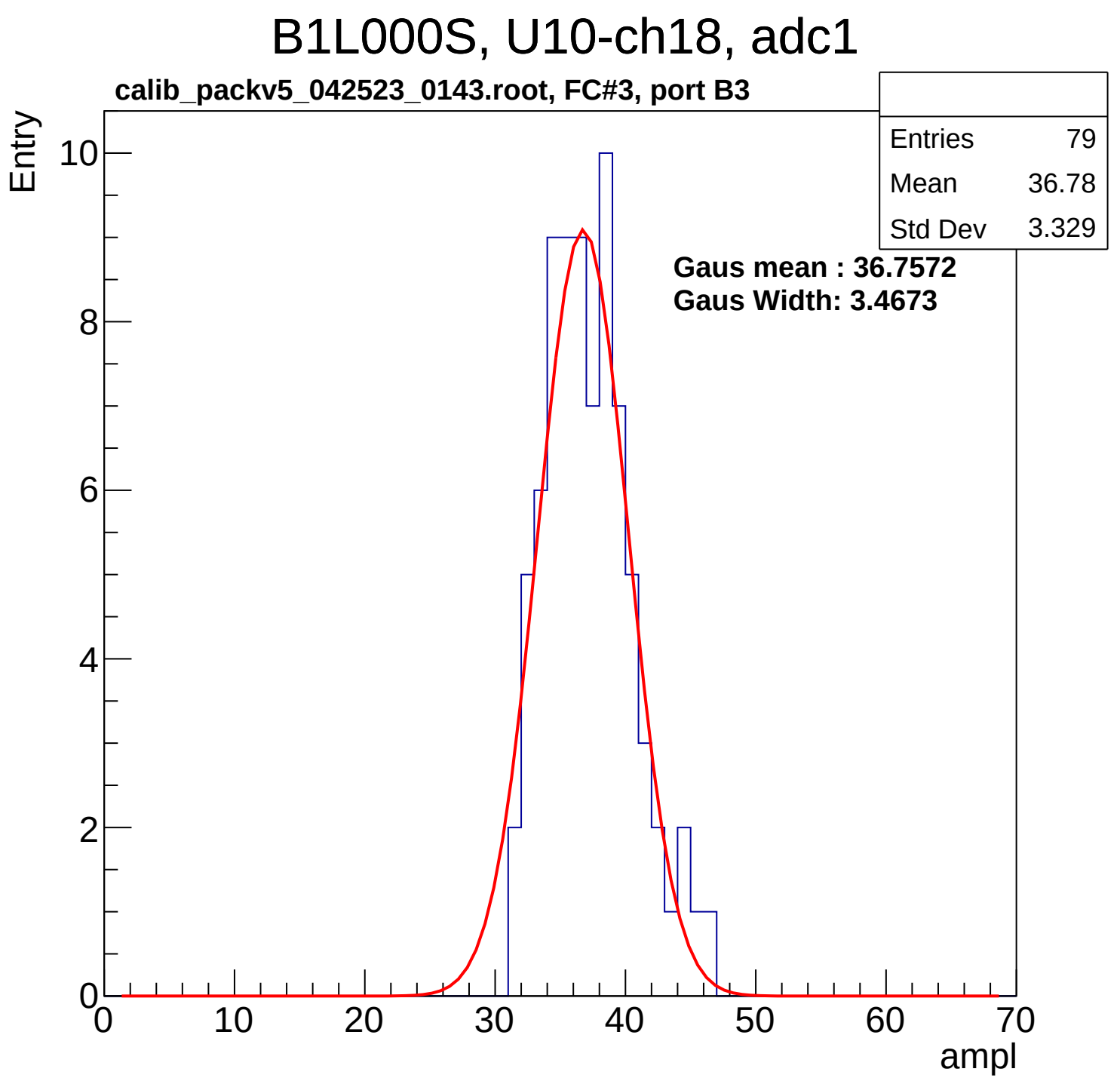
B1L000S, U10-ch18, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 36.78
Std Dev 3.329
Gaus mean : 36.7572
Gaus Width: 3.4673
Entry
ampl
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4
6
8
10
0
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20
30
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60
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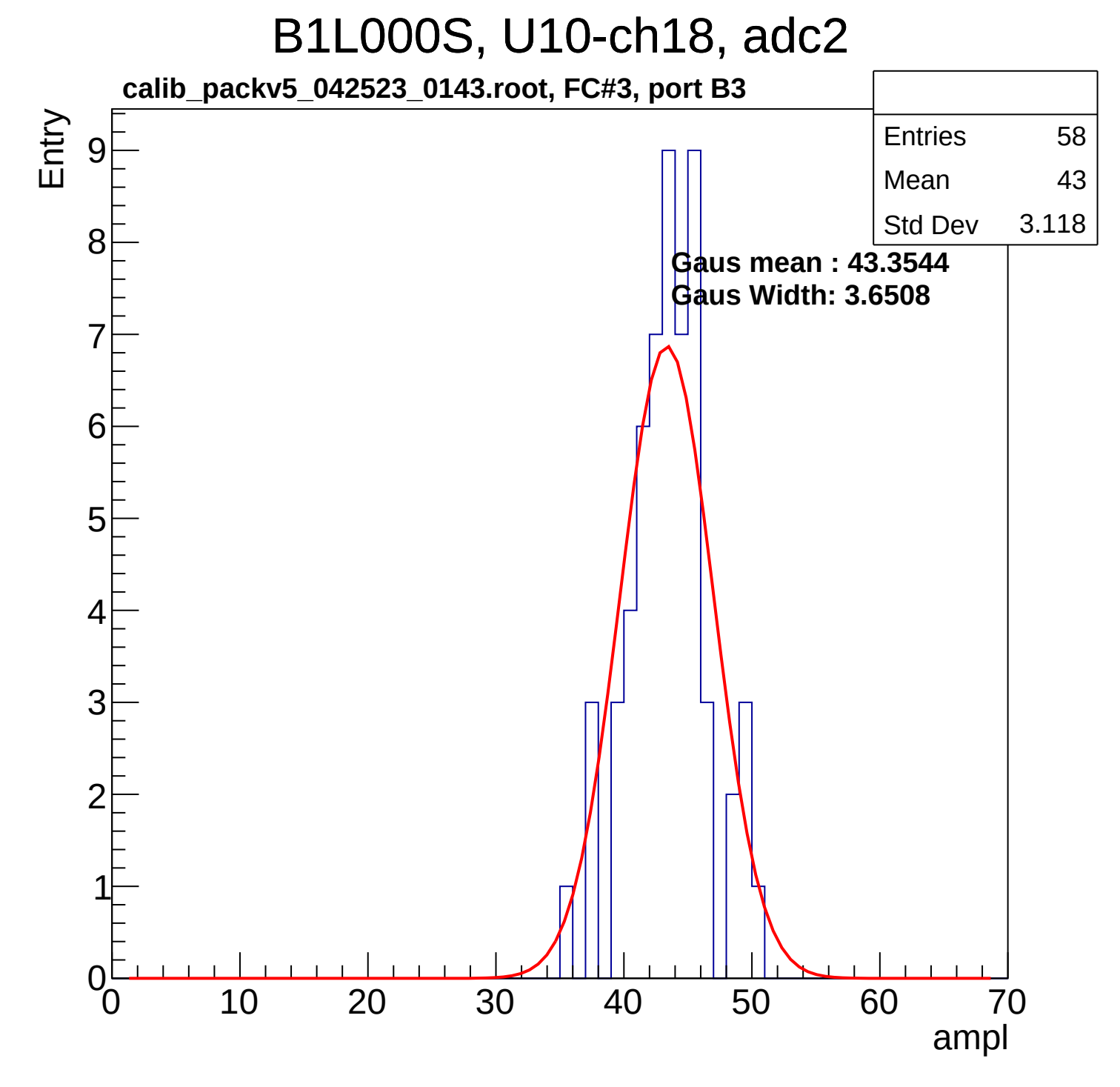
B1L000S, U10-ch18, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 43
Std Dev 3.118
Gaus mean : 43.3544
Gaus Width: 3.6508
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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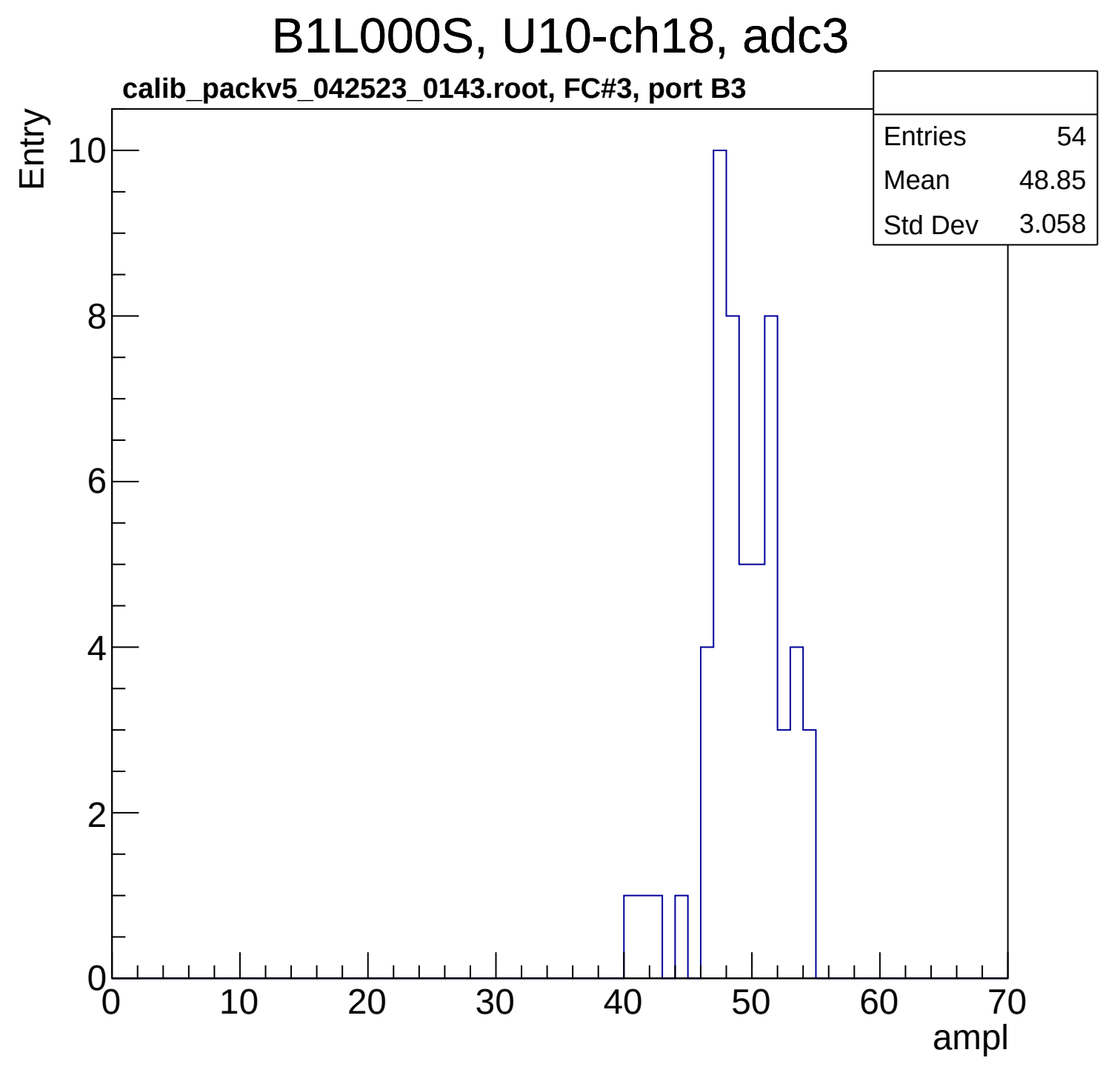
B1L000S, U10-ch18, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 48.85
Std Dev 3.058
Entry
ampl
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0
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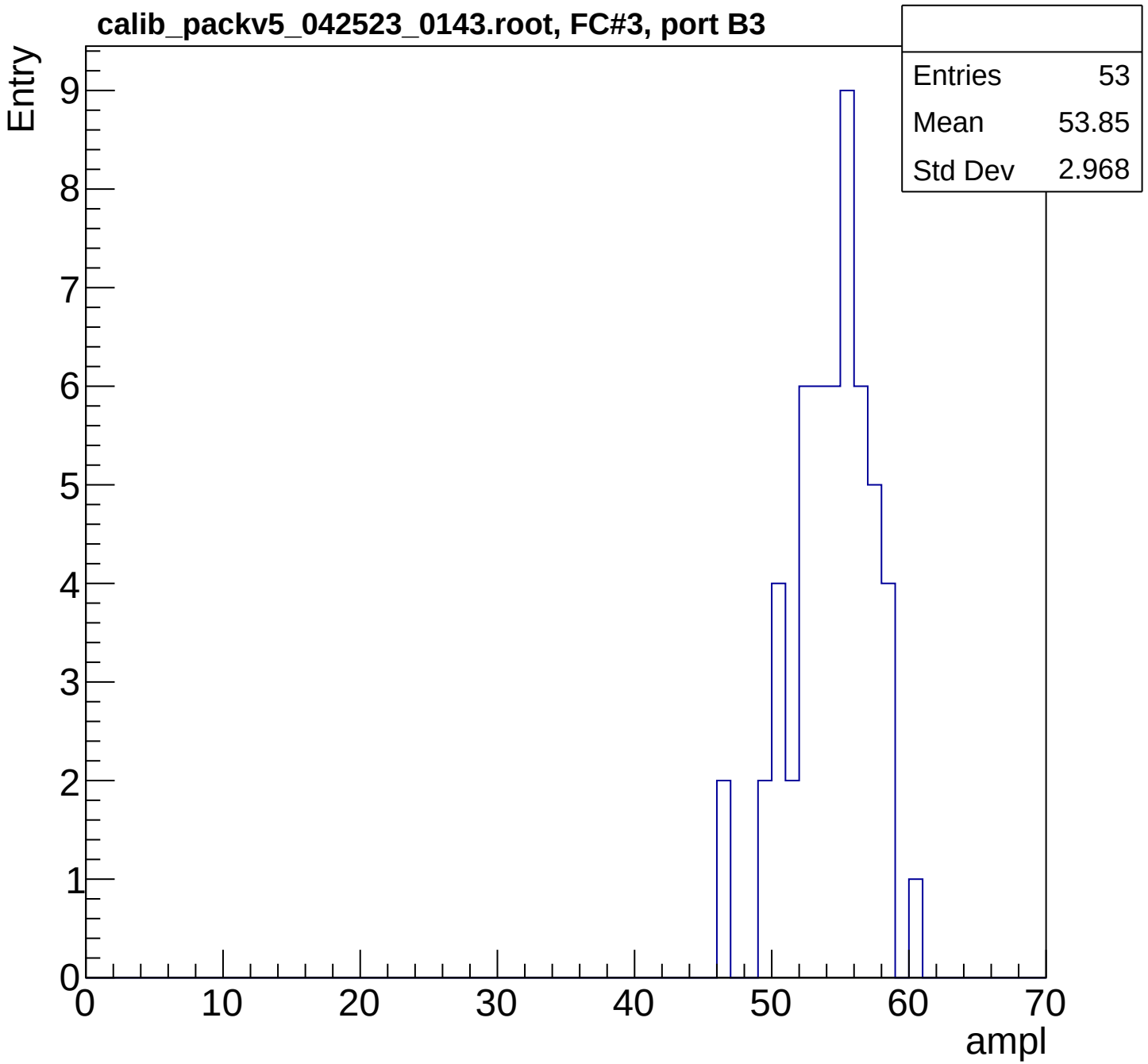
60
70


B1L000S, U10-ch18, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 53.85
Std Dev 2.968
Entry
ampl
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4
5
6
7
8
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0
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60
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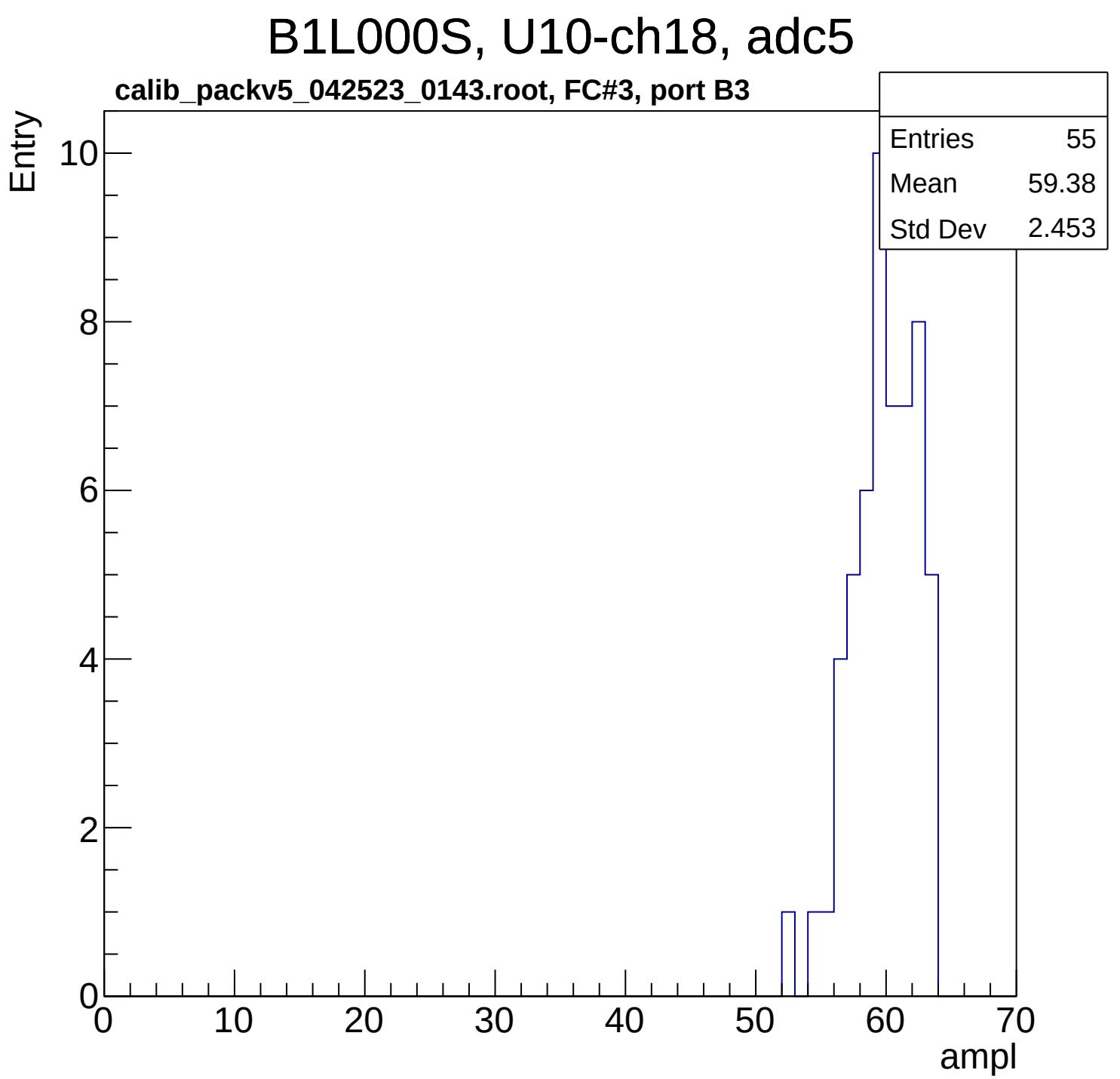
B1L000S, U10-ch18, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 59.38
Std Dev 2.453
Entry
ampl
0
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4
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0
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20
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40
50
60
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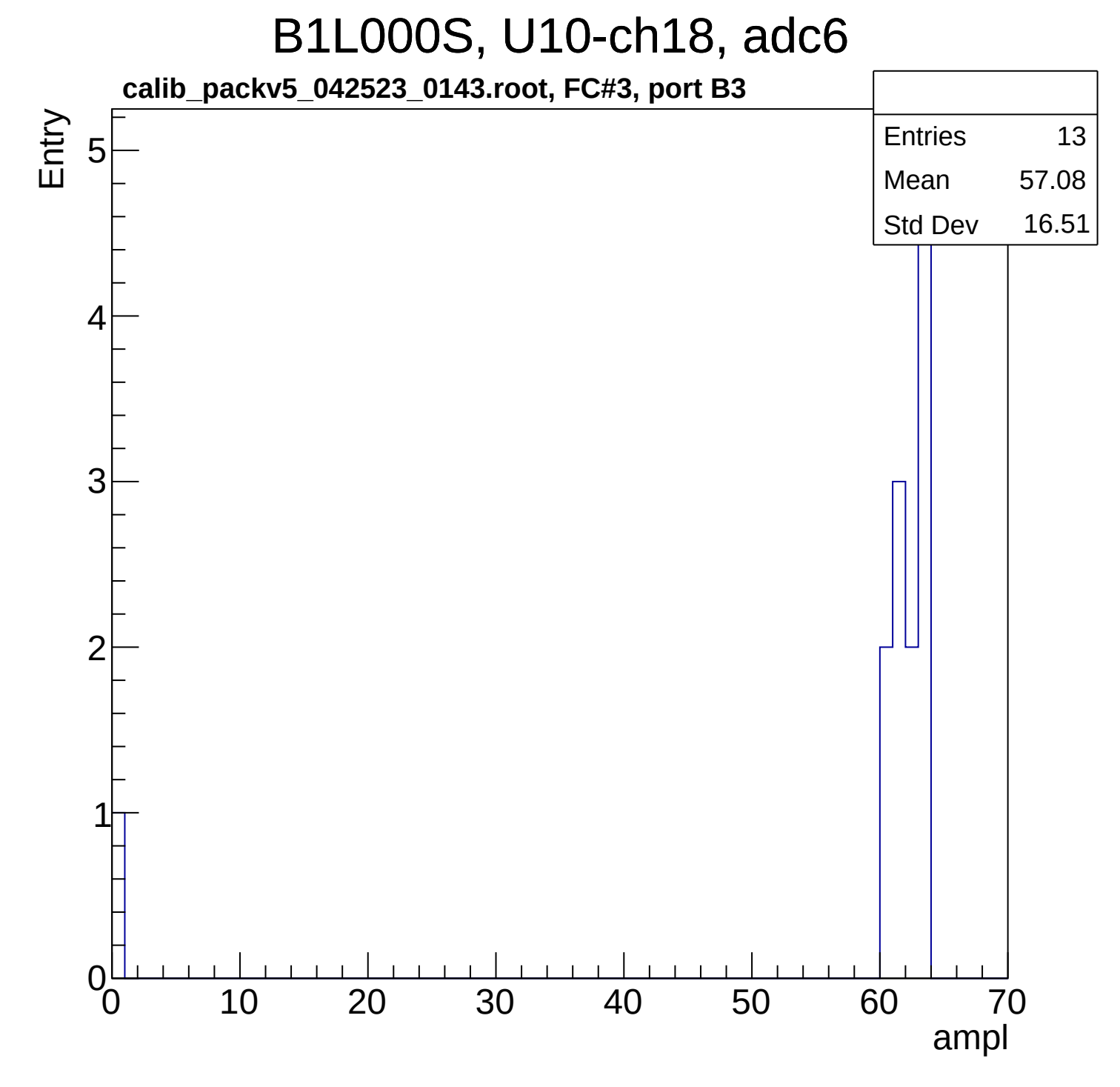
B1L000S, U10-ch18, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.08
Std Dev 16.51
Entry
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2
3
4
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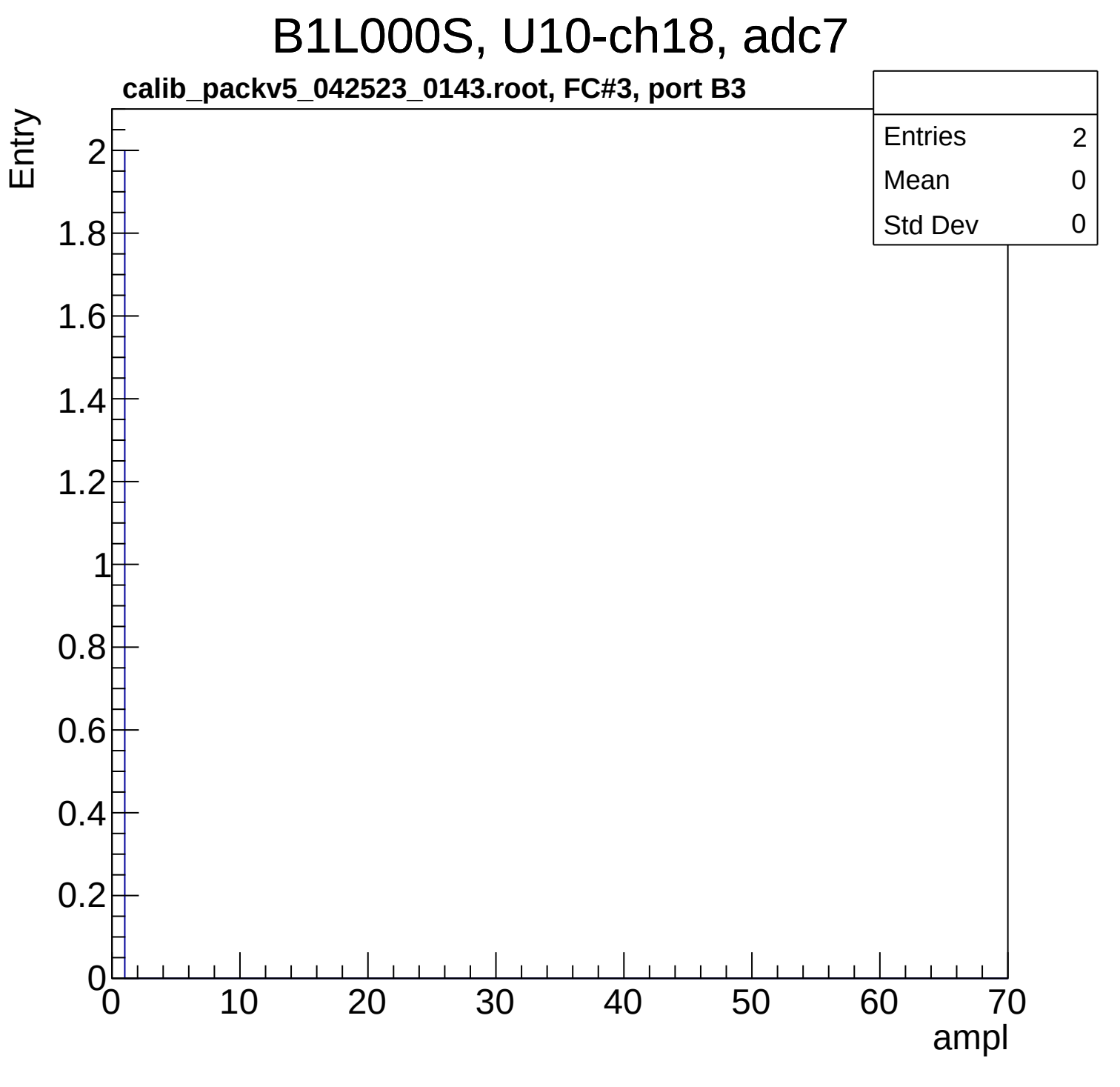
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch18, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
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0
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40
50
60
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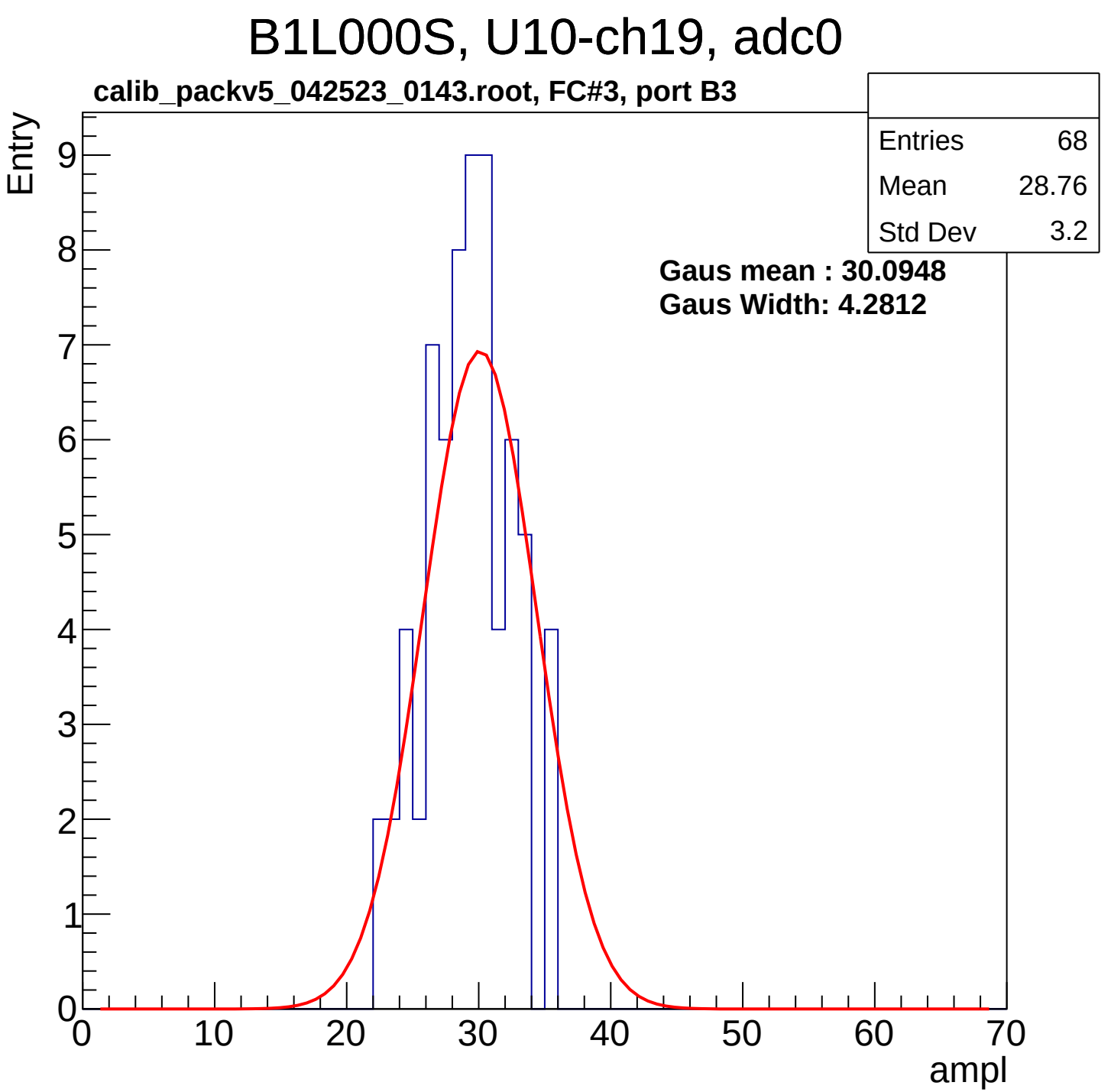
B1L000S, U10-ch19, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 28.76
Std Dev 3.2
Gaus mean : 30.0948
Gaus Width: 4.2812
Entry
ampl
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2
3
4
5
6
7
8
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0
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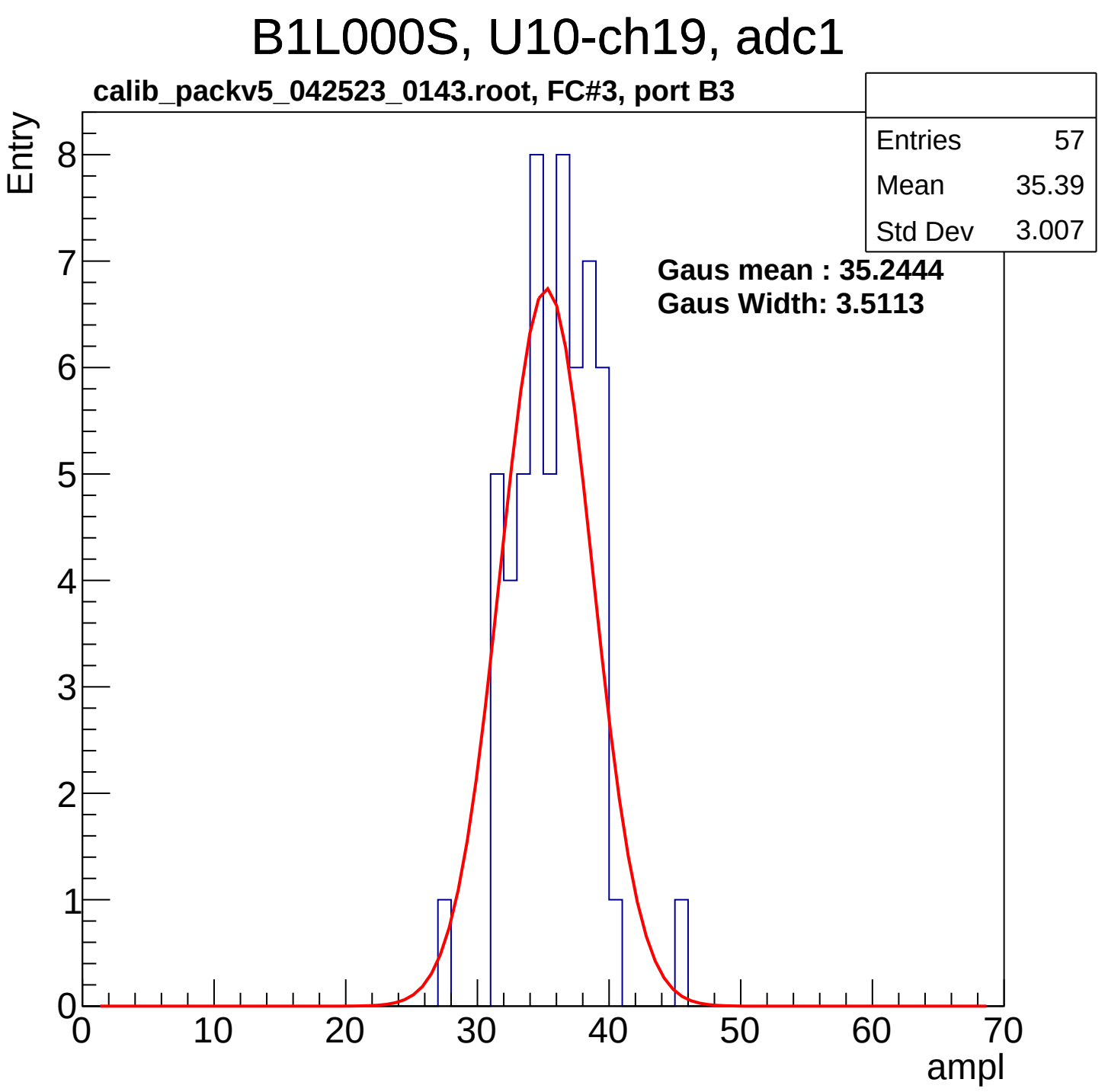
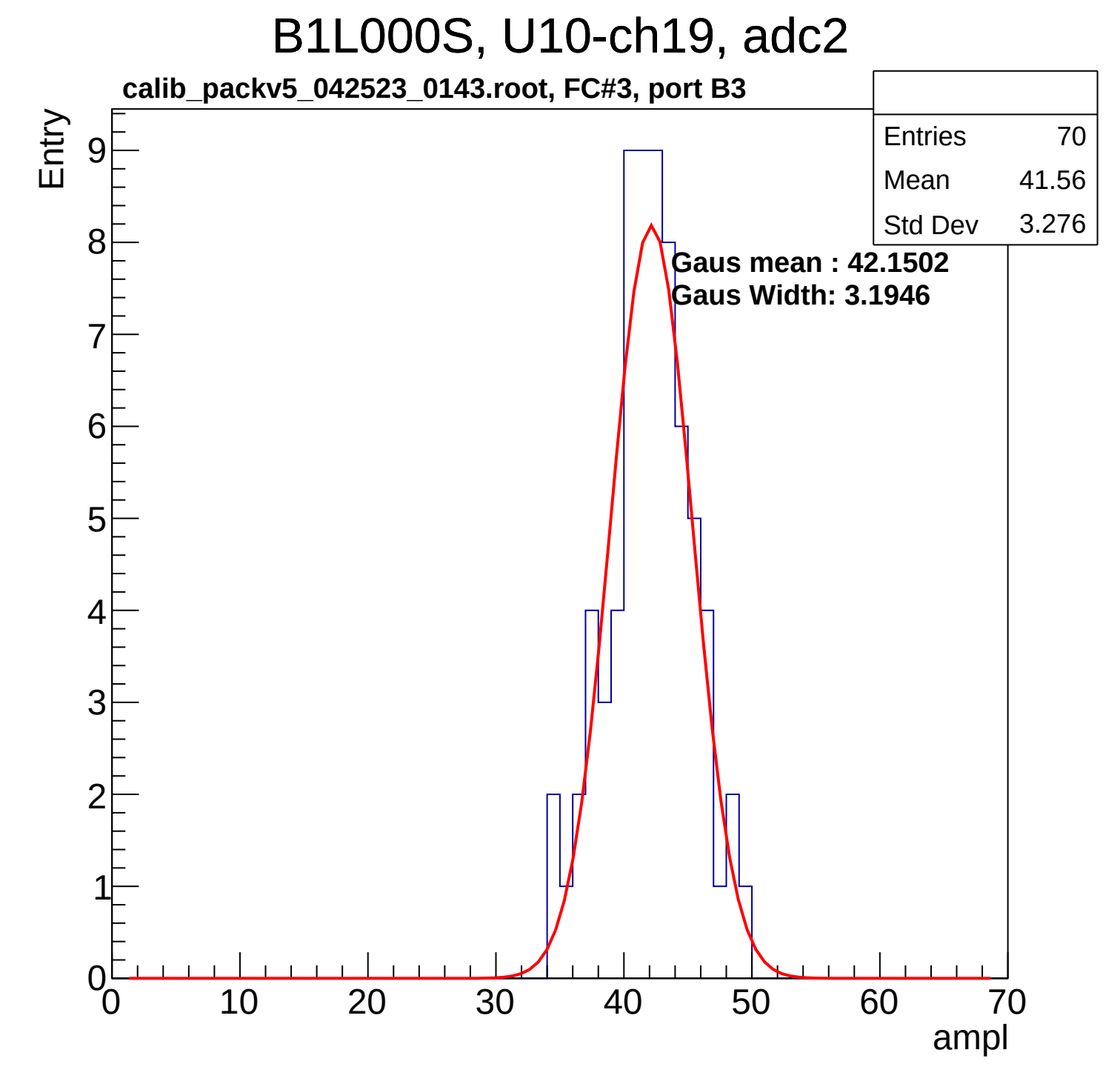
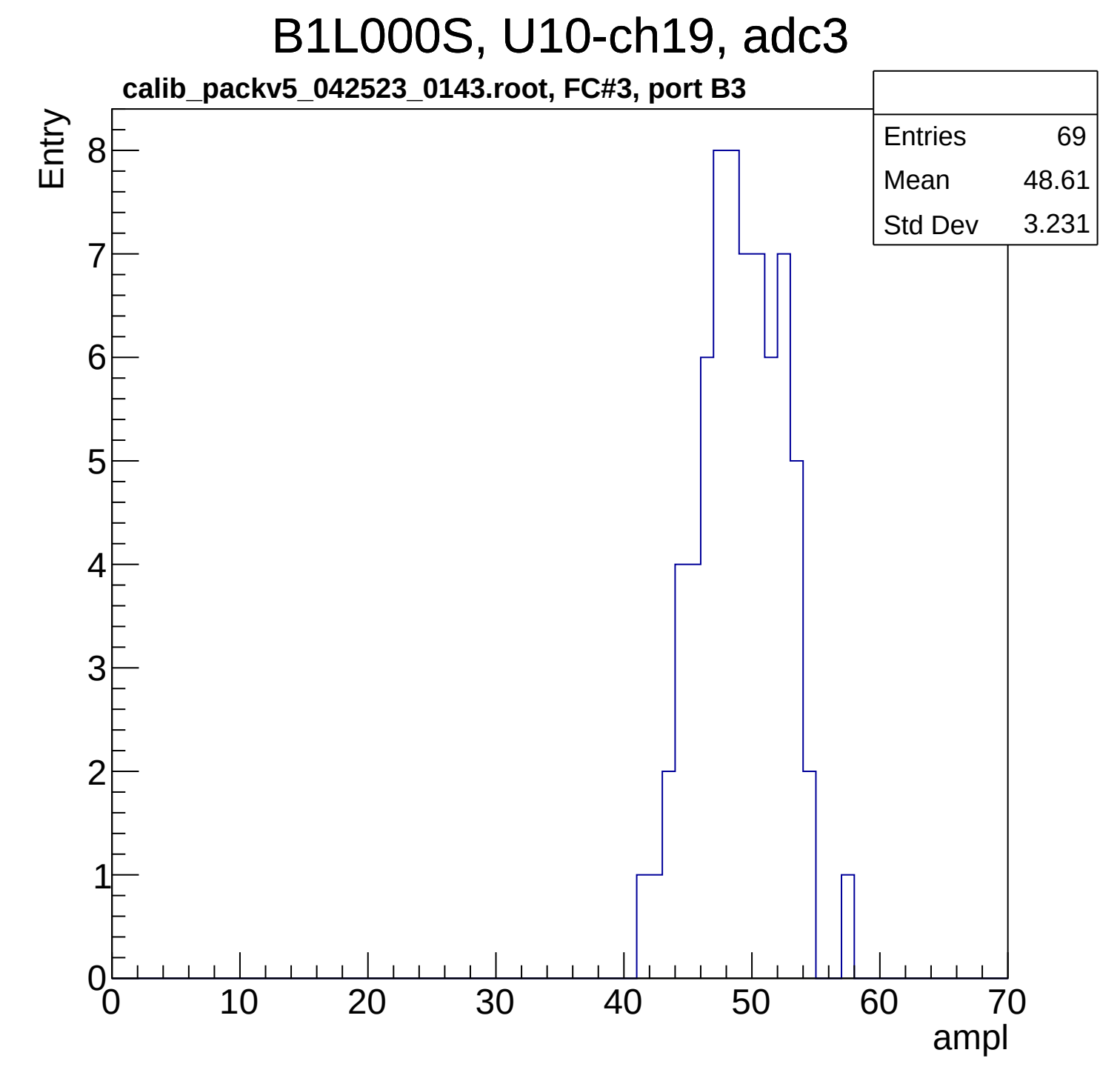

B1L000S, U10-ch19, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 35.39
Std Dev 3.007
Gaus mean : 35.2444
Gaus Width: 3.5113
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch19, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.56
Std Dev 3.276
Gaus mean : 42.1502
Gaus Width: 3.1946
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch19, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.61
Std Dev 3.231
Entry
ampl
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2
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5
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60
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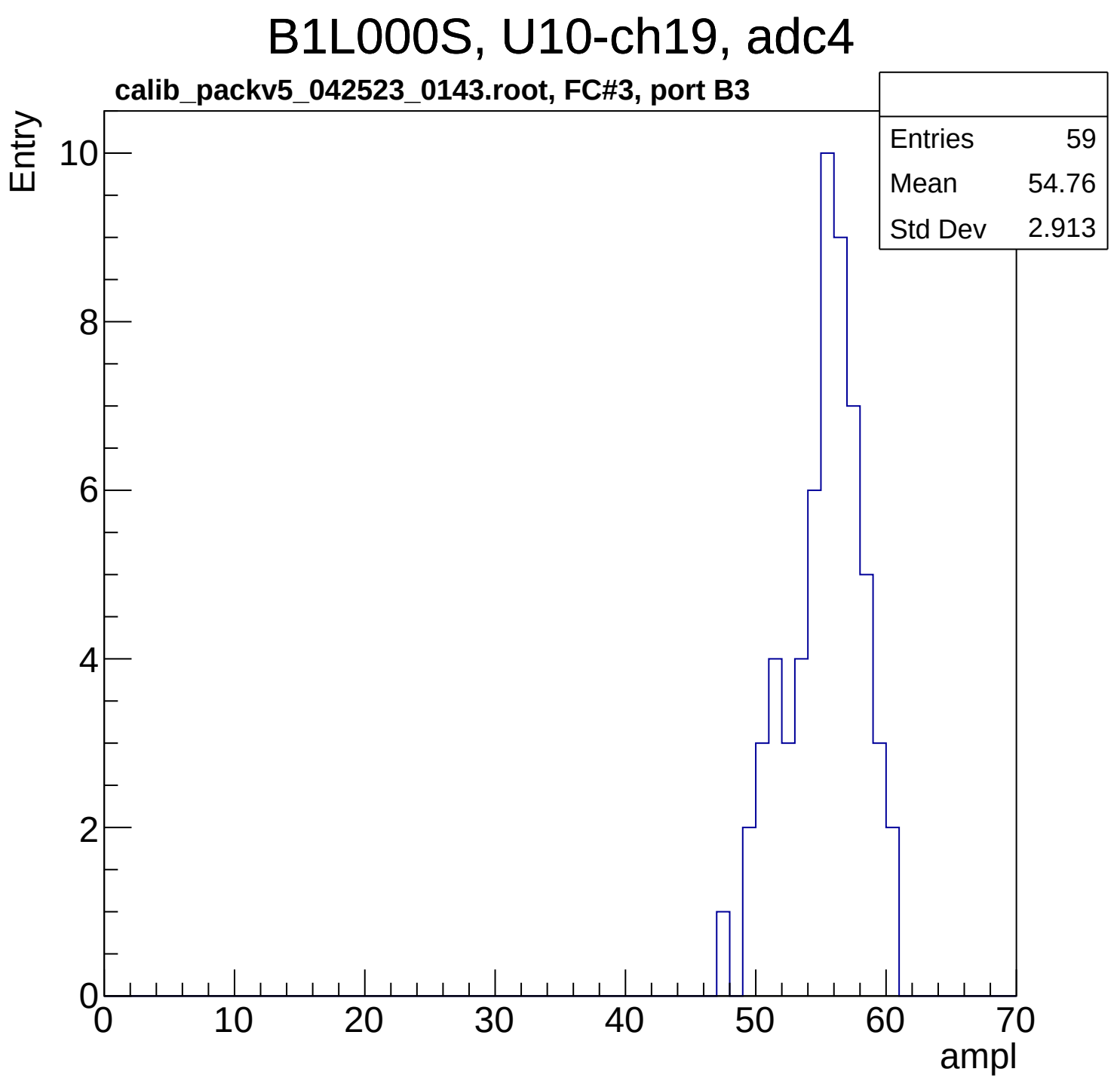
B1L000S, U10-ch19, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 54.76
Std Dev 2.913
Entry
ampl
0
2
4
6
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0
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40
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60
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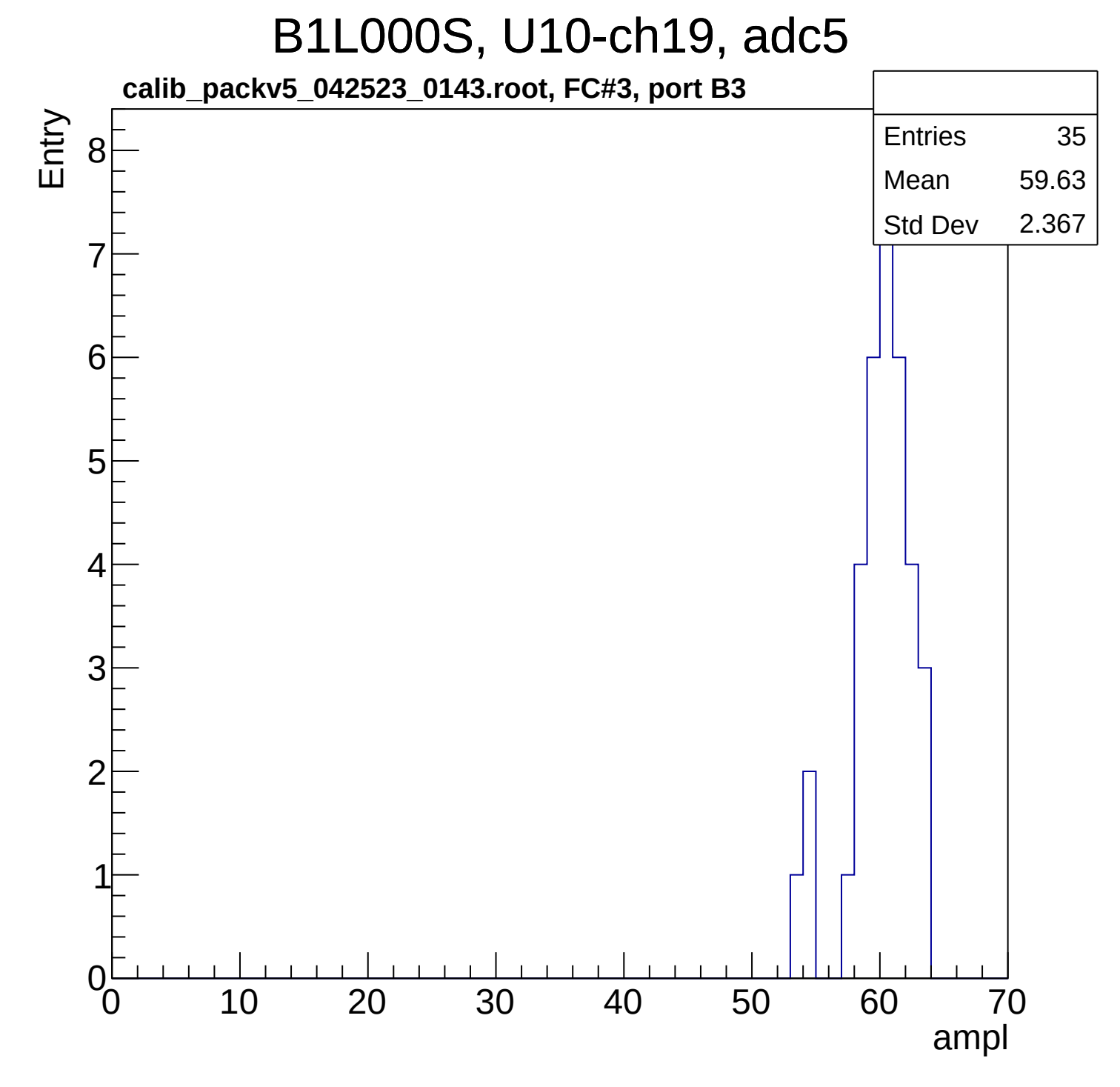
B1L000S, U10-ch19, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 35
Mean 59.63
Std Dev 2.367
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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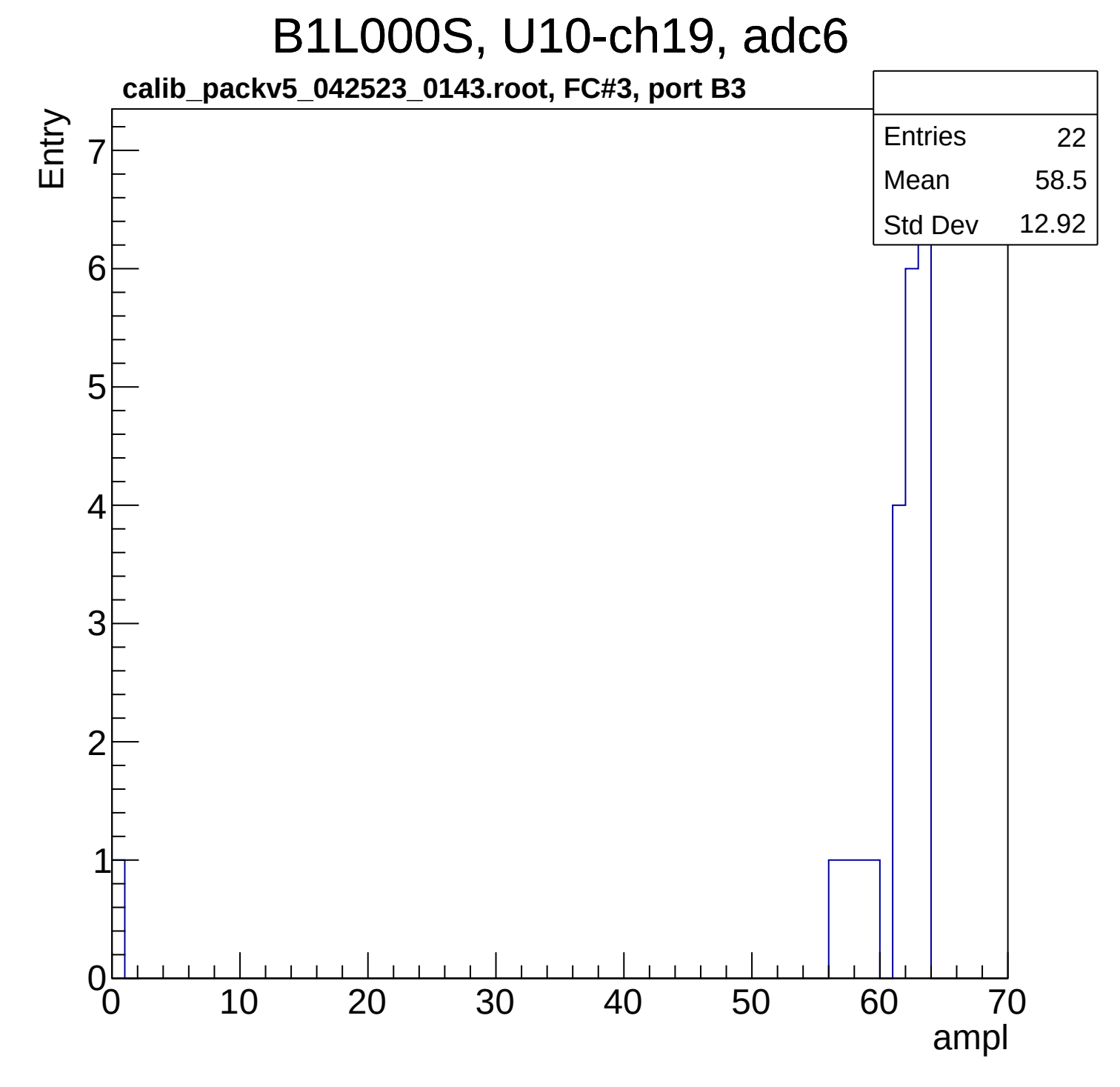
B1L000S, U10-ch19, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.5
Std Dev 12.92
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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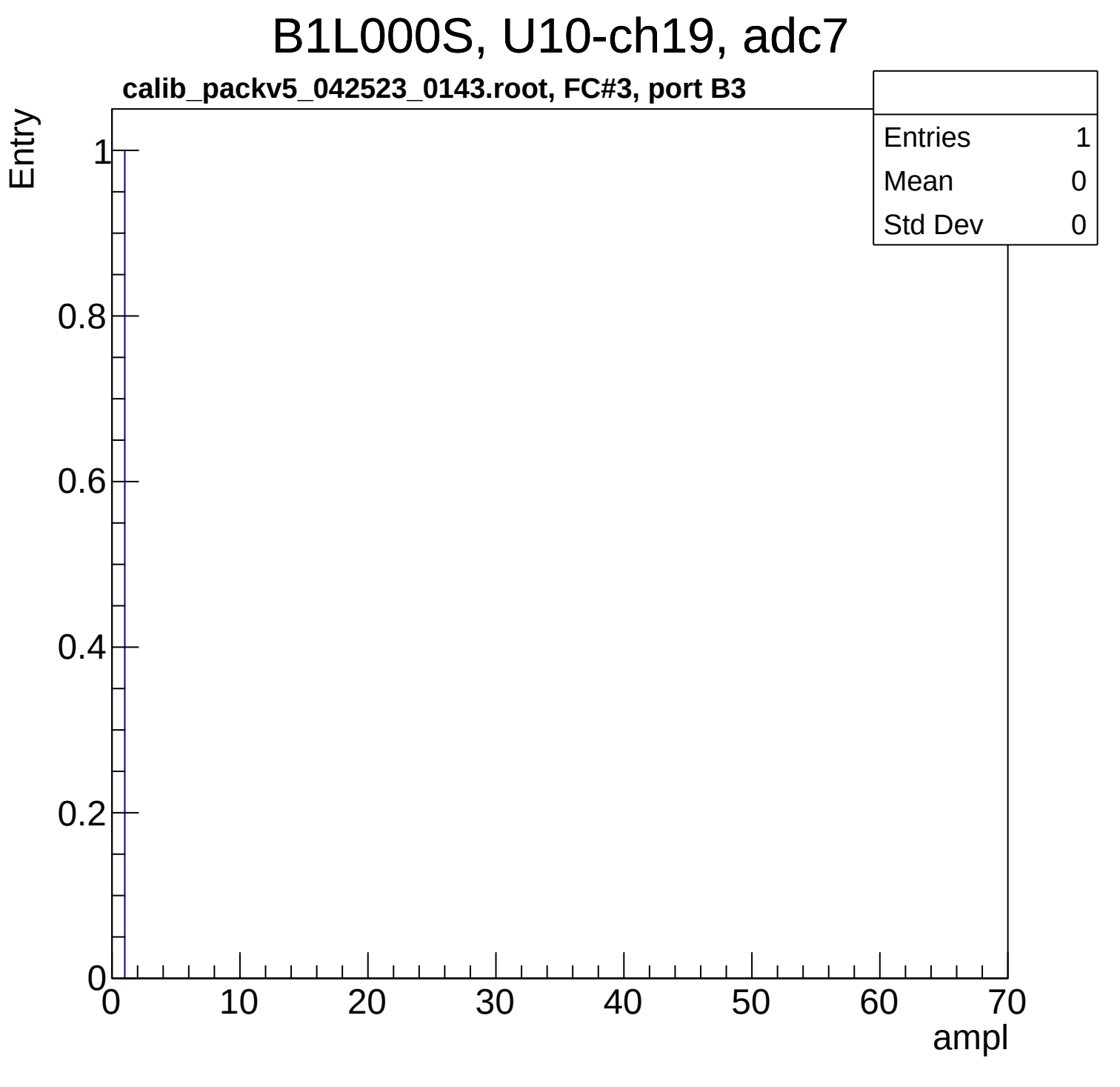
B1L000S, U10-ch19, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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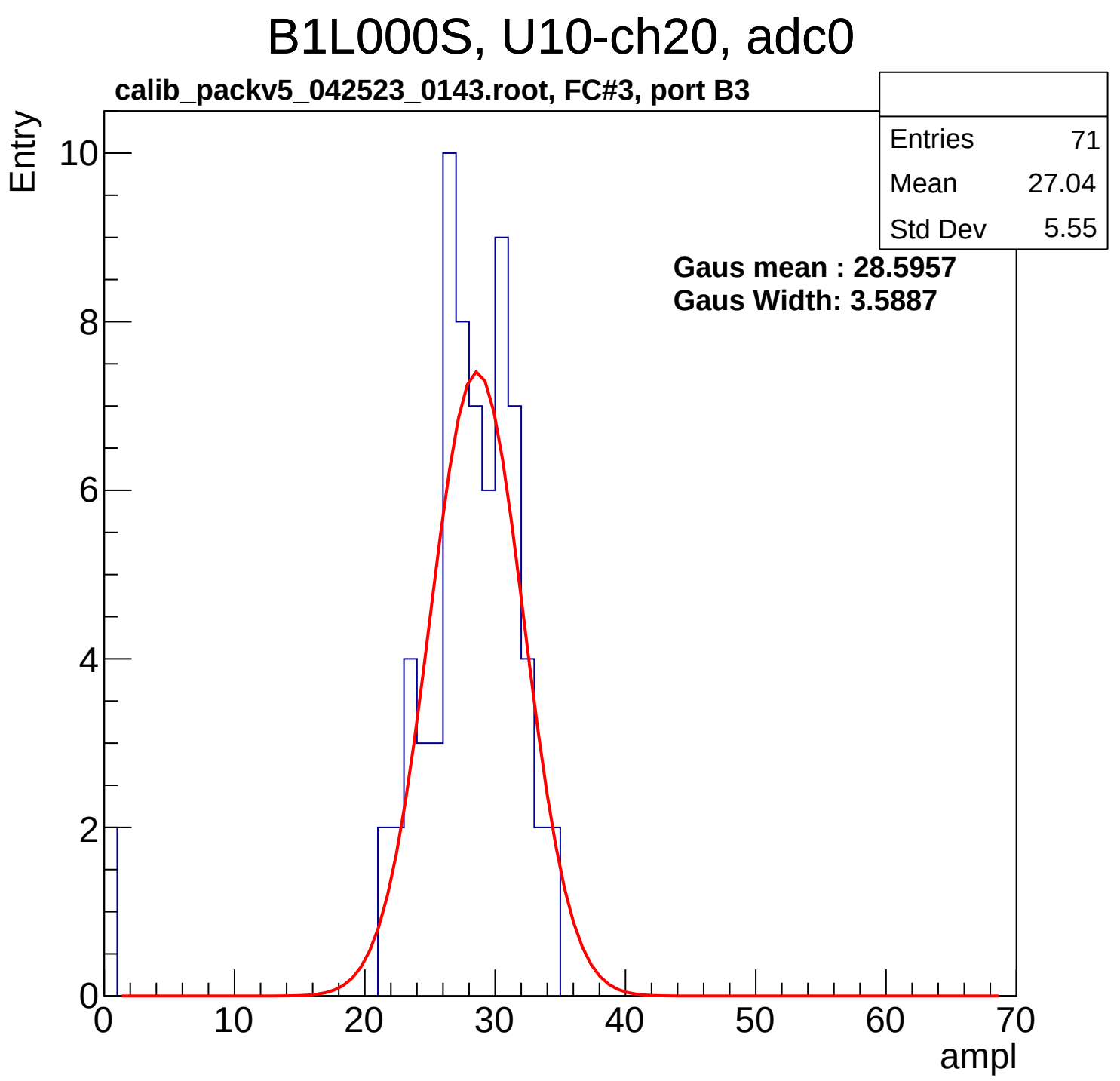
40
50
60
70
ampl


B1L000S, U10-ch20, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.04
Std Dev 5.55
Gaus mean : 28.5957
Gaus Width: 3.5887
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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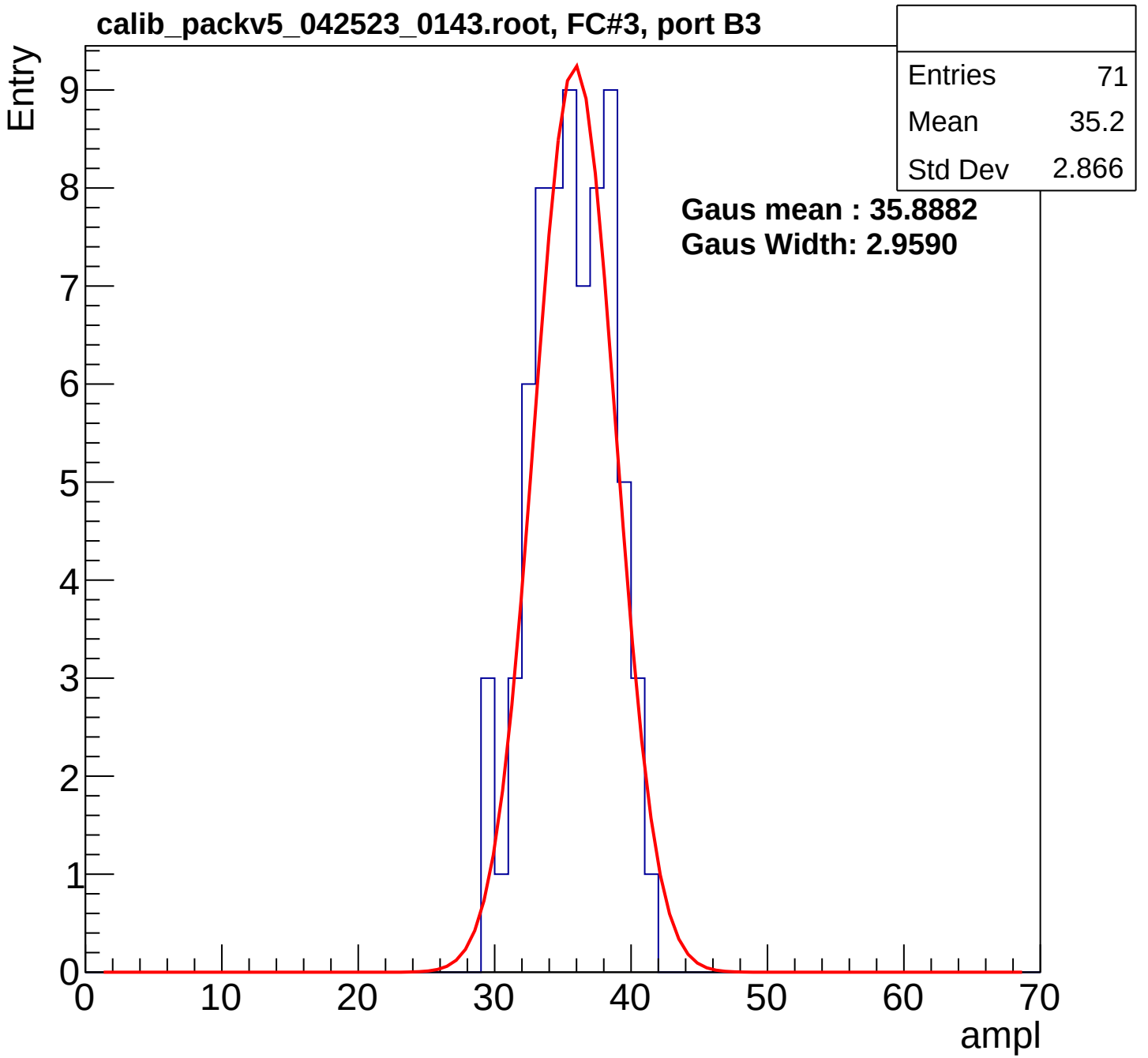
B1L000S, U10-ch20, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.2
Std Dev 2.866
Gaus mean : 35.8882
Gaus Width: 2.9590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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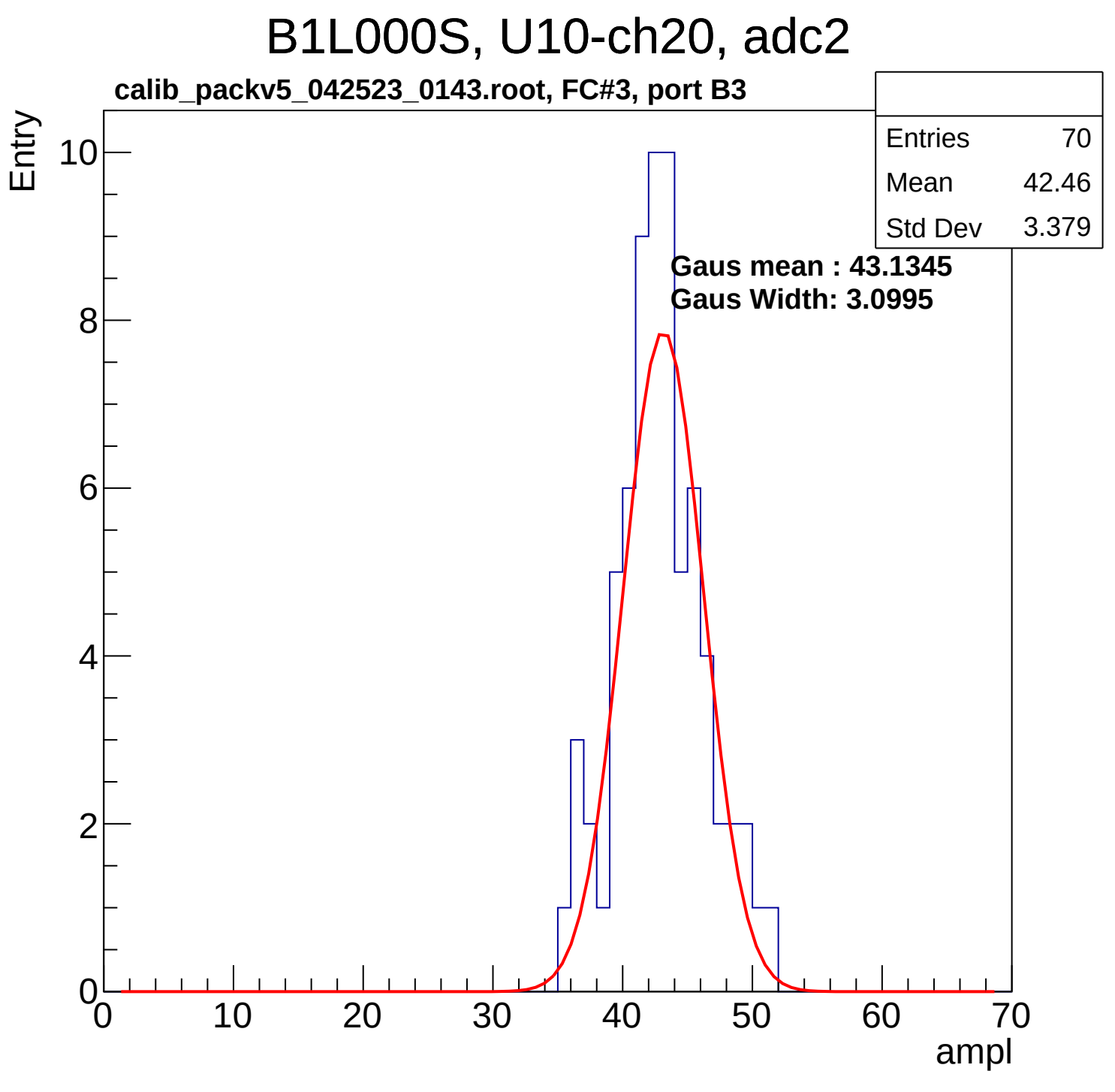
B1L000S, U10-ch20, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.46
Std Dev 3.379
Gaus mean : 43.1345
Gaus Width: 3.0995
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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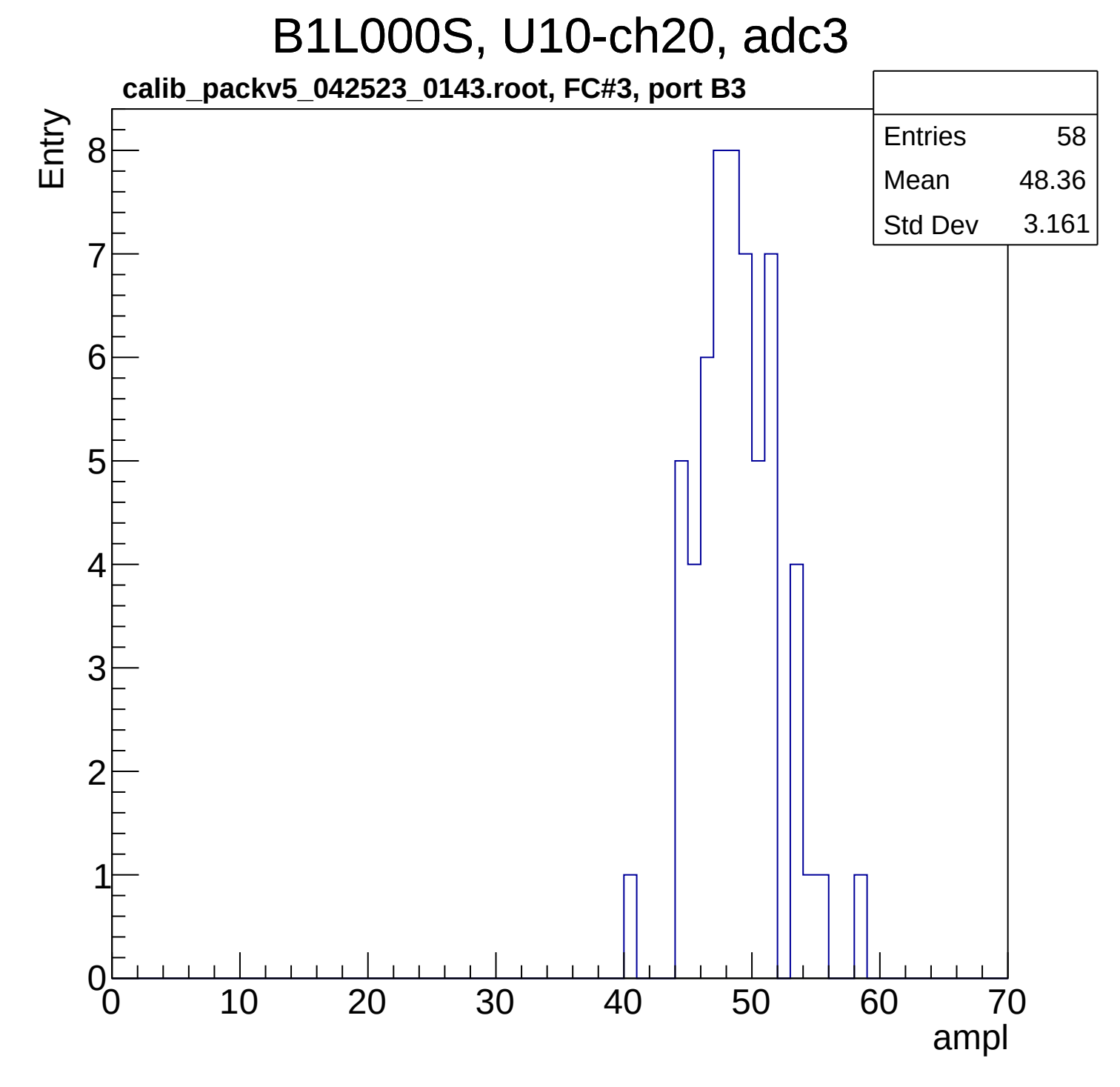
B1L000S, U10-ch20, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 48.36
Std Dev 3.161
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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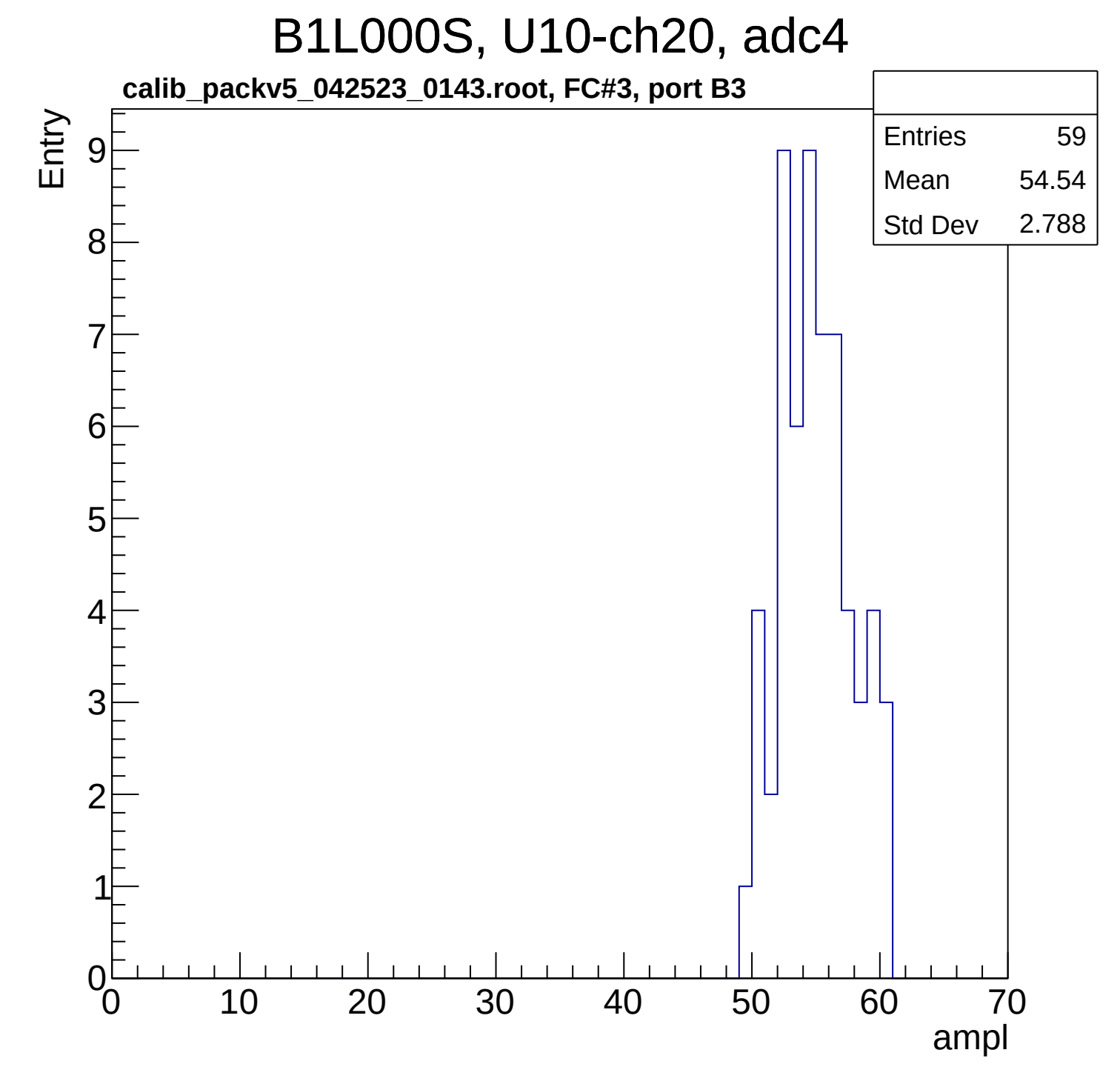
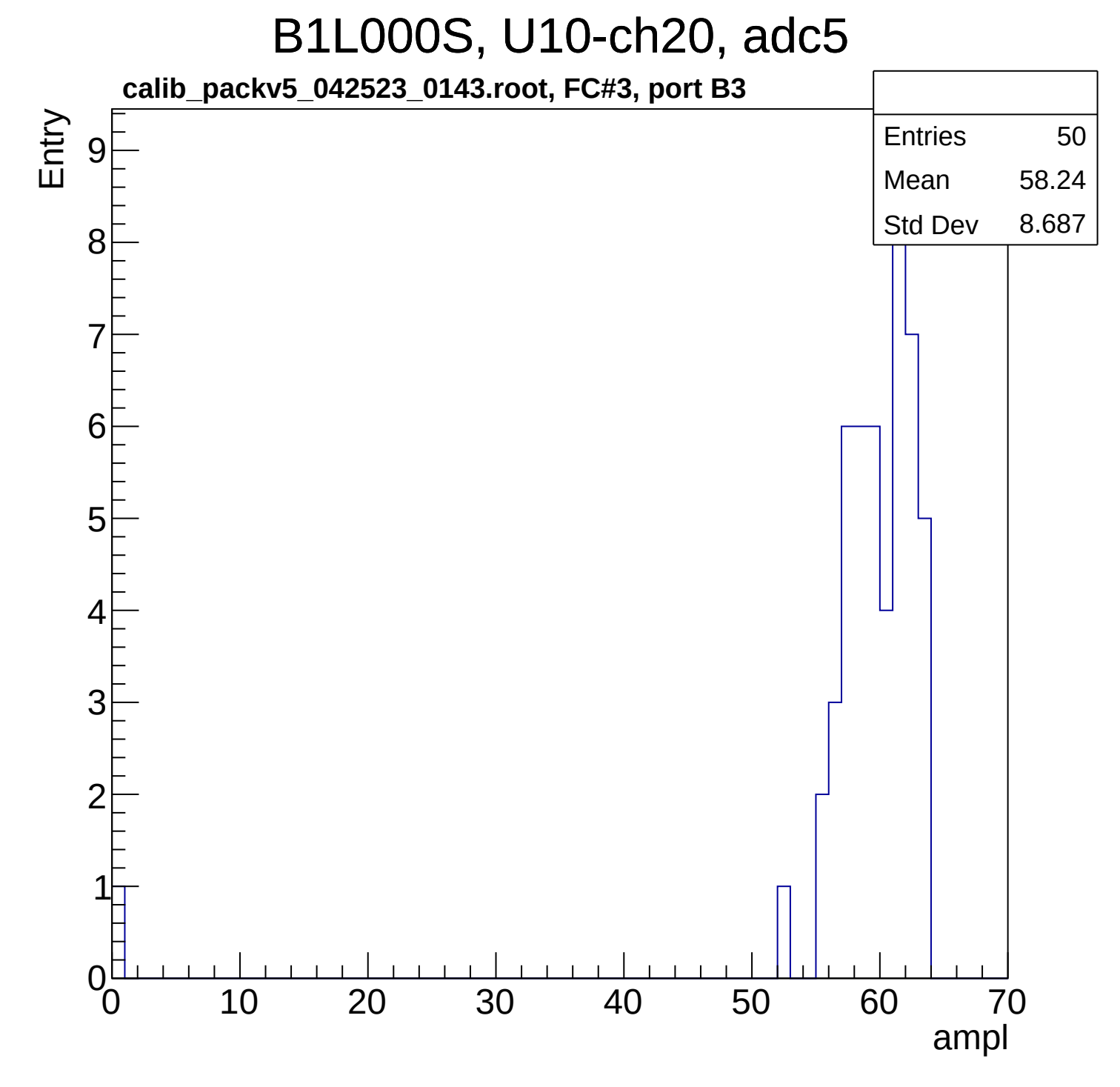

B1L000S, U10-ch20, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 54.54
Std Dev 2.788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch20, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.24
Std Dev 8.687
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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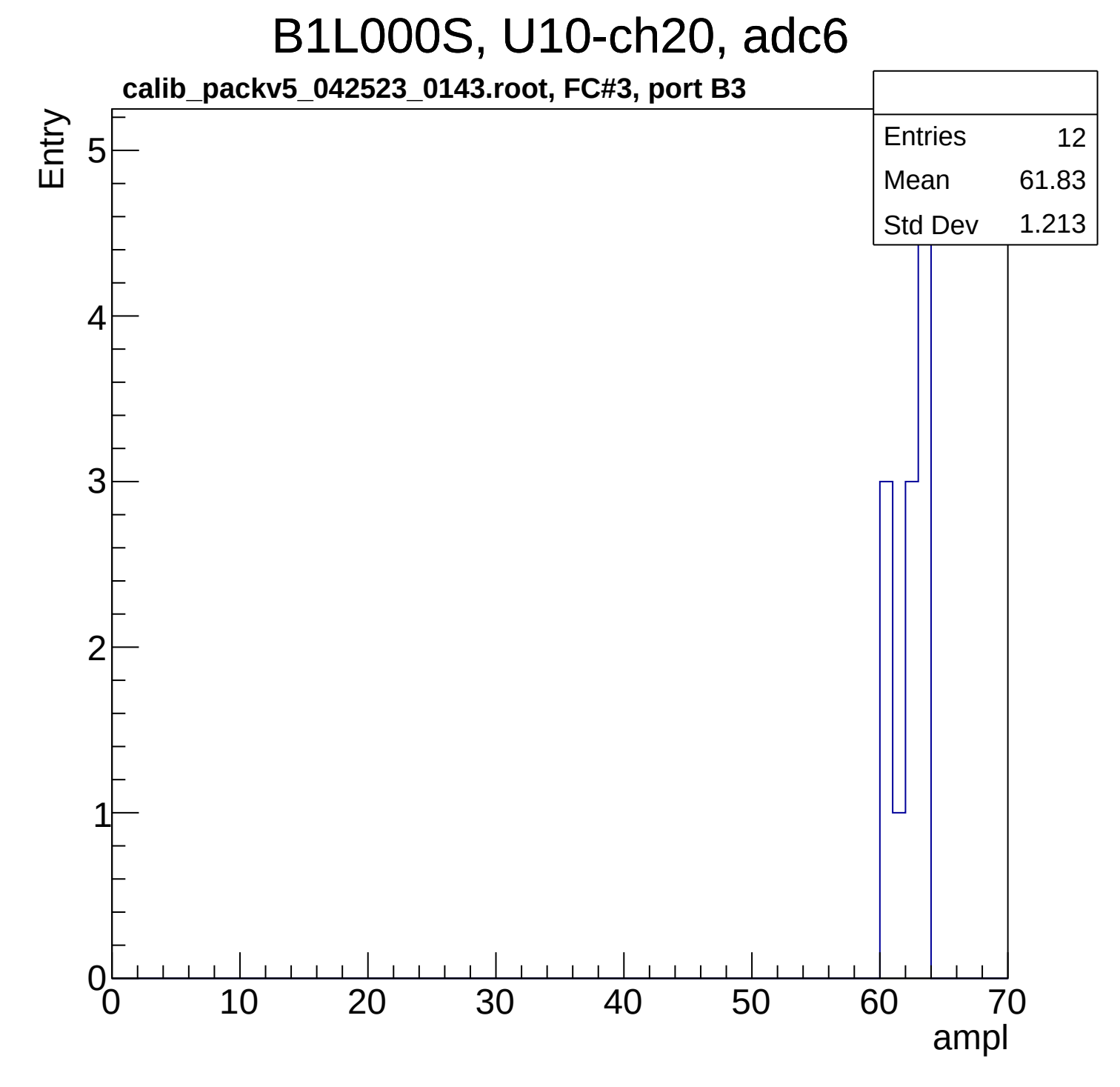
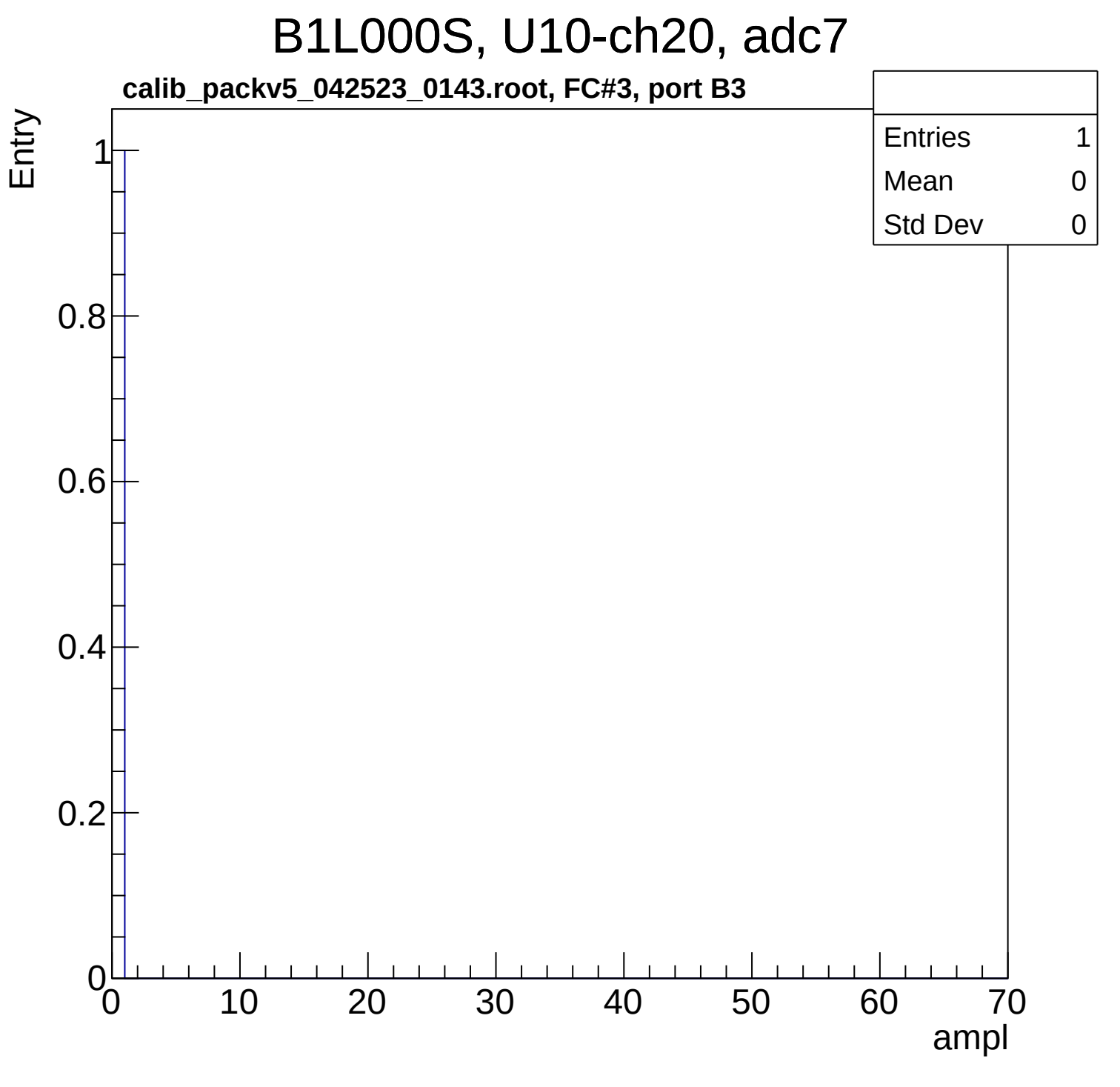

B1L000S, U10-ch20, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.83
Std Dev 1.213
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch20, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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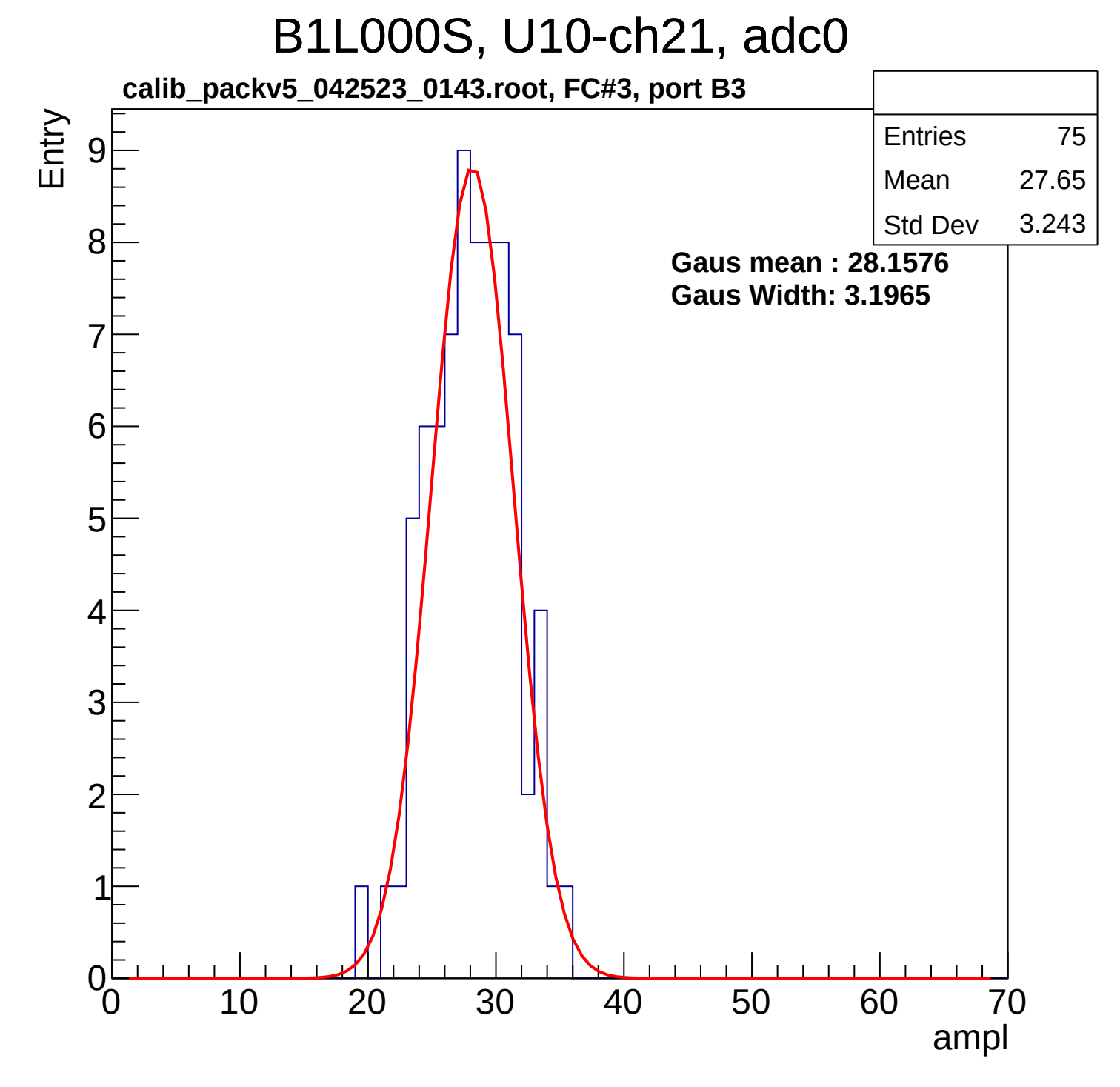
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch21, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 27.65
Std Dev 3.243
Gaus mean : 28.1576
Gaus Width: 3.1965
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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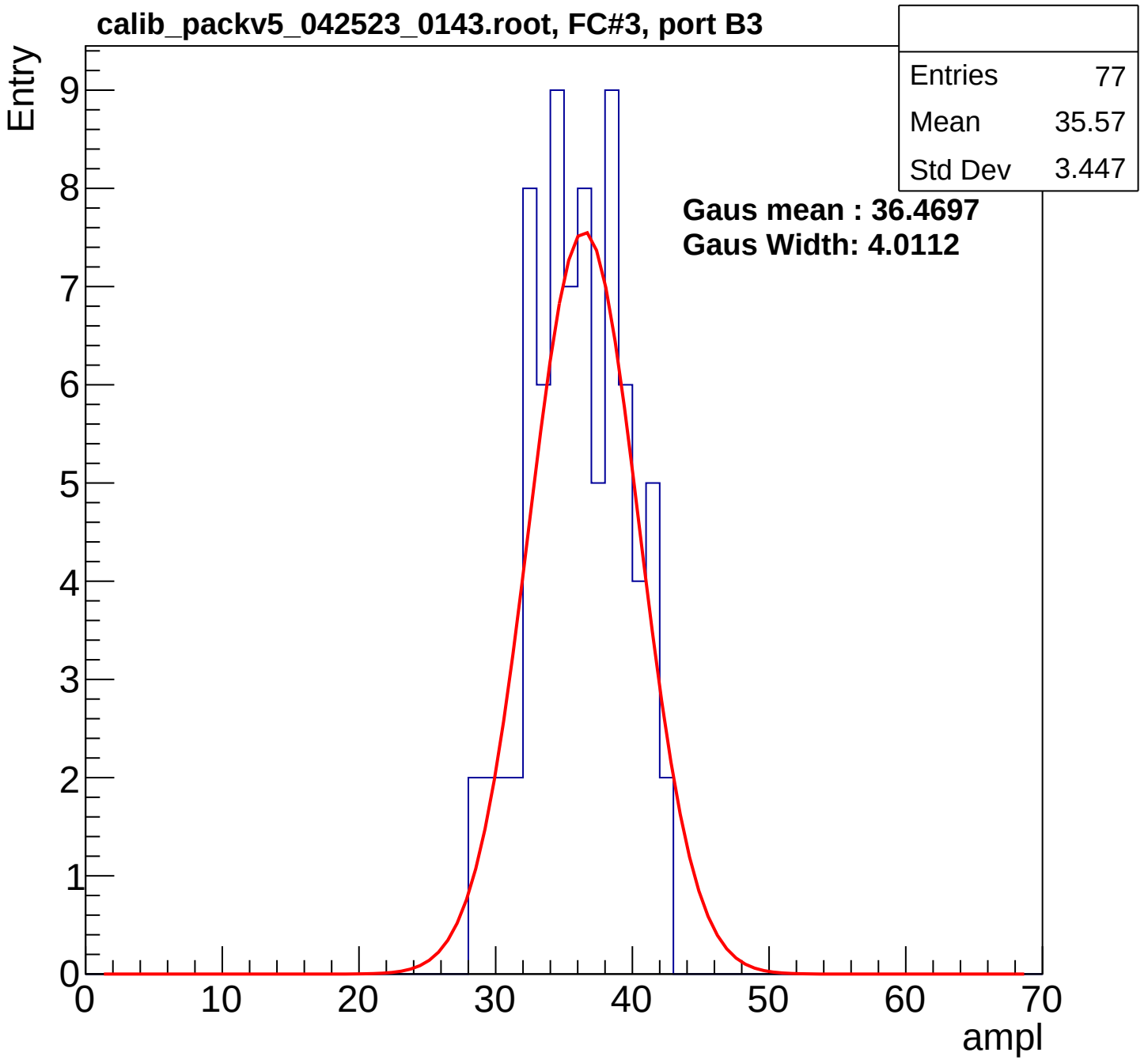
B1L000S, U10-ch21, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 35.57
Std Dev 3.447
Gaus mean : 36.4697
Gaus Width: 4.0112
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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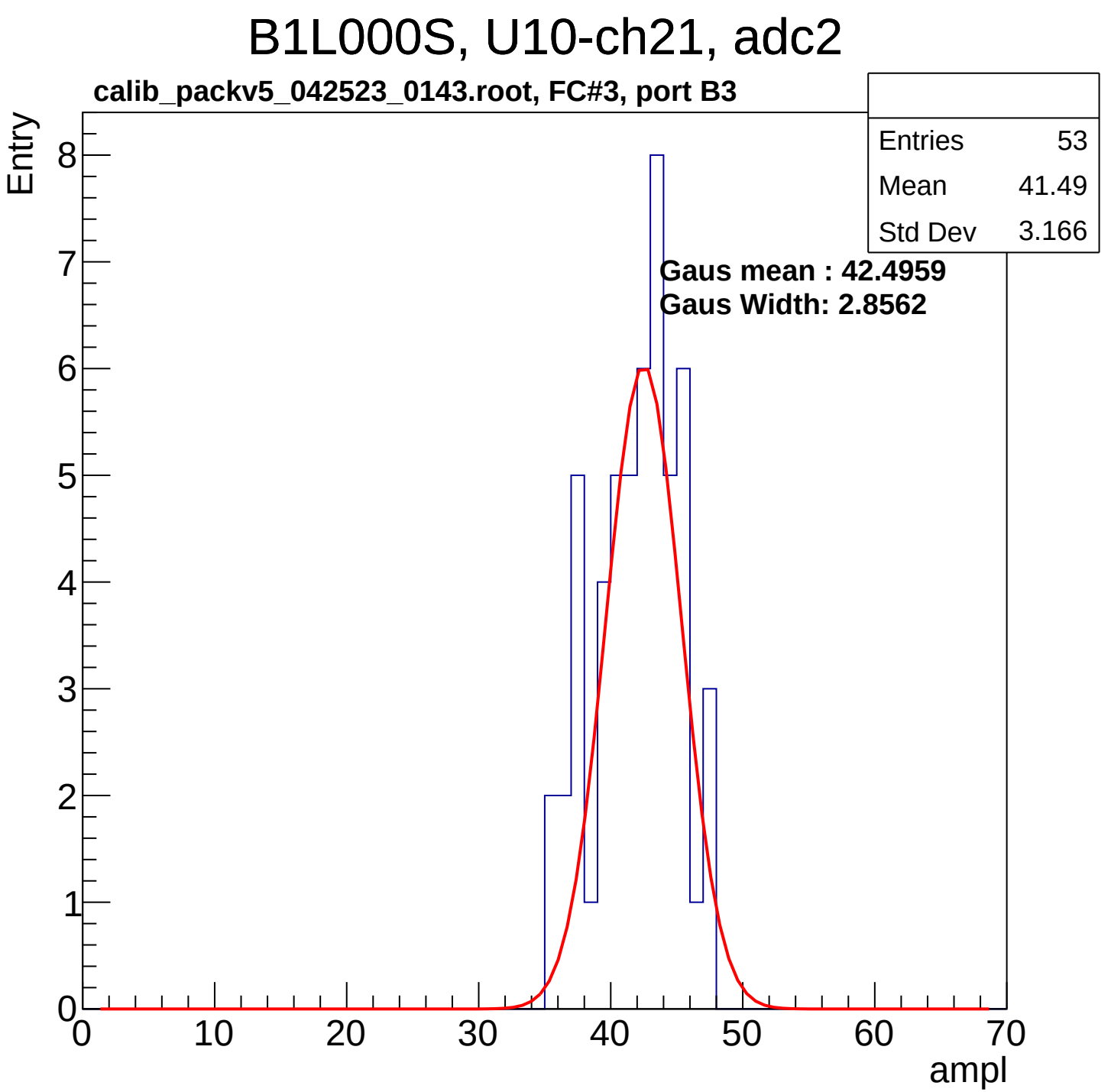
B1L000S, U10-ch21, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 41.49
Std Dev 3.166
Gaus mean : 42.4959
Gaus Width: 2.8562
Entry
ampl
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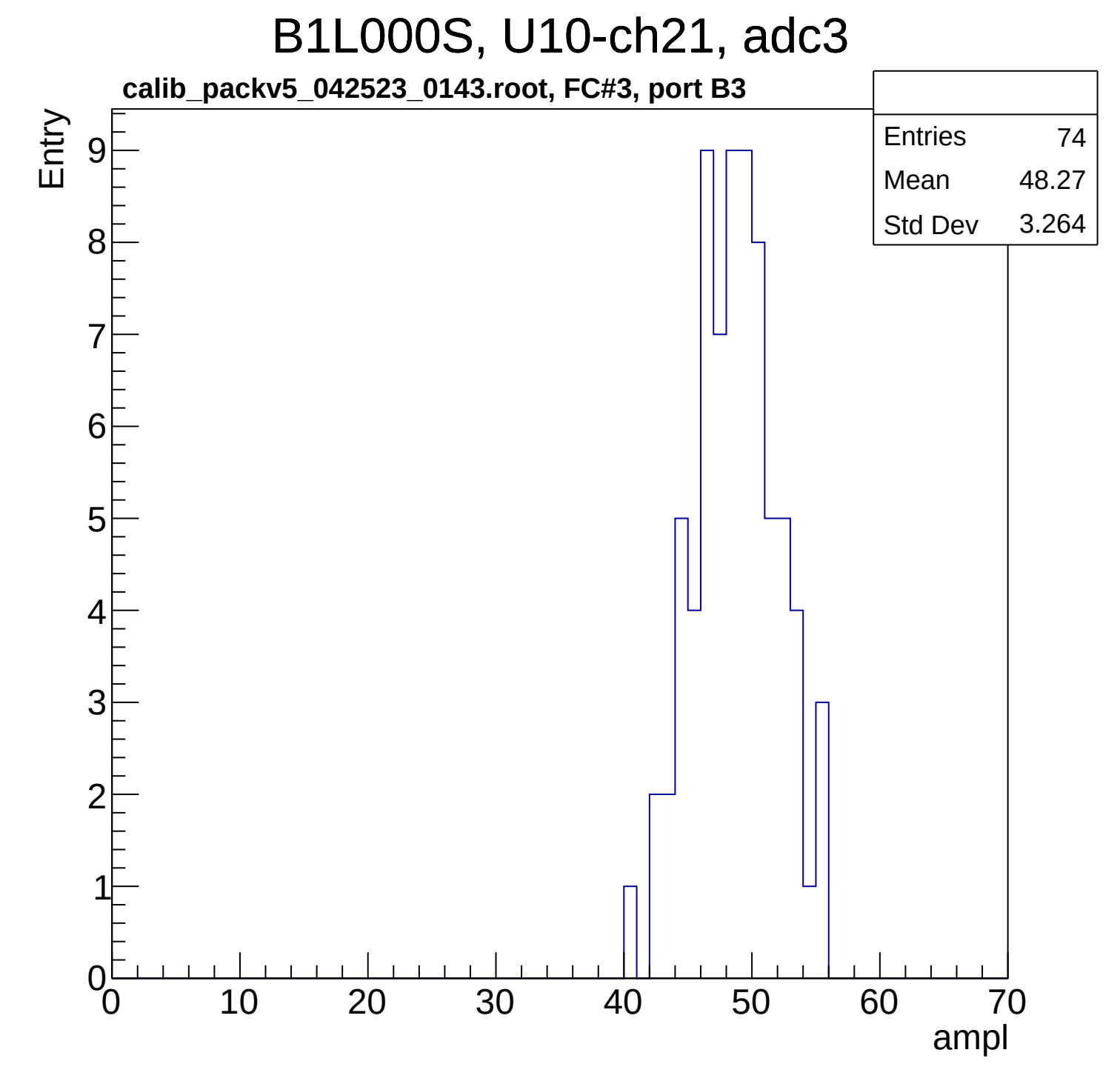
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch21, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 48.27
Std Dev 3.264
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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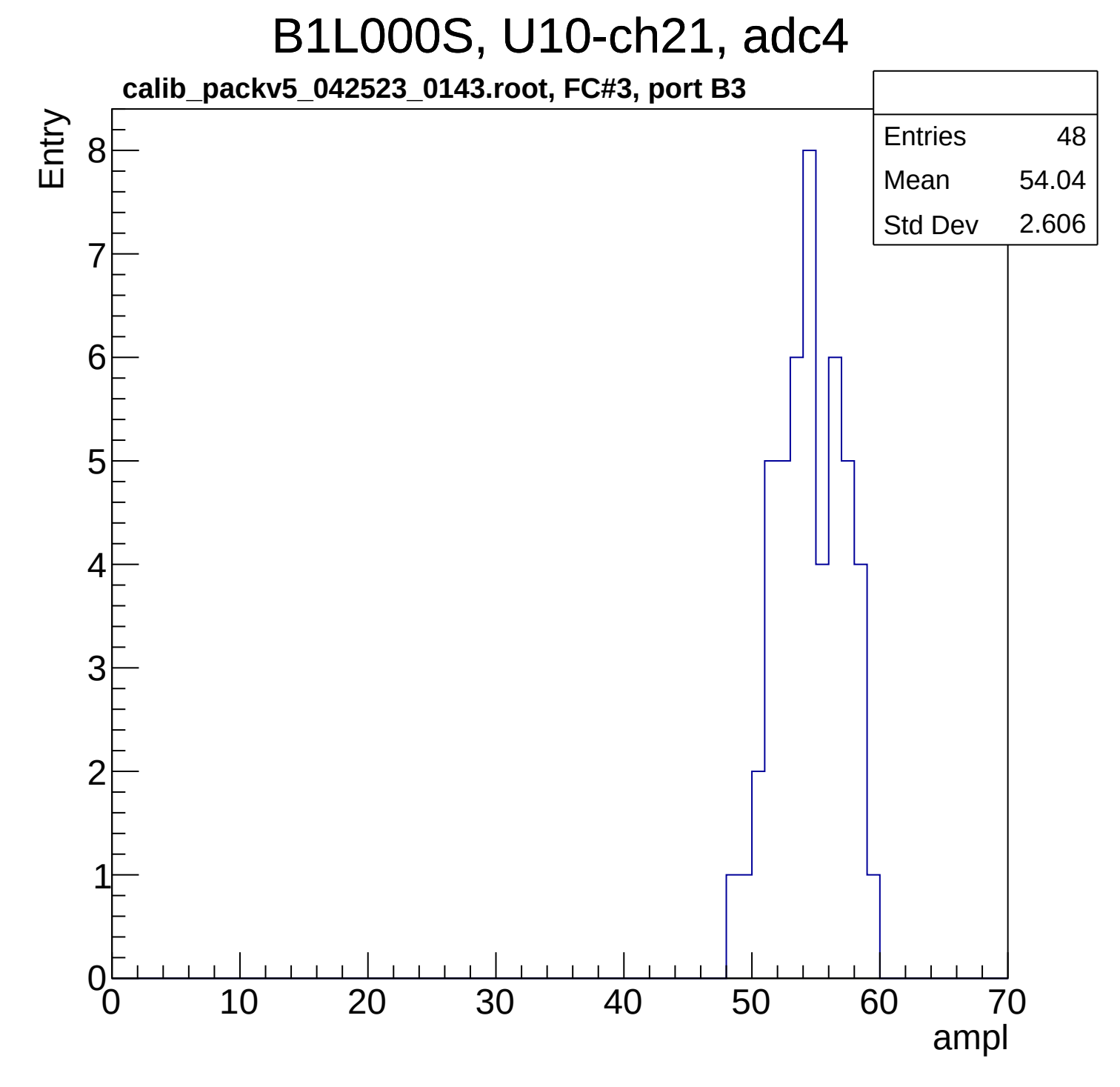
B1L000S, U10-ch21, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 54.04
Std Dev 2.606
Entry
ampl
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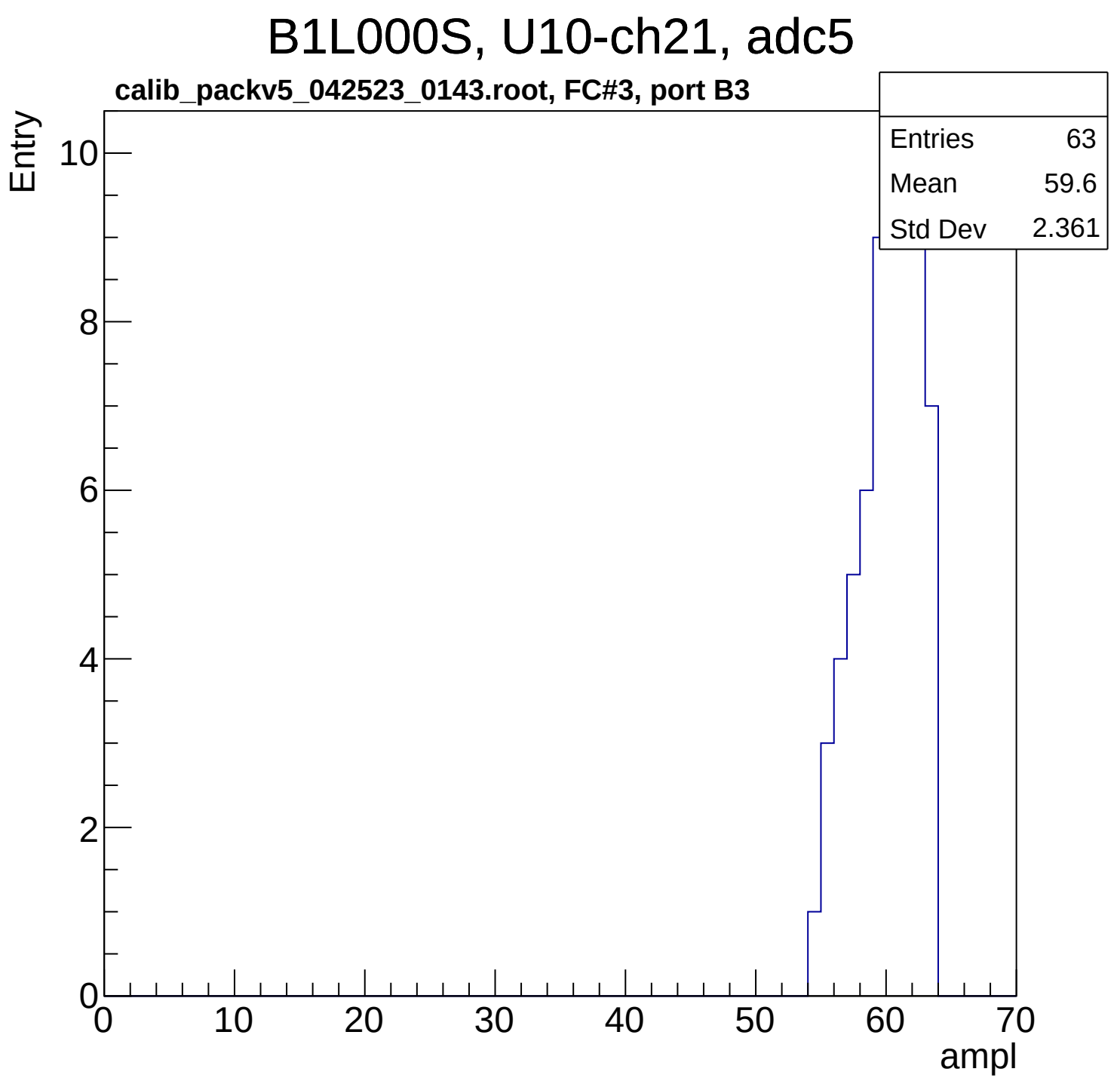
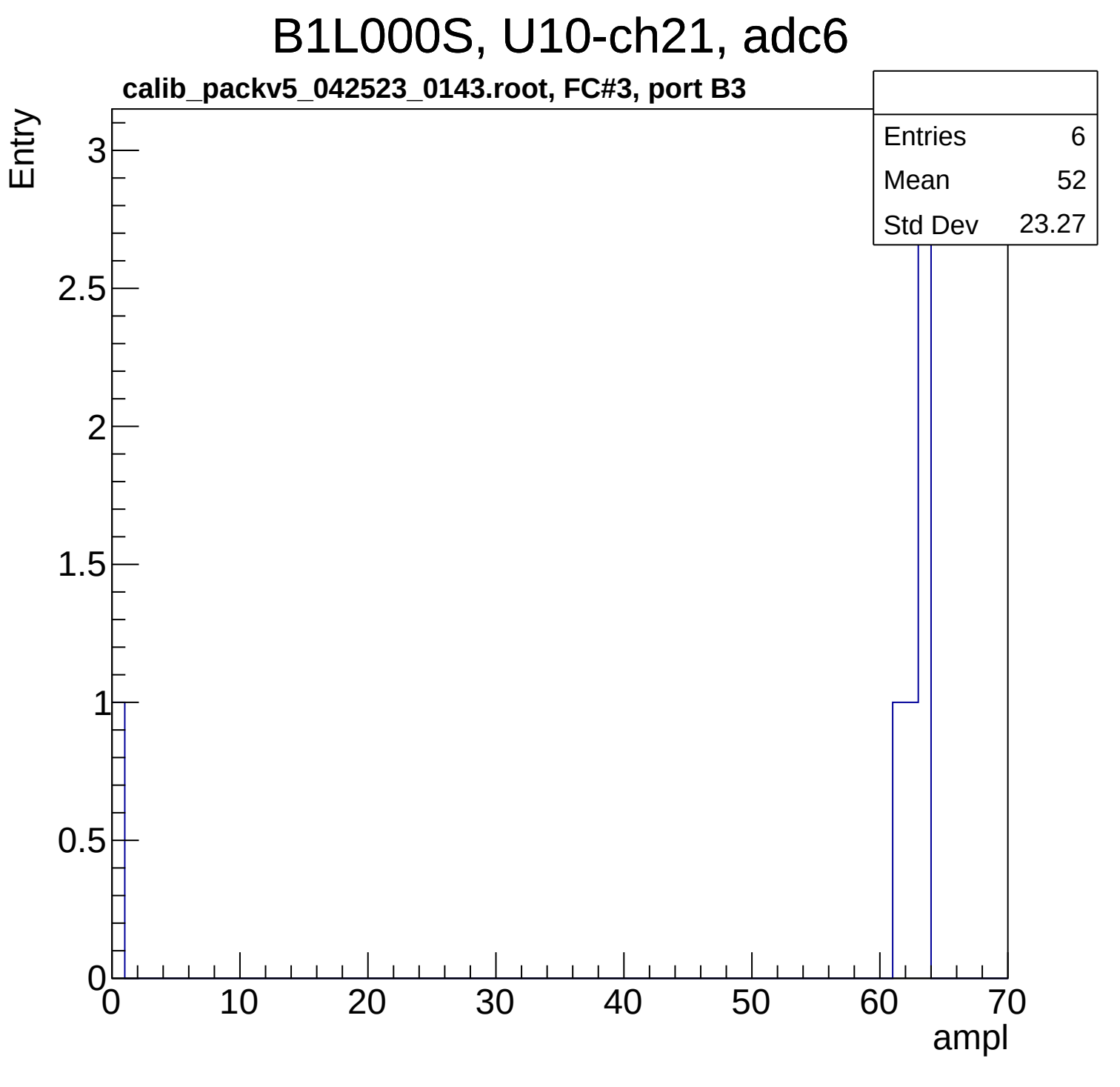
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch21, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 59.6
Std Dev 2.361
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch21, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 52
Std Dev 23.27
Entry
ampl
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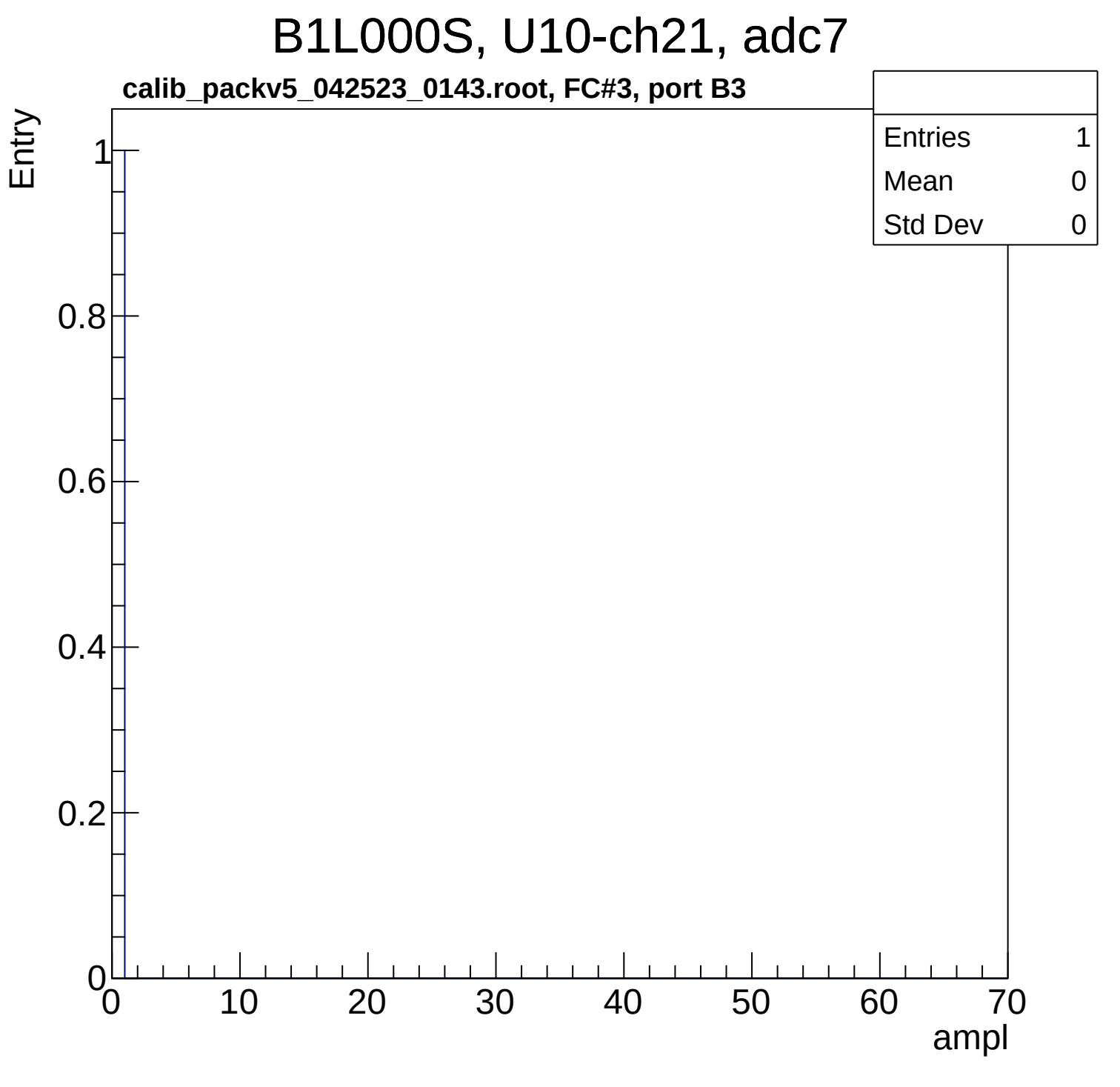
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch21, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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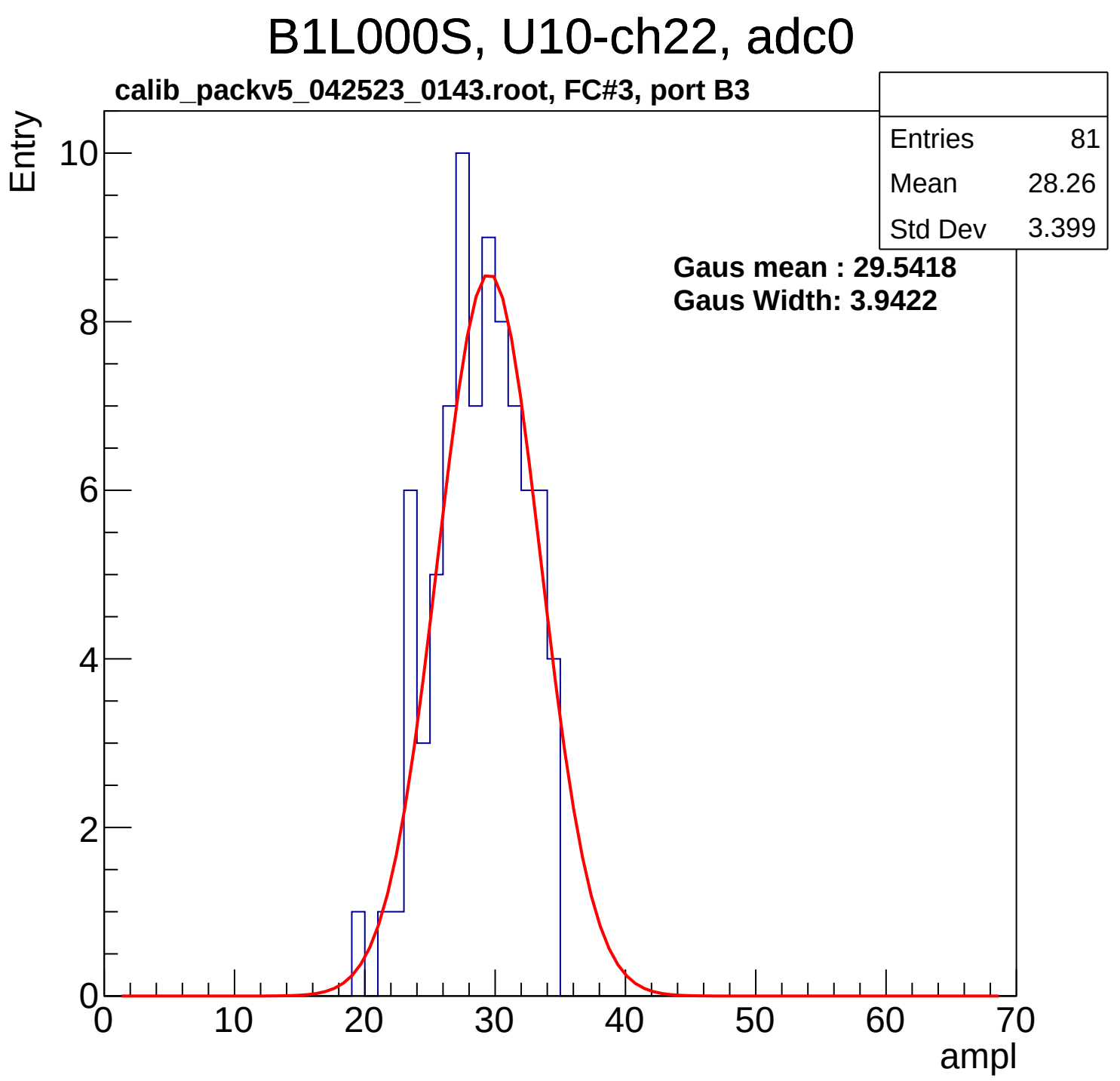
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch22, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 28.26
Std Dev 3.399
Gaus mean : 29.5418
Gaus Width: 3.9422
Entry
ampl
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2
4
6
8
10
0
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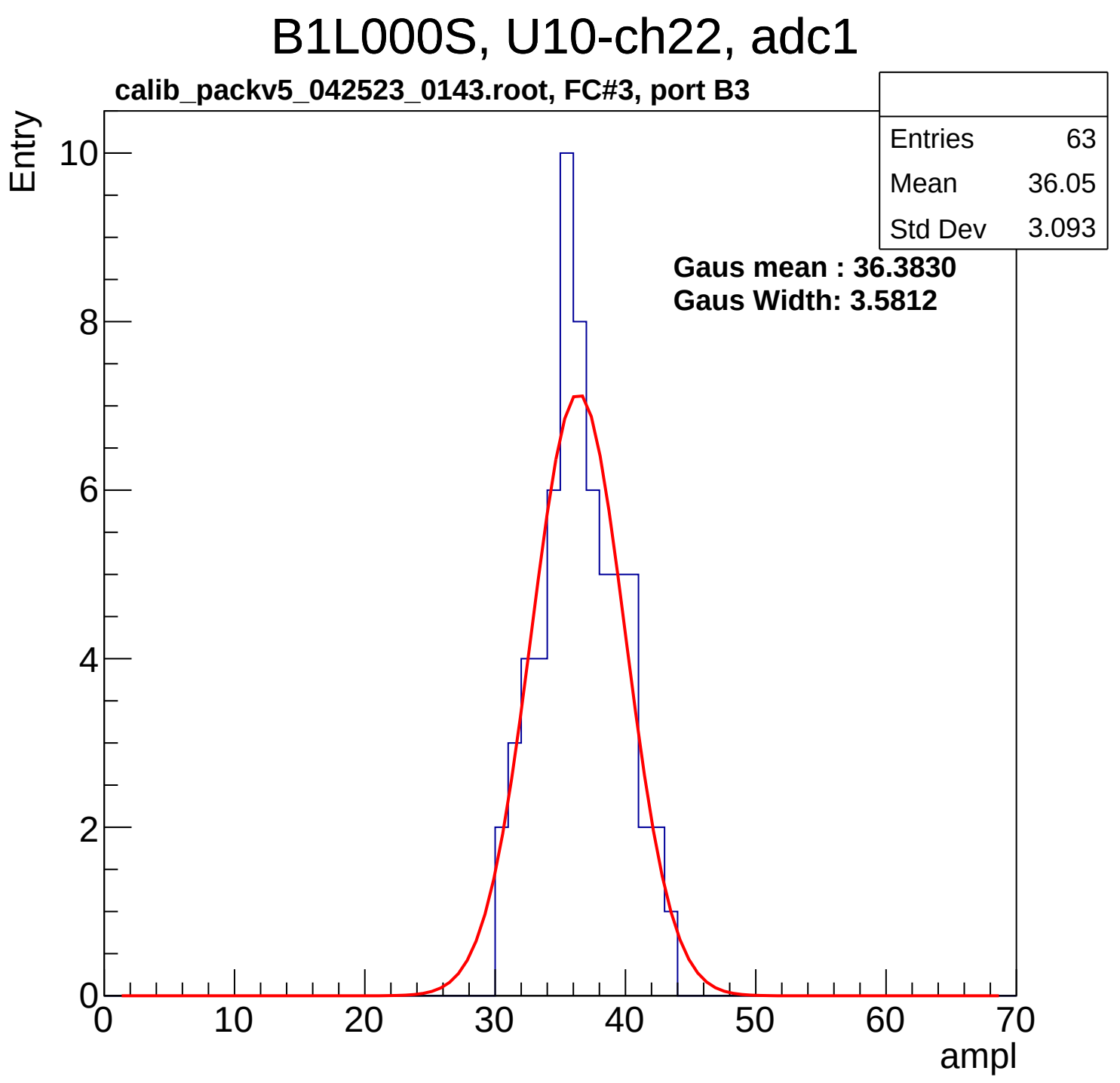
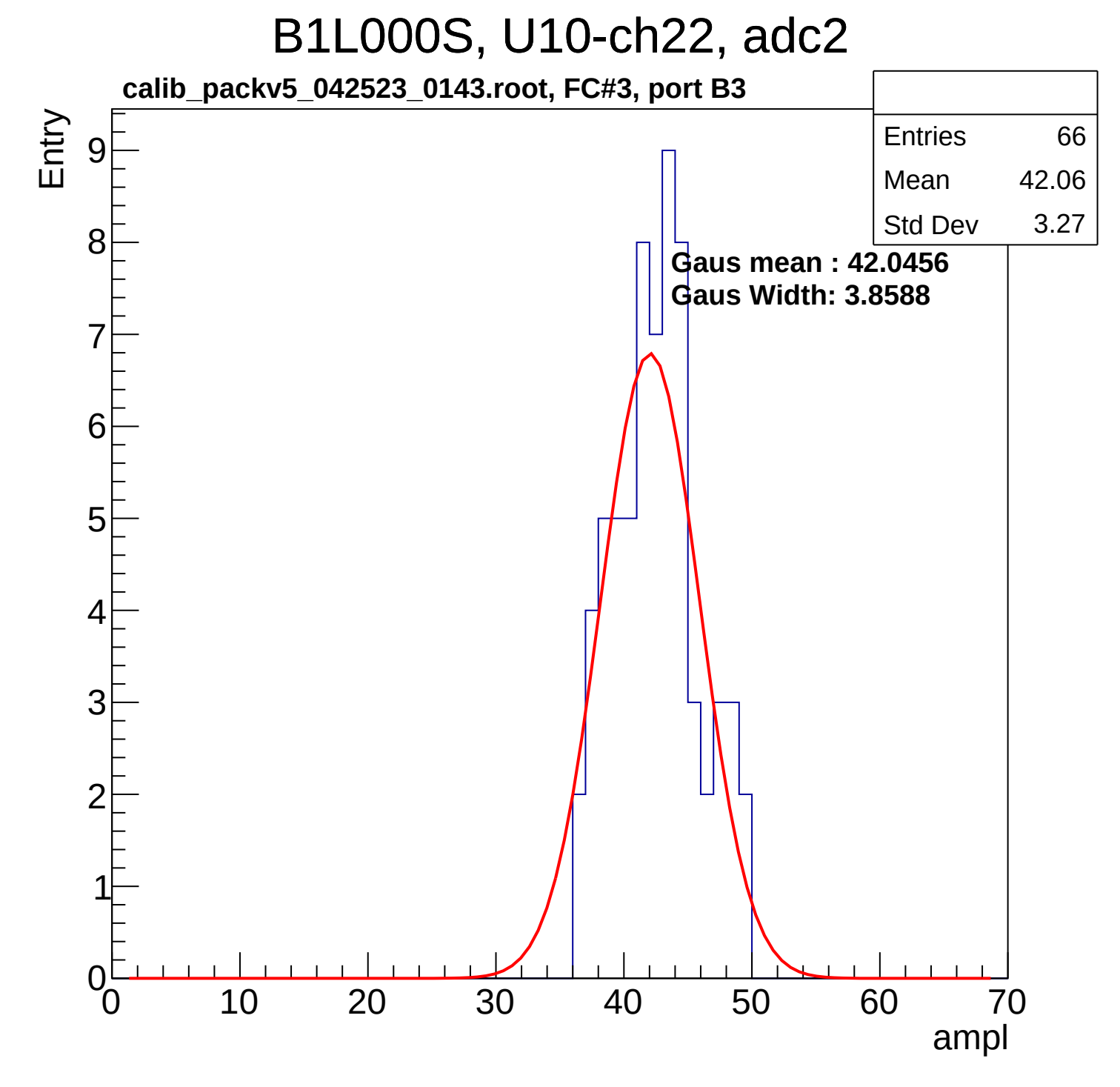
20
30
40
50
60
70


B1L000S, U10-ch22, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 36.05
Std Dev 3.093
Gaus mean : 36.3830
Gaus Width: 3.5812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch22, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 42.06
Std Dev 3.27
Gaus mean : 42.0456
Gaus Width: 3.8588
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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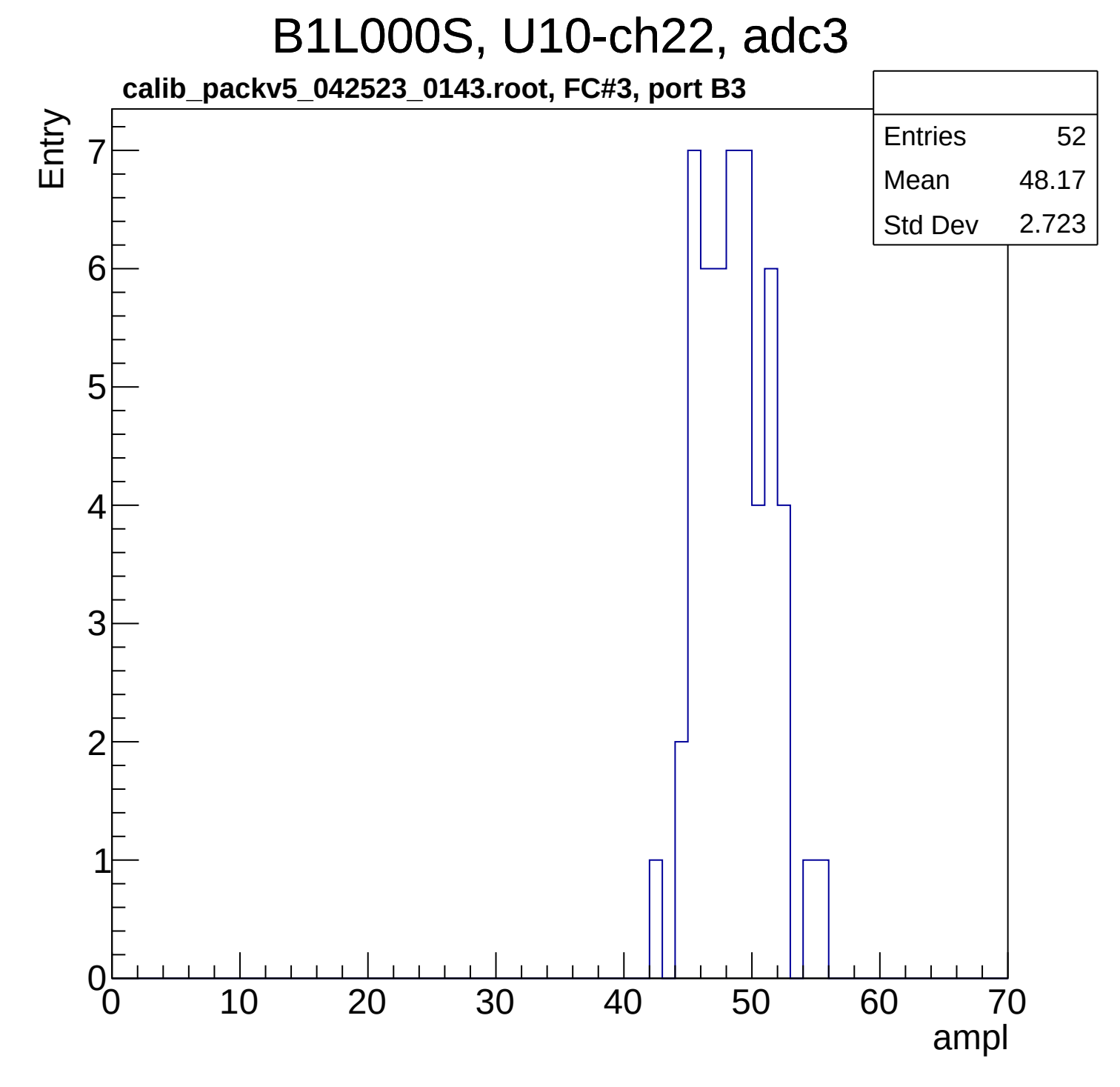
60
70


B1L000S, U10-ch22, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 48.17
Std Dev 2.723
Entry
ampl
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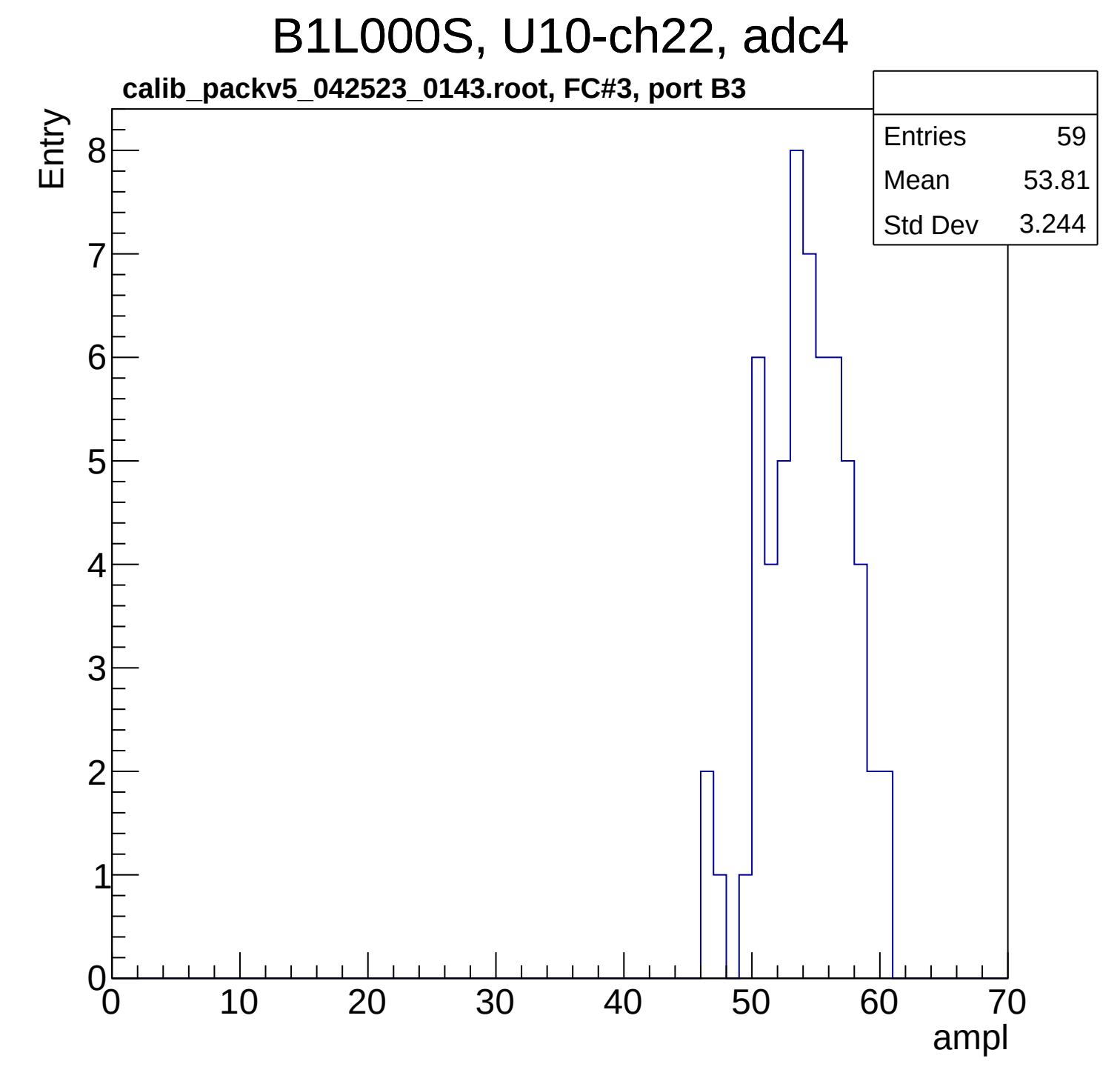
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch22, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.81
Std Dev 3.244
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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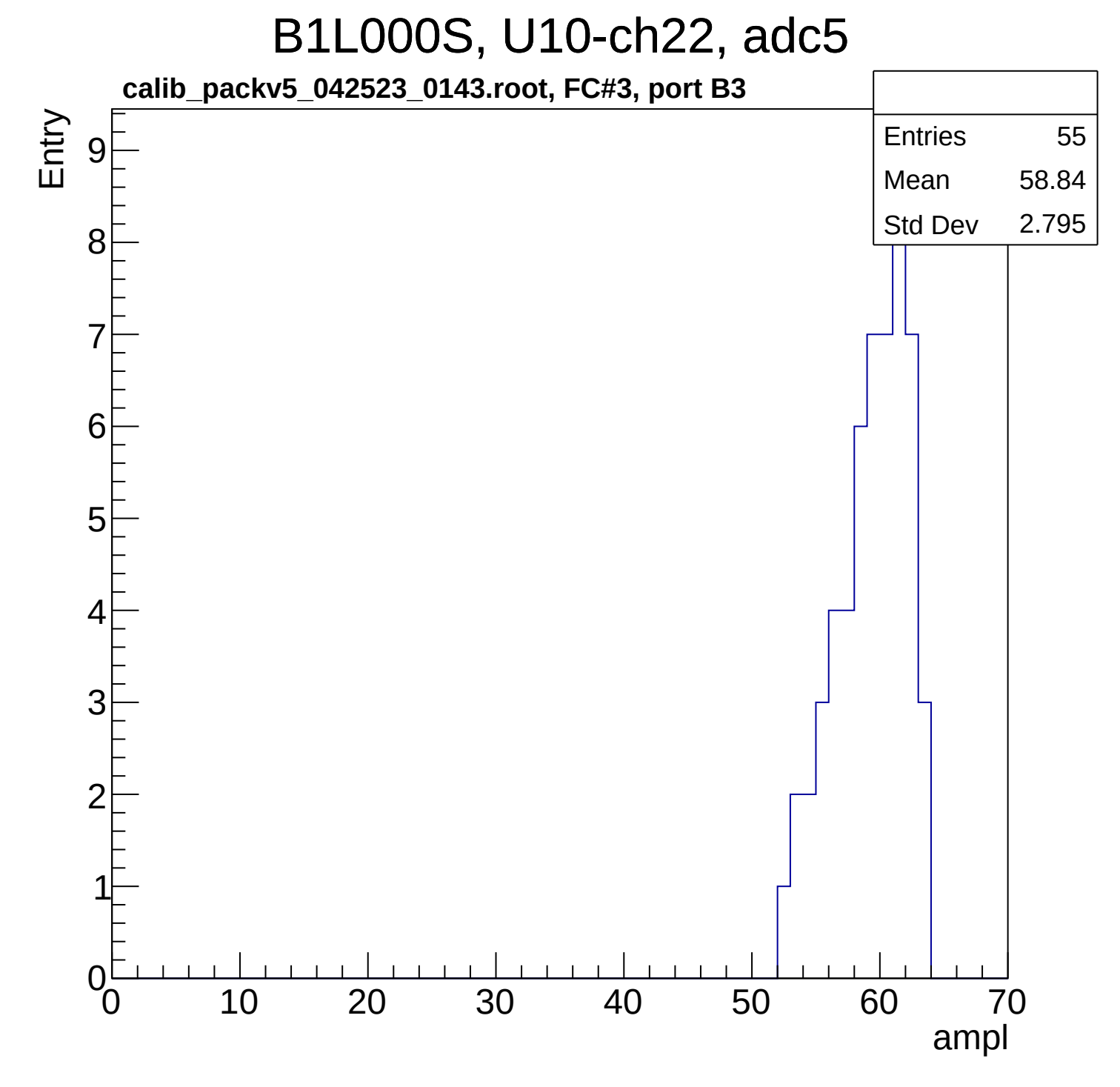
B1L000S, U10-ch22, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.84
Std Dev 2.795
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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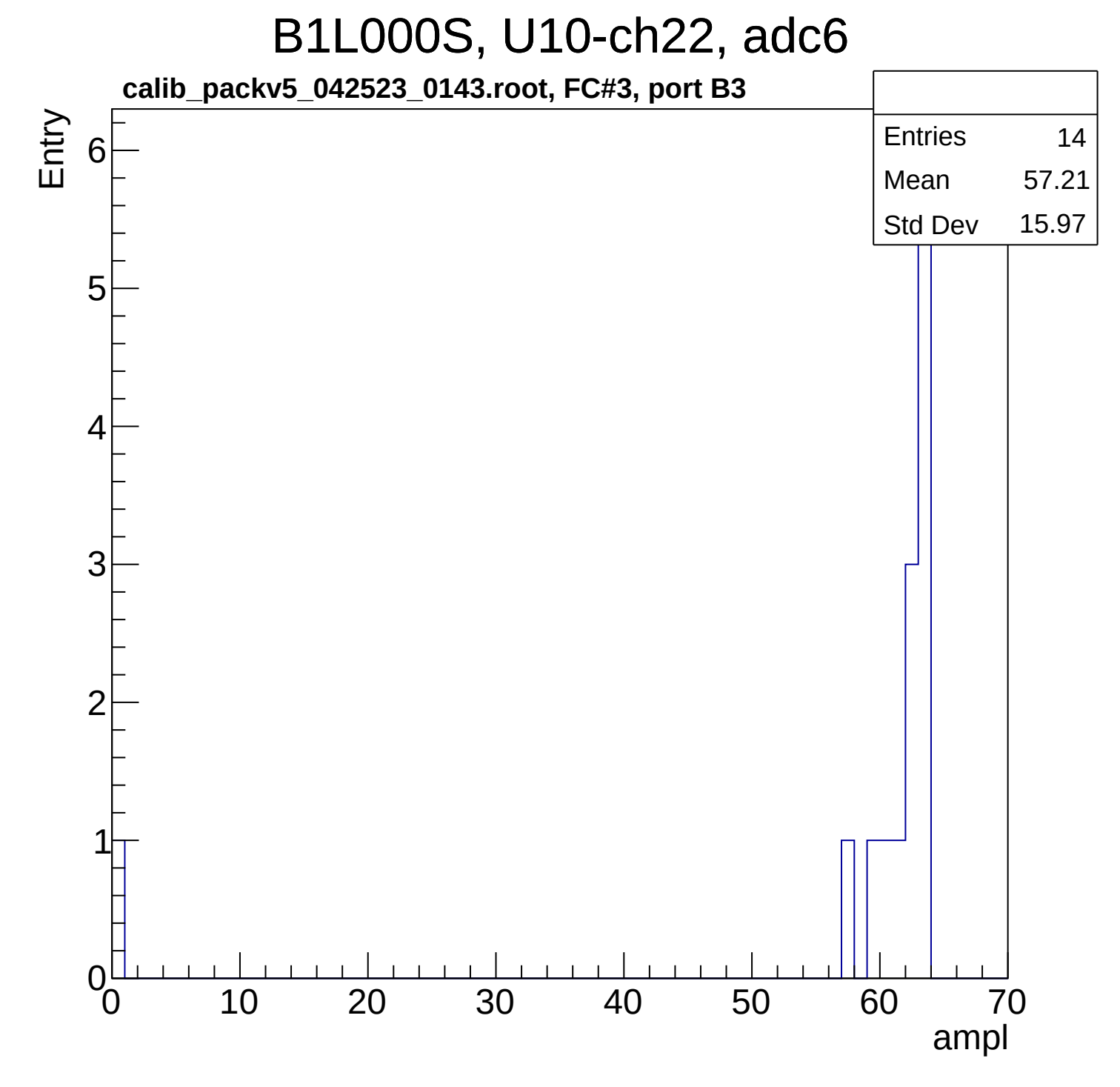
B1L000S, U10-ch22, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.21
Std Dev 15.97
Entry
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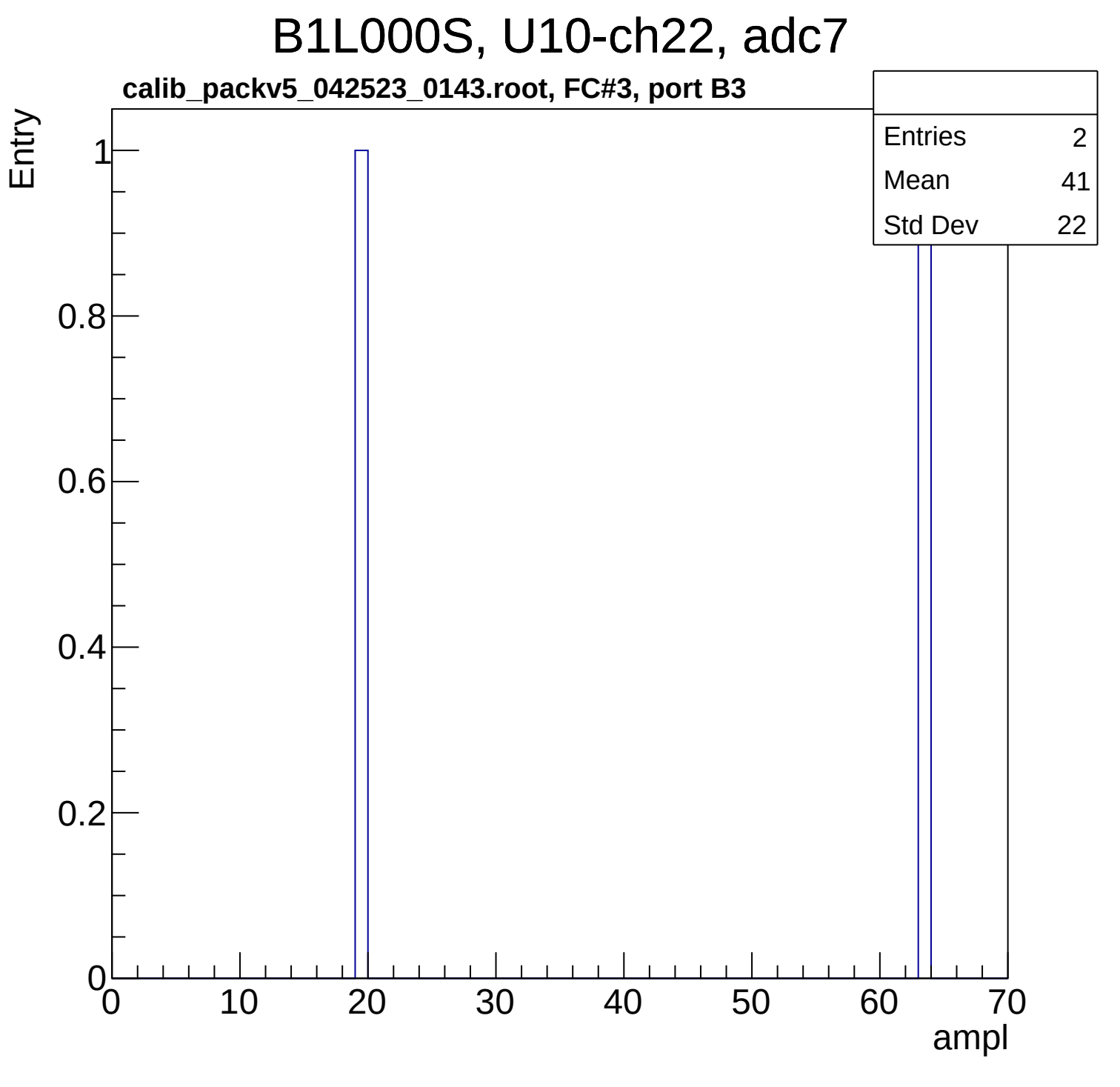
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch22, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41
Std Dev 22
Entry
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0.4
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10
20
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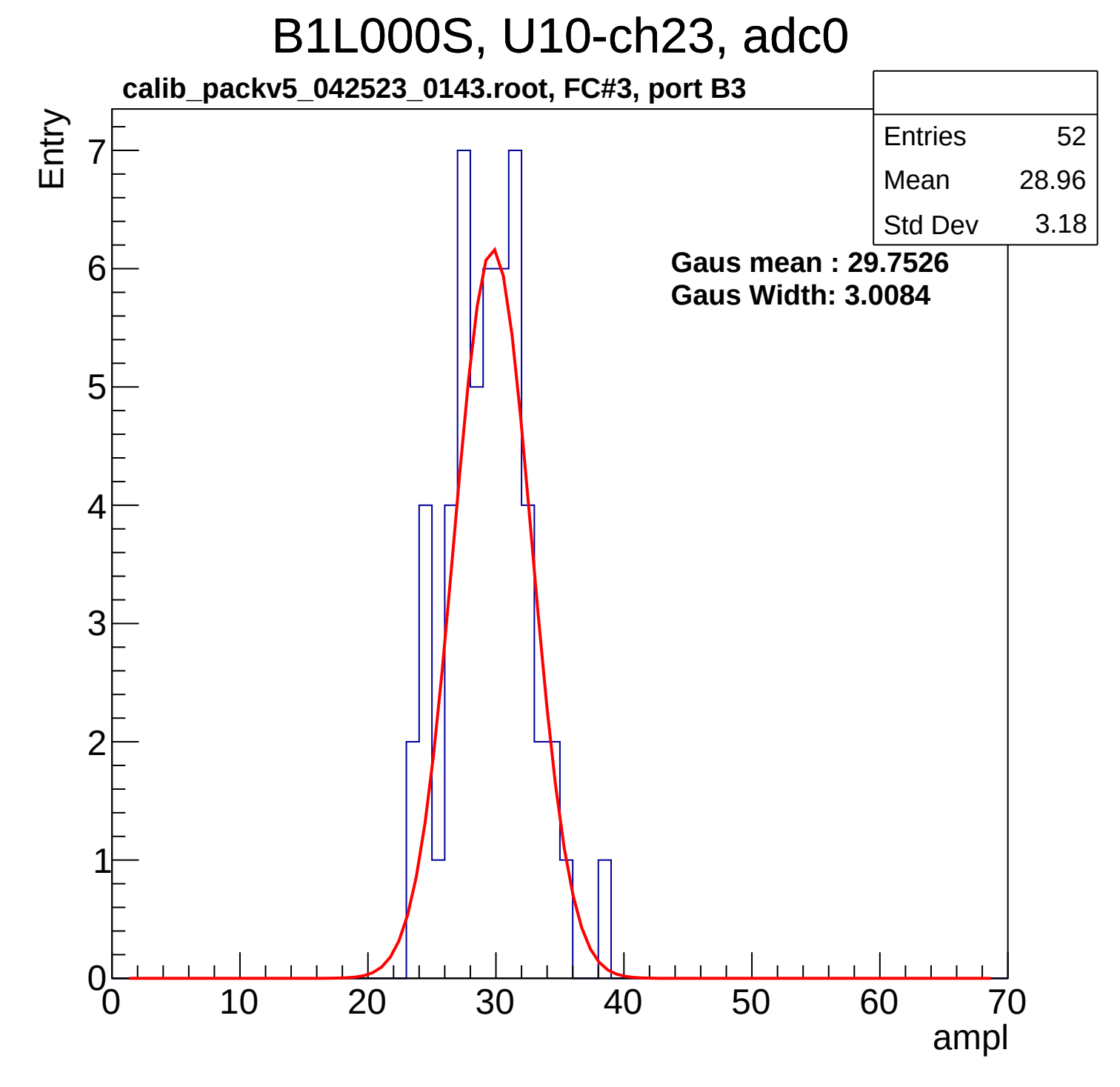
50
60
70
ampl


B1L000S, U10-ch23, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 28.96
Std Dev 3.18
Gaus mean : 29.7526
Gaus Width: 3.0084
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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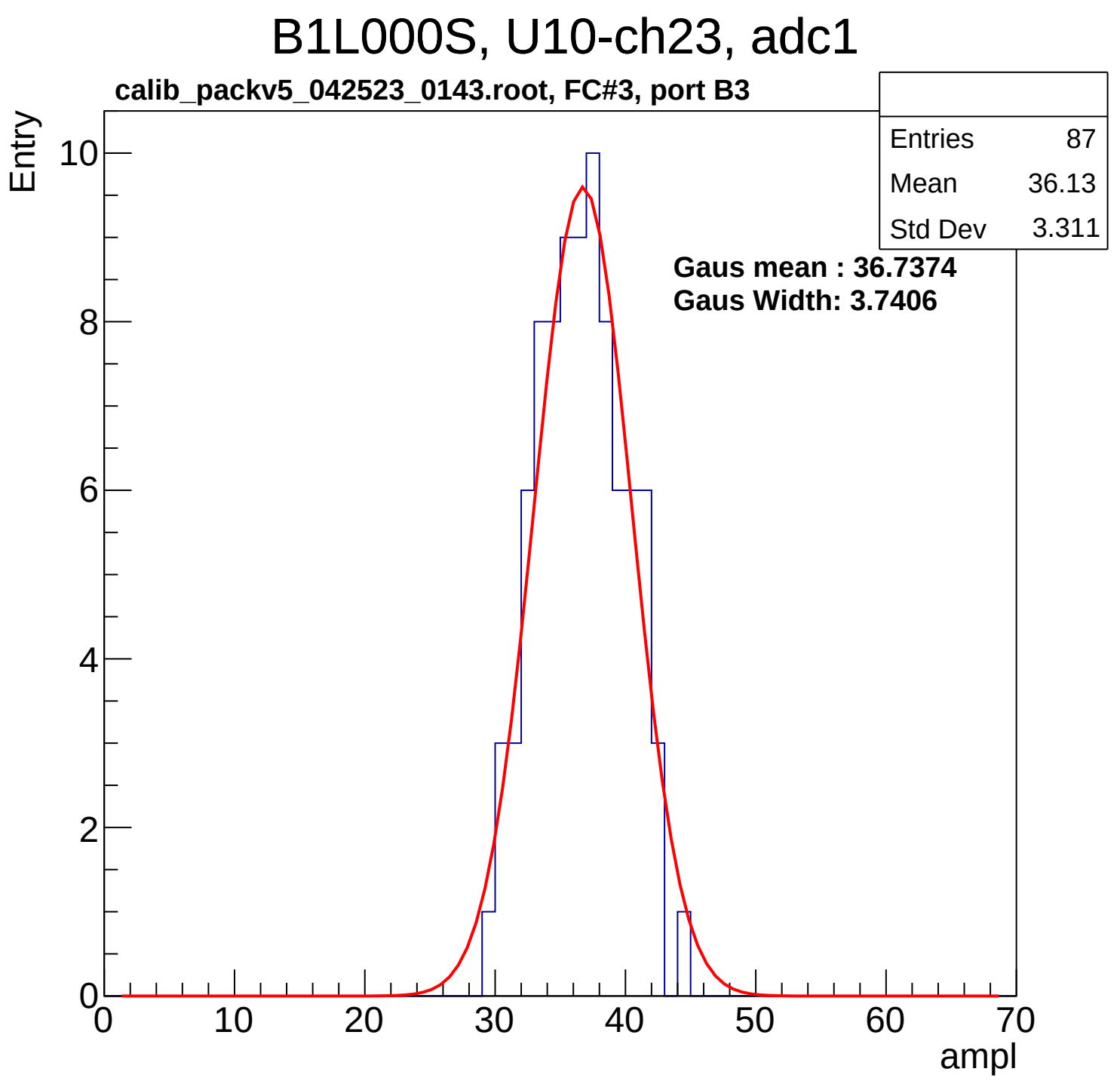
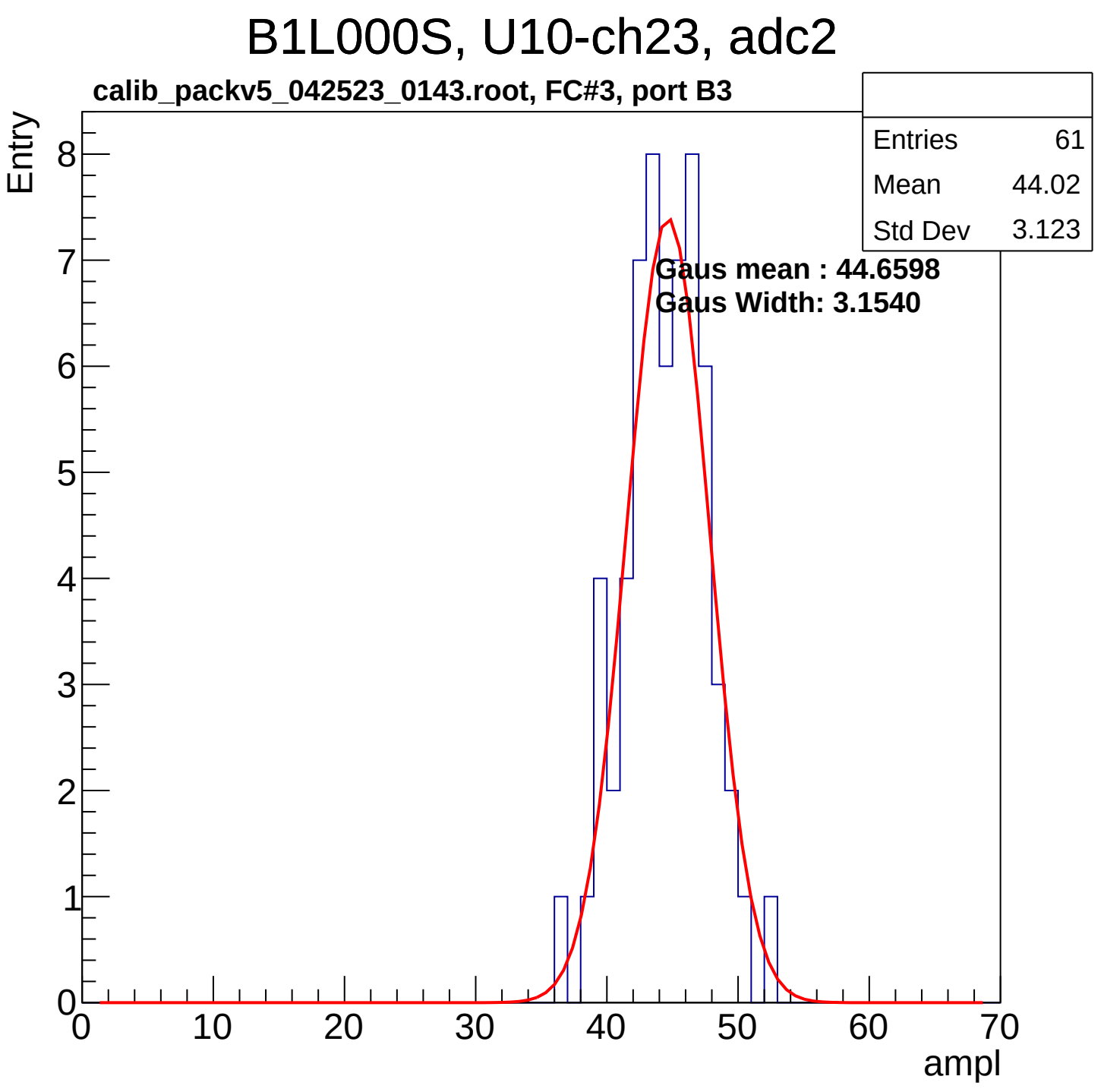

B1L000S, U10-ch23, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 87
Mean 36.13
Std Dev 3.311
Gaus mean : 36.7374
Gaus Width: 3.7406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch23, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 44.02
Std Dev 3.123
Gaus mean : 44.6598
Gaus Width: 3.1540
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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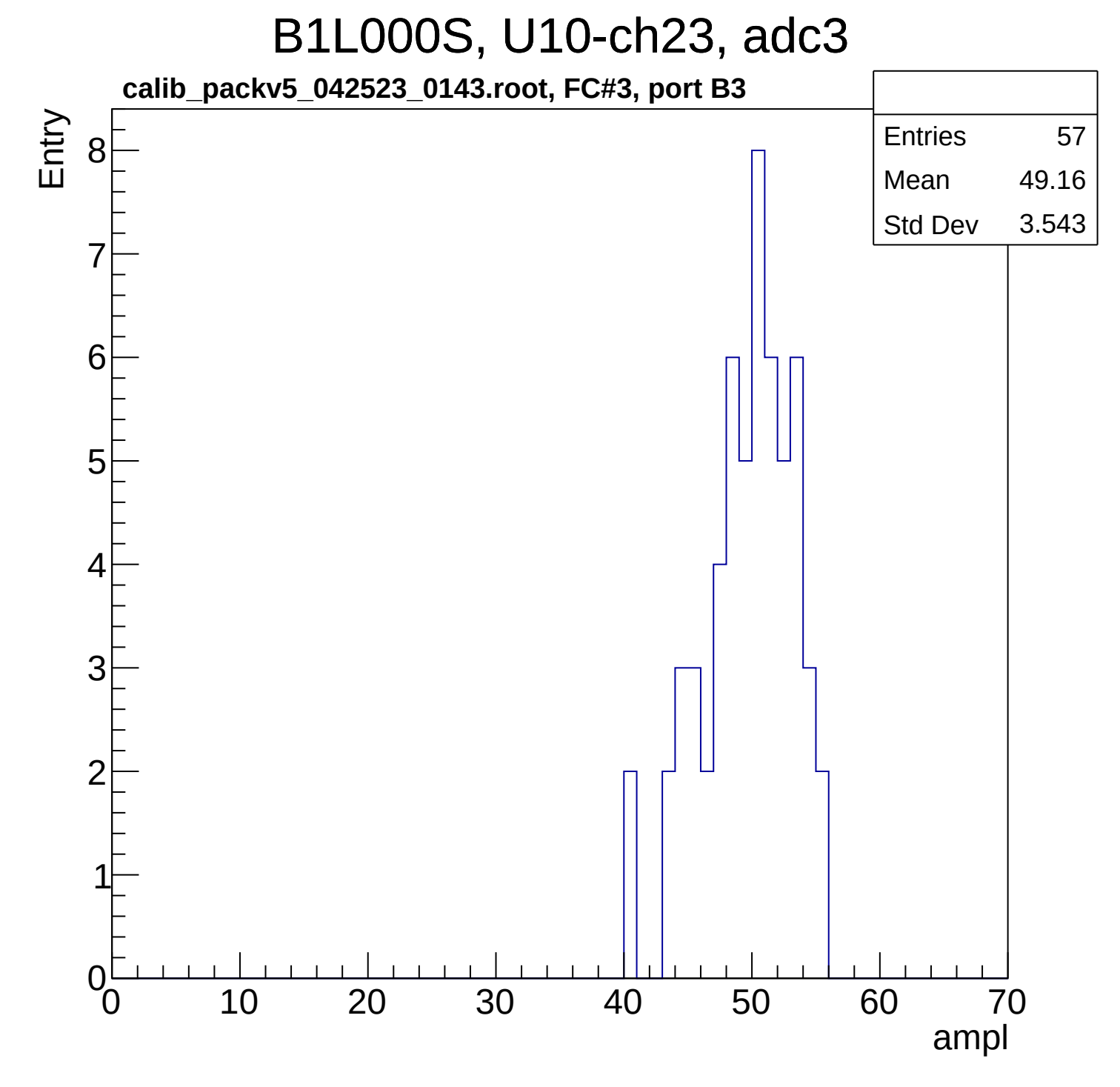
B1L000S, U10-ch23, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 49.16
Std Dev 3.543
Entry
ampl
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0
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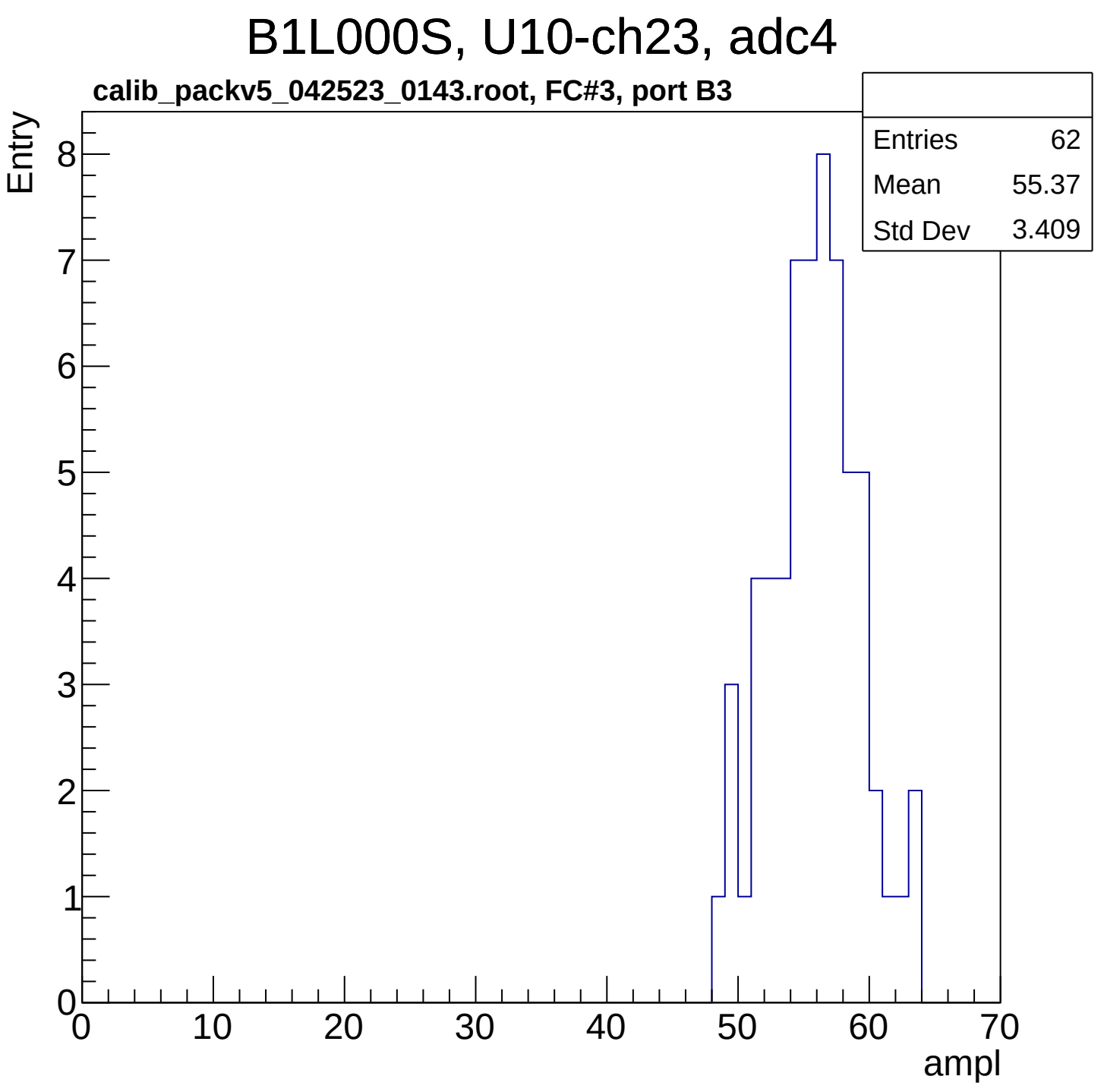
B1L000S, U10-ch23, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 55.37
Std Dev 3.409
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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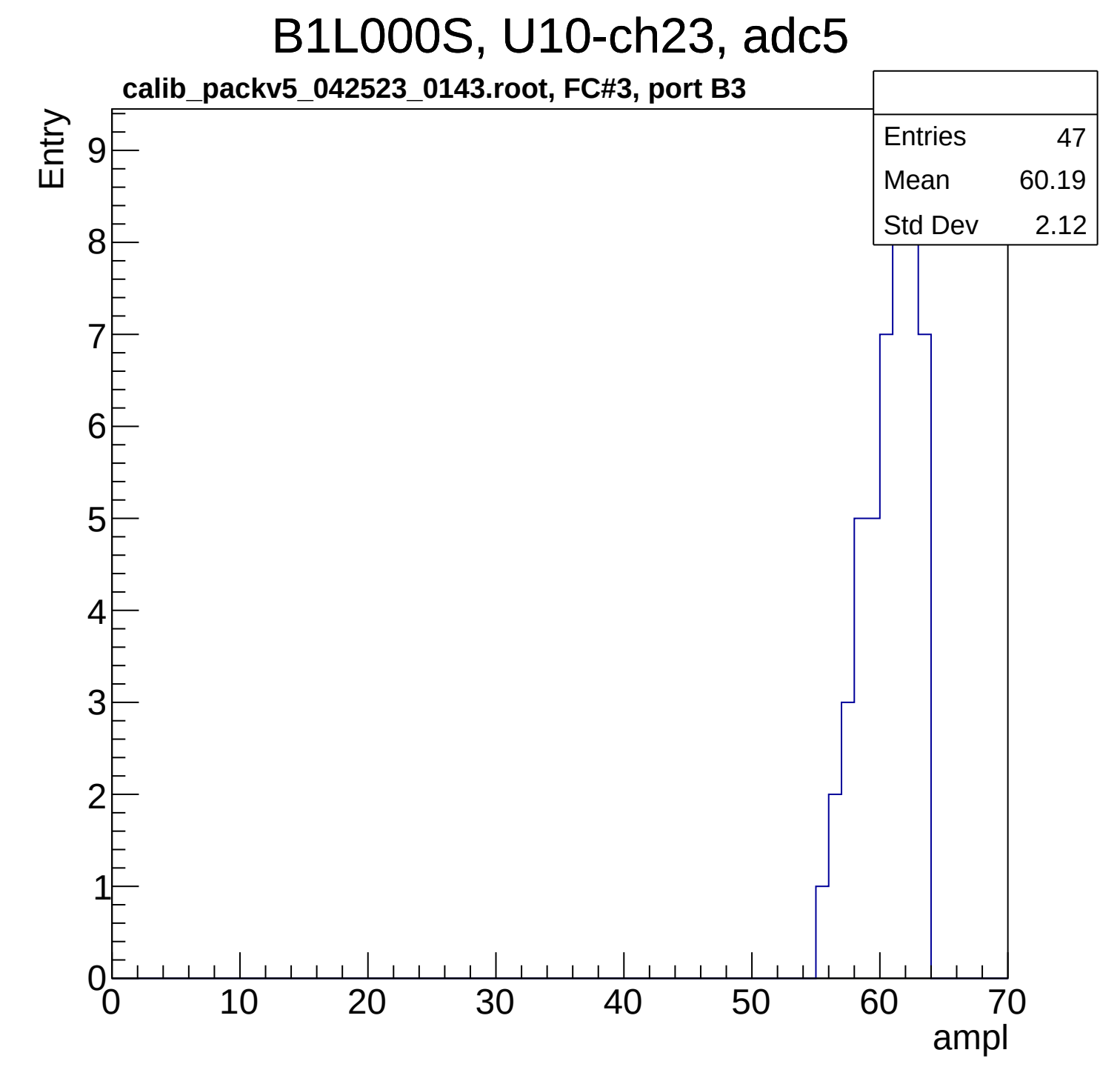
B1L000S, U10-ch23, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 60.19
Std Dev 2.12
Entry
ampl
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2
3
4
5
6
7
8
9
0
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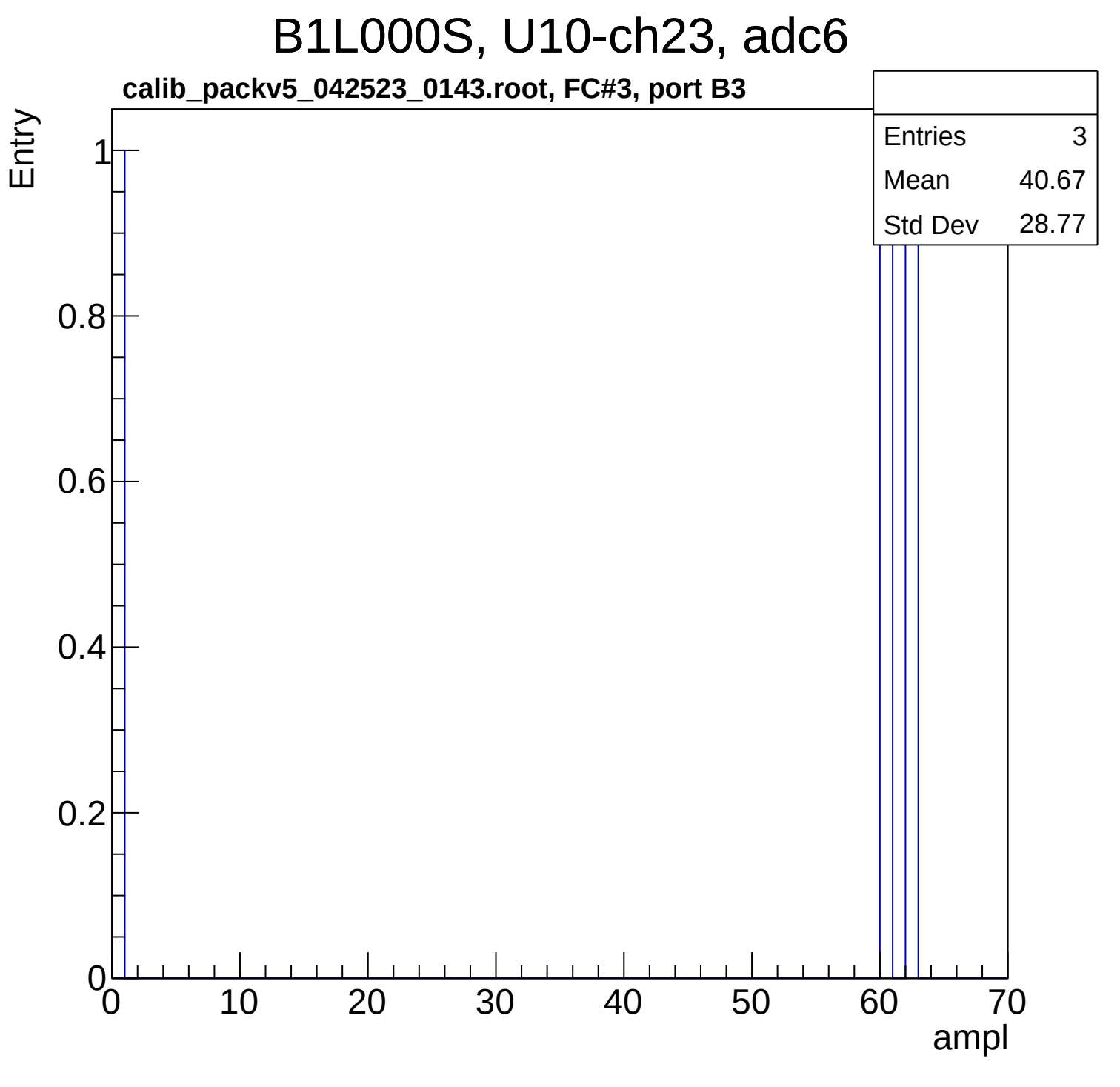
B1L000S, U10-ch23, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 40.67
Std Dev 28.77
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
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60
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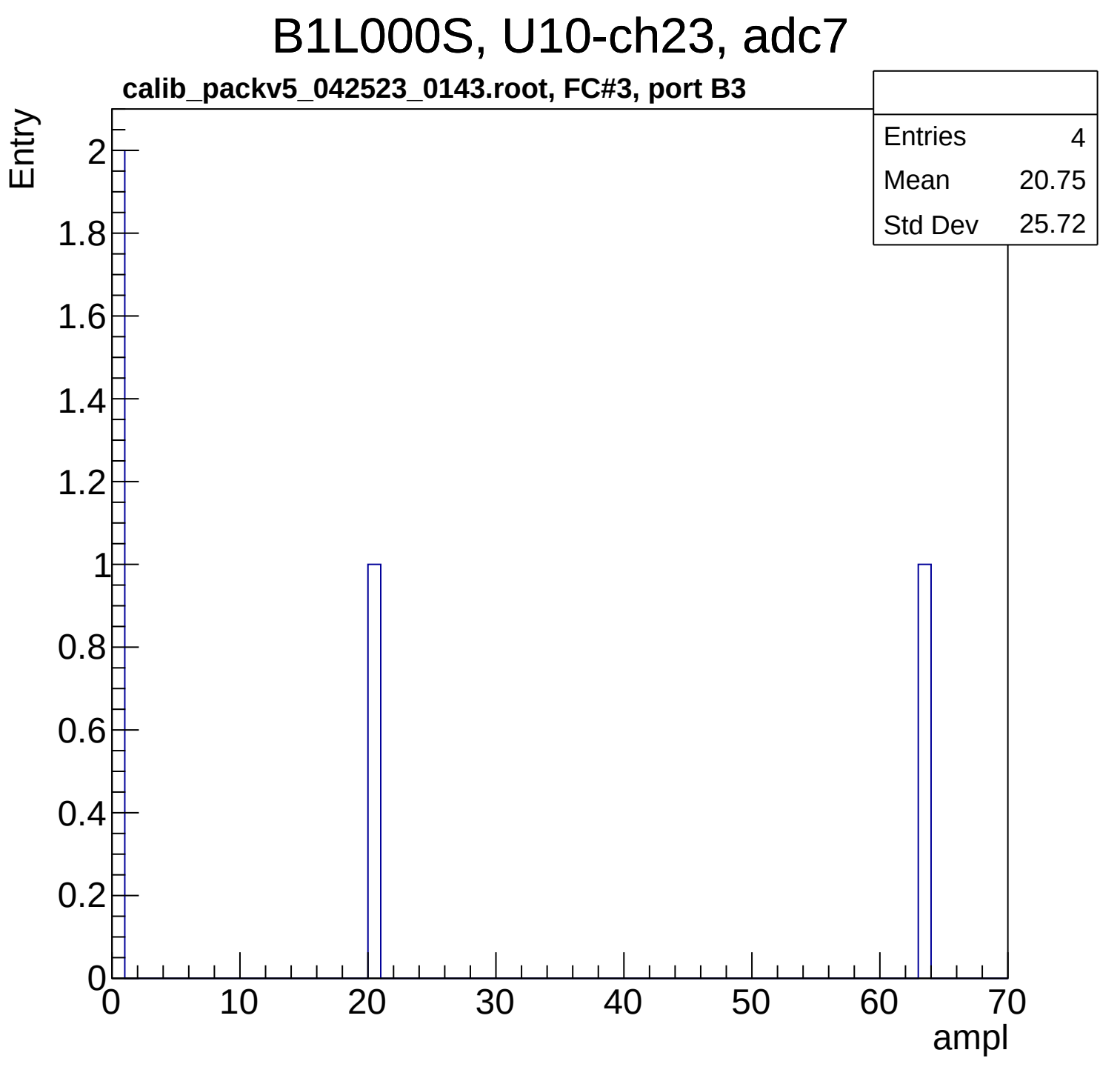
B1L000S, U10-ch23, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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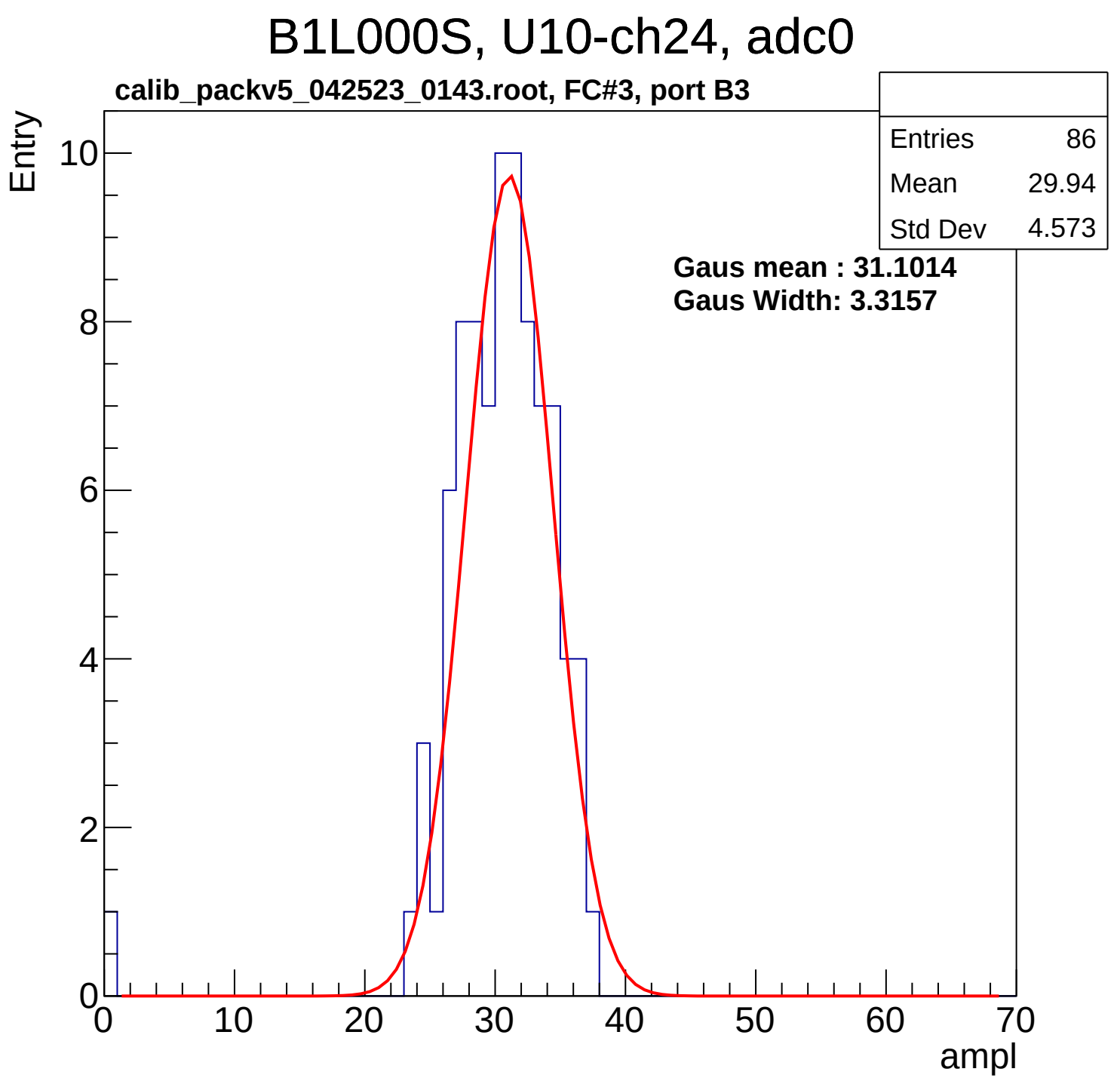
B1L000S, U10-ch24, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 29.94
Std Dev 4.573
Gaus mean : 31.1014
Gaus Width: 3.3157
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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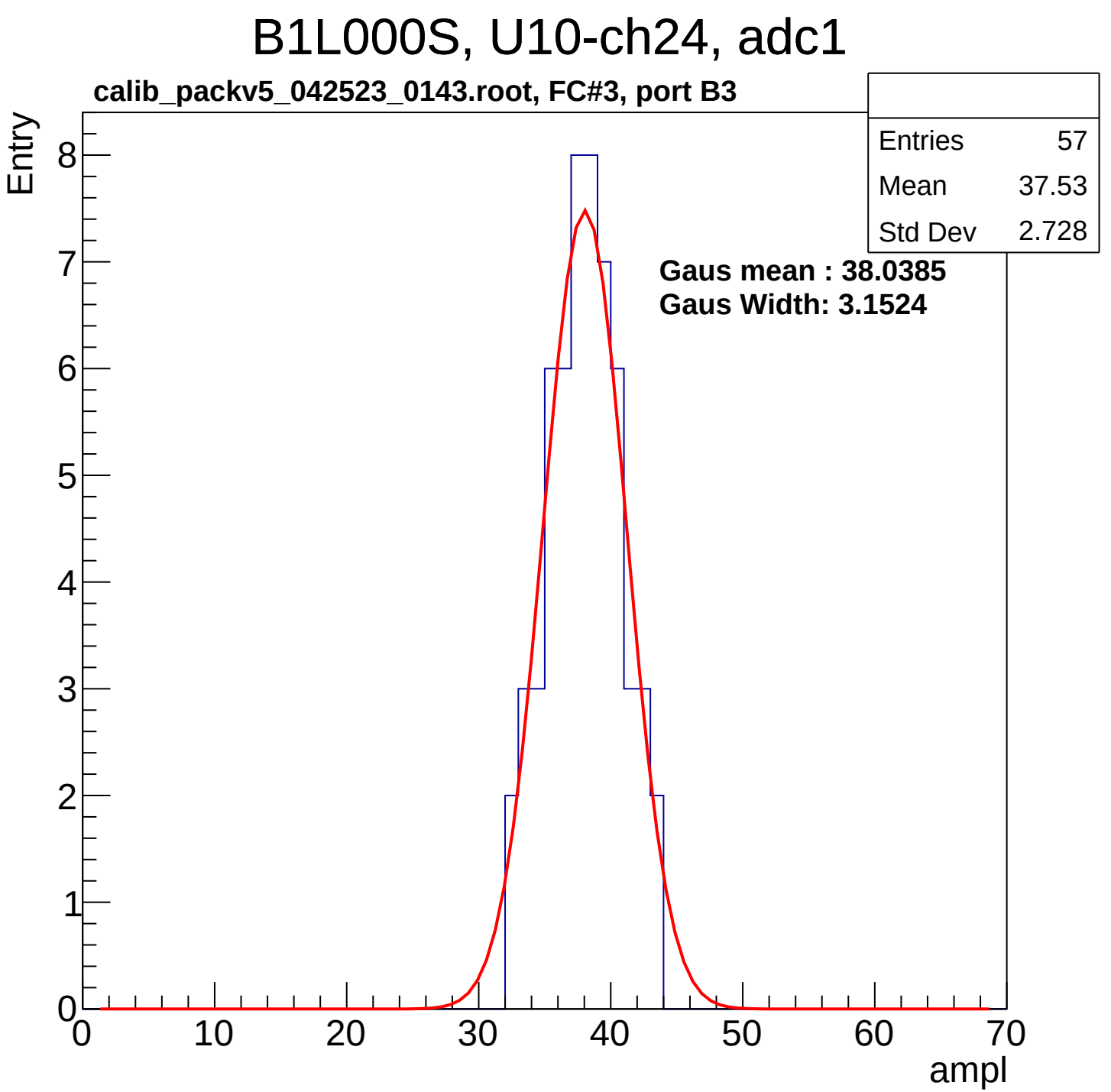
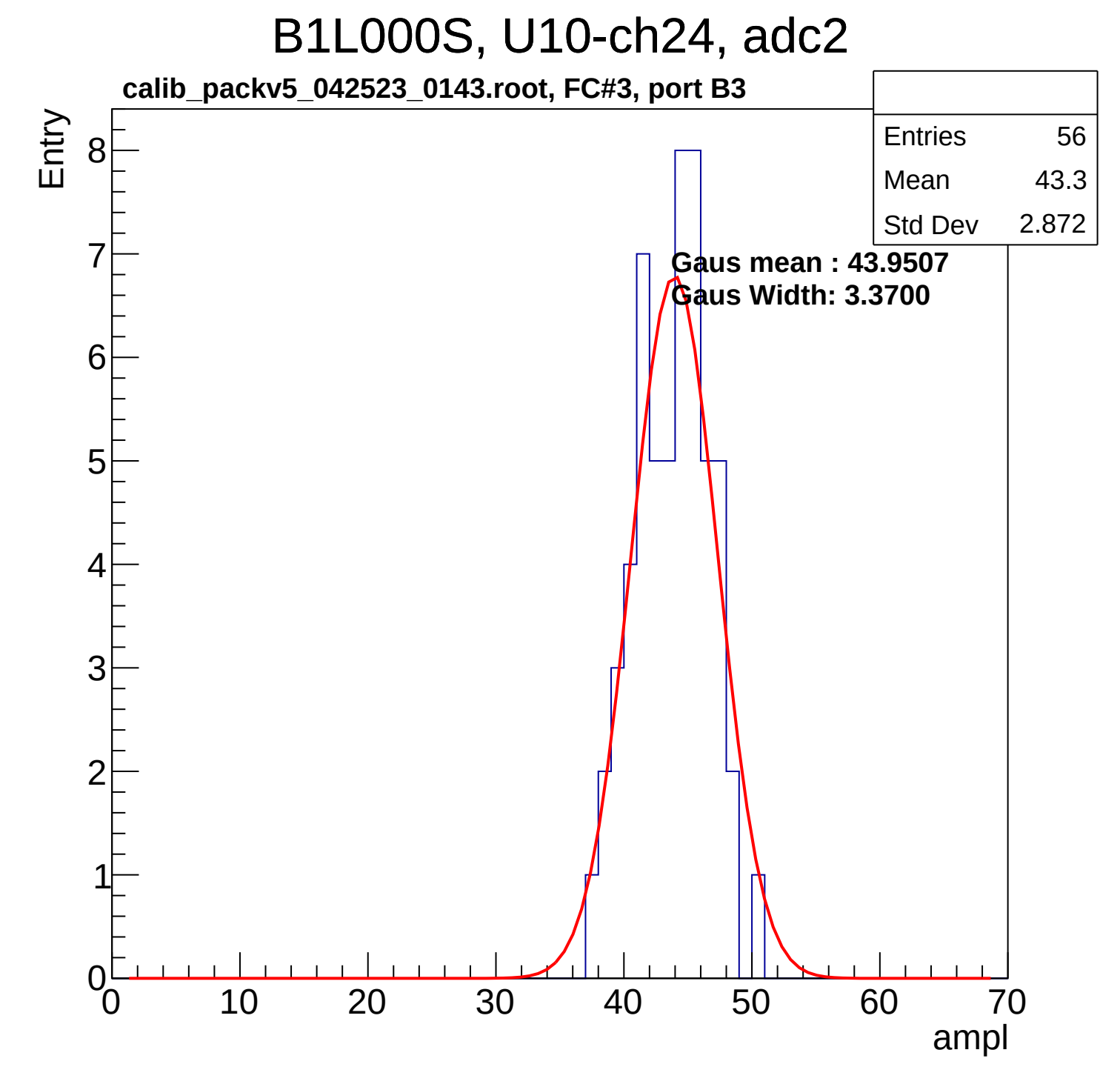
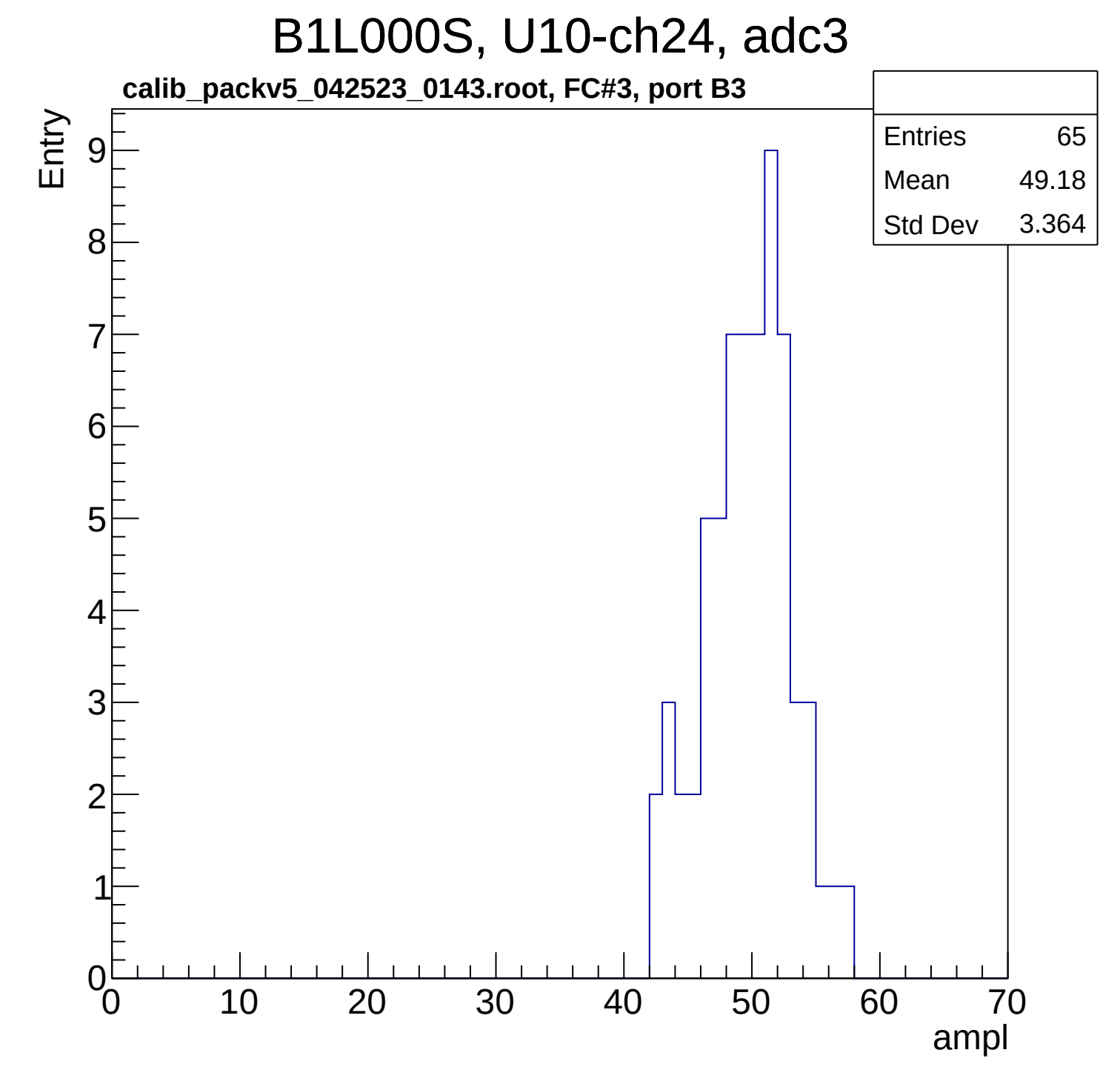
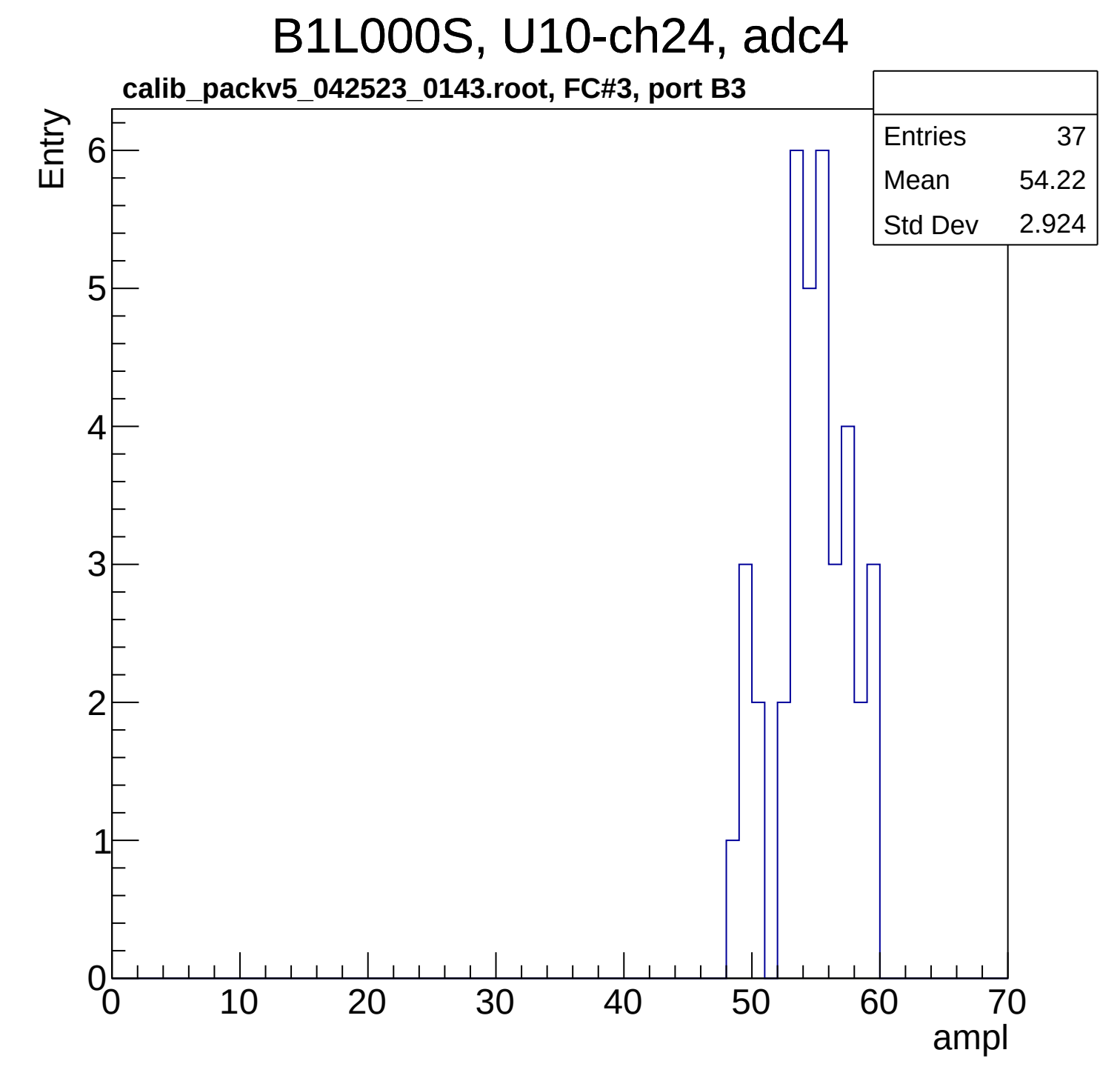
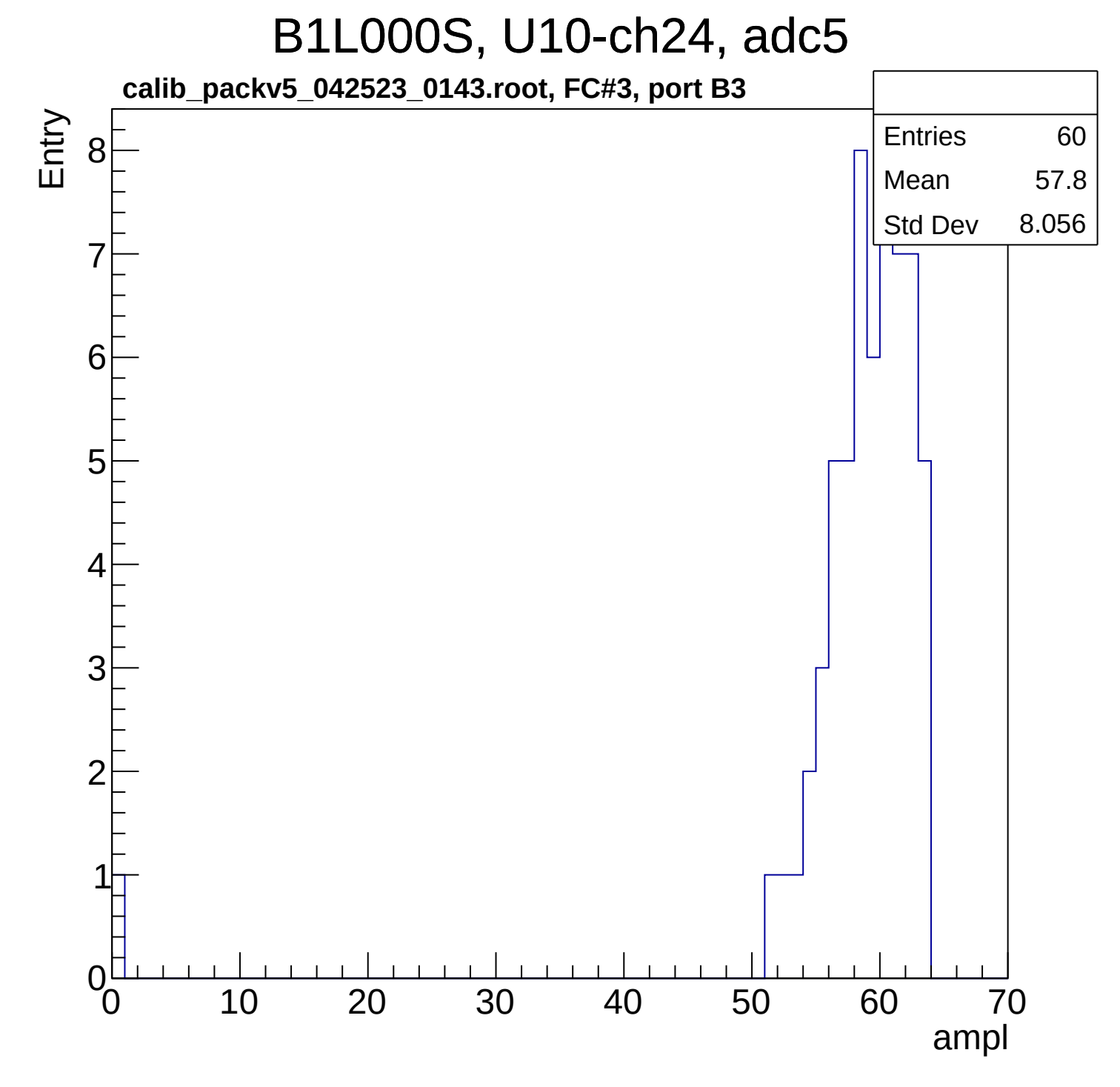
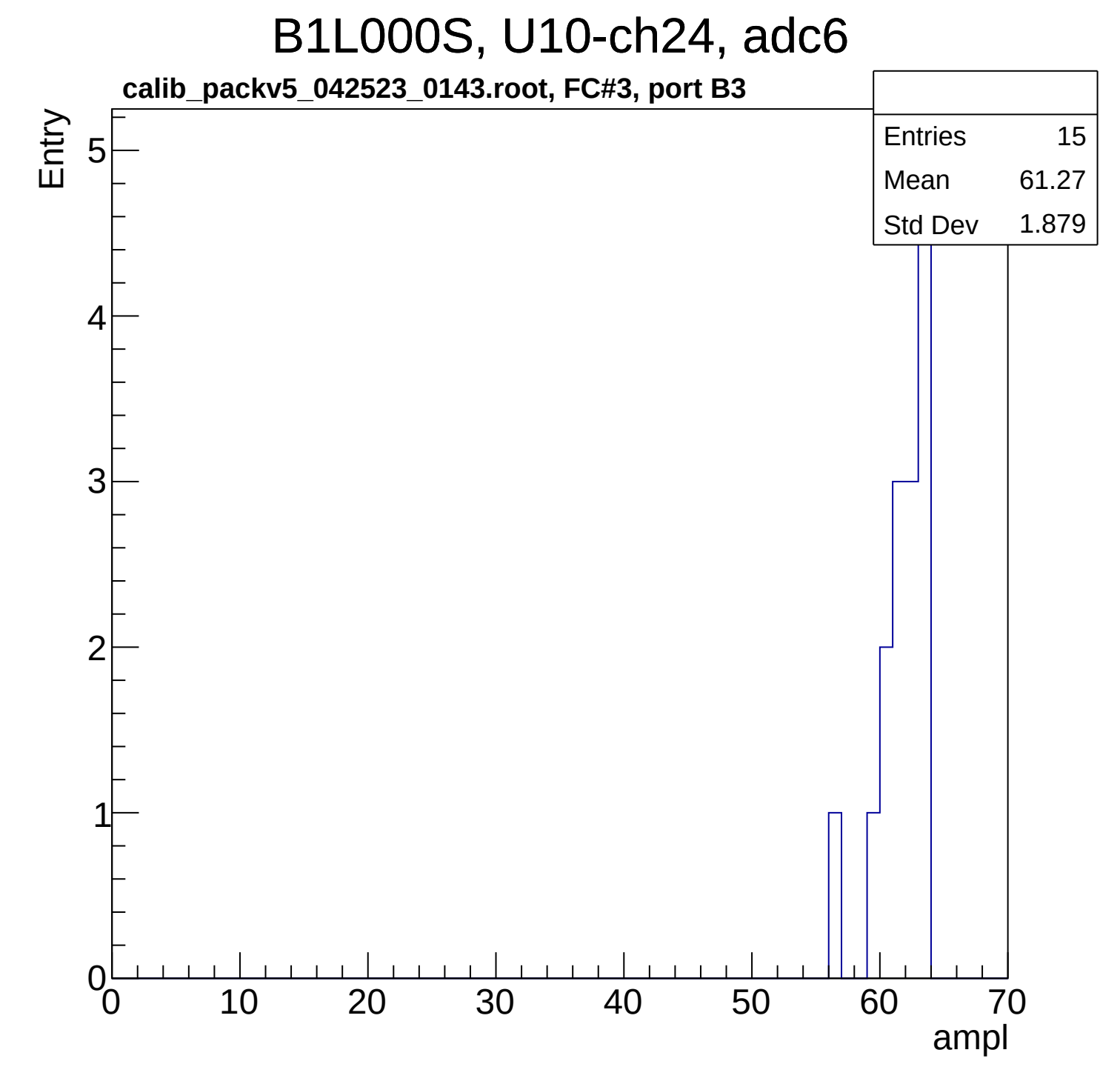

B1L000S, U10-ch24, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 37.53
Std Dev 2.728
Gaus mean : 38.0385
Gaus Width: 3.1524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

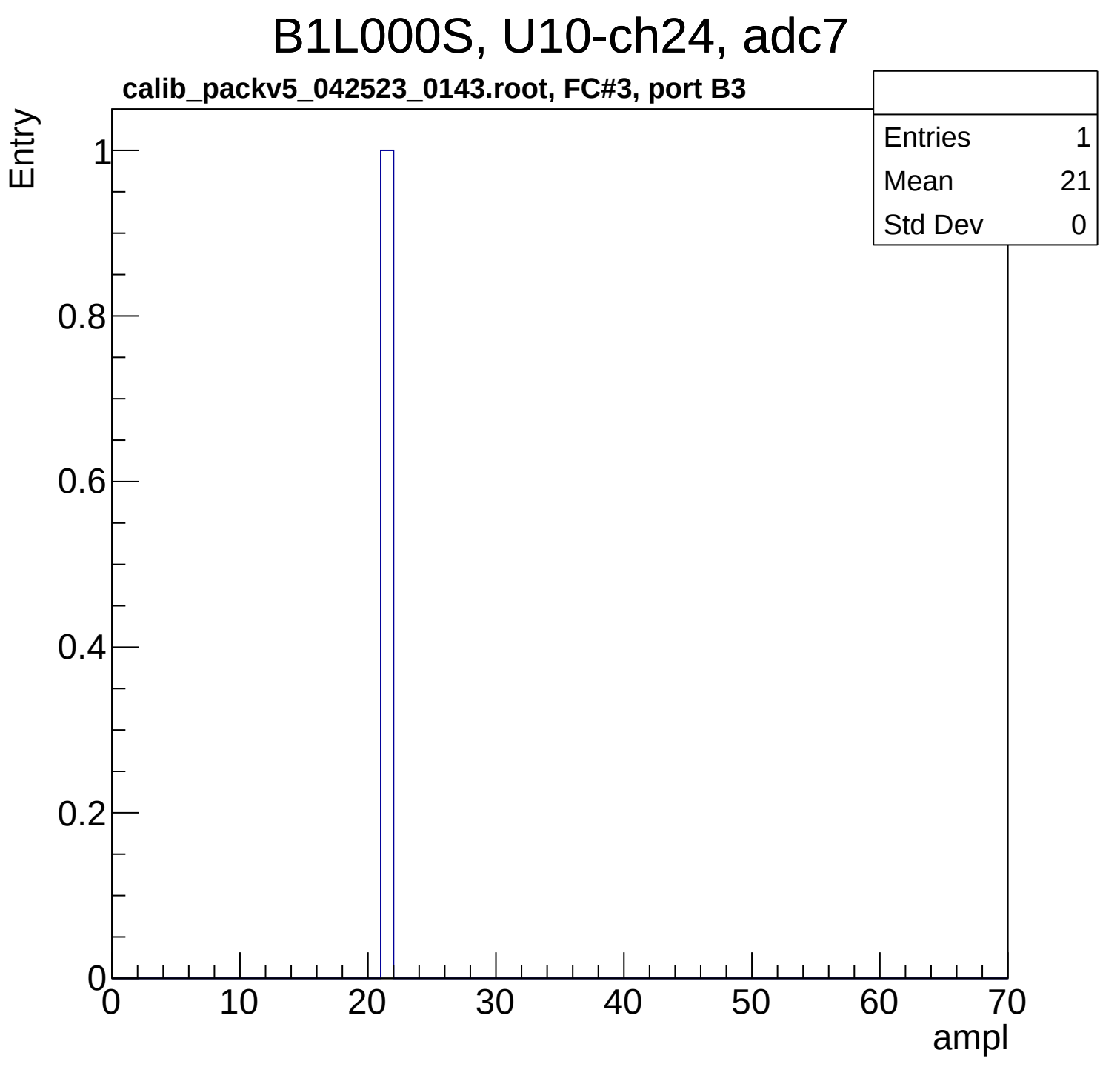

B1L000S, U10-ch24, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 43.3
Std Dev 2.872
Gaus mean : 43.9507
Gaus Width: 3.3700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

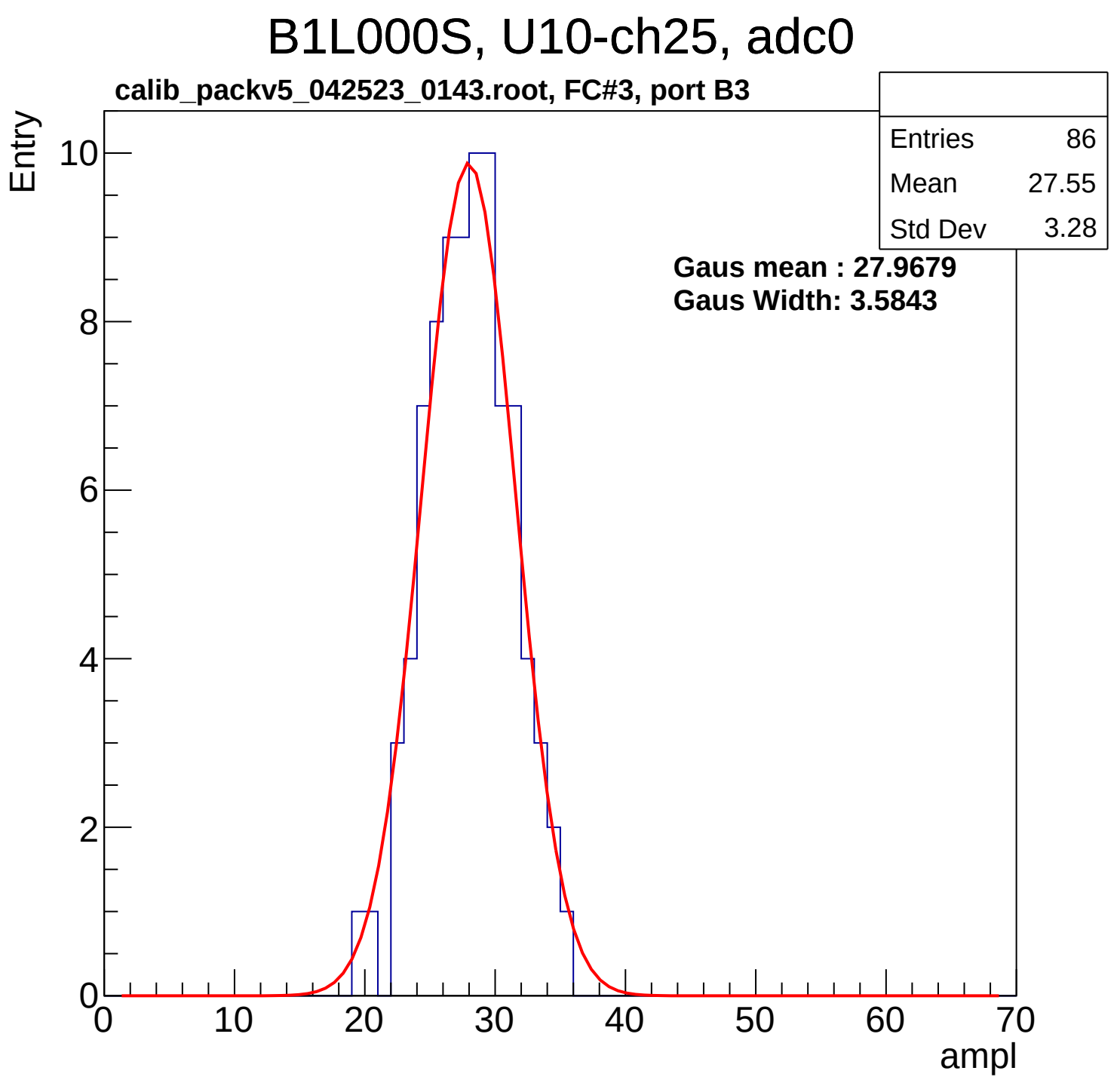

B1L000S, U10-ch24, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 49.18
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

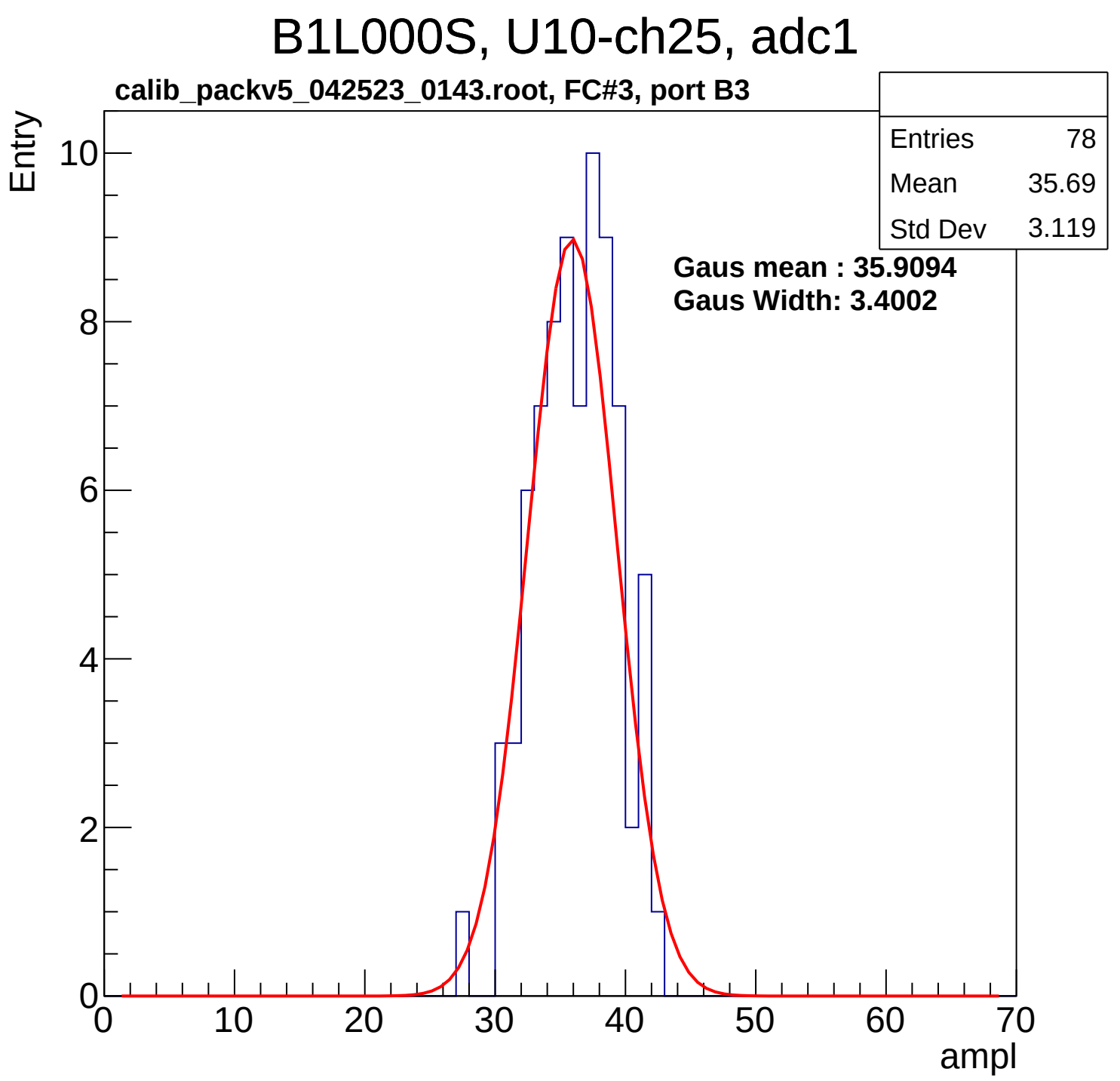

B1L000S, U10-ch24, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 54.22
Std Dev 2.924
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

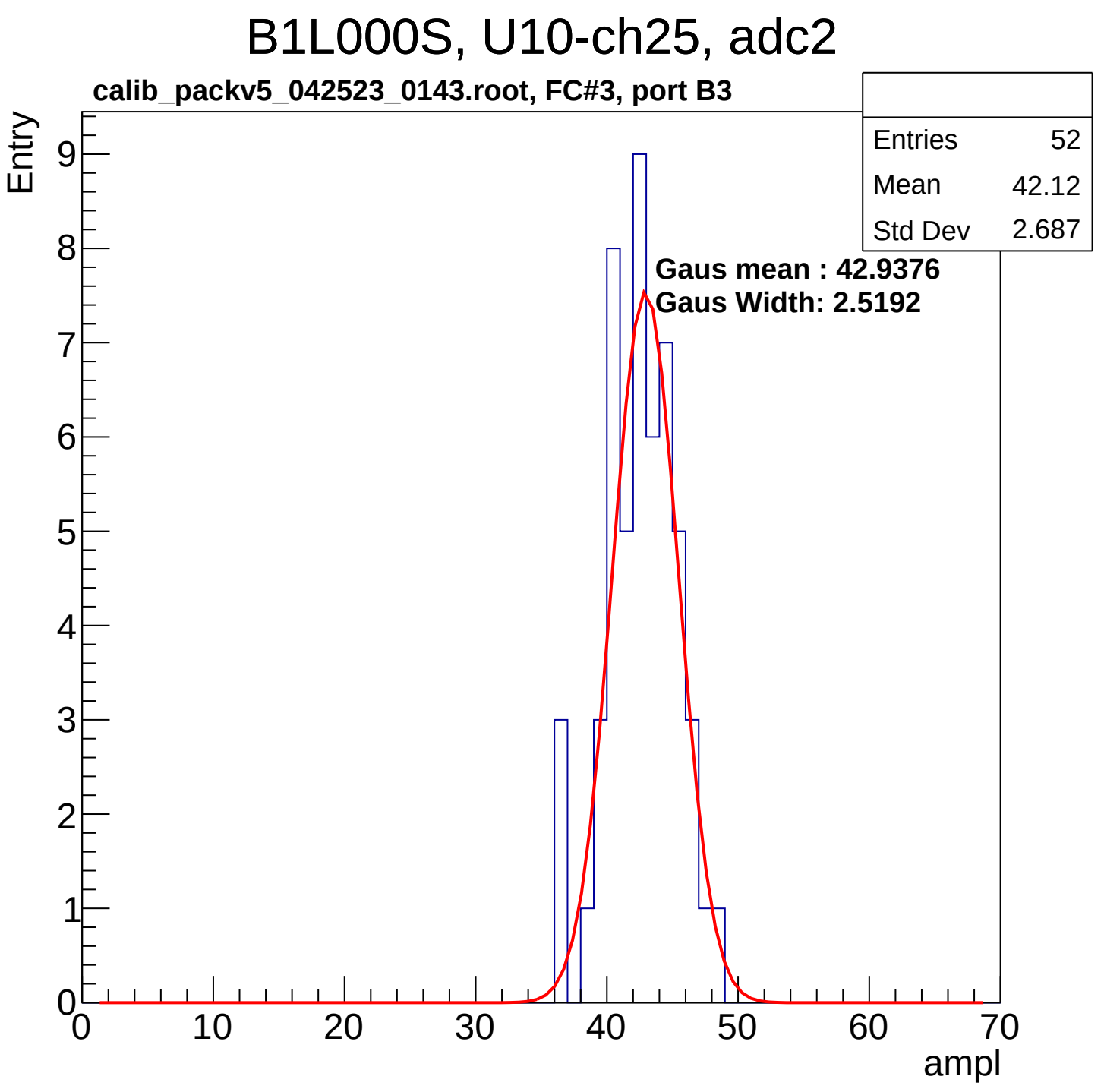

B1L000S, U10-ch24, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 57.8
Std Dev 8.056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

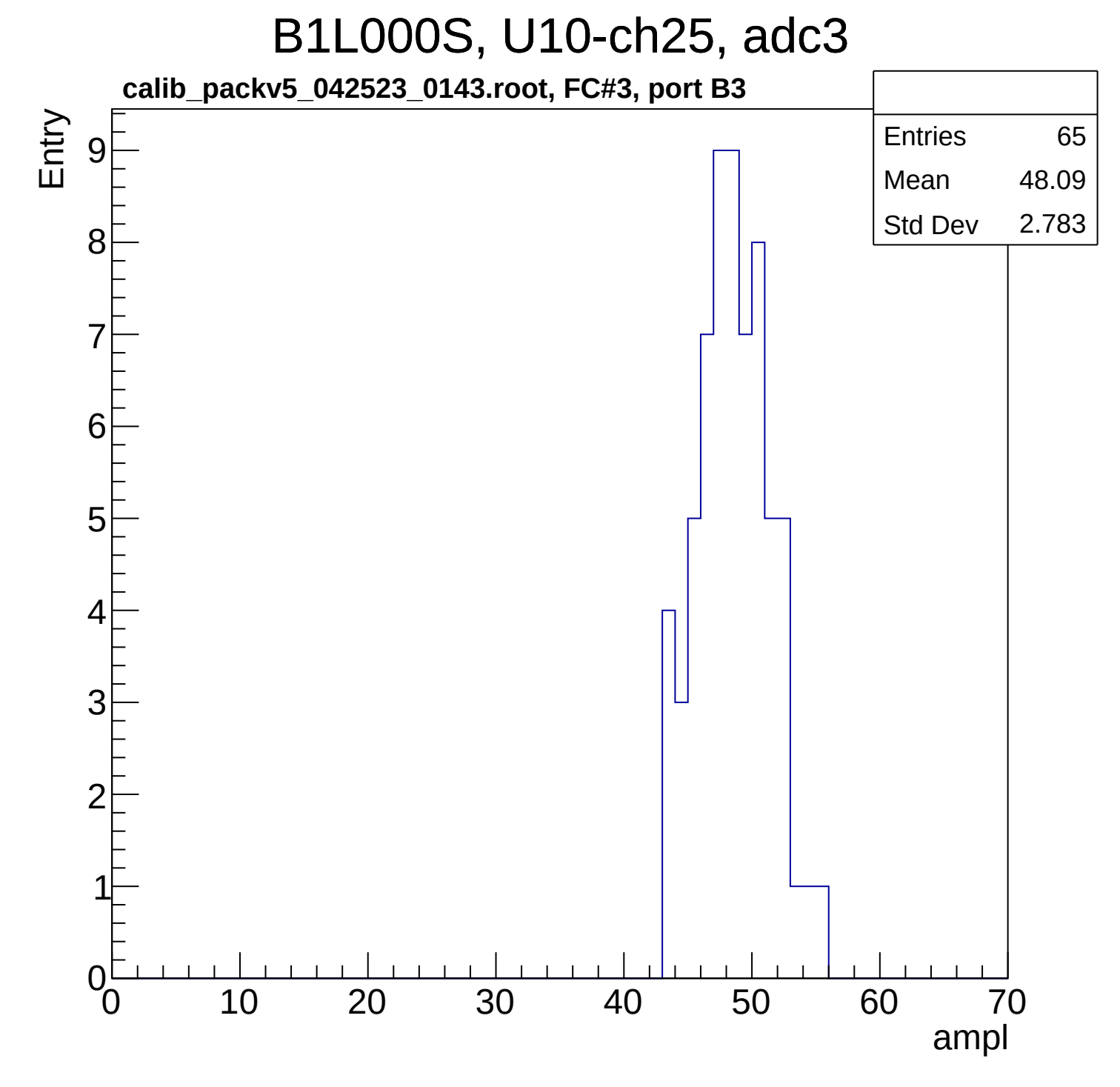

B1L000S, U10-ch24, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.27
Std Dev 1.879
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

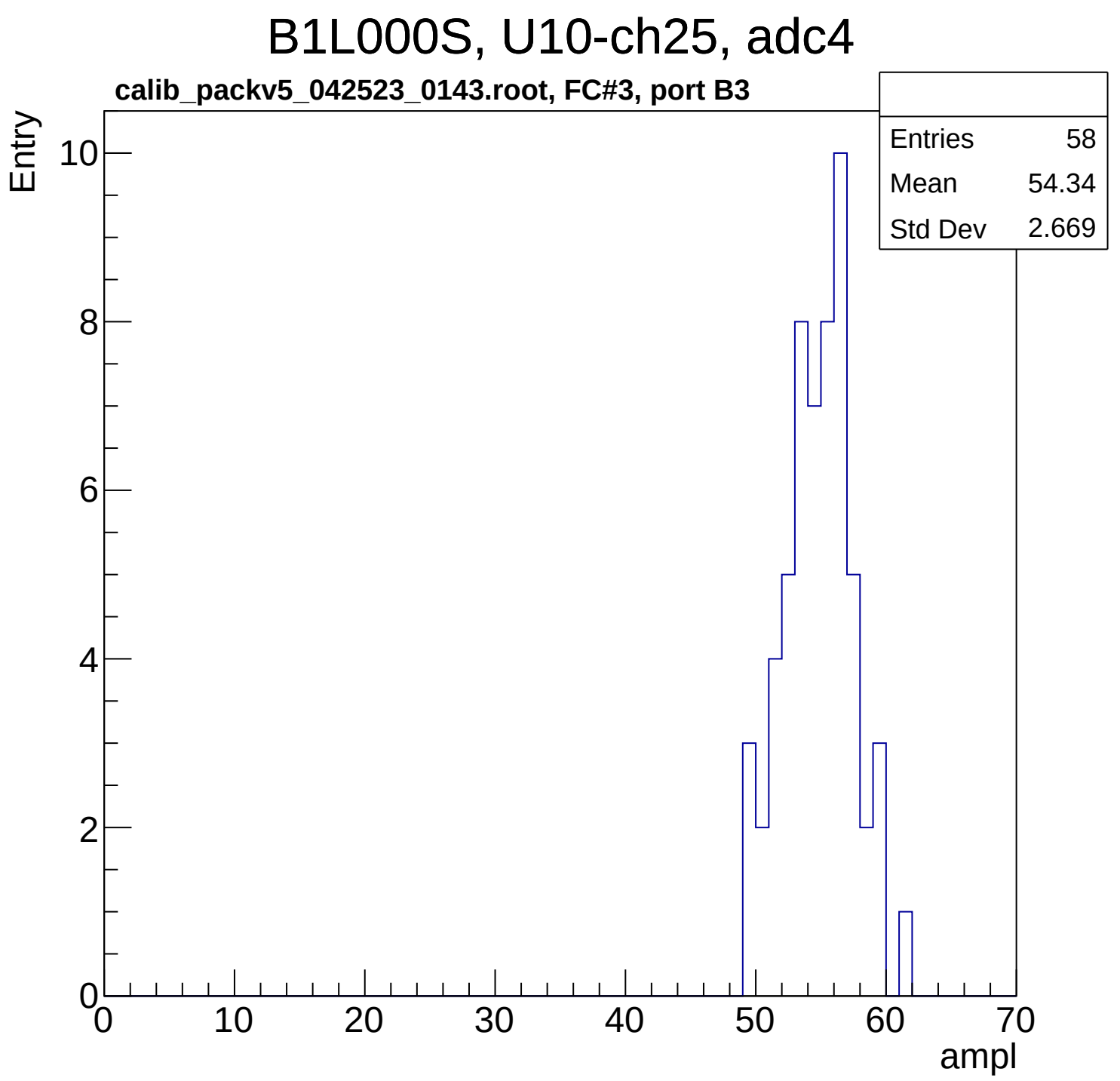

B1L000S, U10-ch24, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

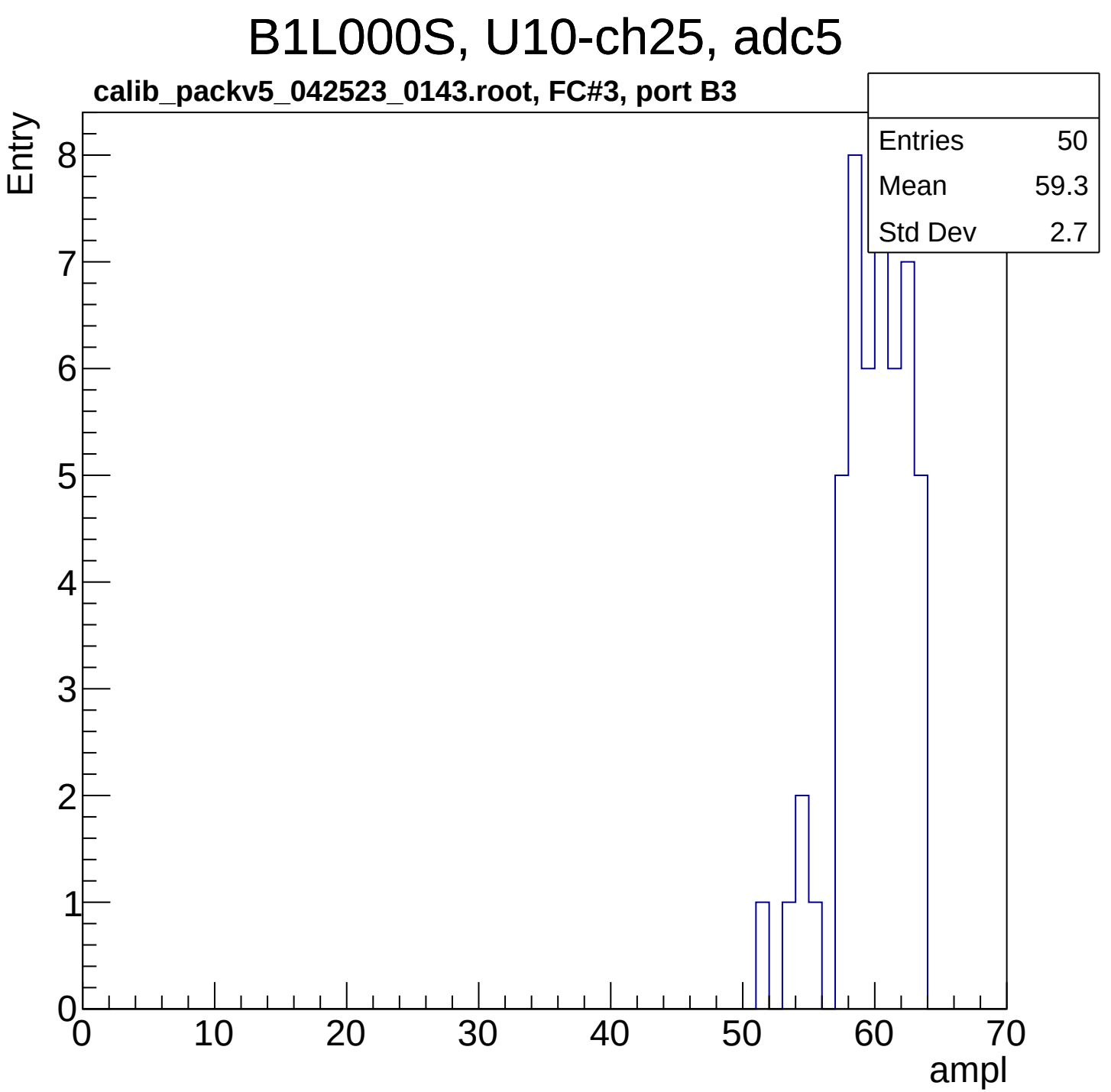

B1L000S, U10-ch25, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 27.55
Std Dev 3.28
Gaus mean : 27.9679
Gaus Width: 3.5843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

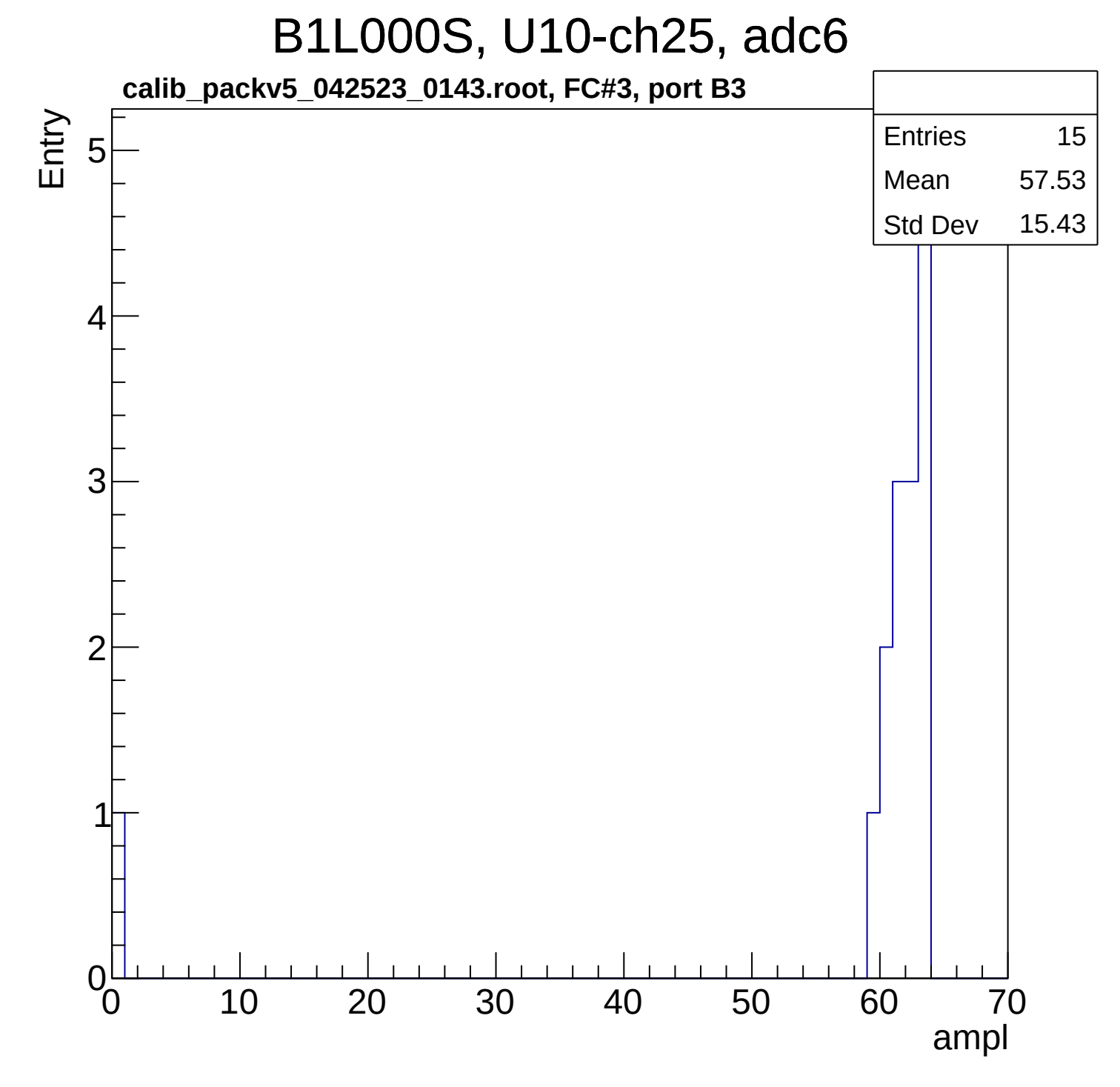

B1L000S, U10-ch25, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 35.69
Std Dev 3.119
Gaus mean : 35.9094
Gaus Width: 3.4002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

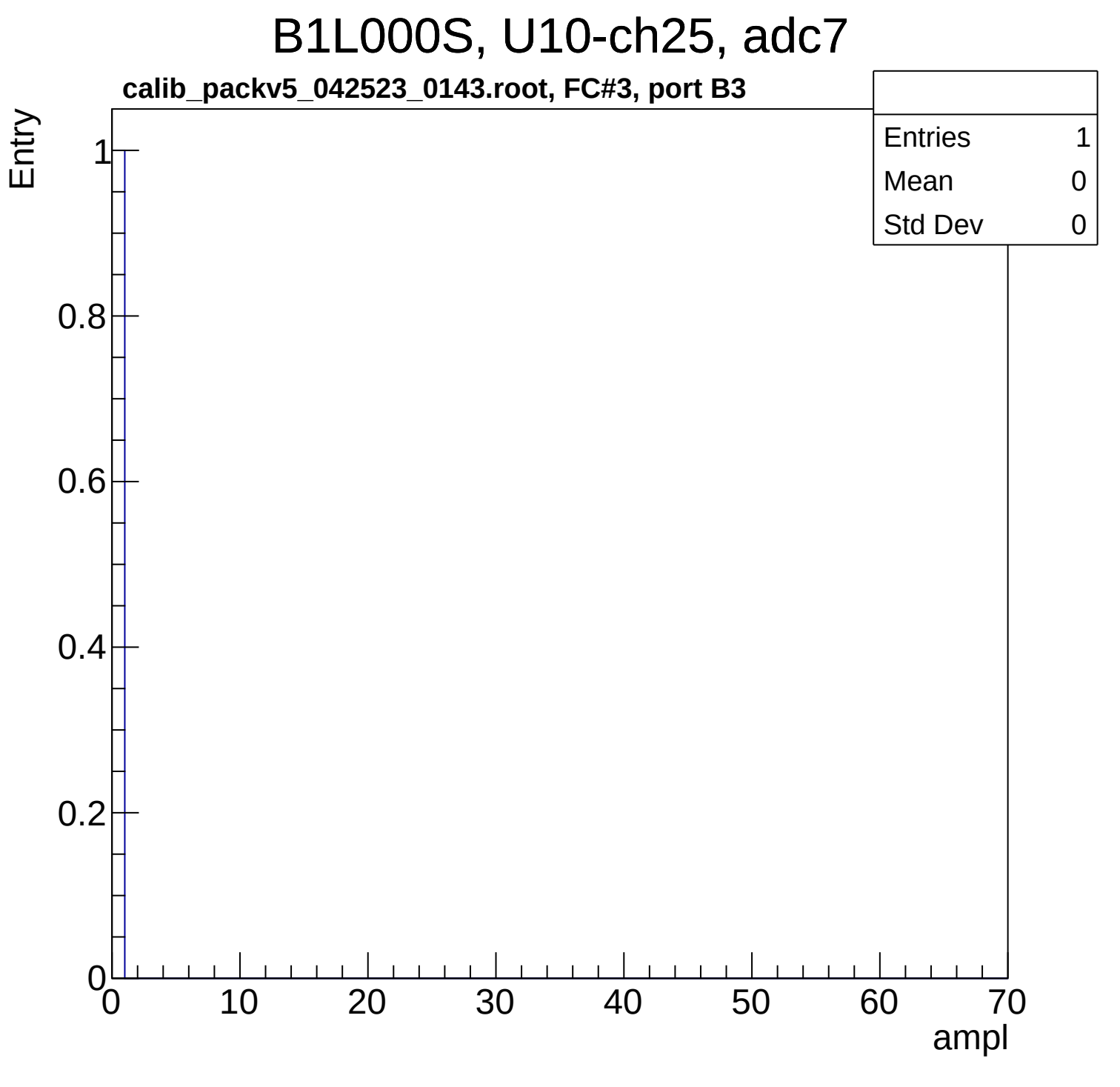

B1L000S, U10-ch25, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 42.12
Std Dev 2.687
Gaus mean : 42.9376
Gaus Width: 2.5192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

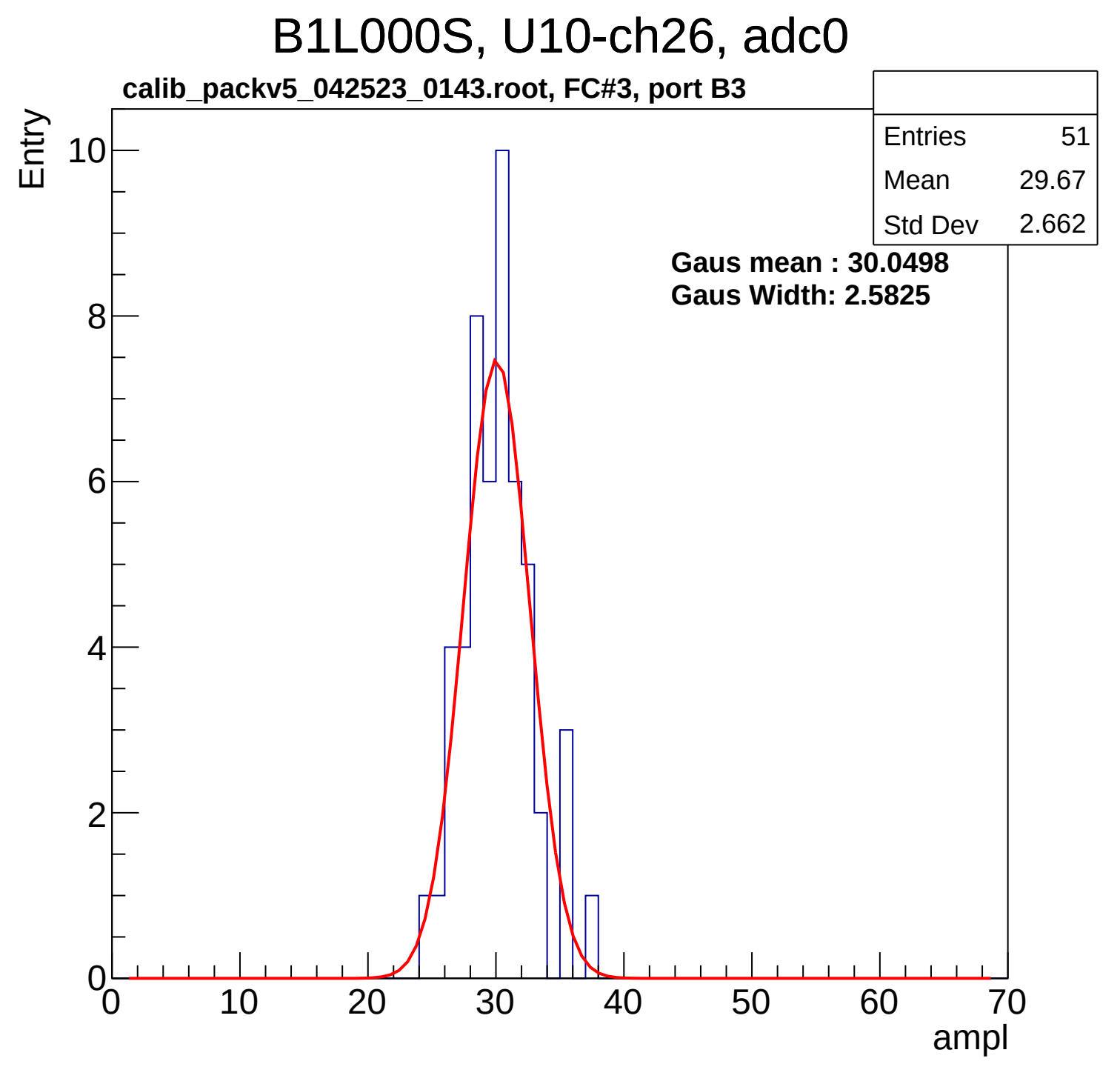

B1L000S, U10-ch25, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.09
Std Dev 2.783
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

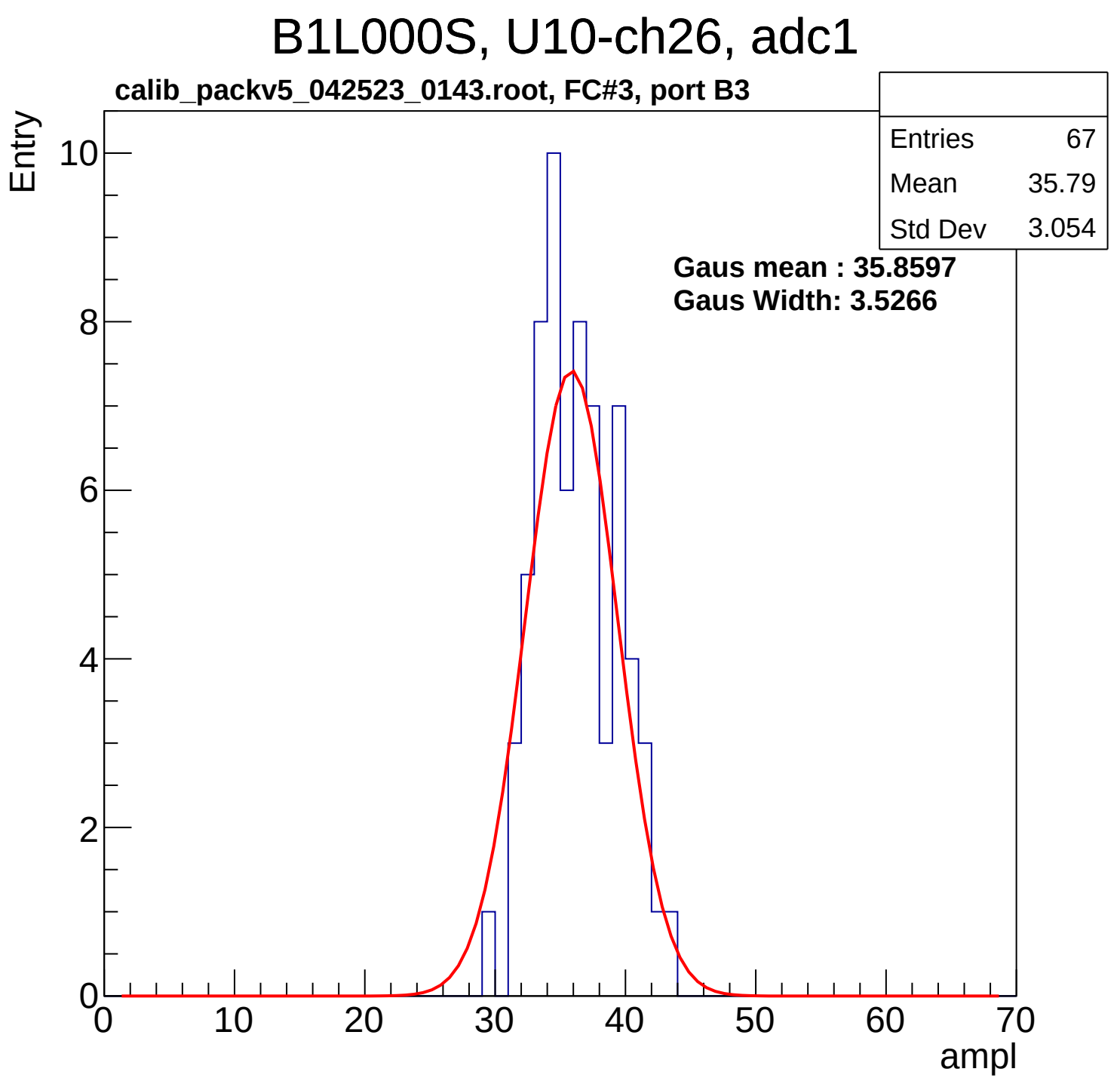

B1L000S, U10-ch25, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.34
Std Dev 2.669
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

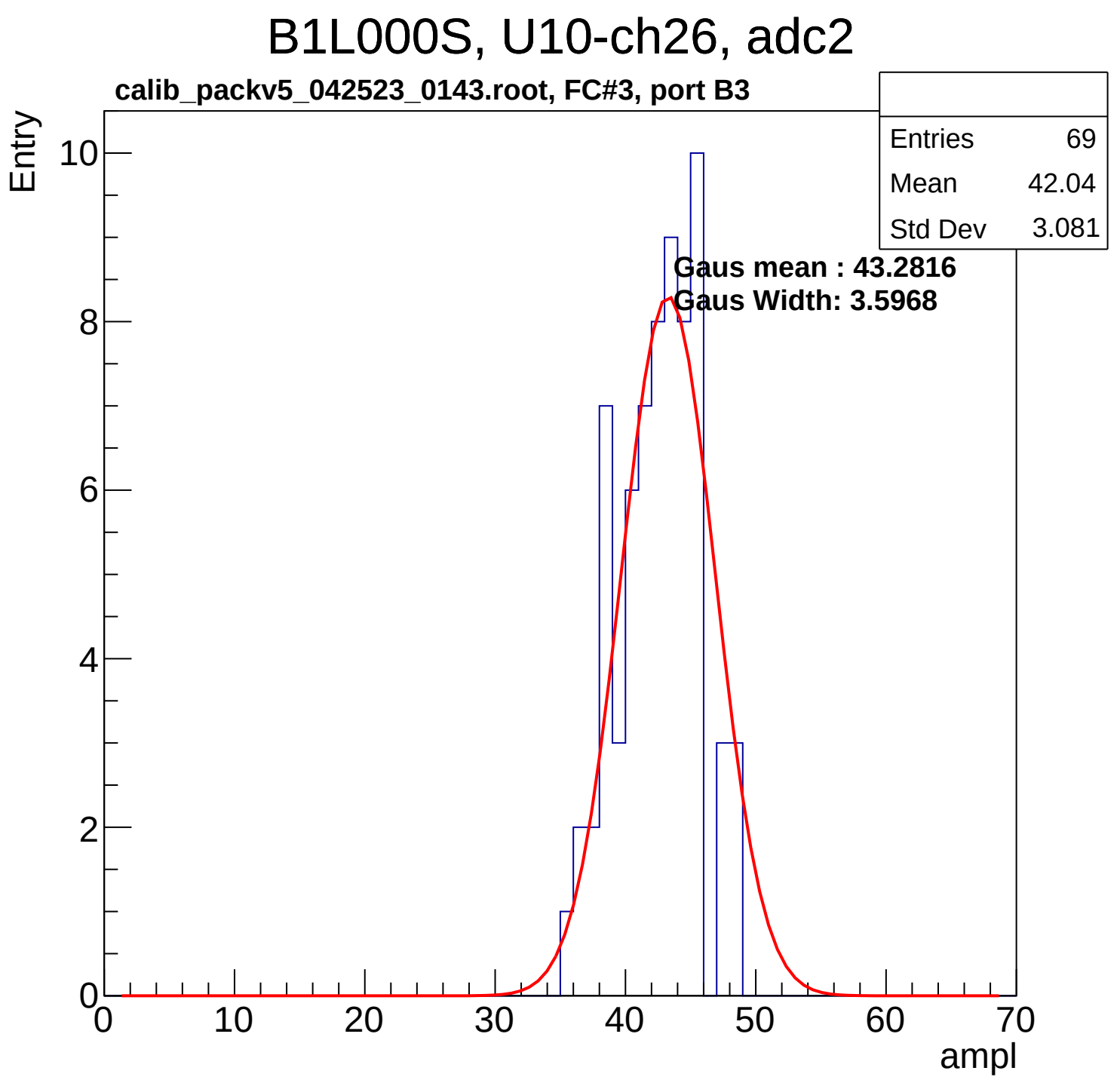

B1L000S, U10-ch25, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 59.3
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

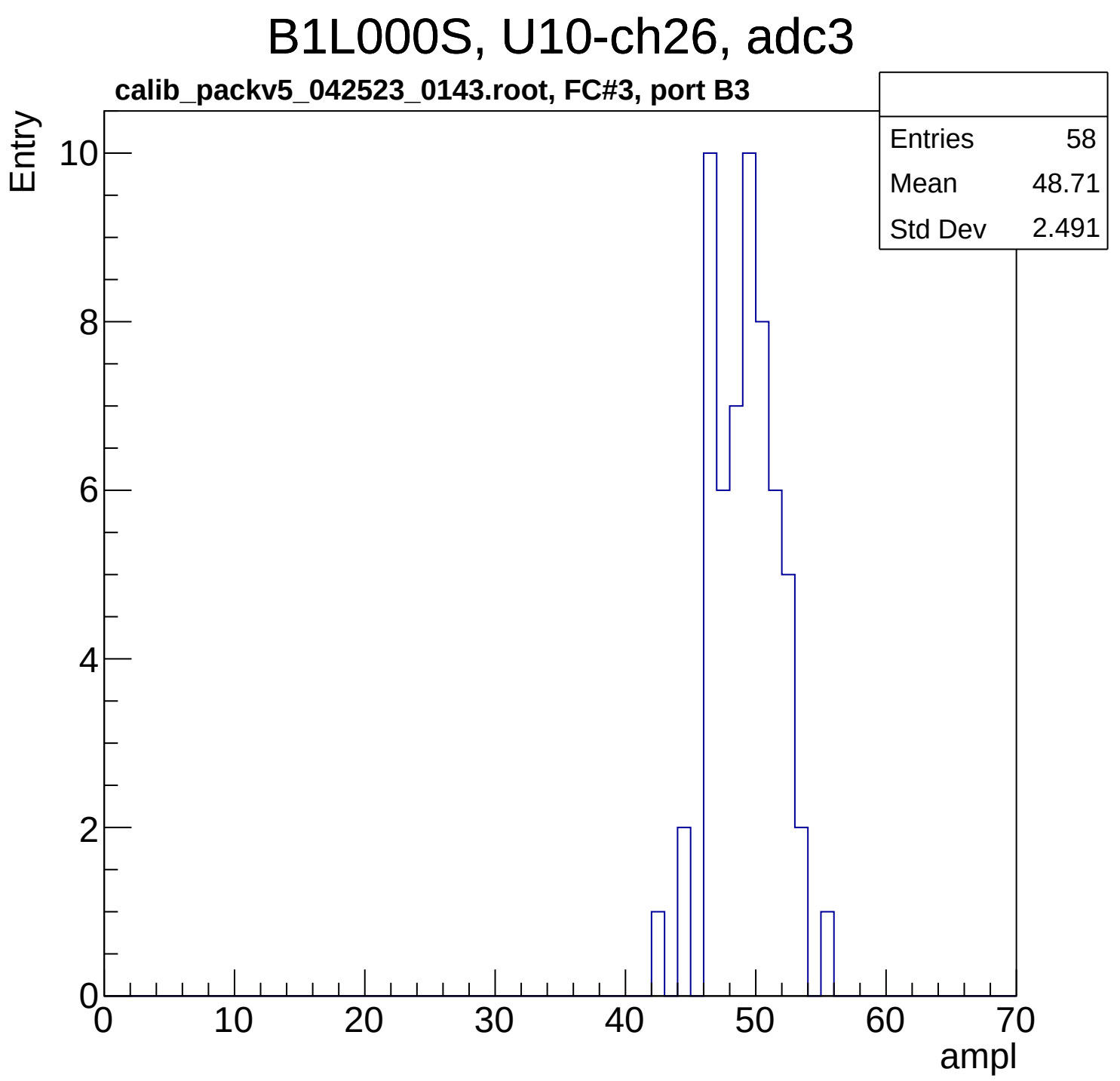

B1L000S, U10-ch25, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.53
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

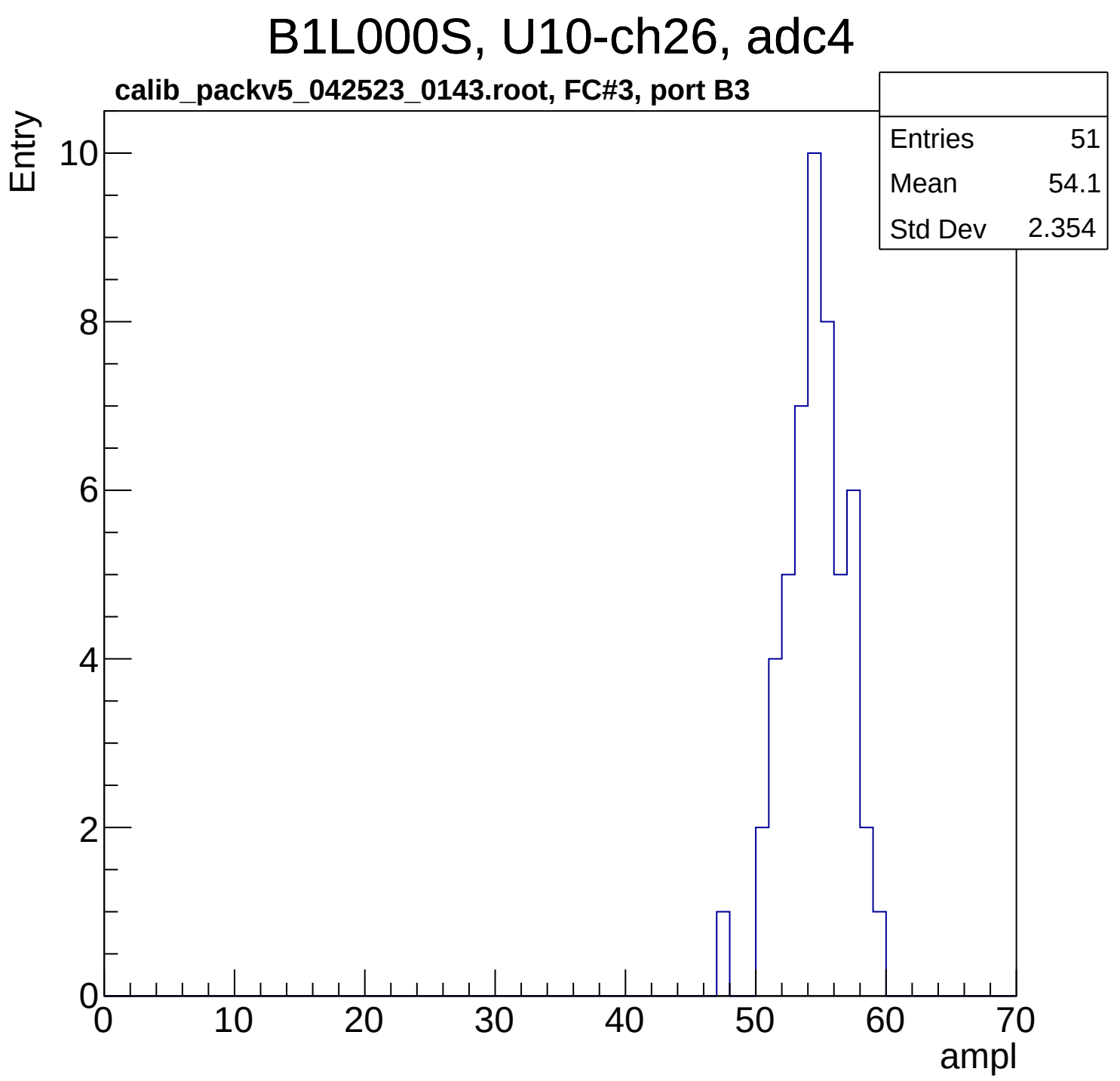

B1L000S, U10-ch25, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

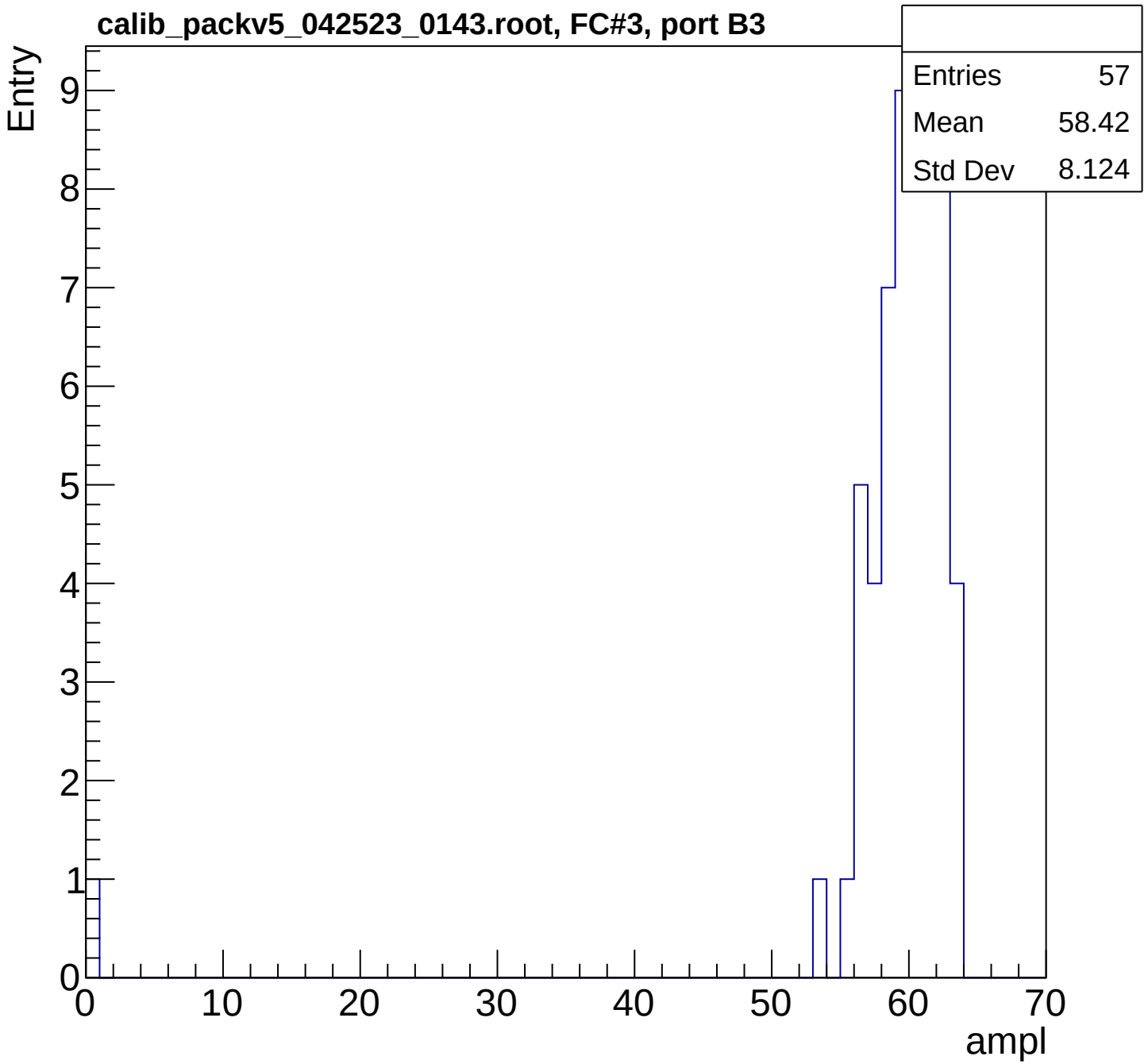

B1L000S, U10-ch26, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 29.67
Std Dev 2.662
Gaus mean : 30.0498
Gaus Width: 2.5825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

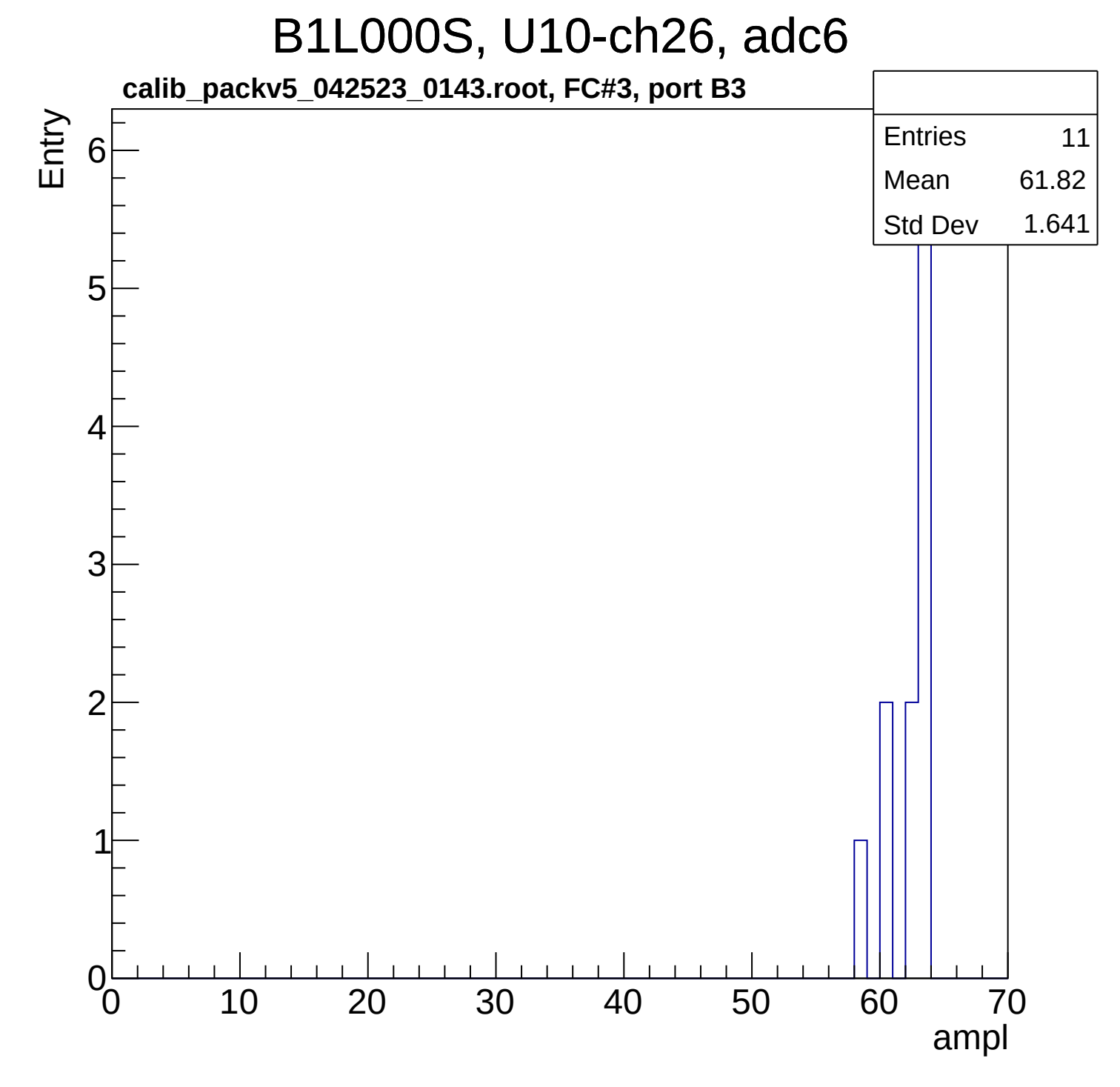

B1L000S, U10-ch26, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.79
Std Dev 3.054
Gaus mean : 35.8597
Gaus Width: 3.5266
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

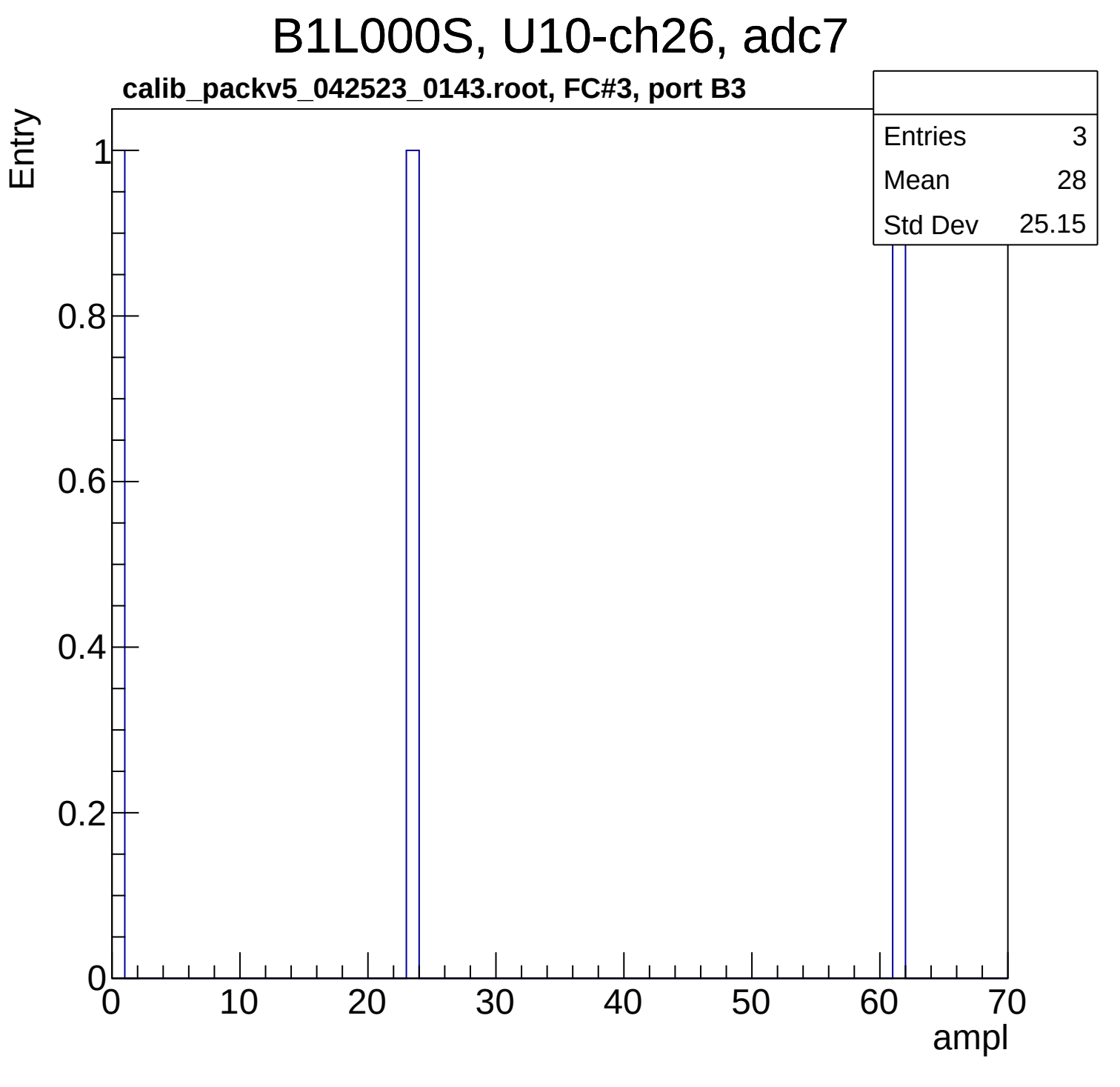

B1L000S, U10-ch26, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 42.04
Std Dev 3.081
Gaus mean : 43.2816
Gaus Width: 3.5968
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

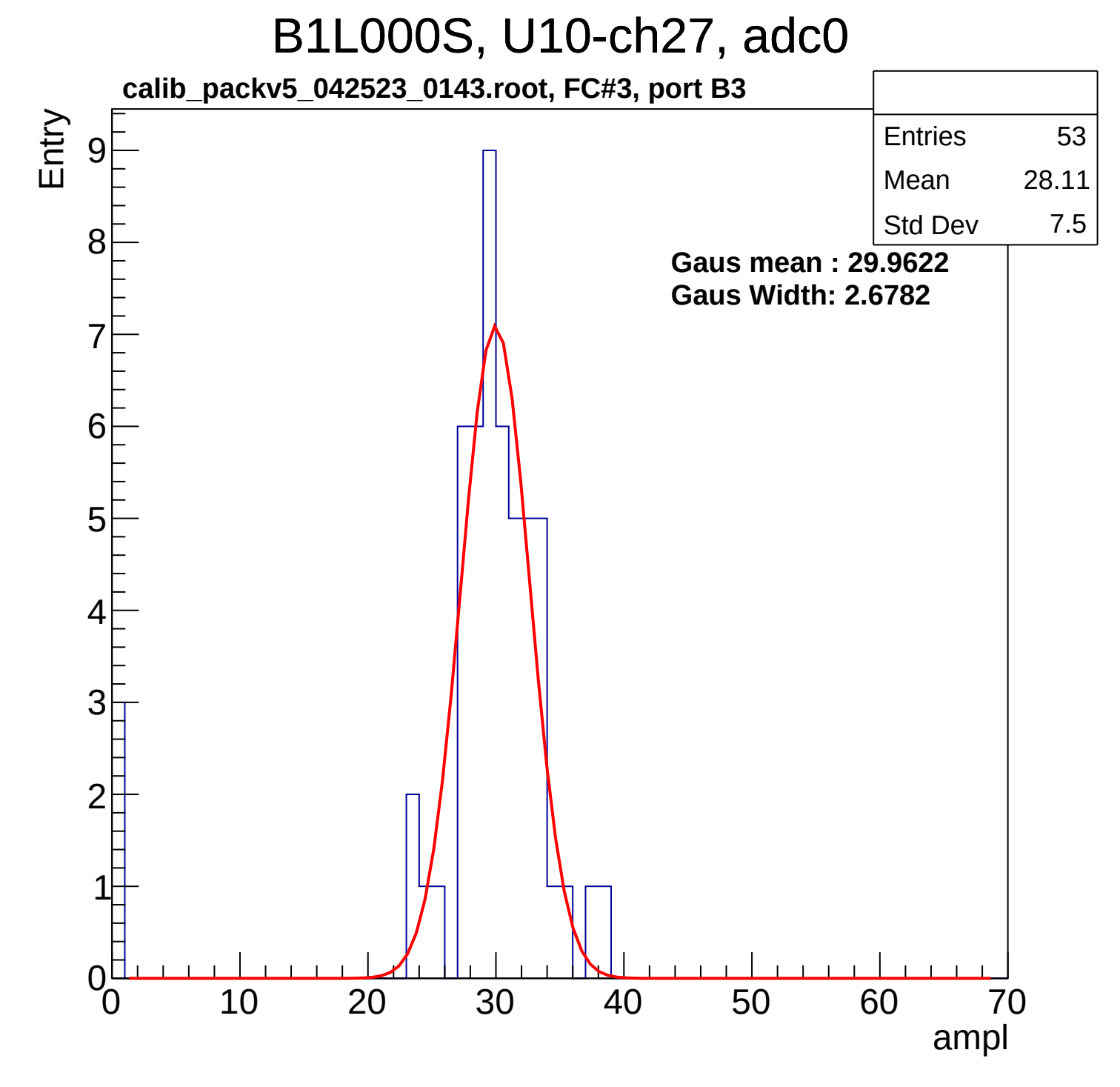

B1L000S, U10-ch26, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 48.71
Std Dev 2.491
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

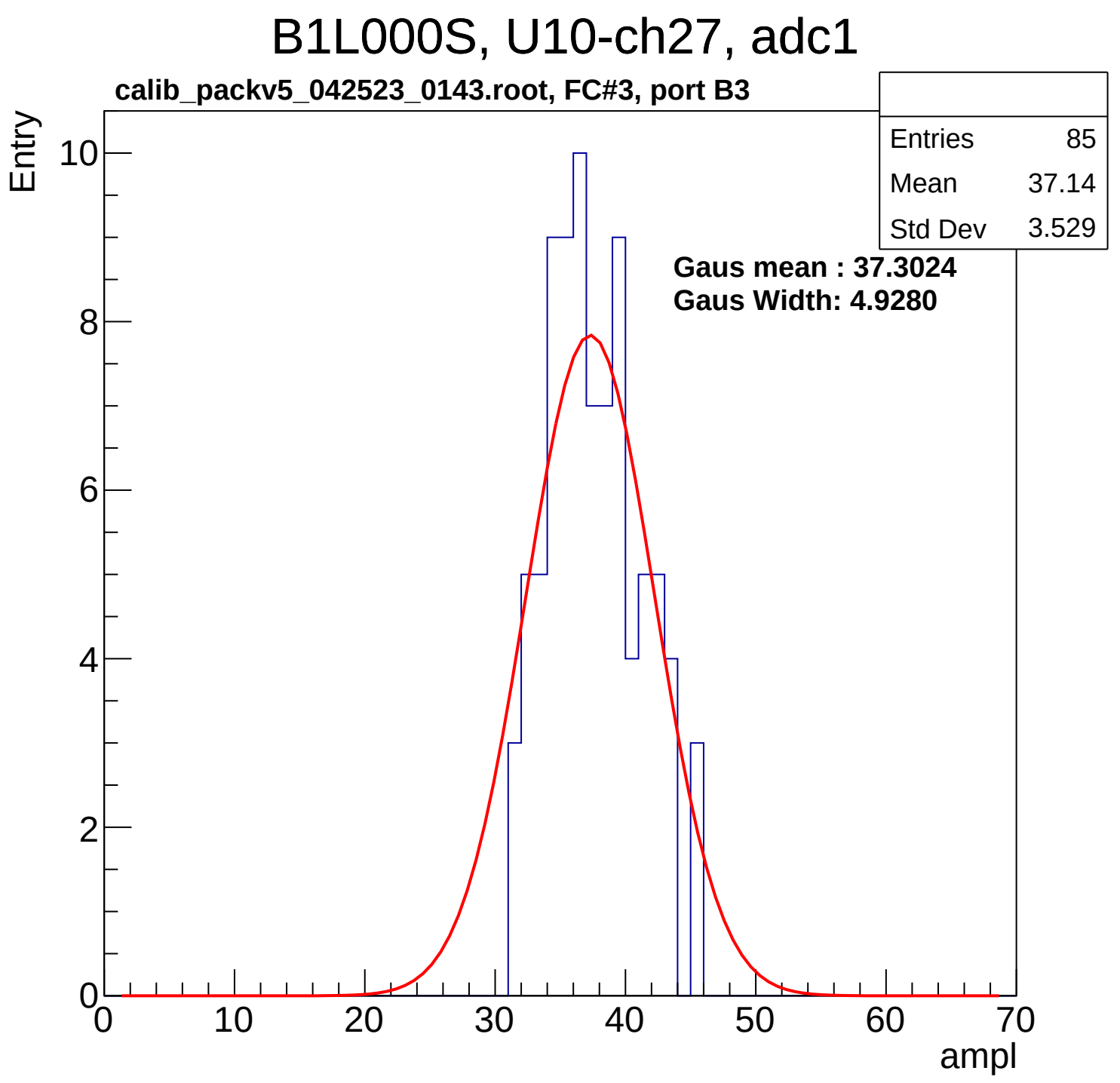

B1L000S, U10-ch26, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.1
Std Dev 2.354
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch26, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.42
Std Dev 8.124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch26, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.82
Std Dev 1.641
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch26, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 25.15
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch27, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 28.11
Std Dev 7.5
Gaus mean : 29.9622
Gaus Width: 2.6782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch27, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 85
Mean 37.14
Std Dev 3.529
Gaus mean : 37.3024
Gaus Width: 4.9280
Entry
ampl
0
2
4
6
8
10
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60
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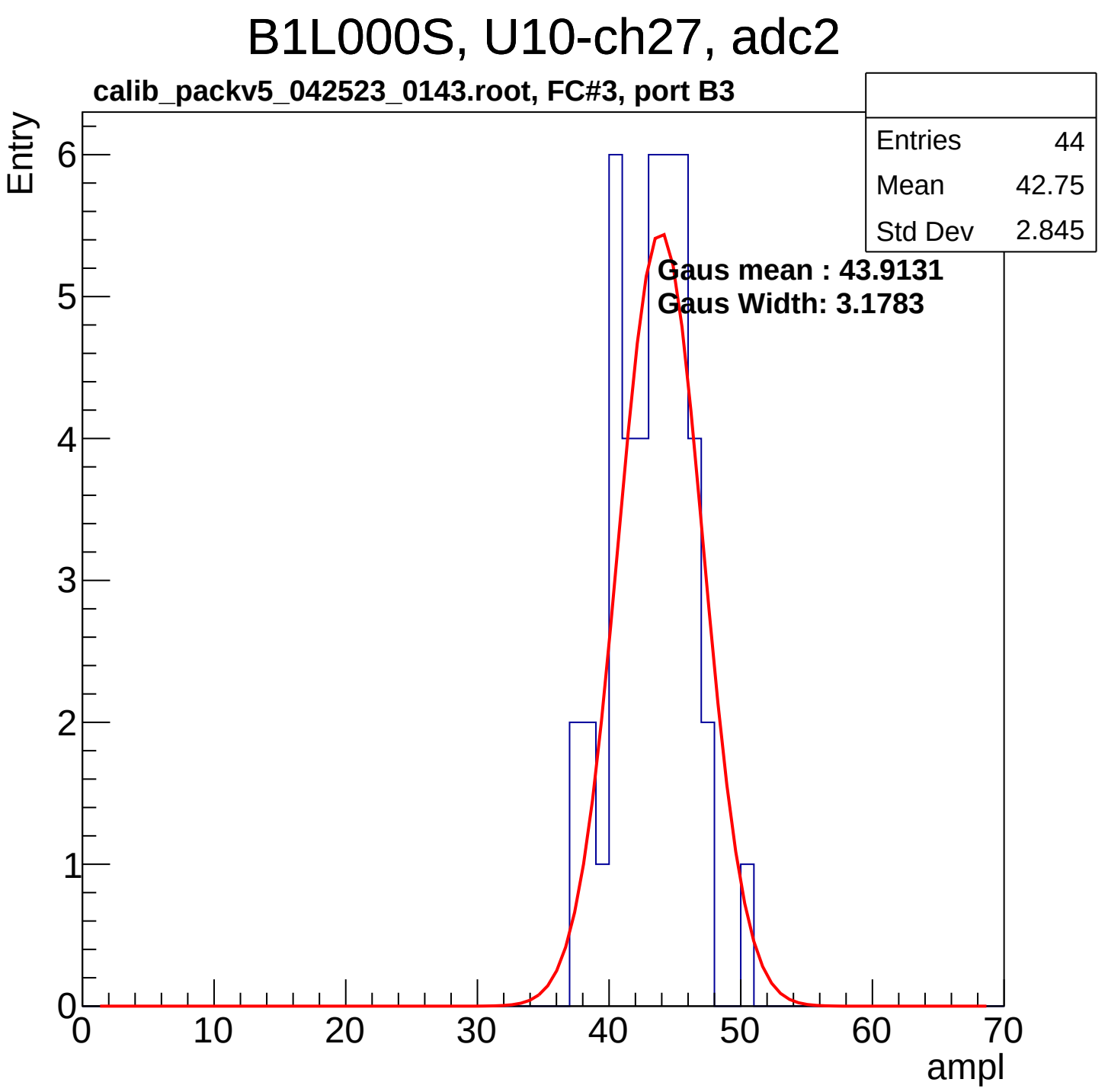
B1L000S, U10-ch27, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 42.75
Std Dev 2.845
Gaus mean : 43.9131
Gaus Width: 3.1783
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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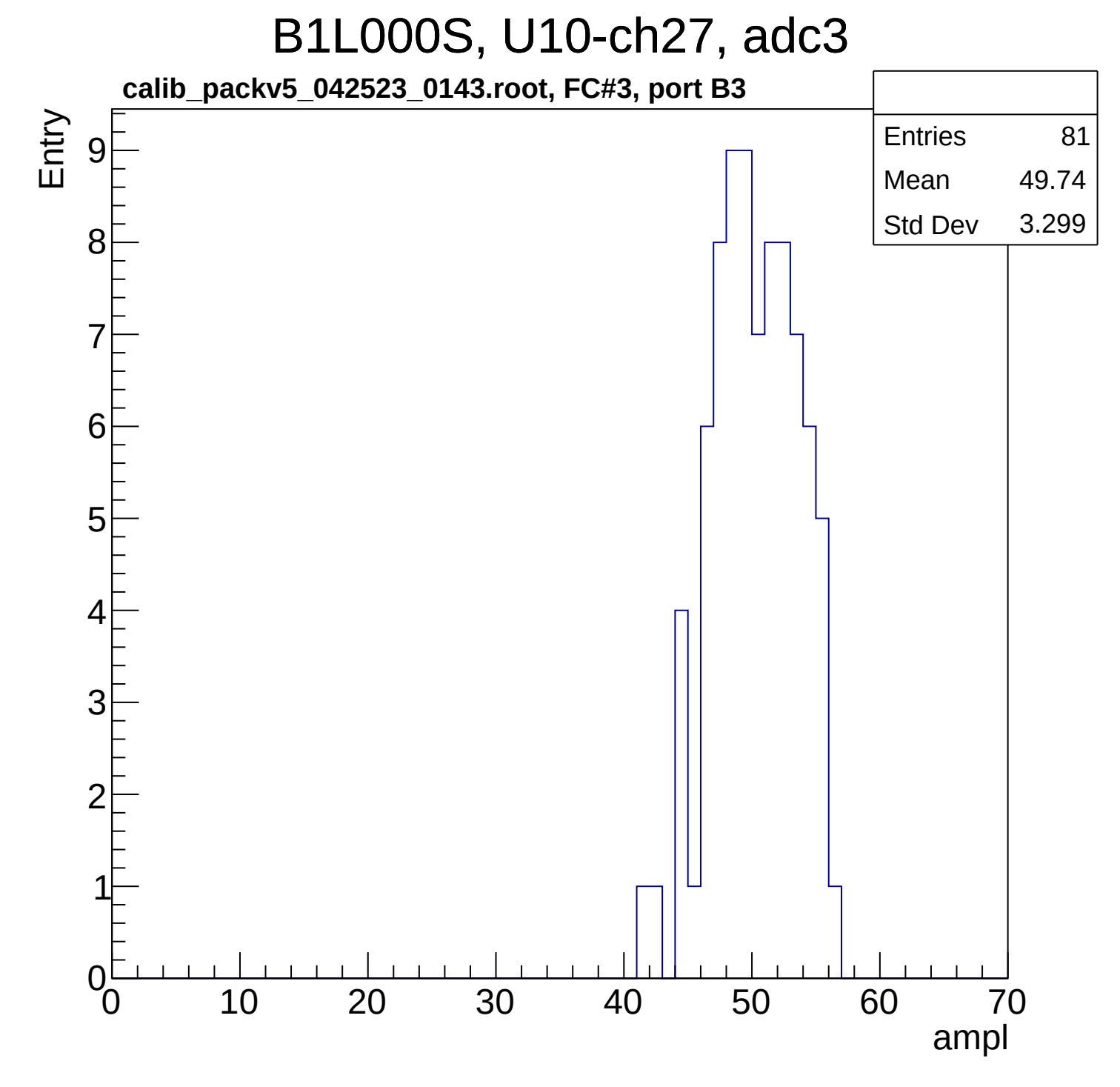
B1L000S, U10-ch27, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 49.74
Std Dev 3.299
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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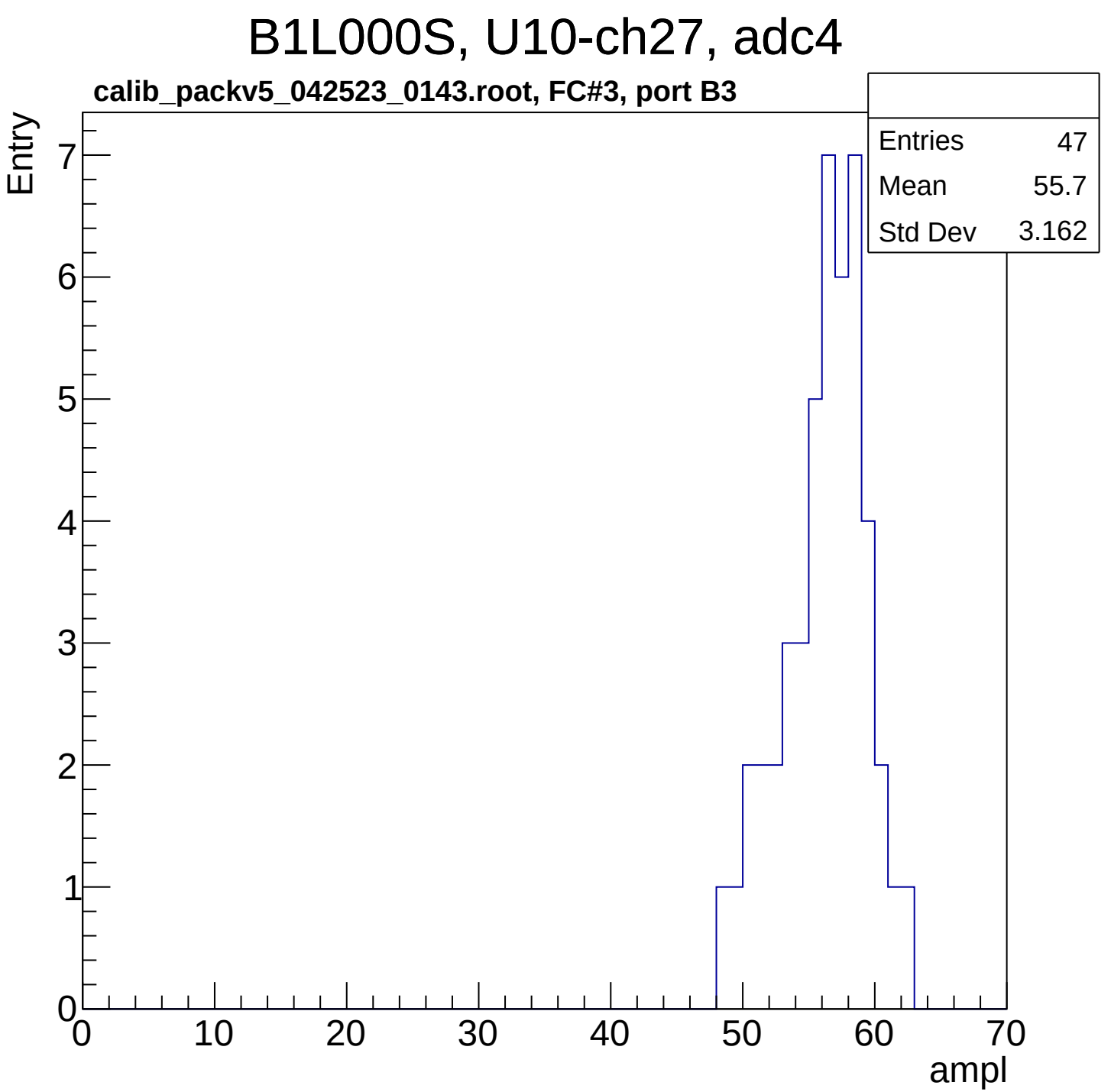
B1L000S, U10-ch27, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 55.7
Std Dev 3.162
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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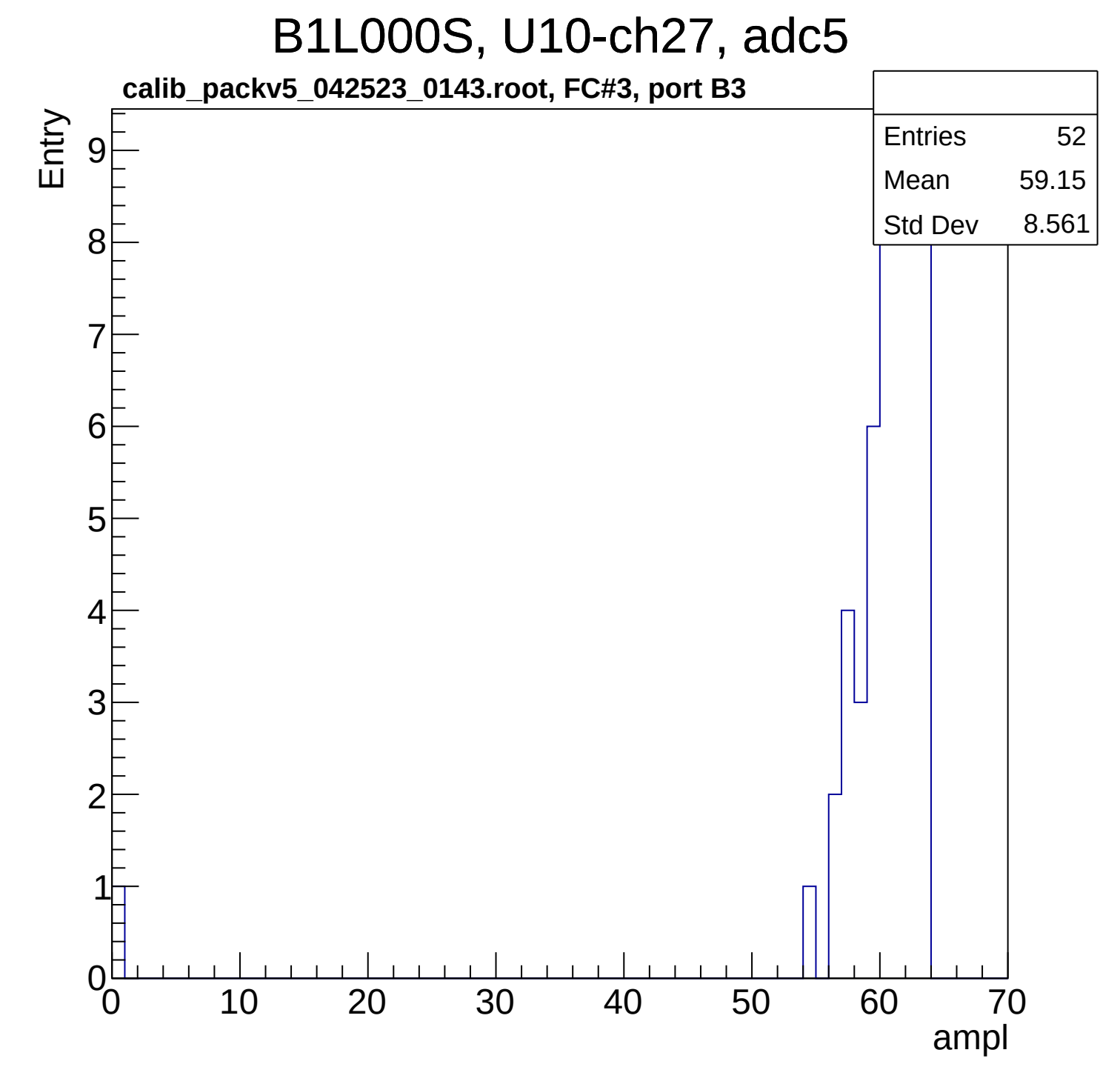
B1L000S, U10-ch27, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 59.15
Std Dev 8.561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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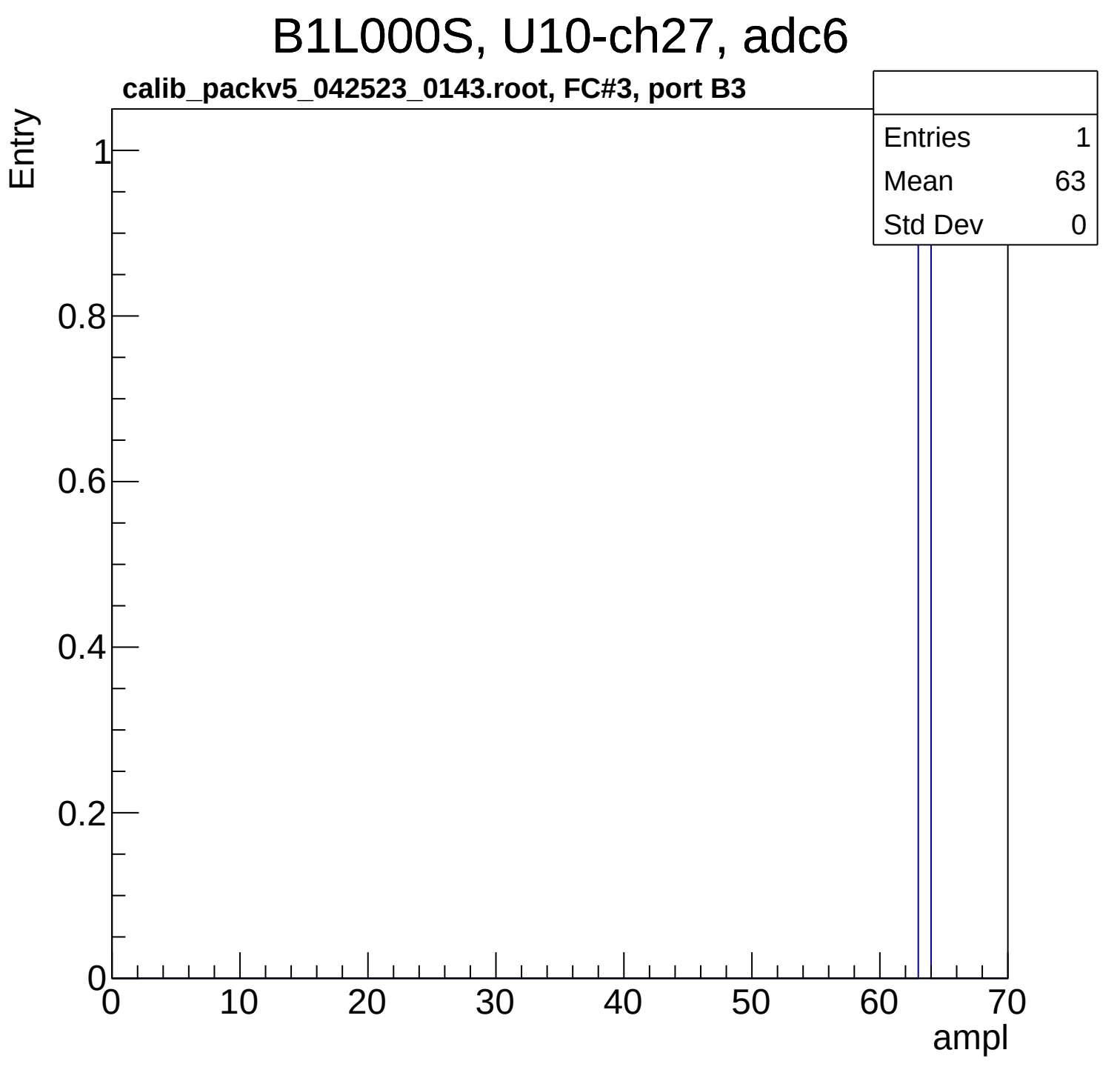
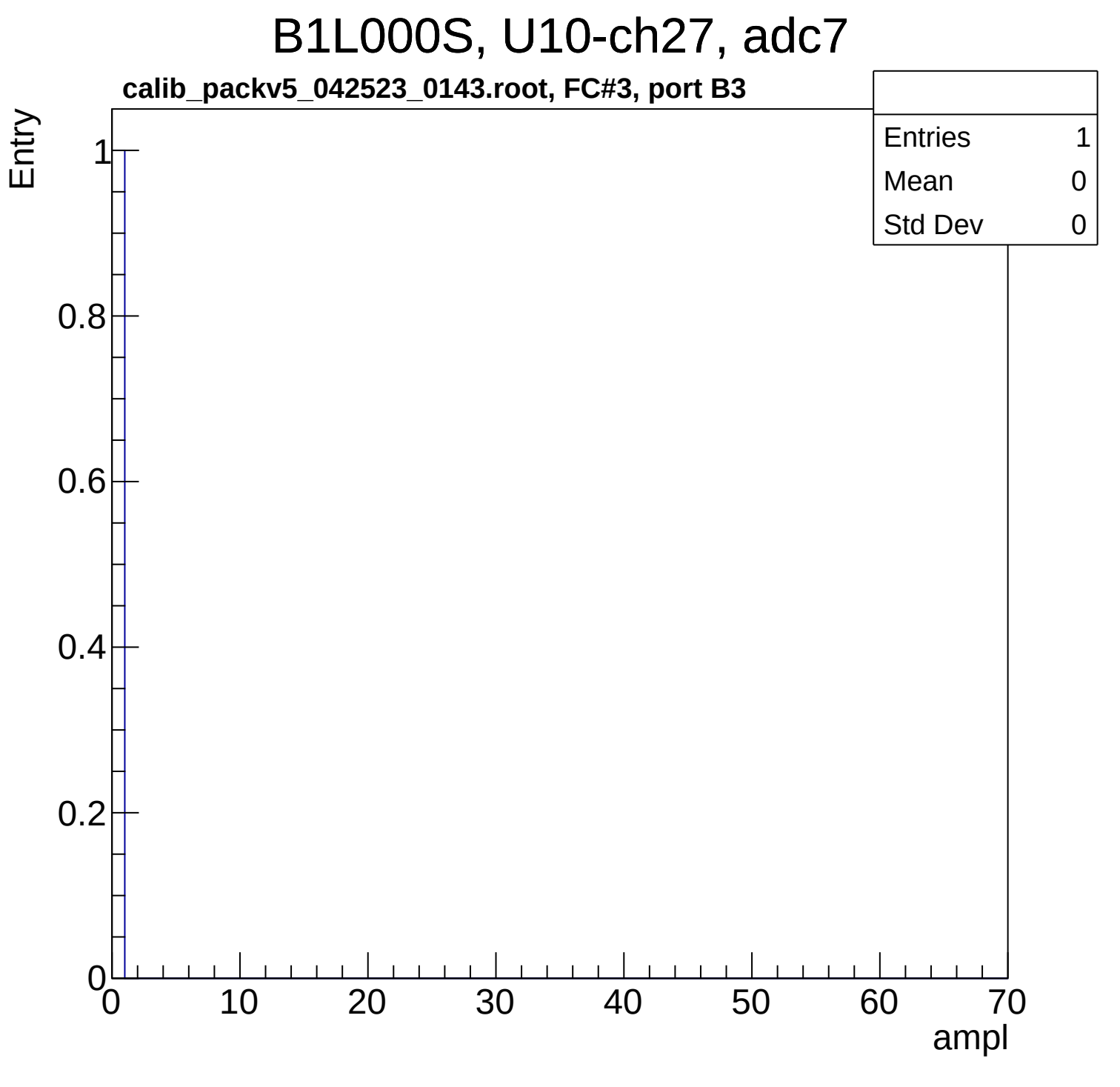
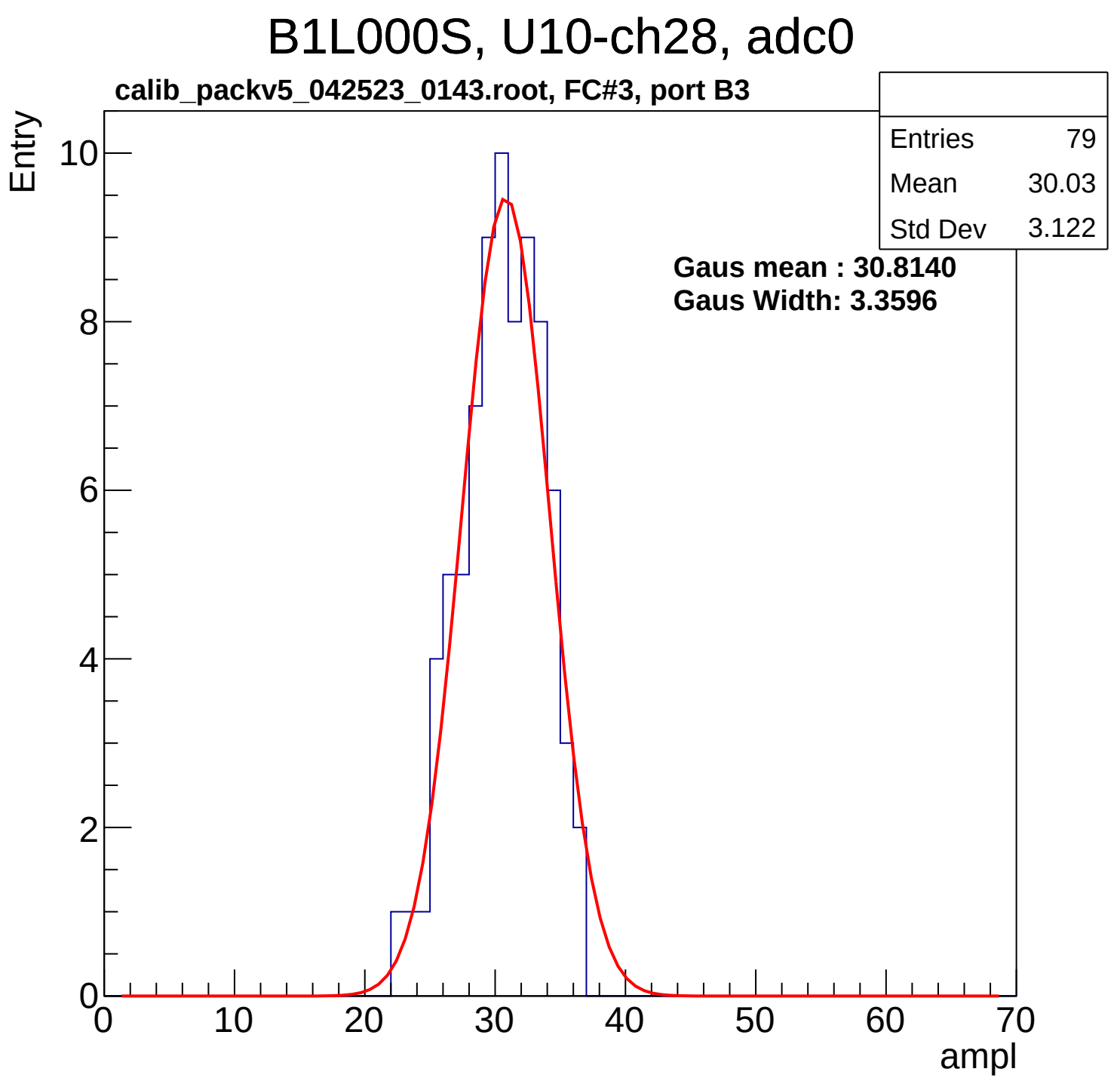
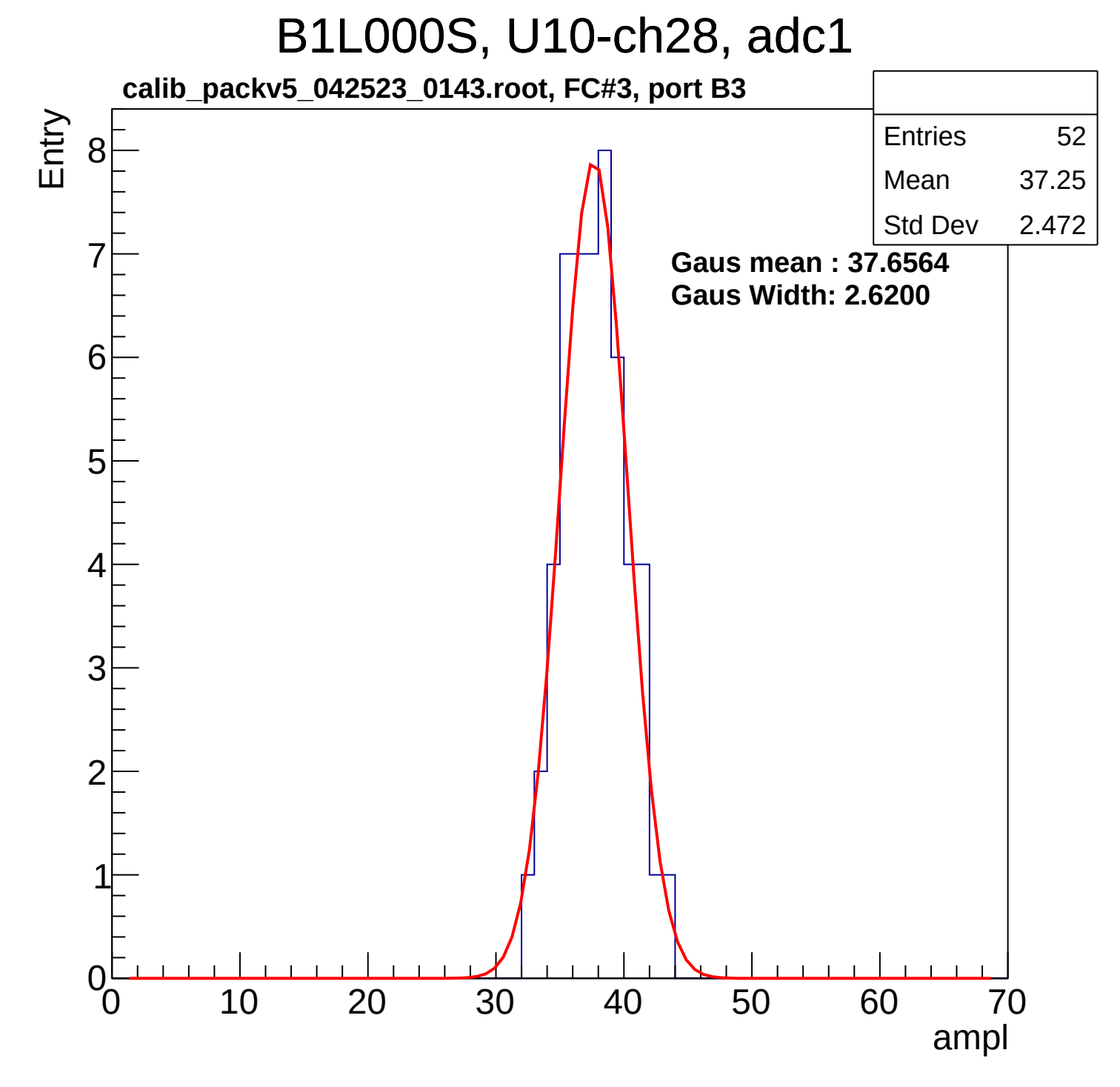
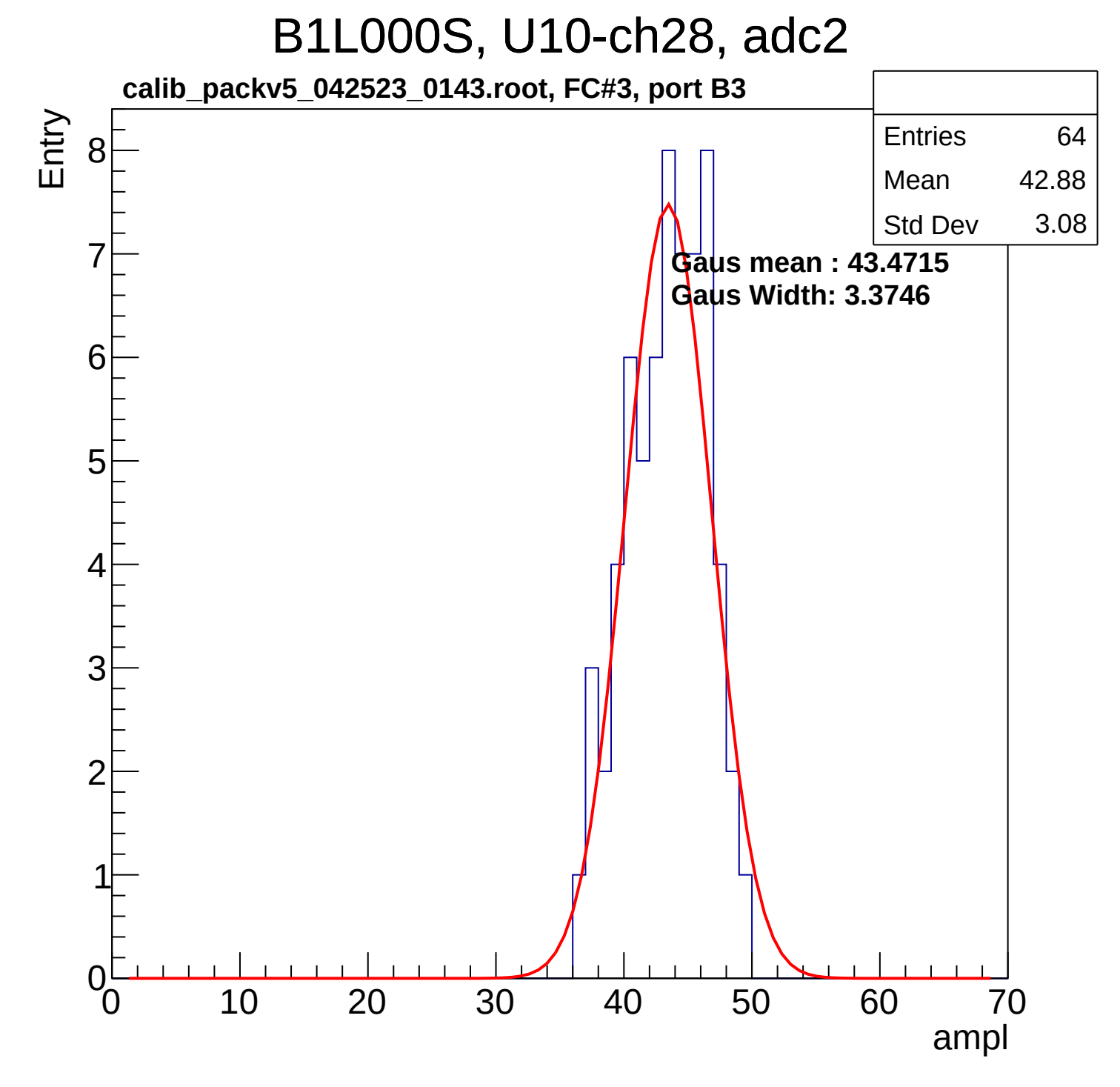
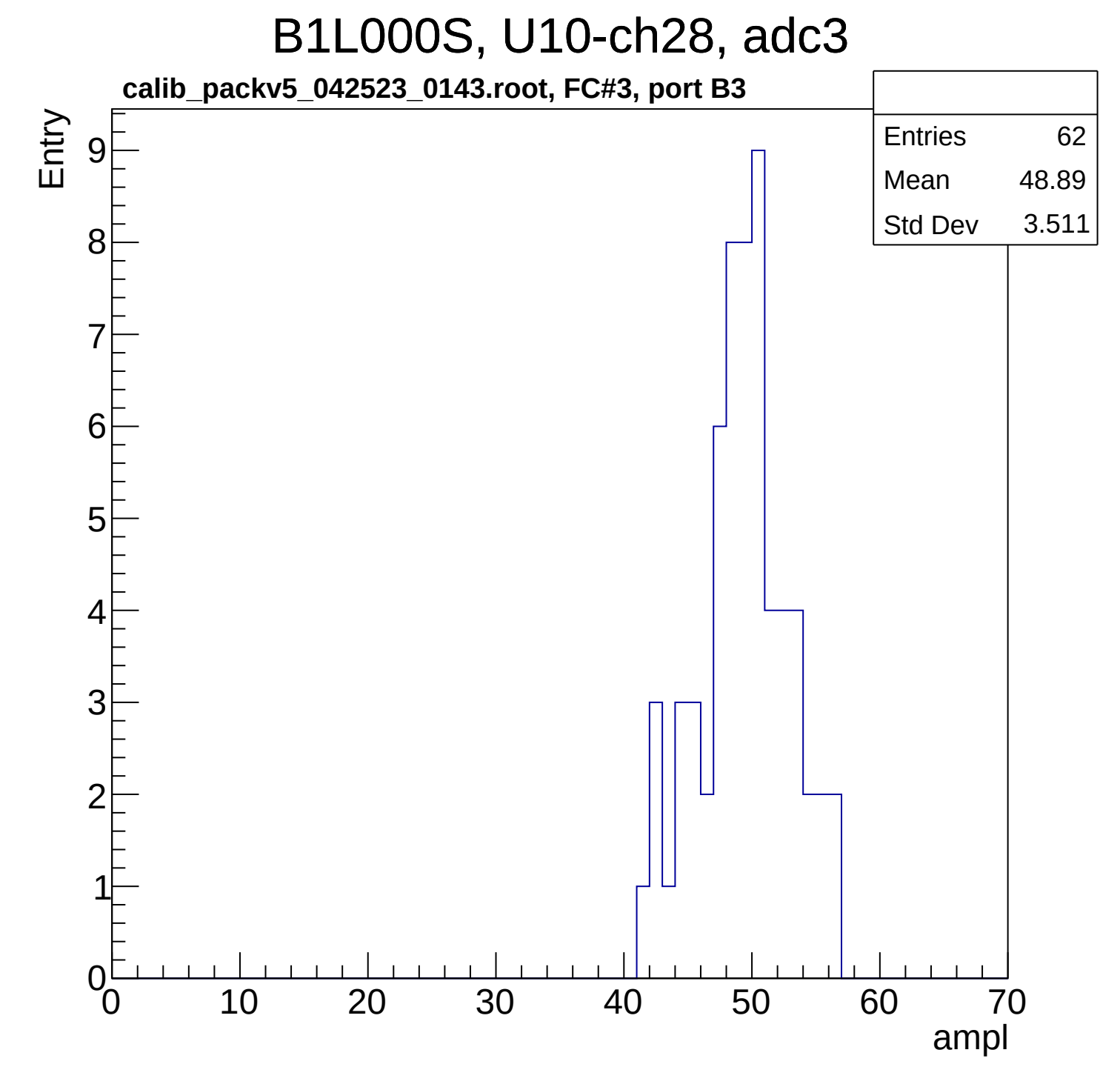

B1L000S, U10-ch27, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

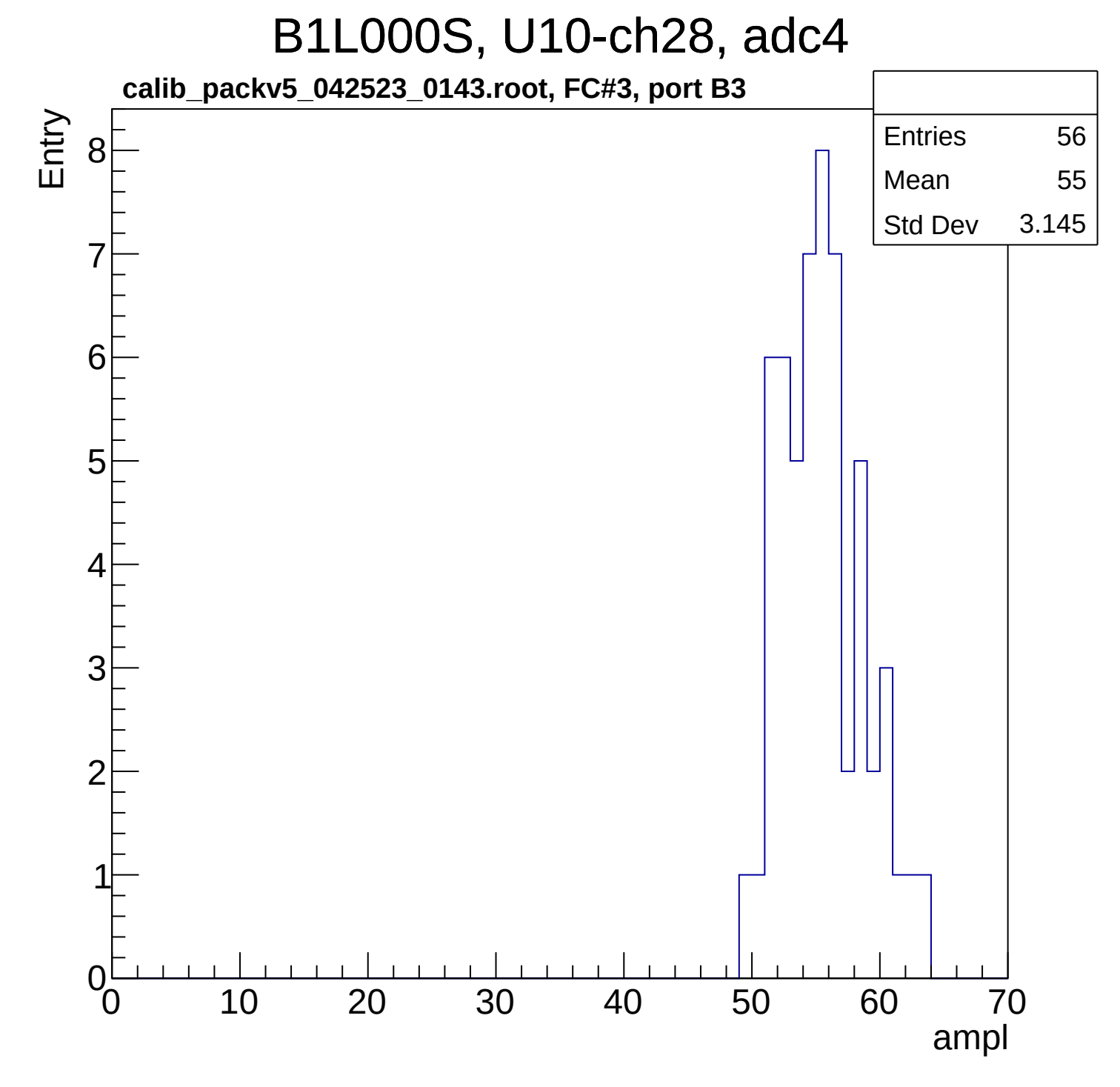

B1L000S, U10-ch27, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch28, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 30.03
Std Dev 3.122
Gaus mean : 30.8140
Gaus Width: 3.3596
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 37.25
Std Dev 2.472
Gaus mean : 37.6564
Gaus Width: 2.6200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.88
Std Dev 3.08
Gaus mean : 43.4715
Gaus Width: 3.3746
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 48.89
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 55
Std Dev 3.145
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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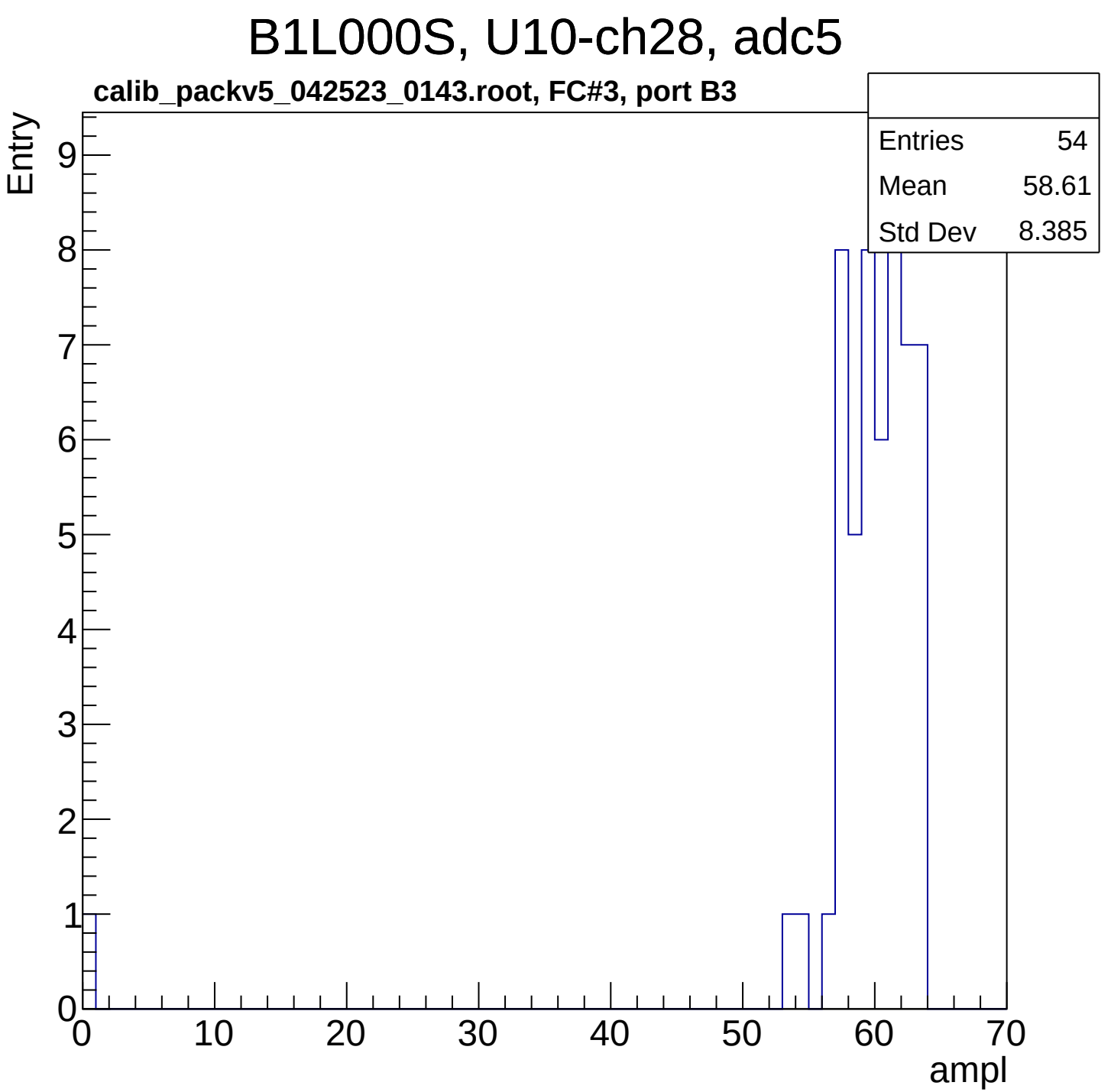
B1L000S, U10-ch28, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.61
Std Dev 8.385
Entry
ampl
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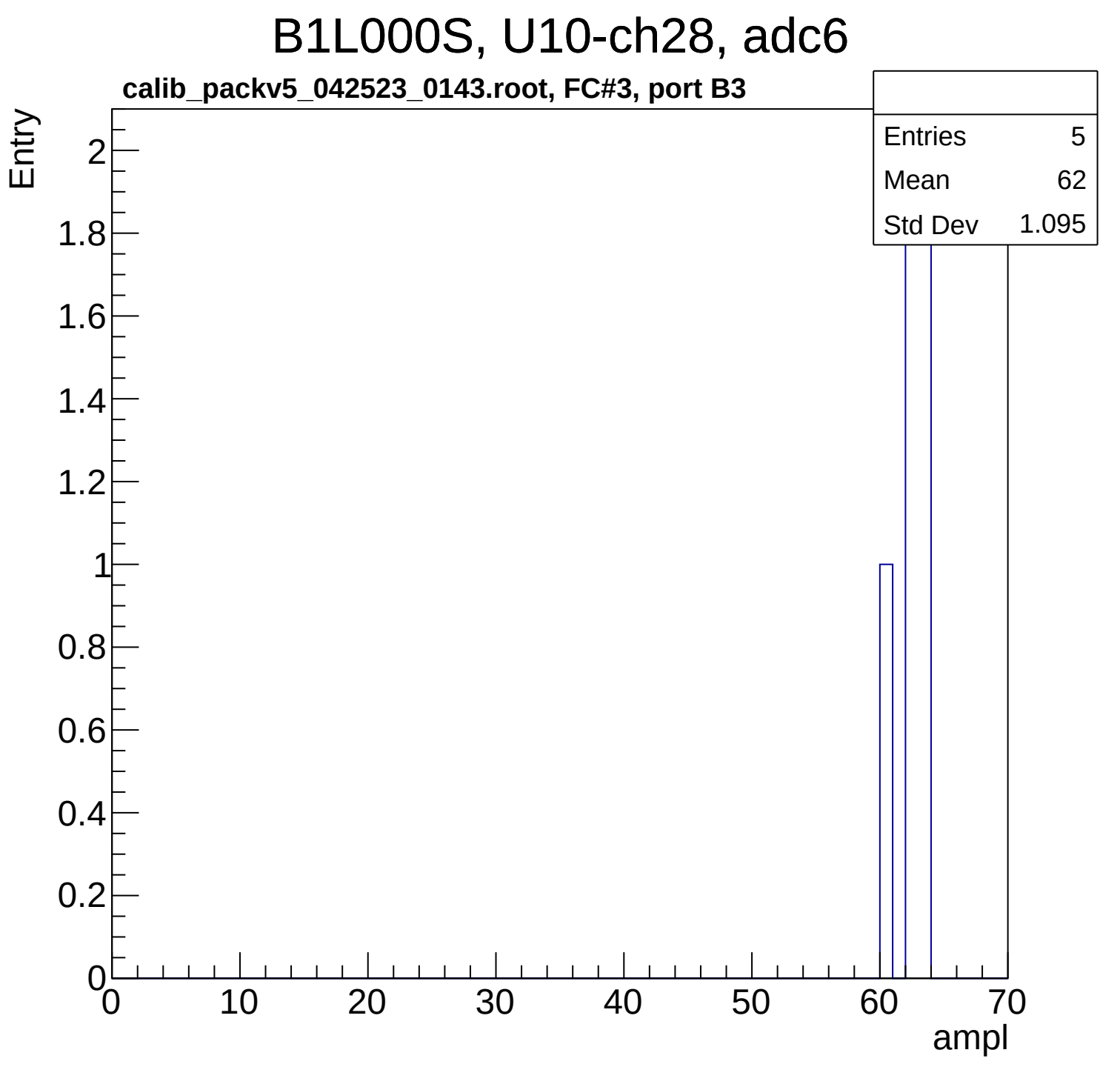
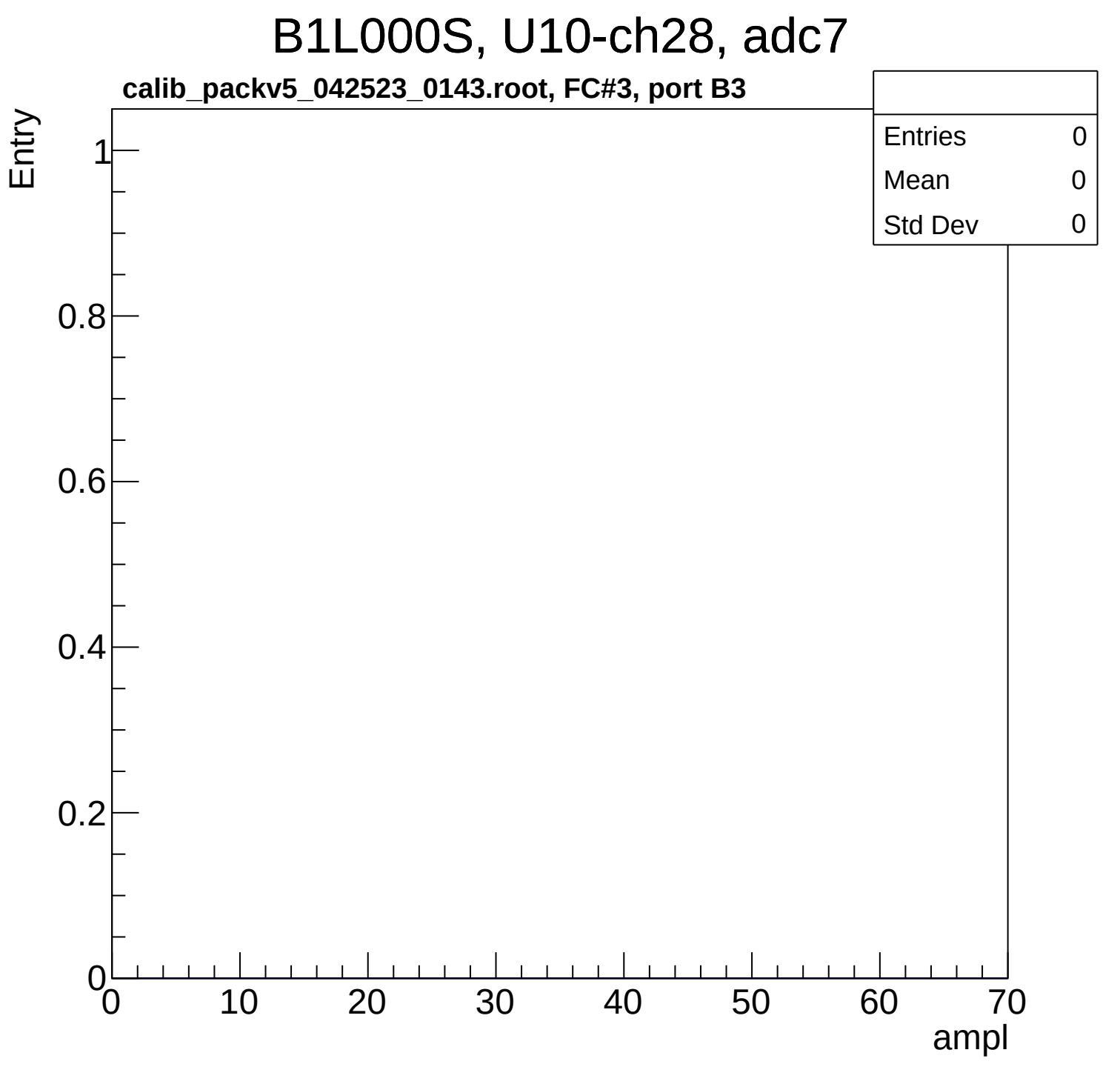
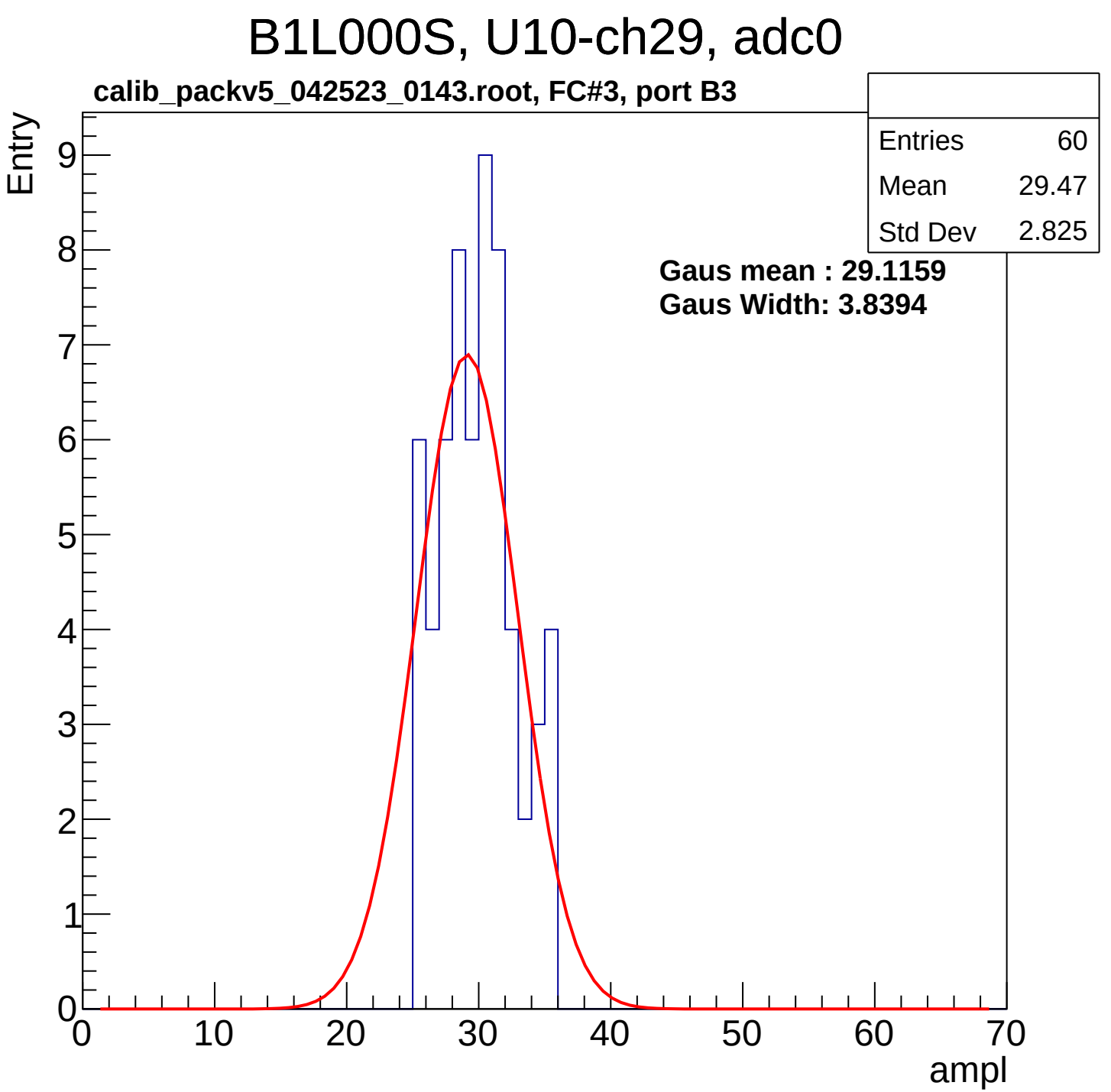
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch28, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch29, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 29.47
Std Dev 2.825
Gaus mean : 29.1159
Gaus Width: 3.8394
Entry
ampl
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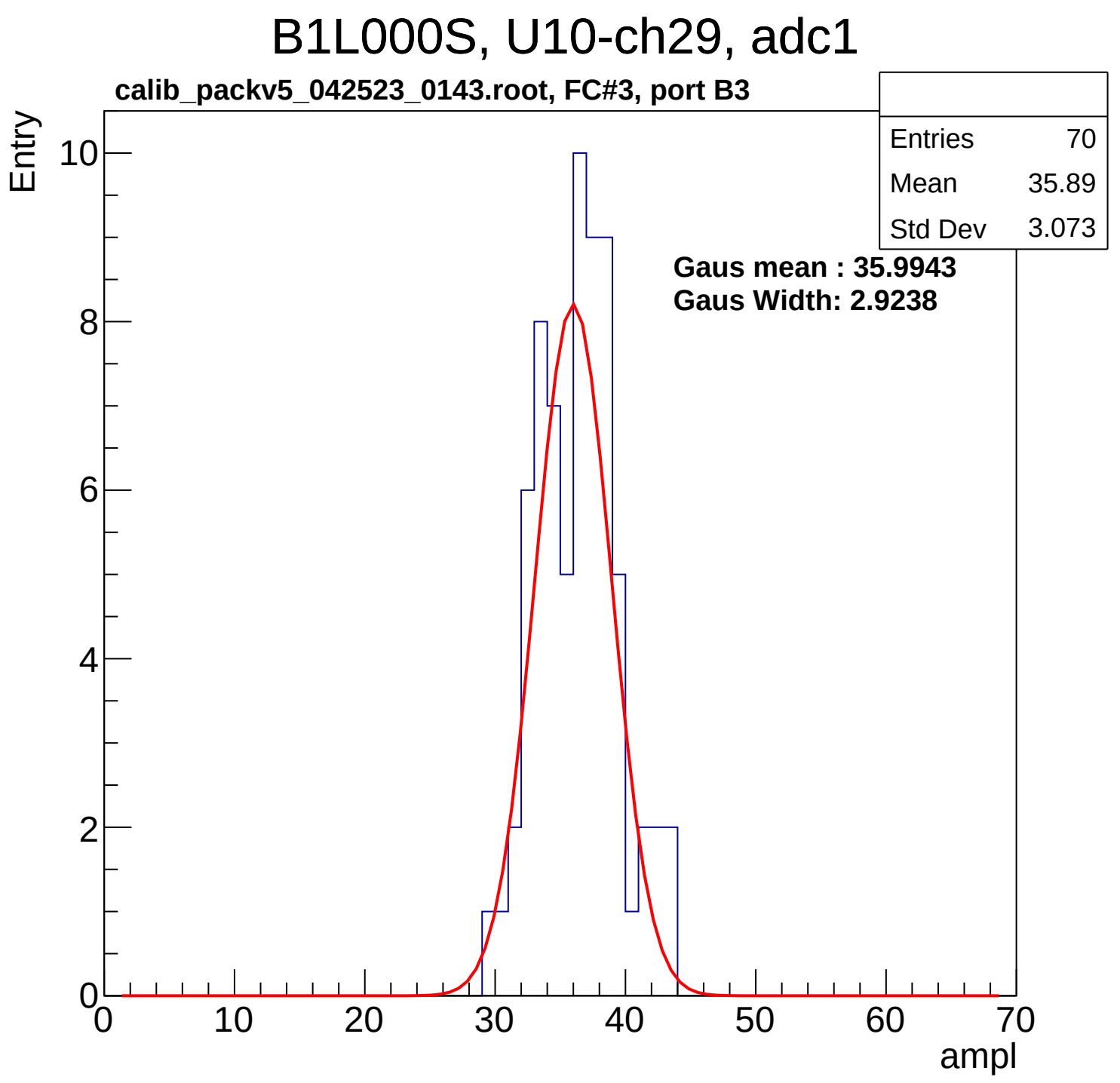
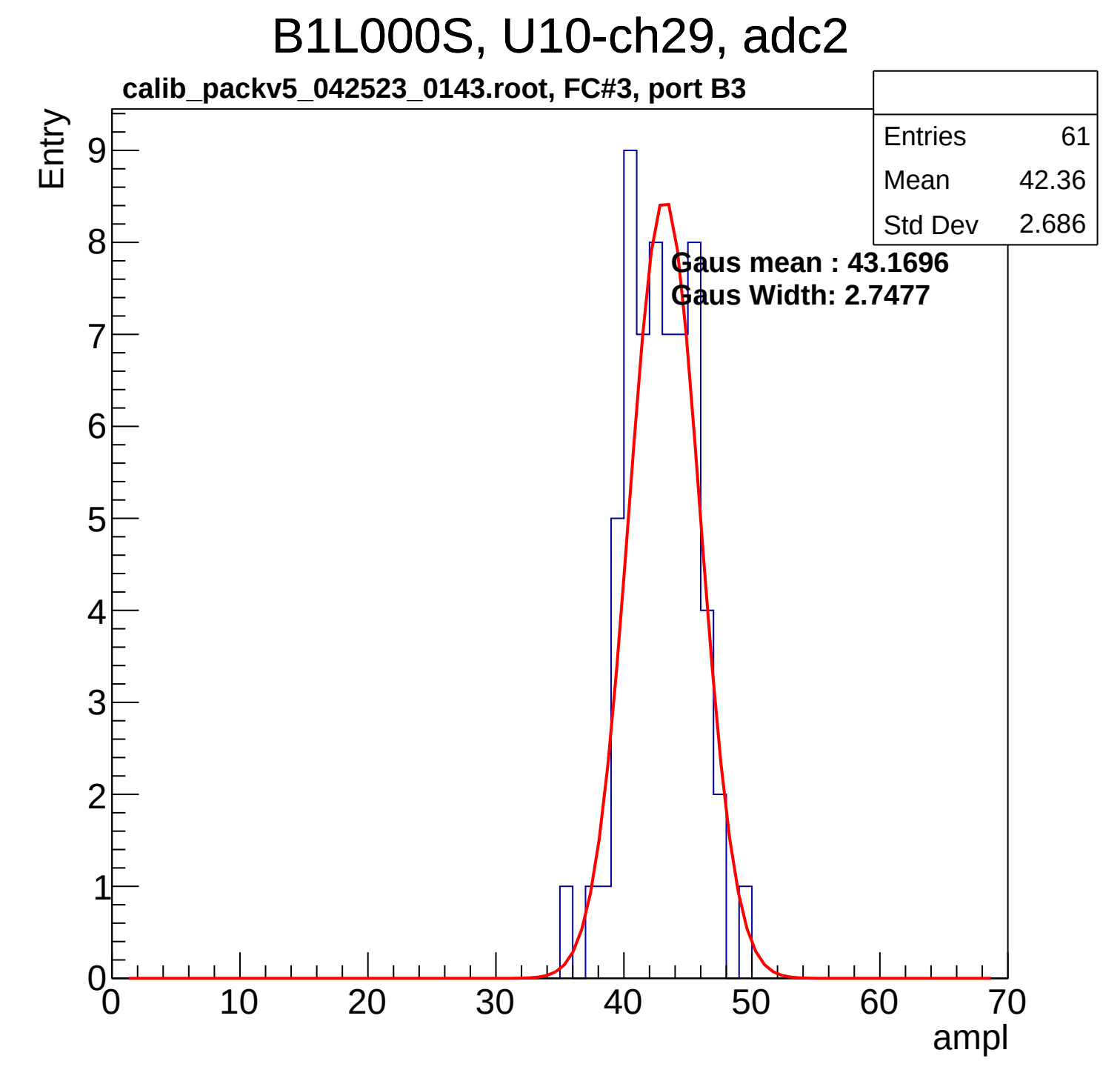
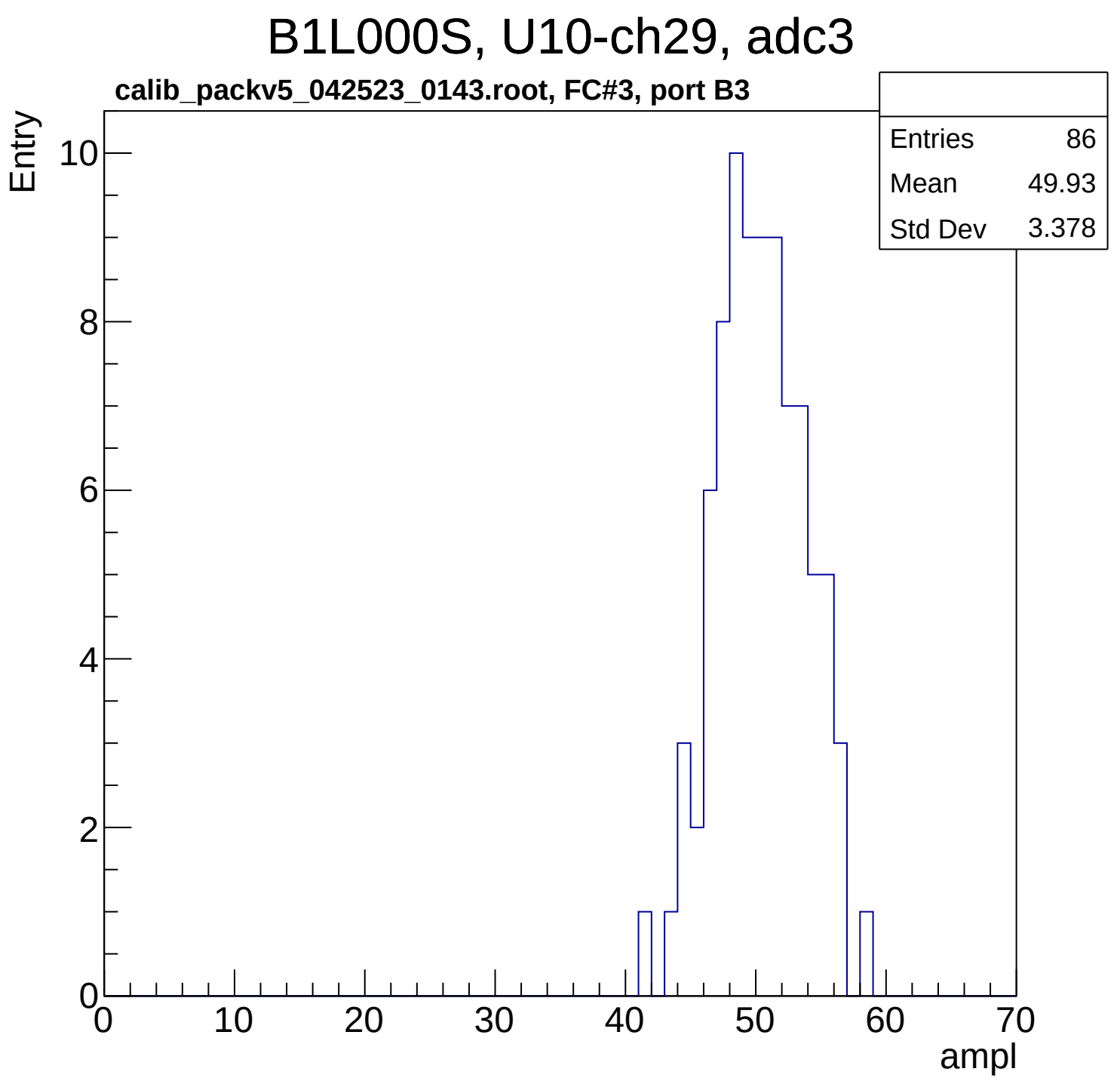
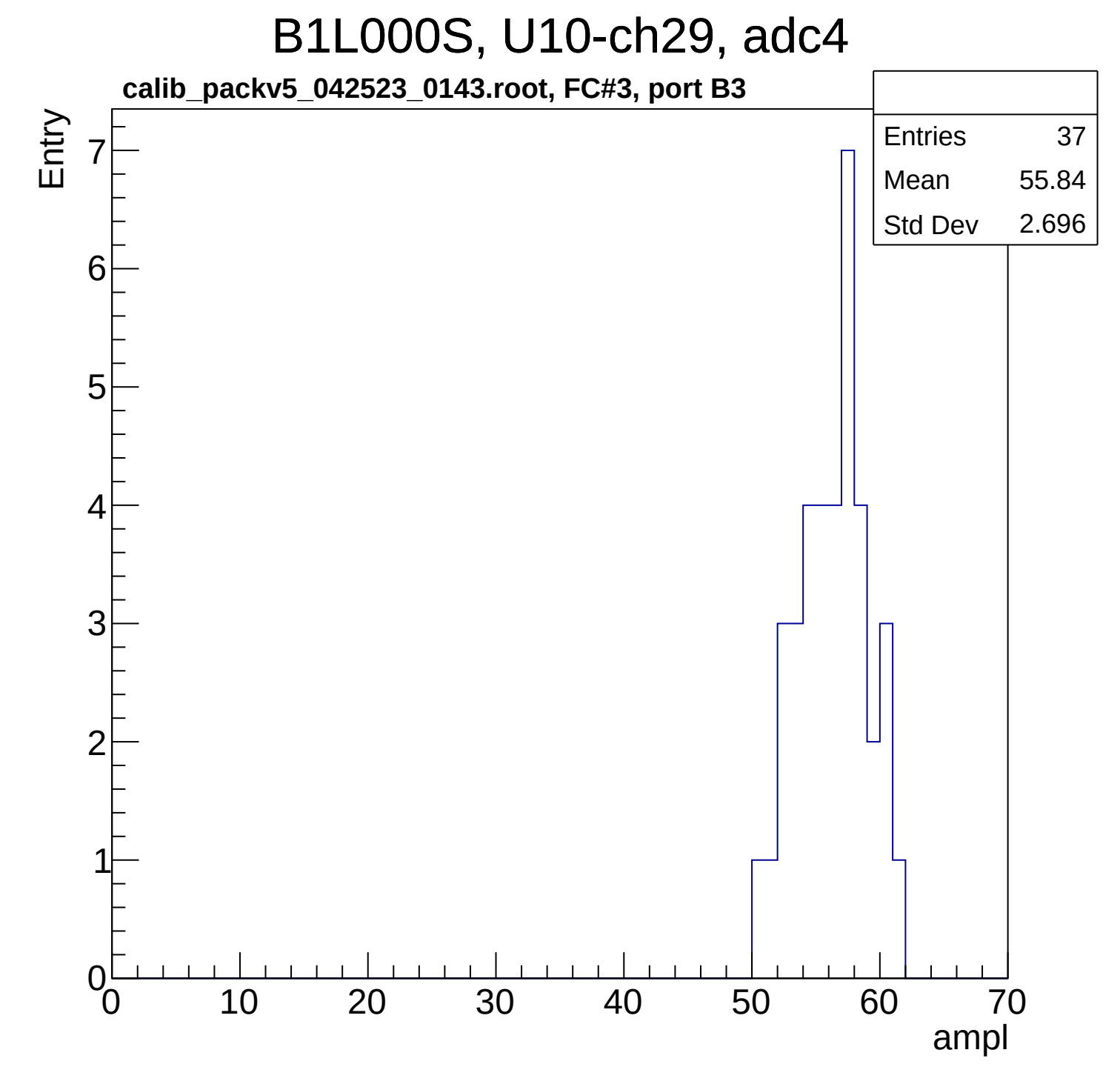
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch29, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 35.89
Std Dev 3.073
Gaus mean : 35.9943
Gaus Width: 2.9238
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch29, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 42.36
Std Dev 2.686
Gaus mean : 43.1696
Gaus Width: 2.7477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch29, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 49.93
Std Dev 3.378
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch29, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 55.84
Std Dev 2.696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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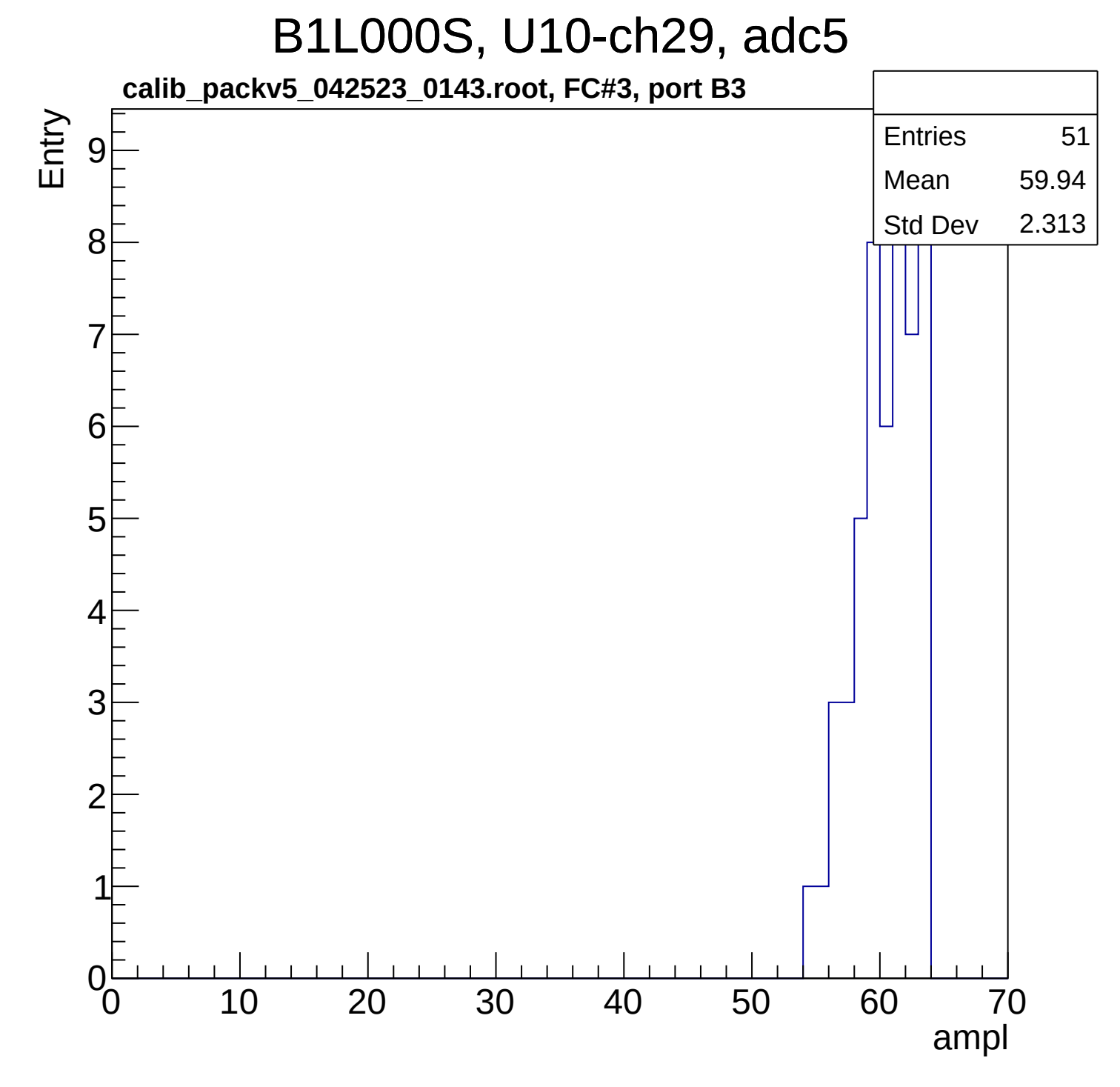
B1L000S, U10-ch29, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 59.94
Std Dev 2.313
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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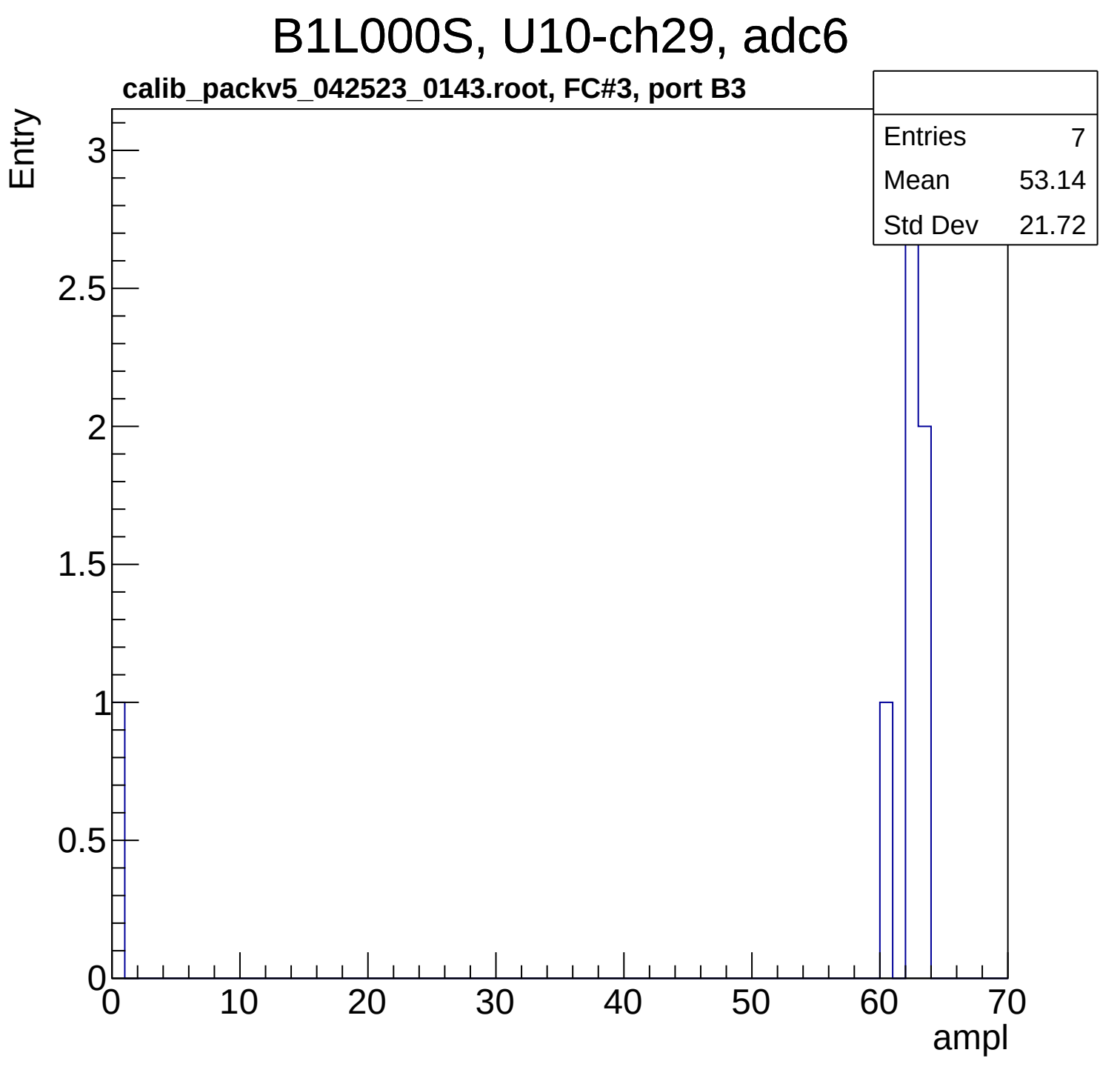
B1L000S, U10-ch29, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
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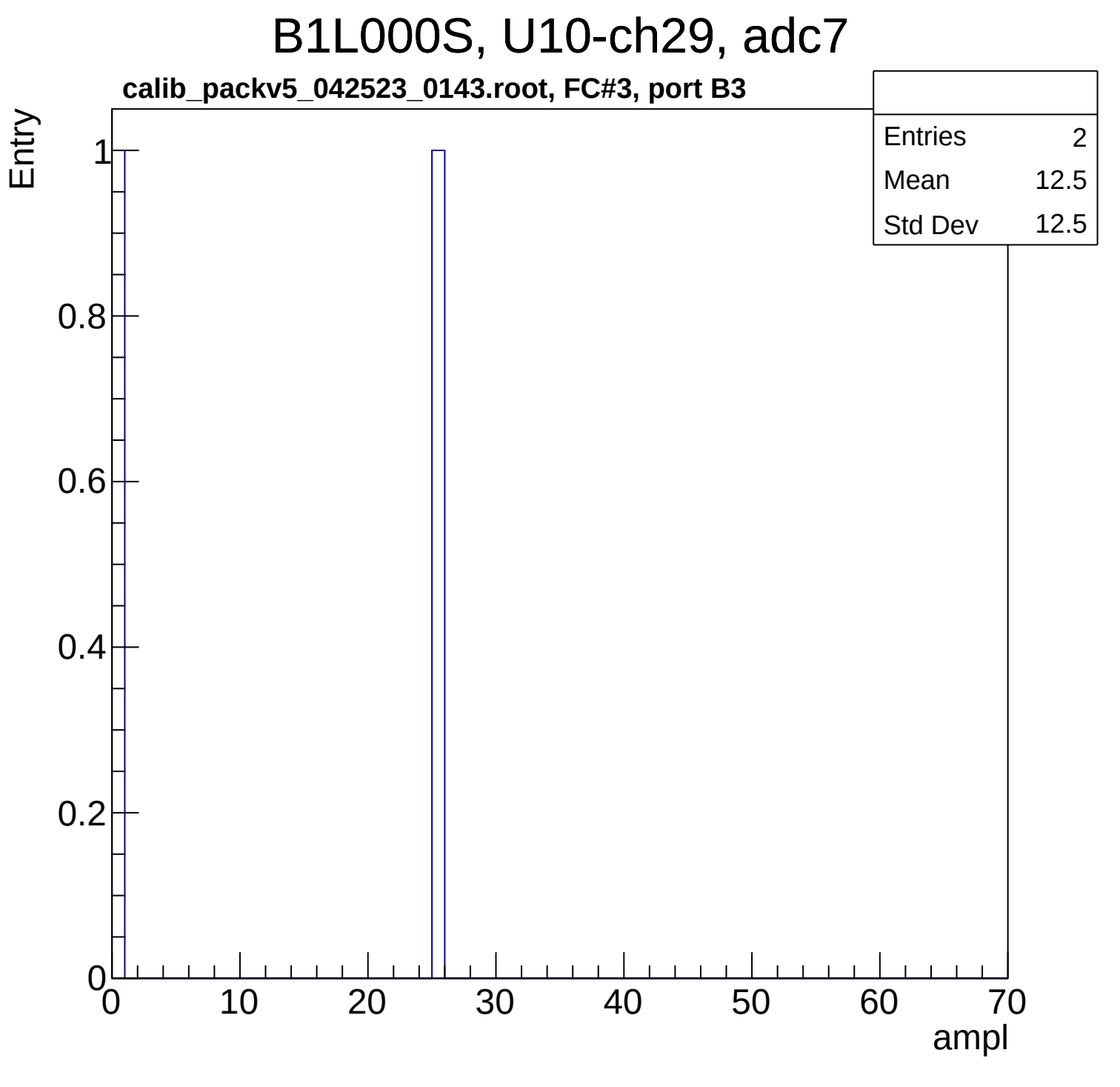
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch29, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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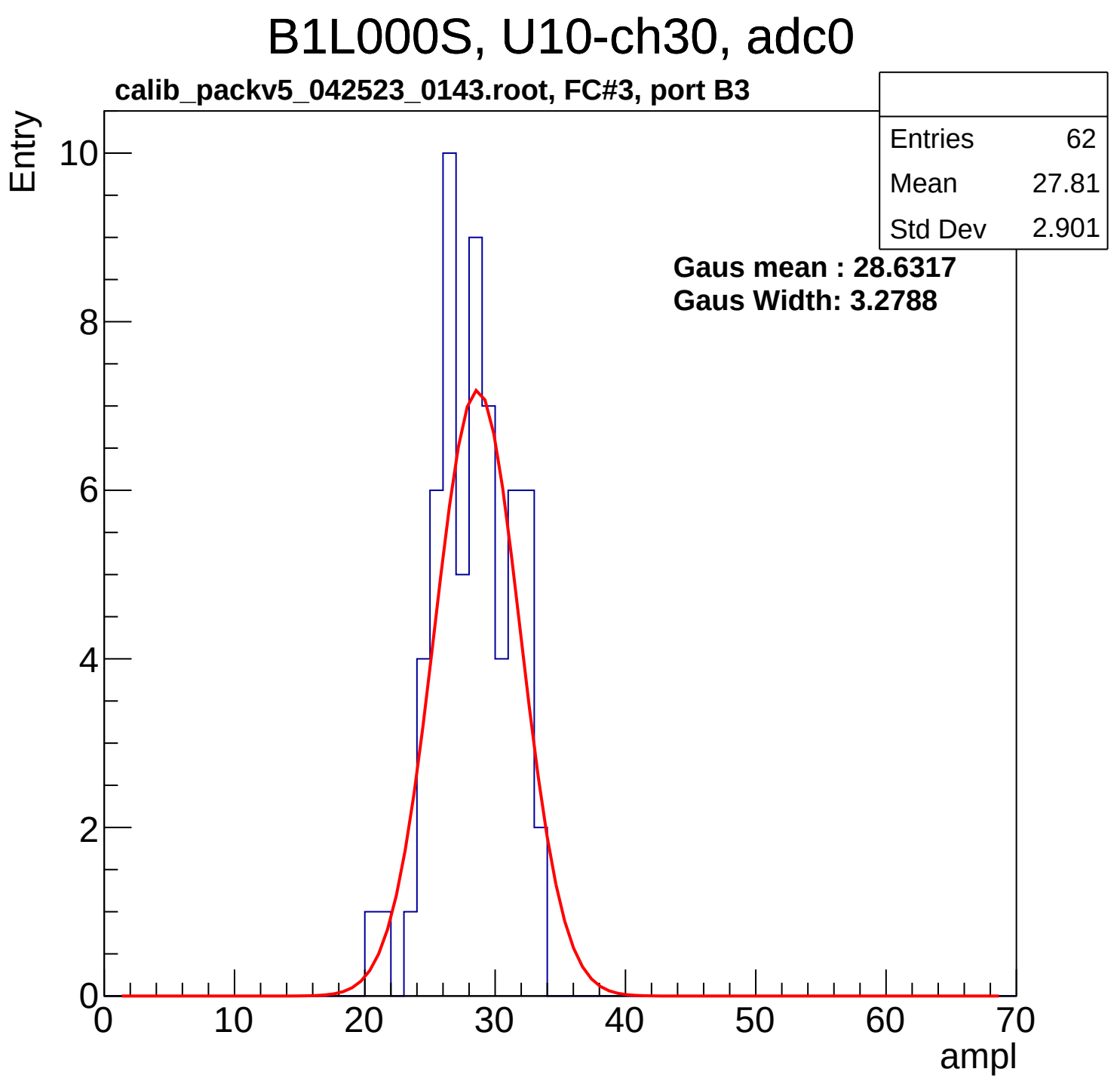
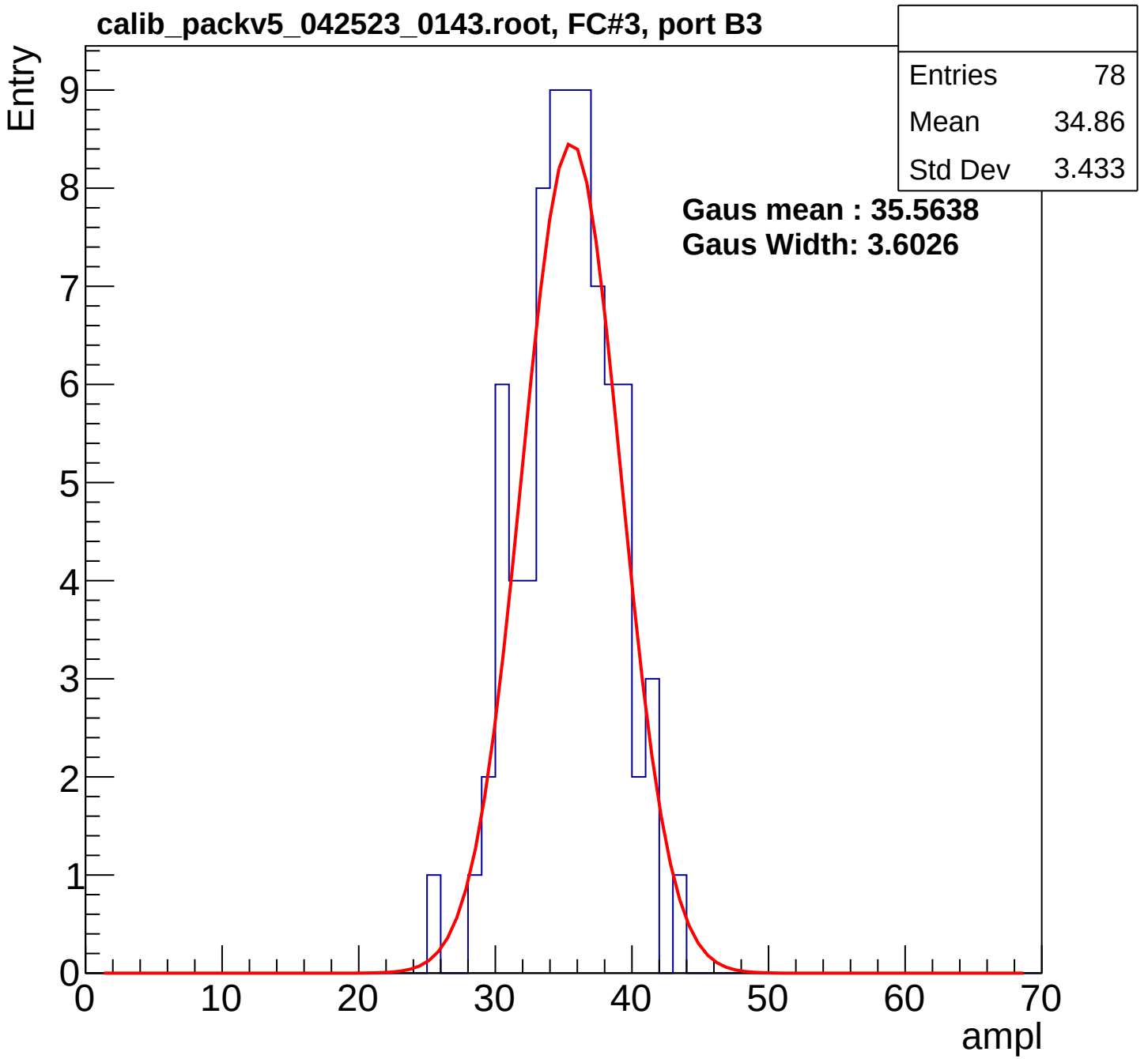
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch30, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.81
Std Dev 2.901
Gaus mean : 28.6317
Gaus Width: 3.2788
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch30, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 34.86
Std Dev 3.433
Gaus mean : 35.5638
Gaus Width: 3.6026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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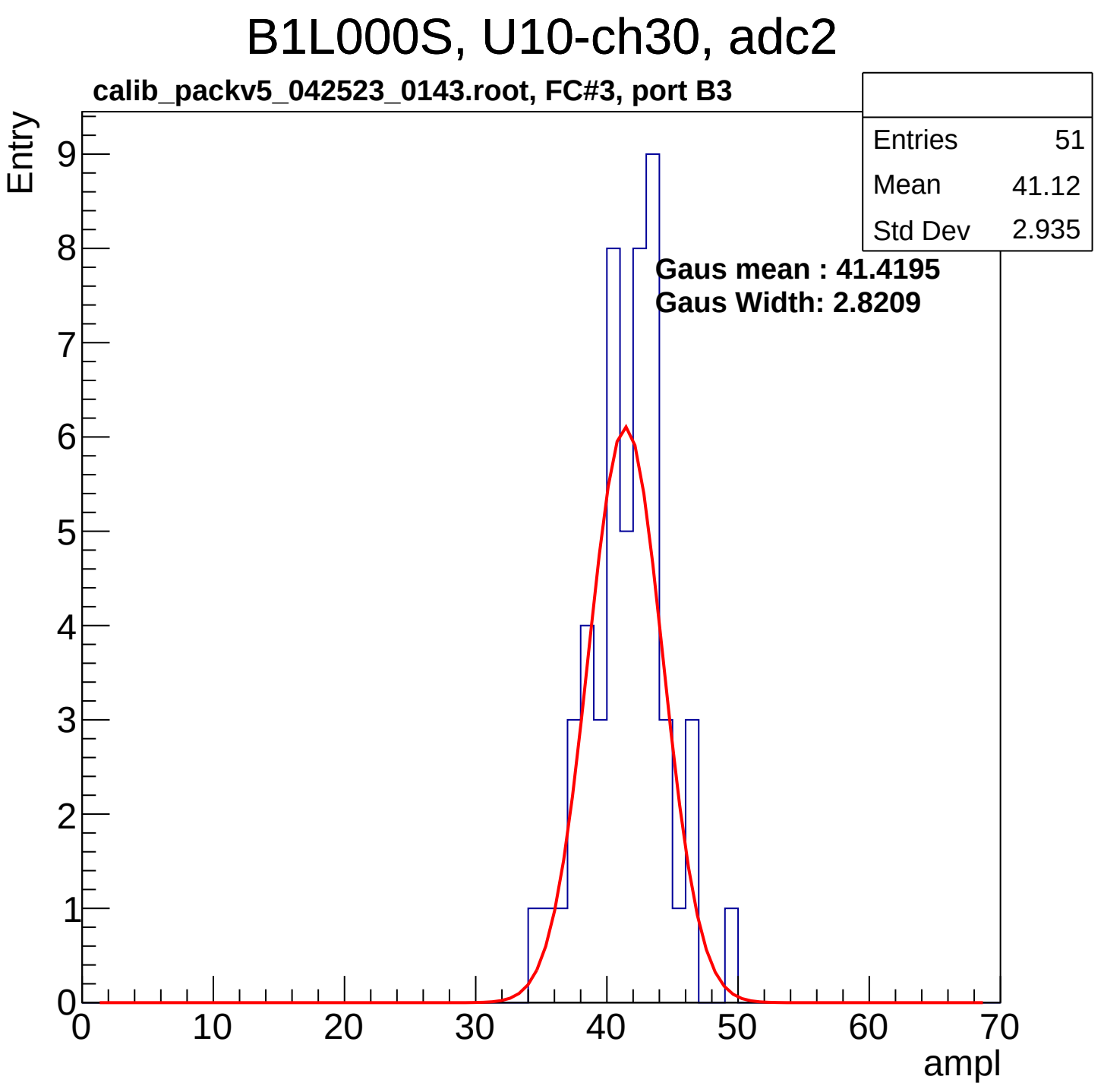
B1L000S, U10-ch30, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 41.12
Std Dev 2.935
Gaus mean : 41.4195
Gaus Width: 2.8209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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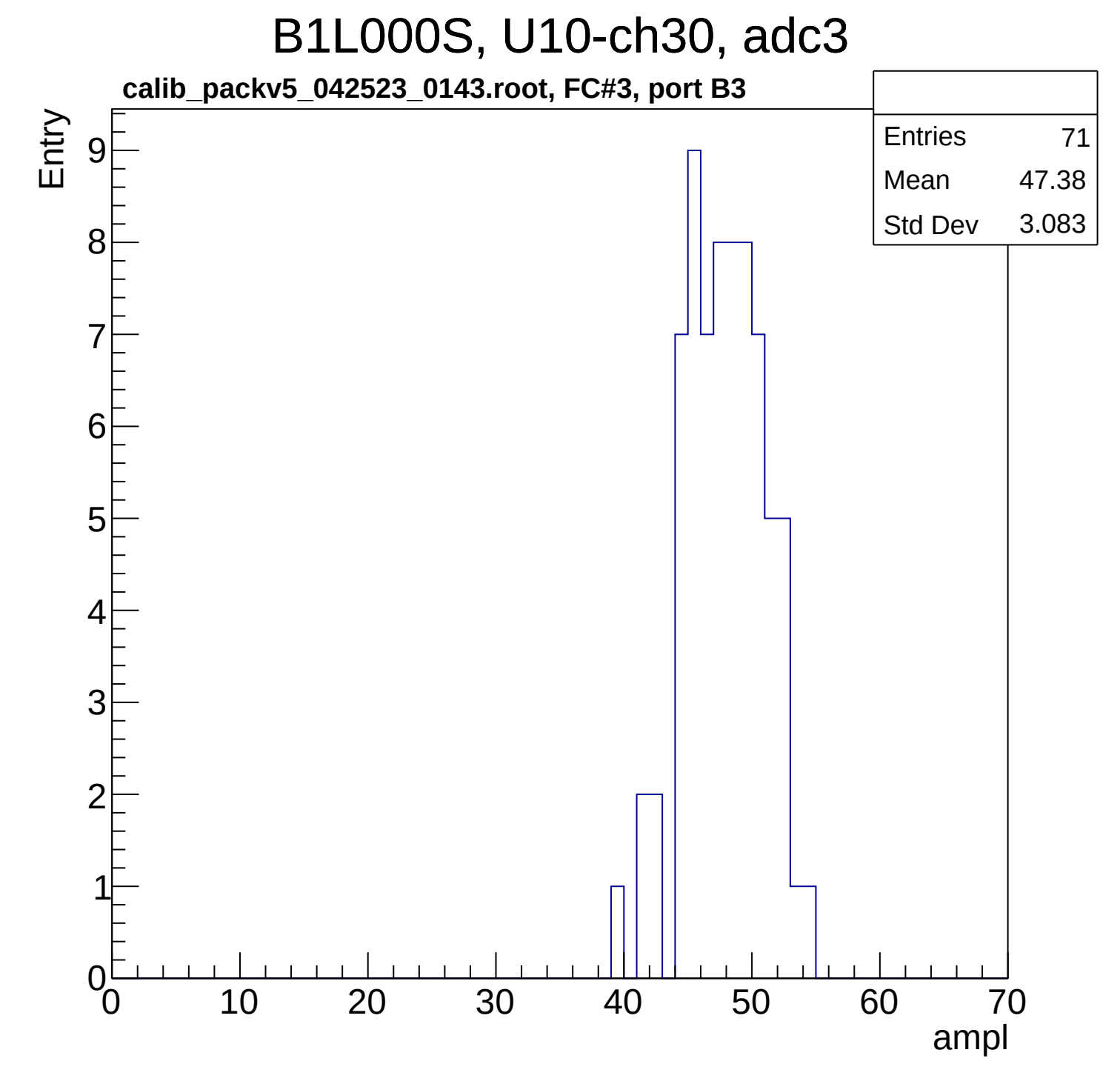
60
70


B1L000S, U10-ch30, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 47.38
Std Dev 3.083
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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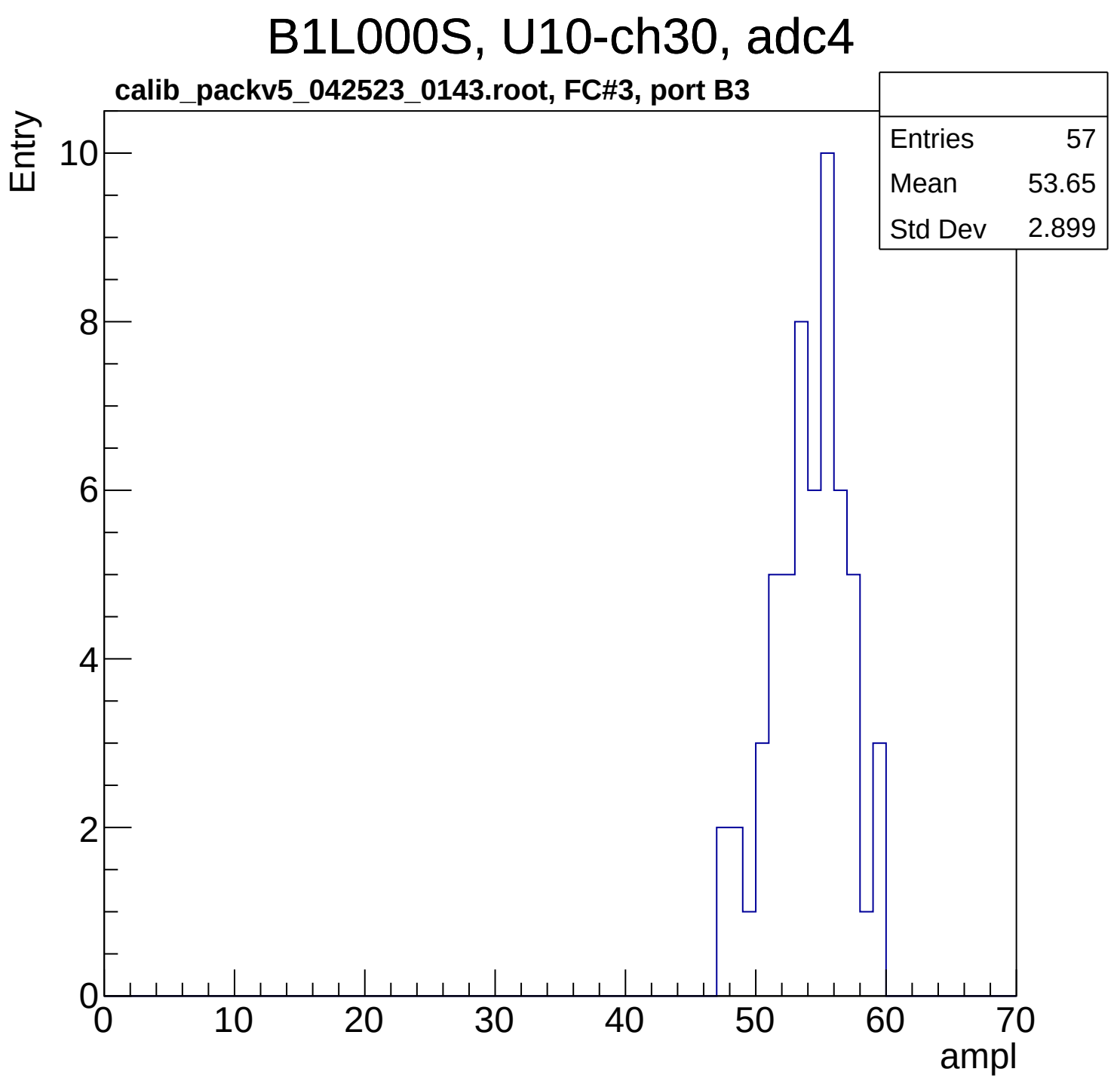
B1L000S, U10-ch30, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.65
Std Dev 2.899
Entry
ampl
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2
4
6
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10
0
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20
30
40
50
60
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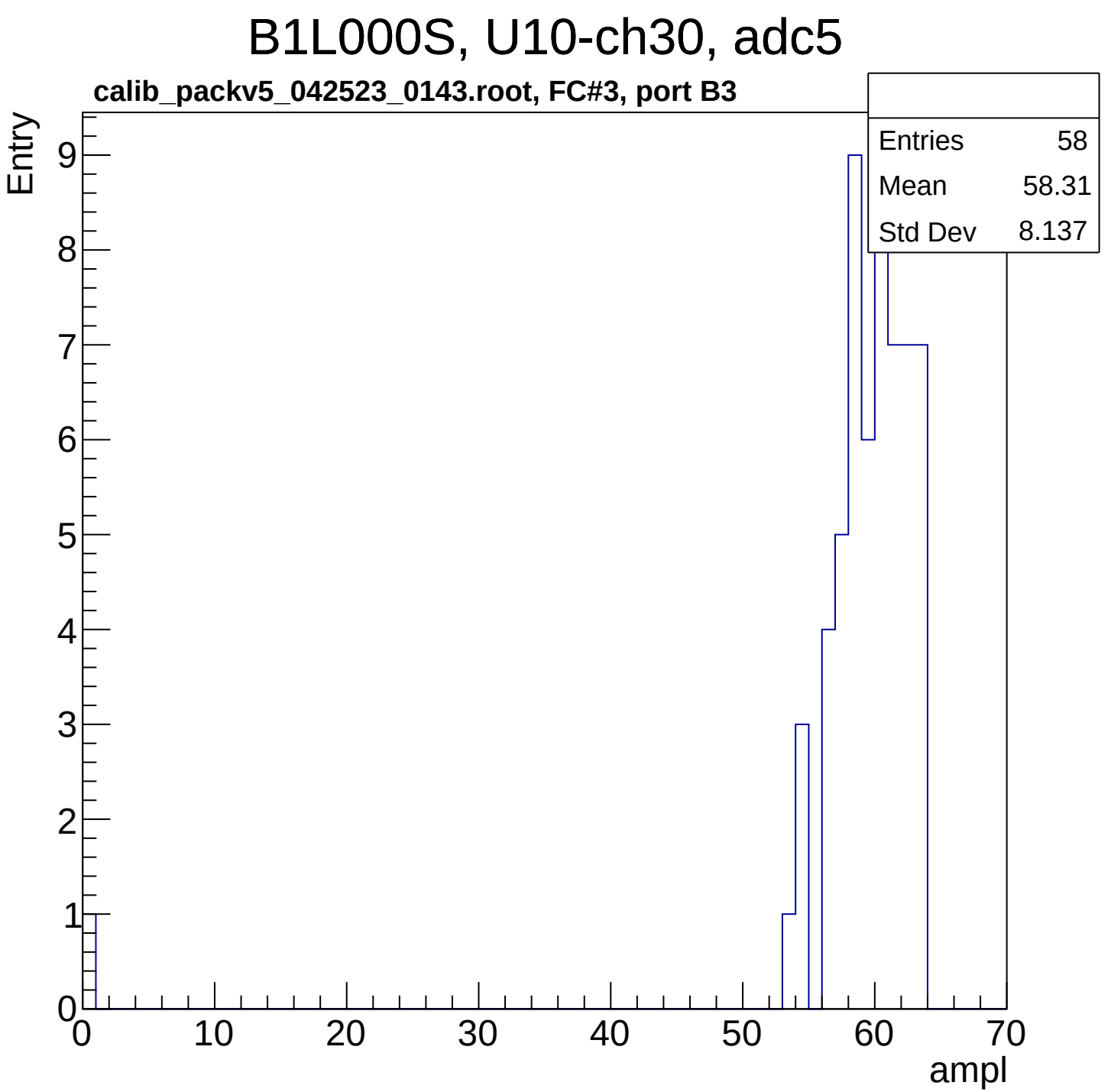
B1L000S, U10-ch30, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.31
Std Dev 8.137
Entry
ampl
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4
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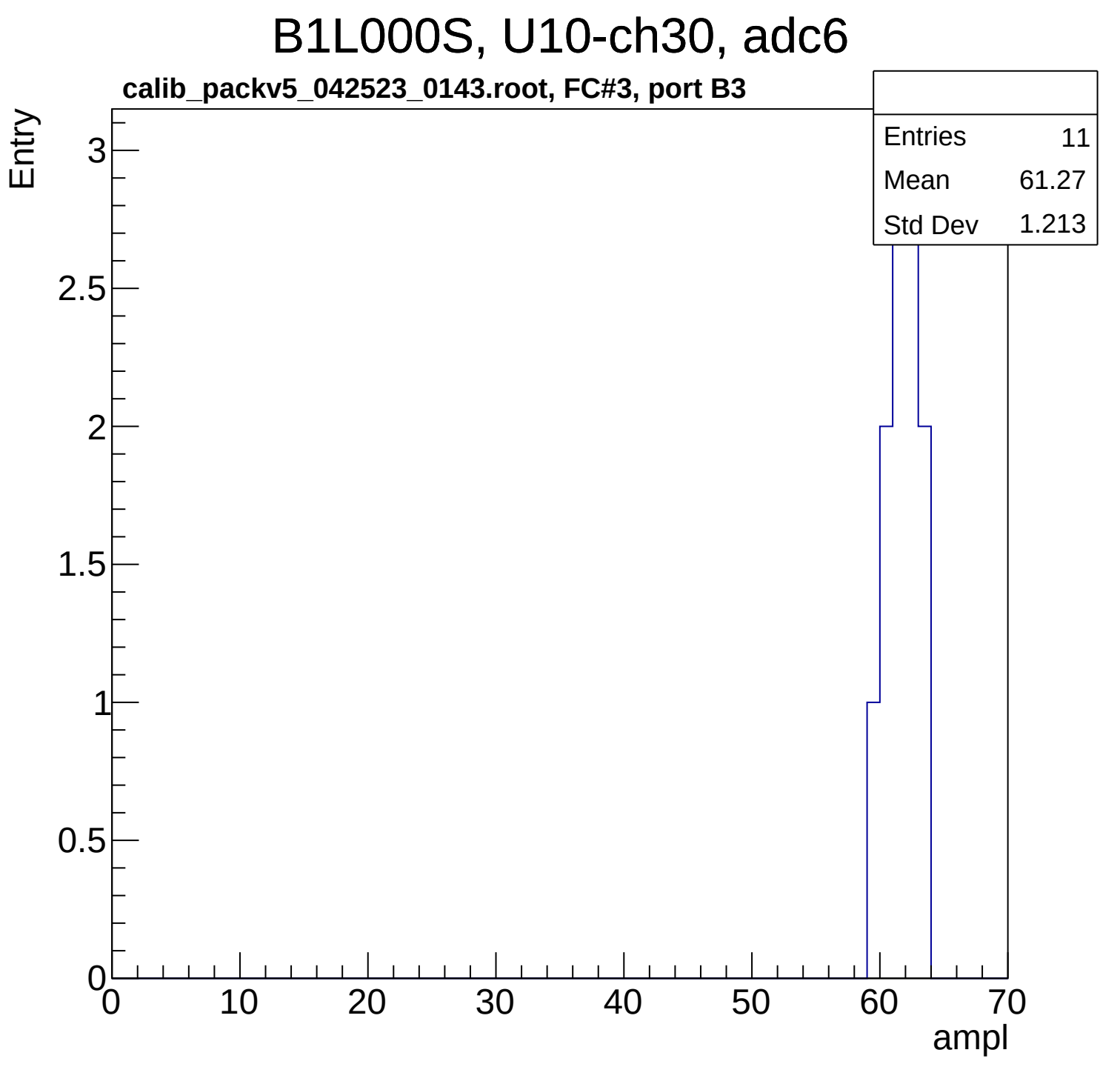
B1L000S, U10-ch30, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.27
Std Dev 1.213
Entry
ampl
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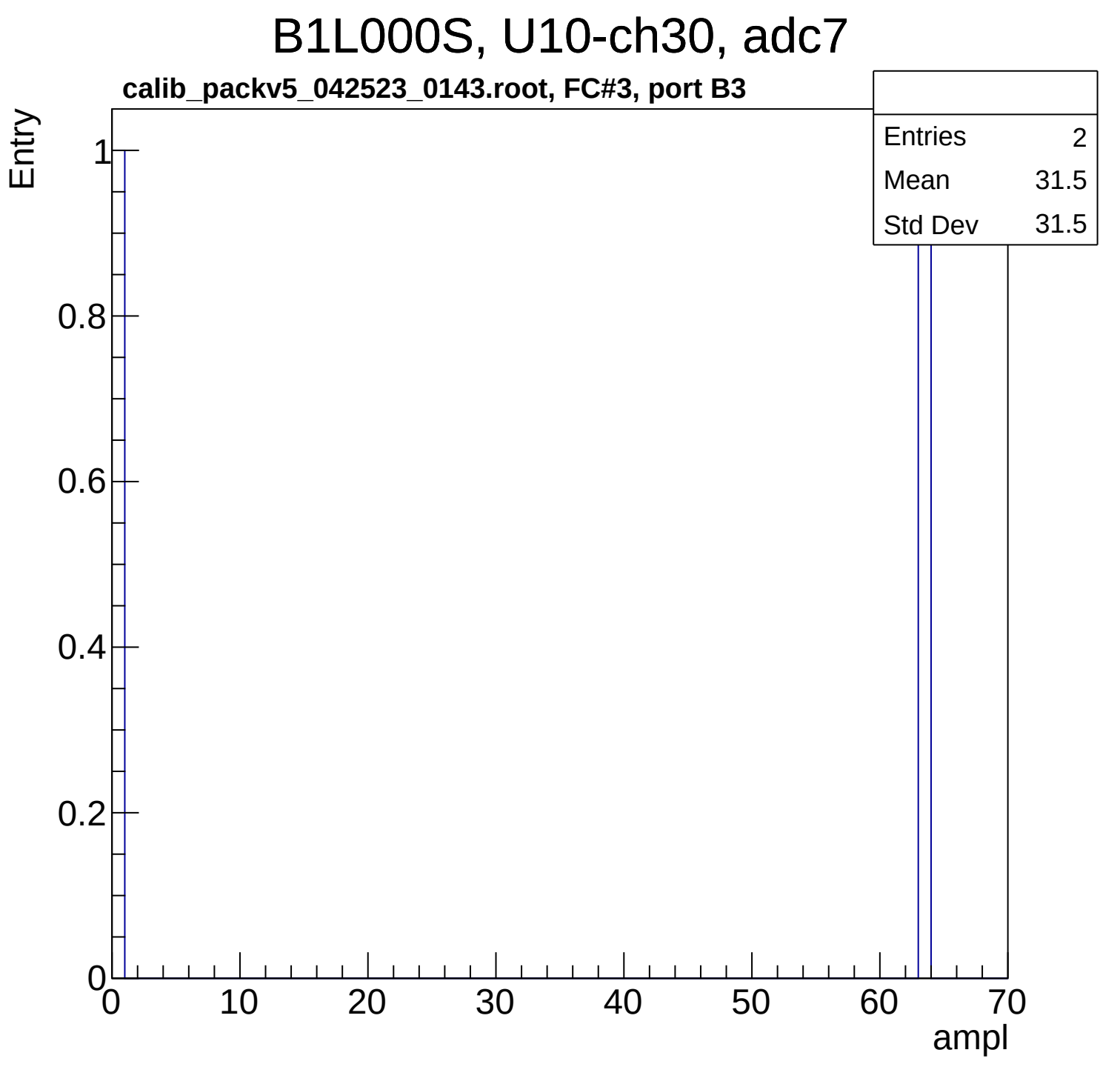
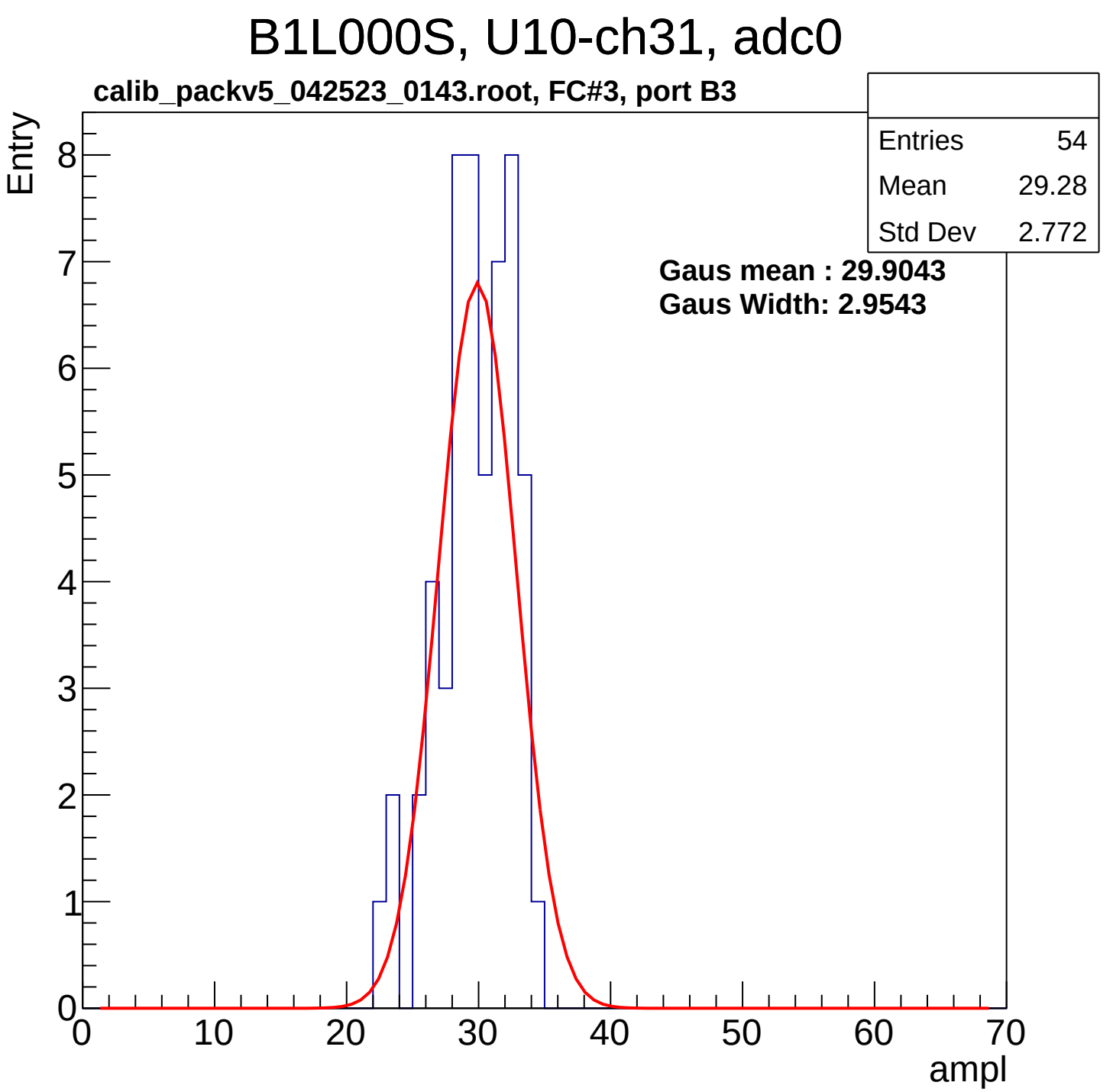
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch30, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch31, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 29.28
Std Dev 2.772
Gaus mean : 29.9043
Gaus Width: 2.9543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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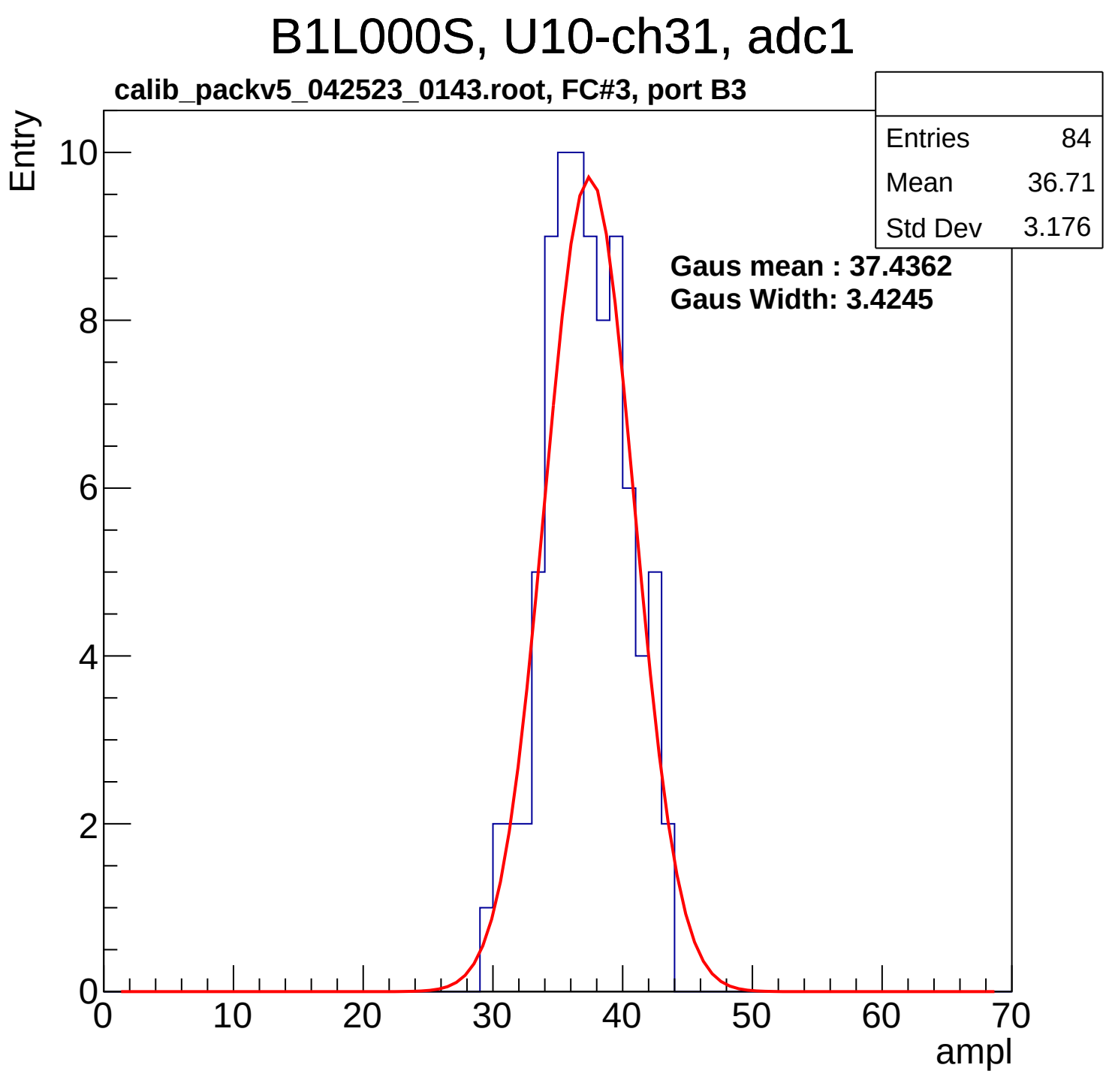
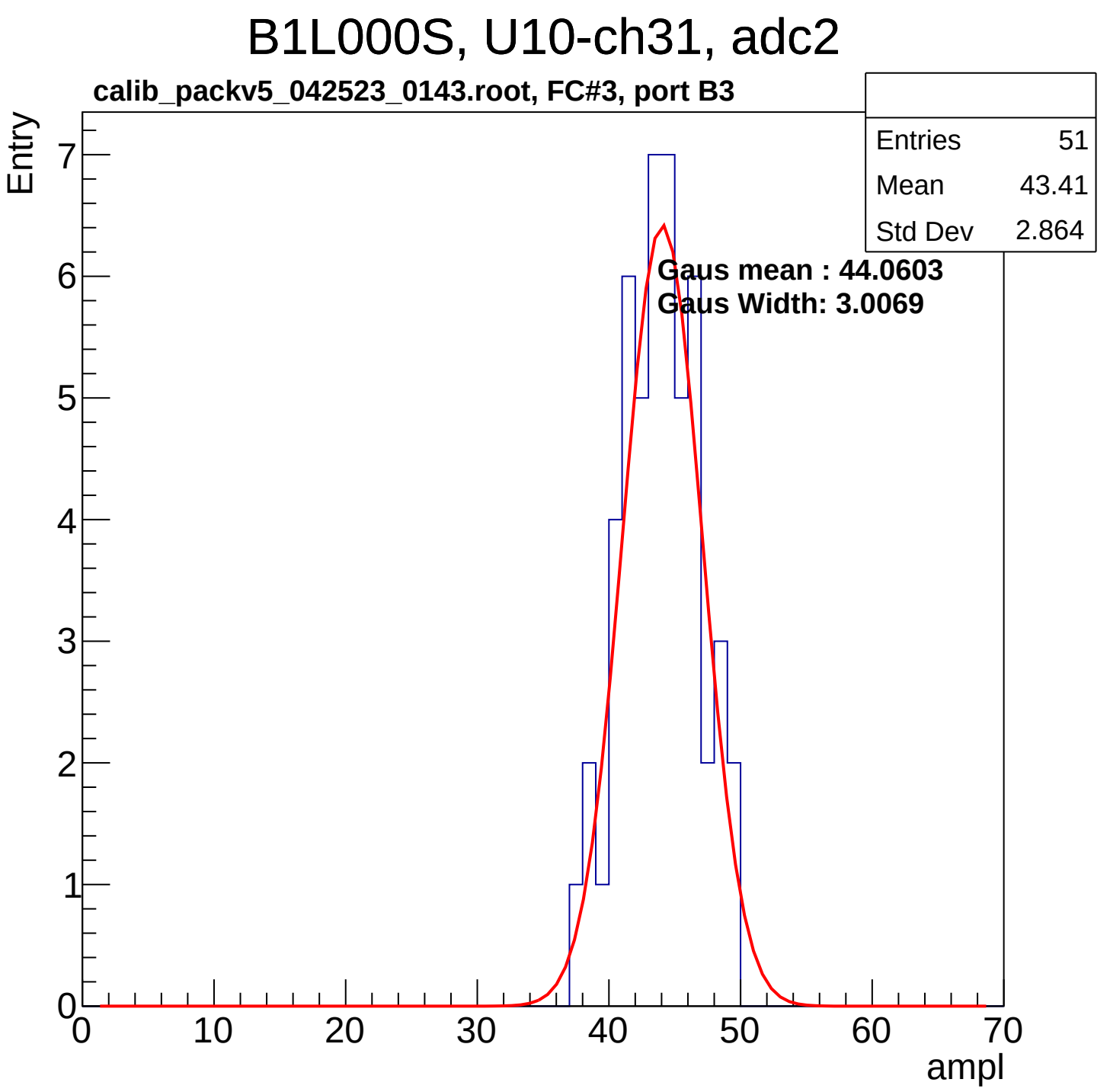

B1L000S, U10-ch31, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 36.71
Std Dev 3.176
Gaus mean : 37.4362
Gaus Width: 3.4245
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch31, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 43.41
Std Dev 2.864
Gaus mean : 44.0603
Gaus Width: 3.0069
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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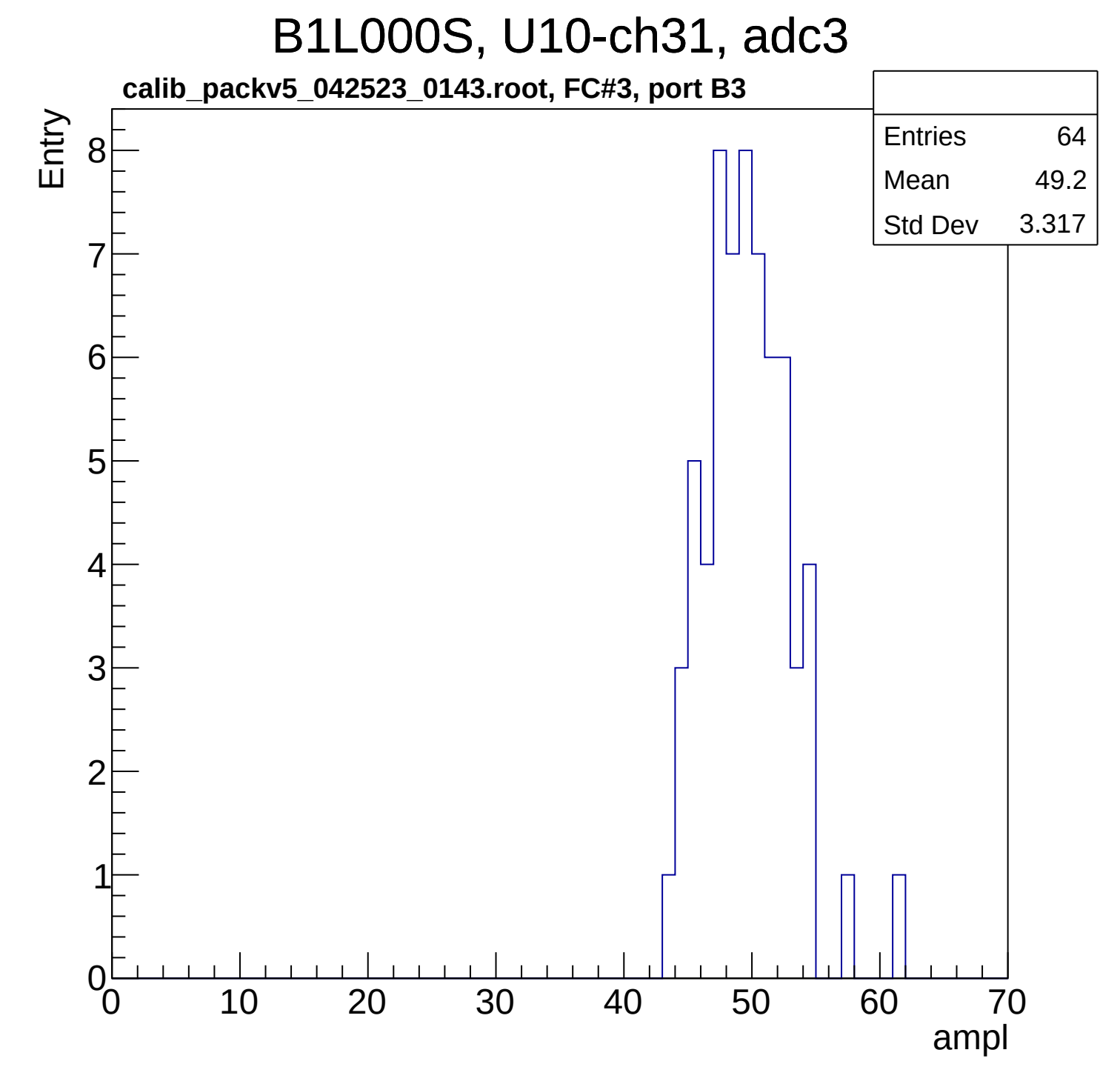
B1L000S, U10-ch31, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 49.2
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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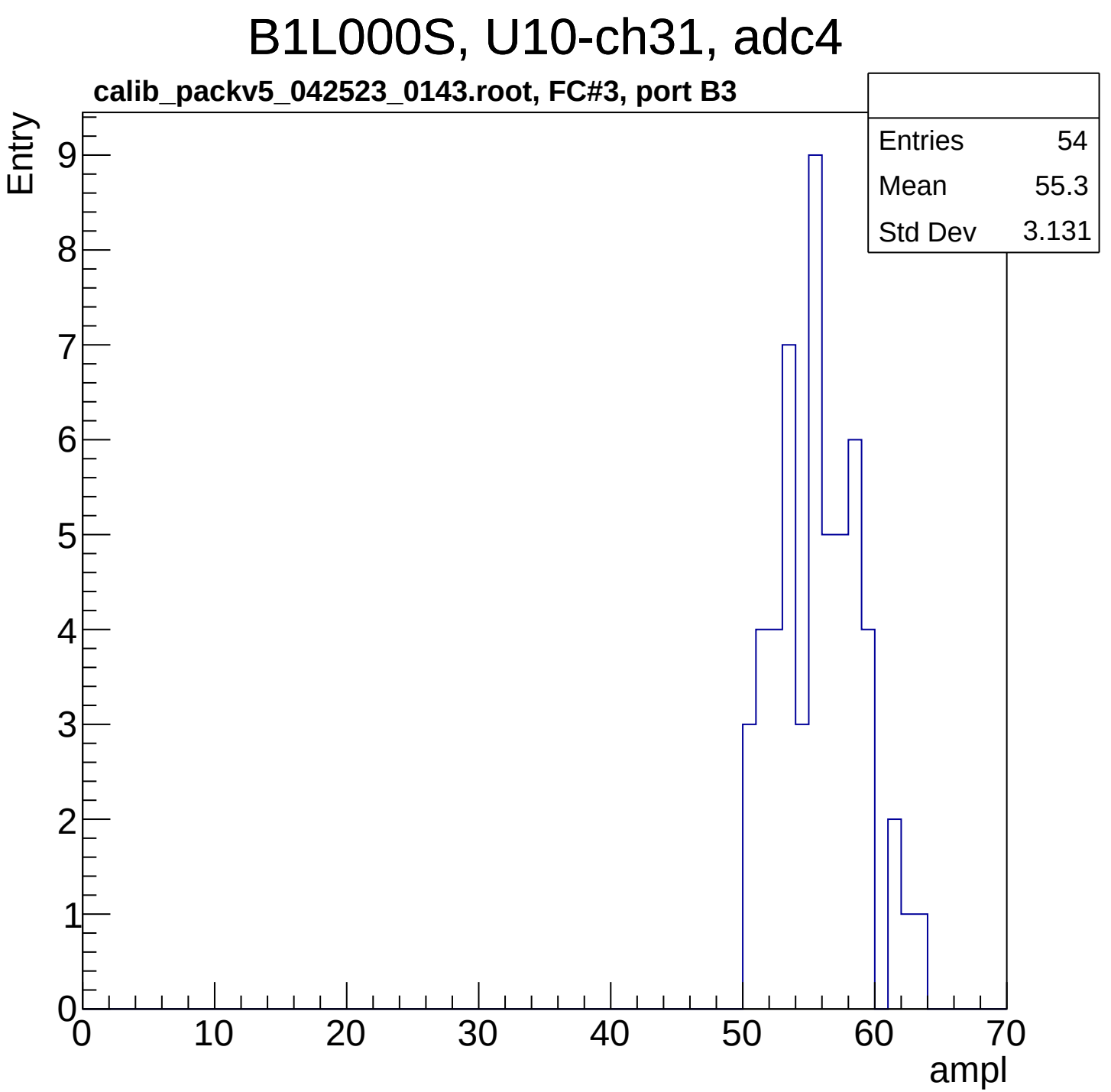
B1L000S, U10-ch31, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 55.3
Std Dev 3.131
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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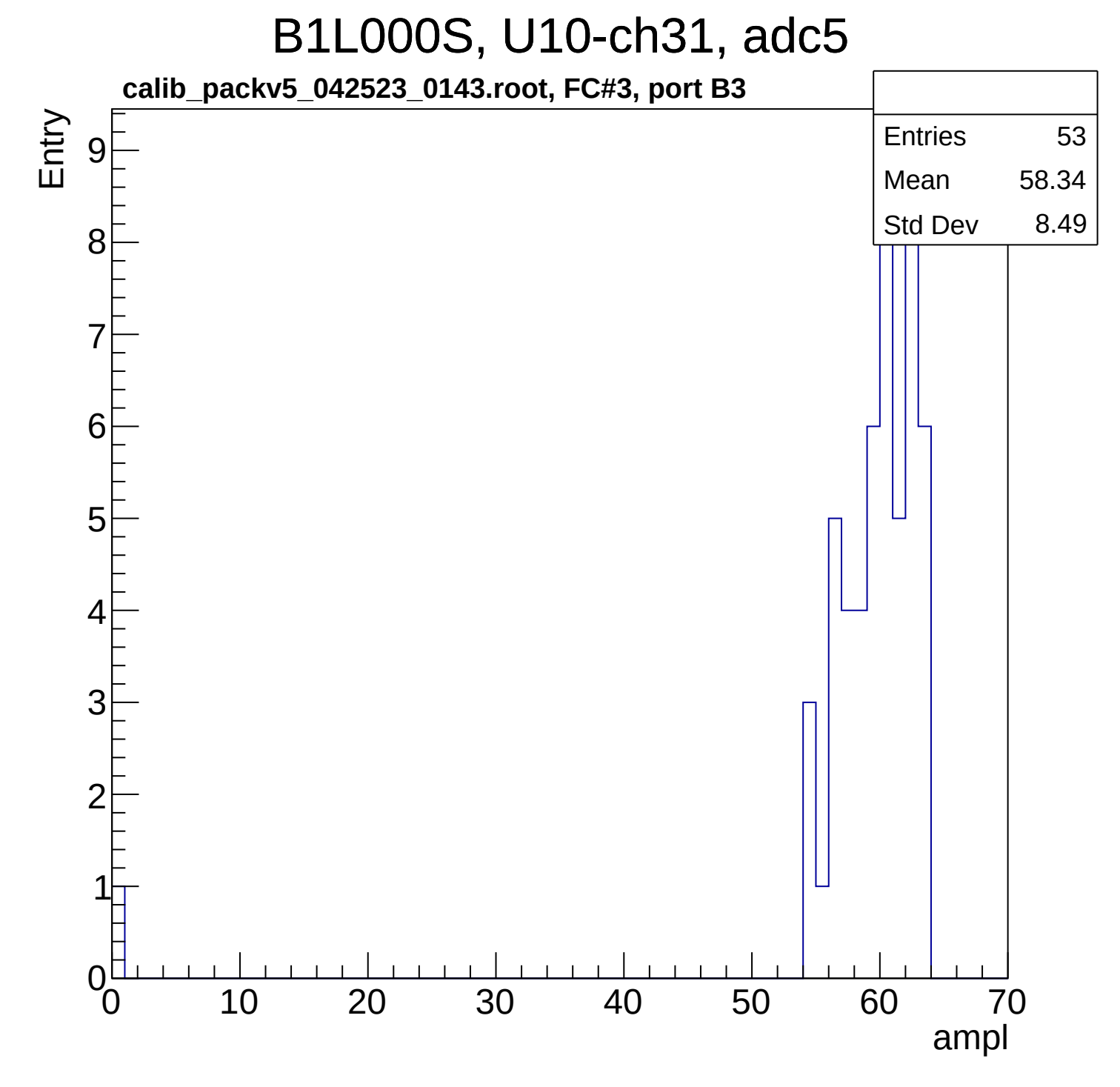
B1L000S, U10-ch31, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.34
Std Dev 8.49
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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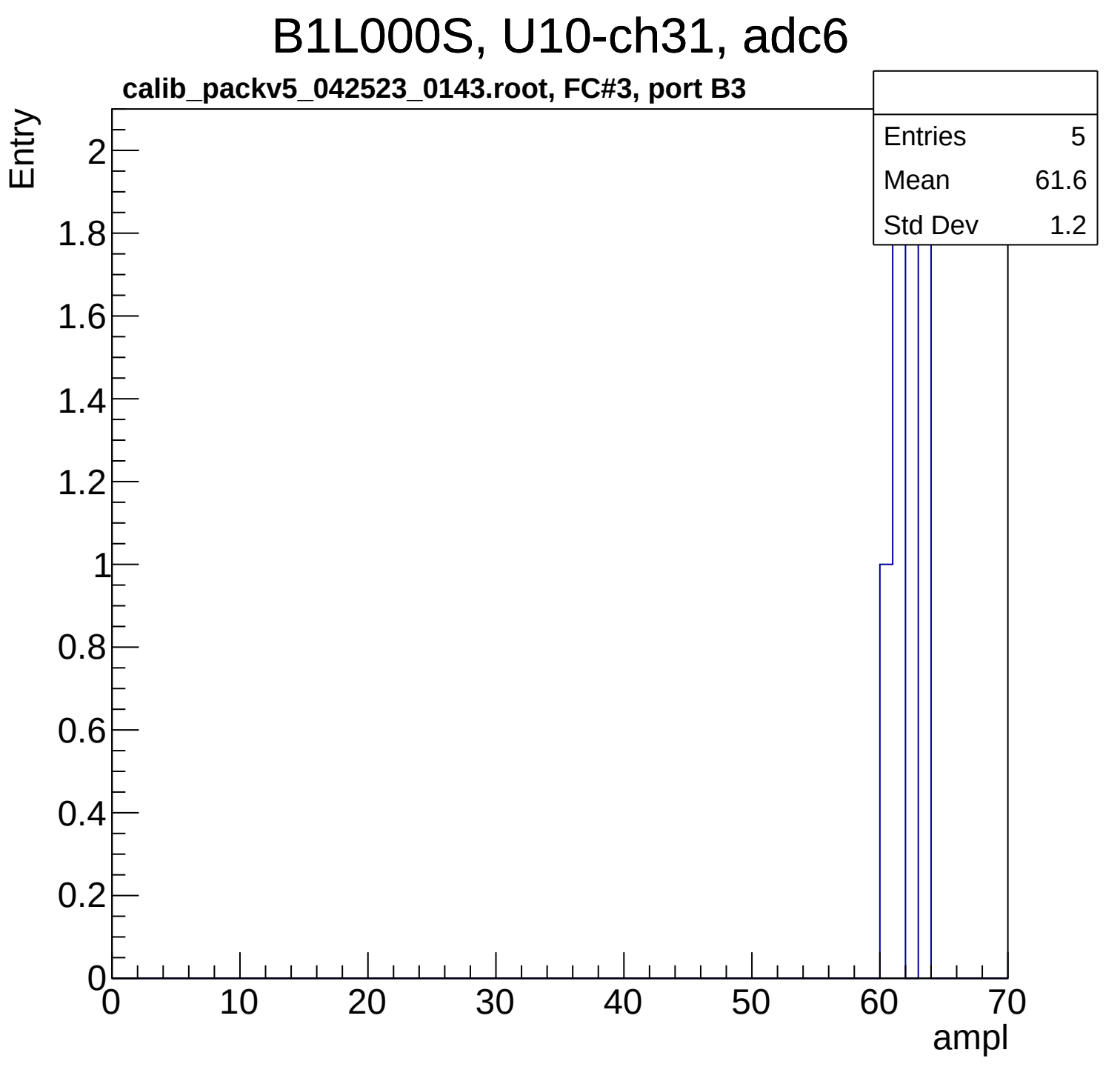
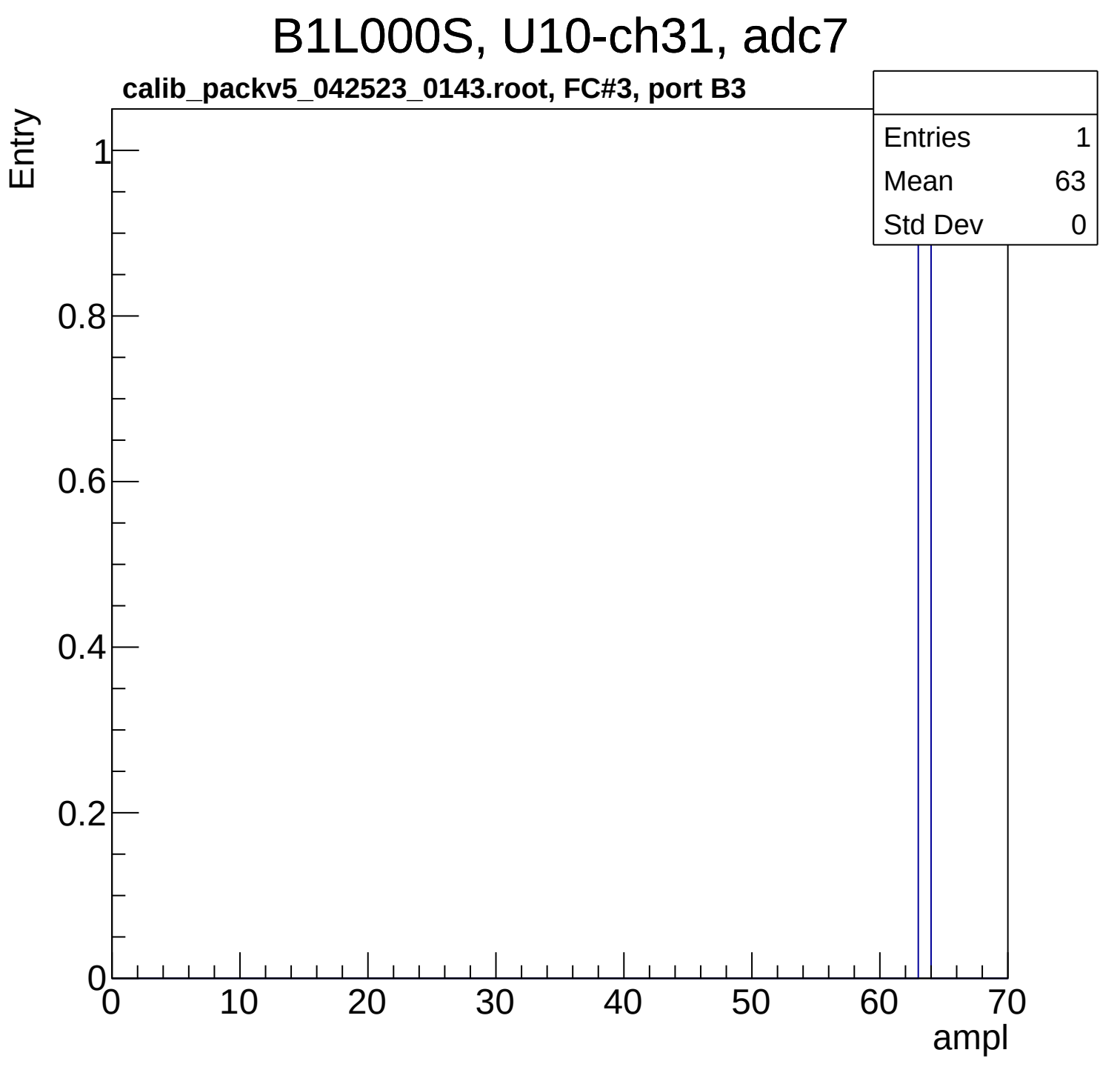
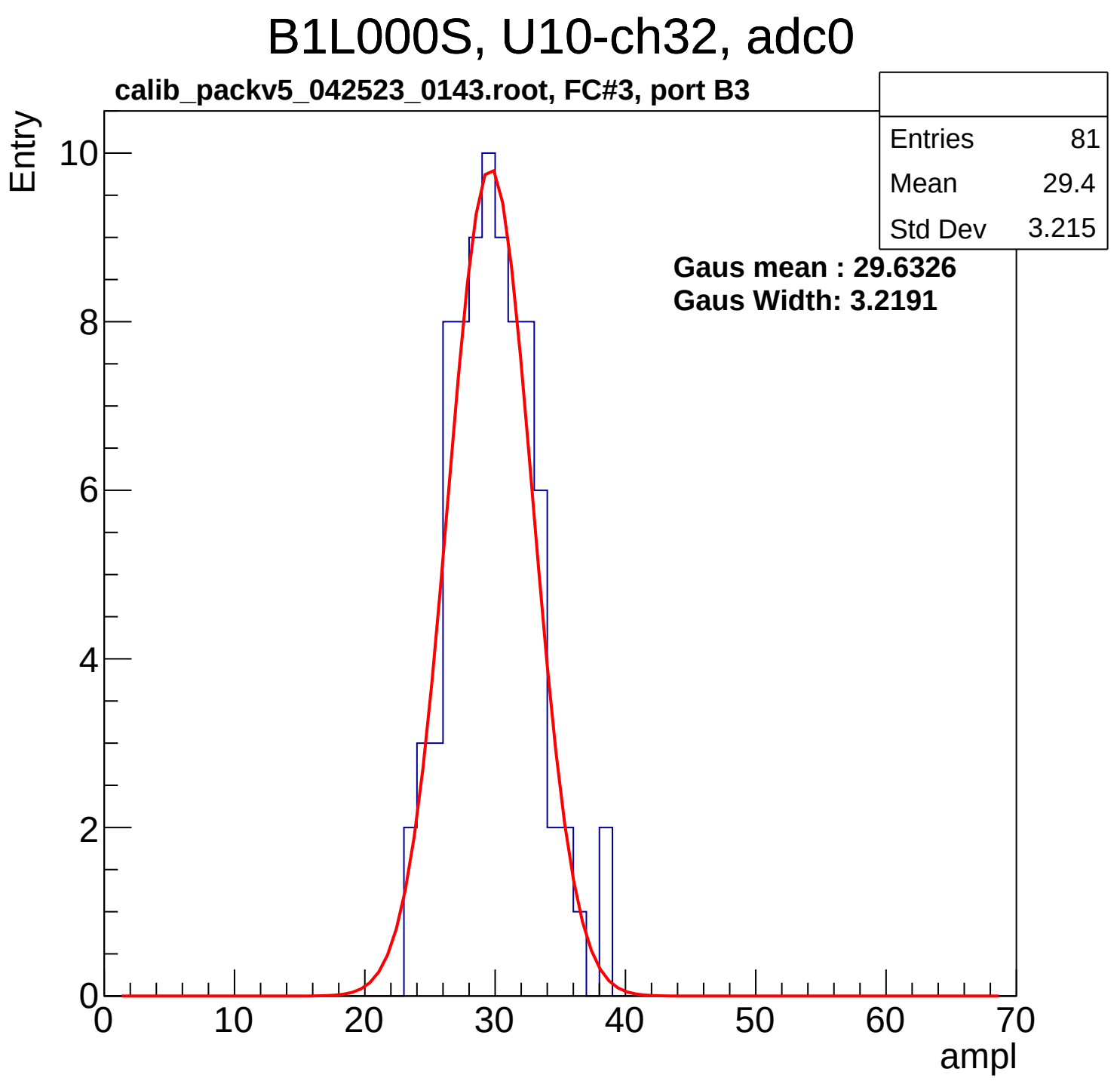
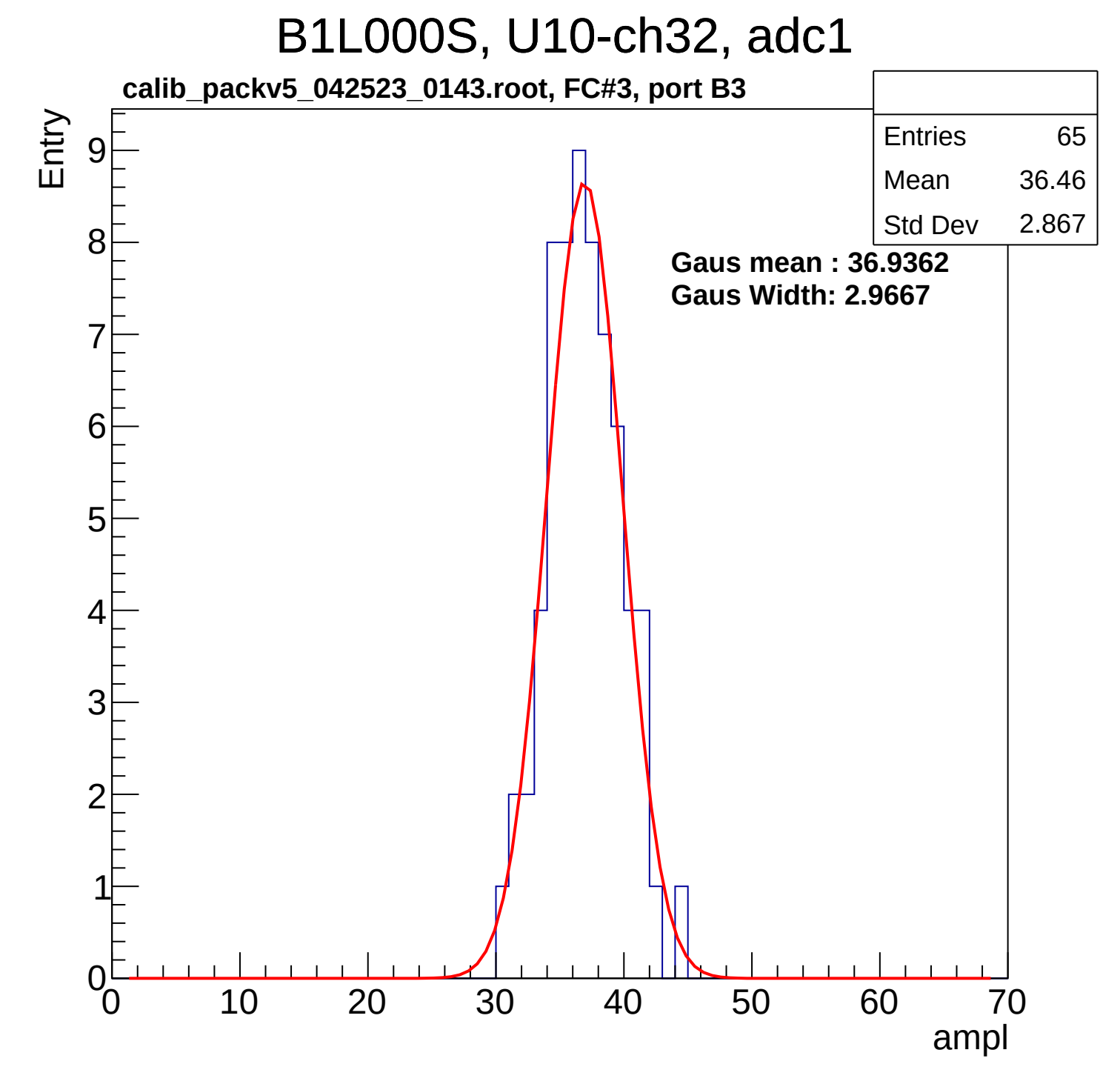
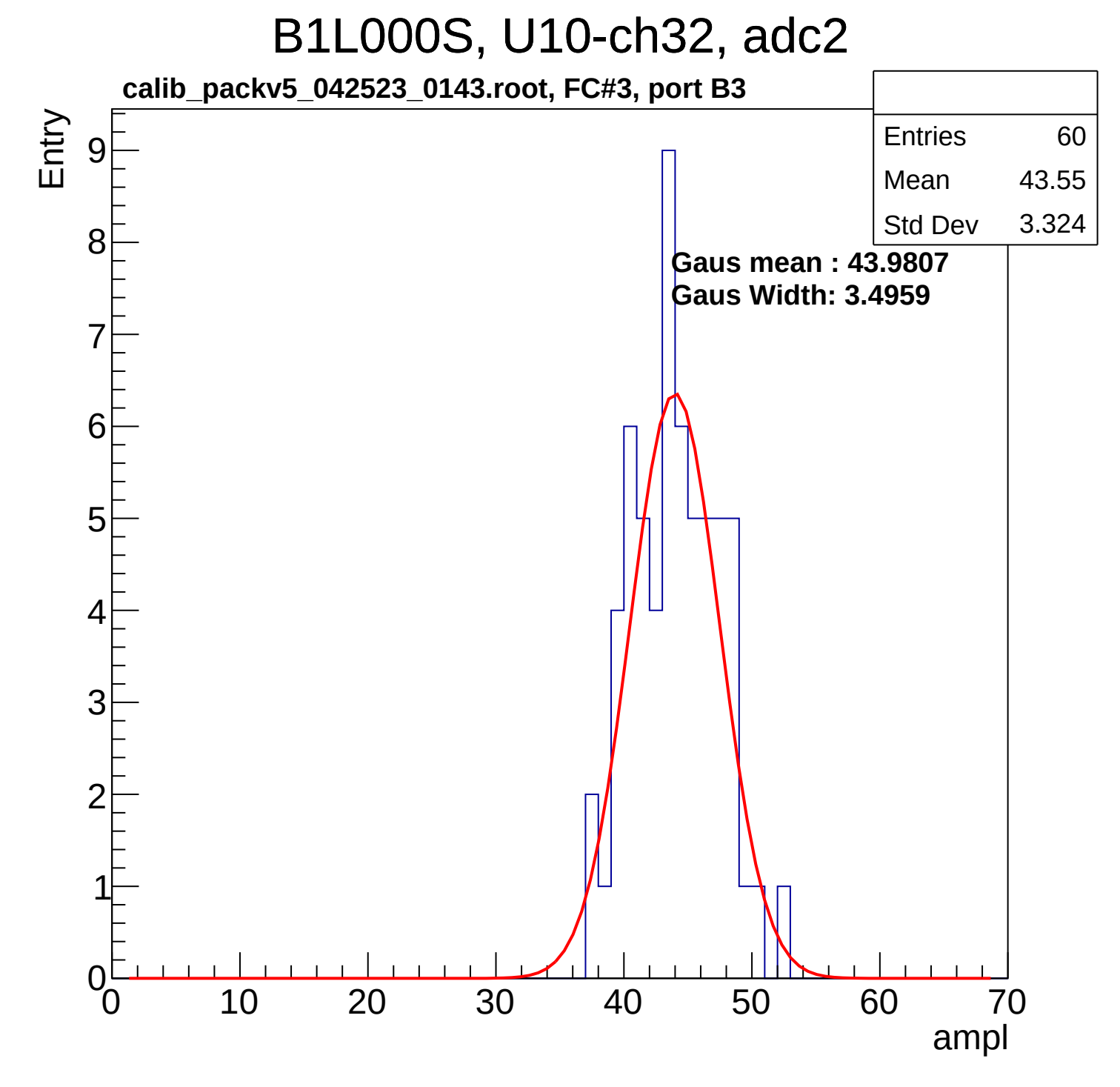

B1L000S, U10-ch31, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 61.6
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch31, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch32, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 29.4
Std Dev 3.215
Gaus mean : 29.6326
Gaus Width: 3.2191
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch32, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 36.46
Std Dev 2.867
Gaus mean : 36.9362
Gaus Width: 2.9667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch32, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 43.55
Std Dev 3.324
Gaus mean : 43.9807
Gaus Width: 3.4959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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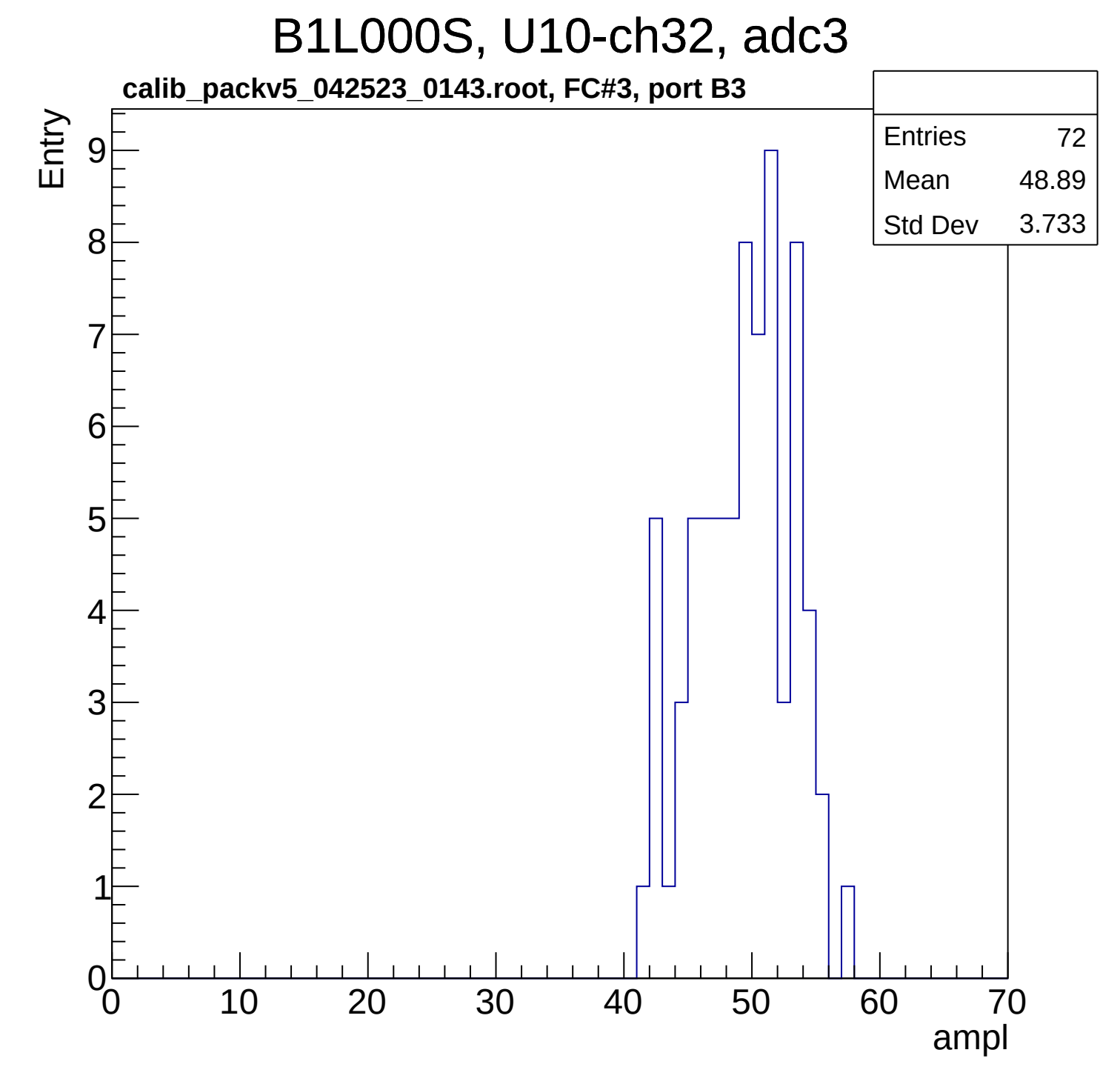
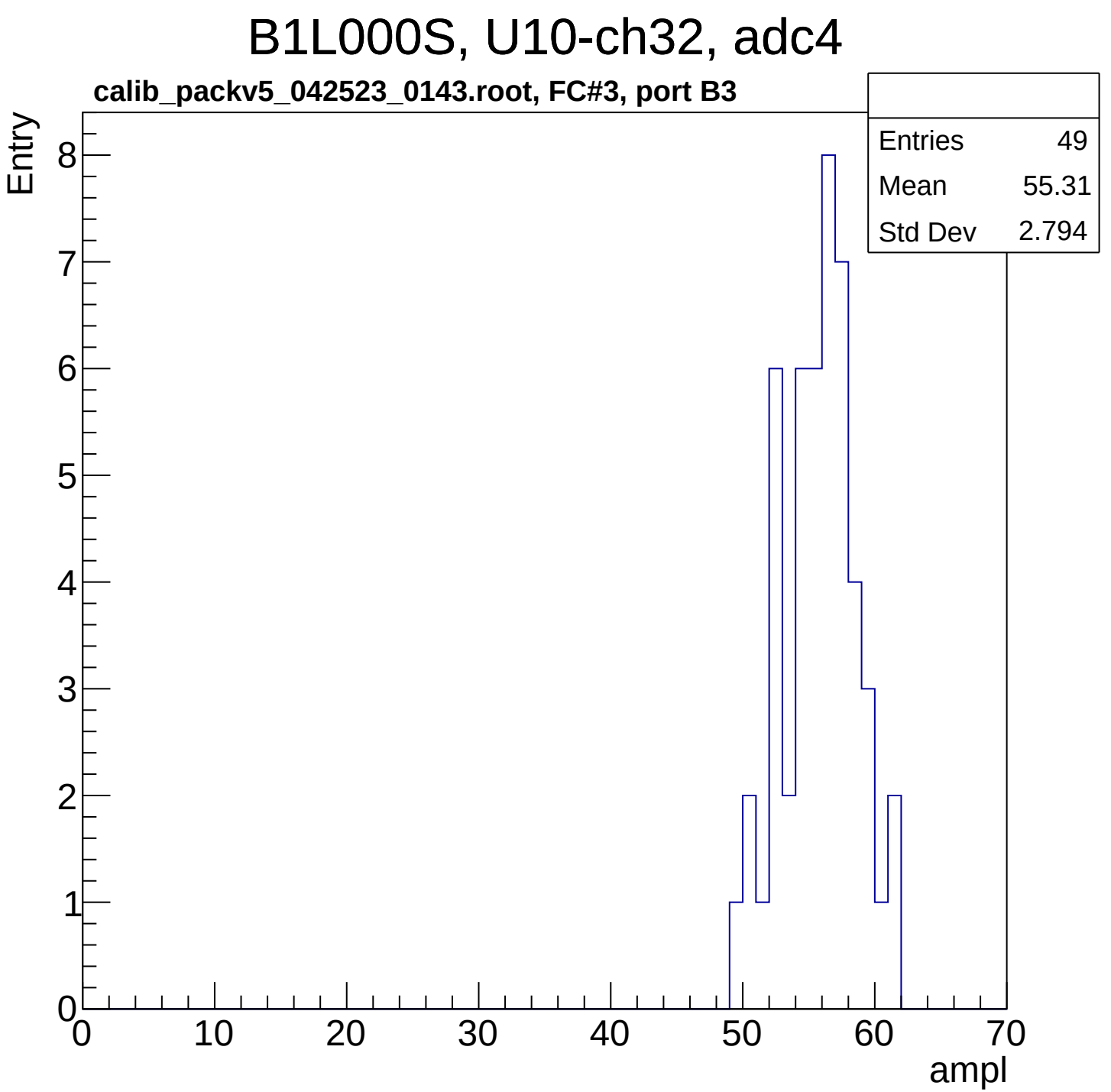
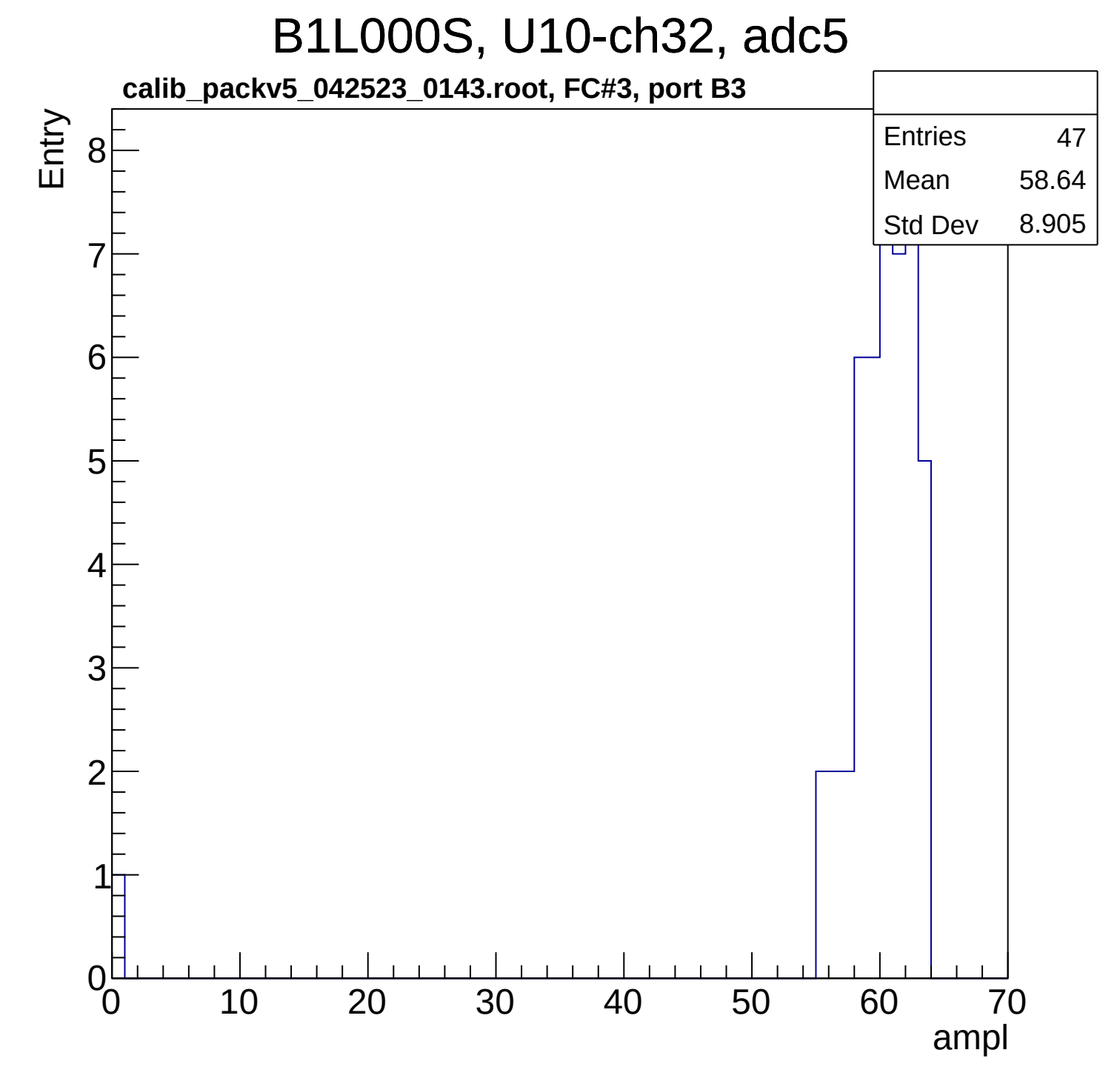

B1L000S, U10-ch32, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.89
Std Dev 3.733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch32, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 55.31
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch32, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 58.64
Std Dev 8.905
Entry
ampl
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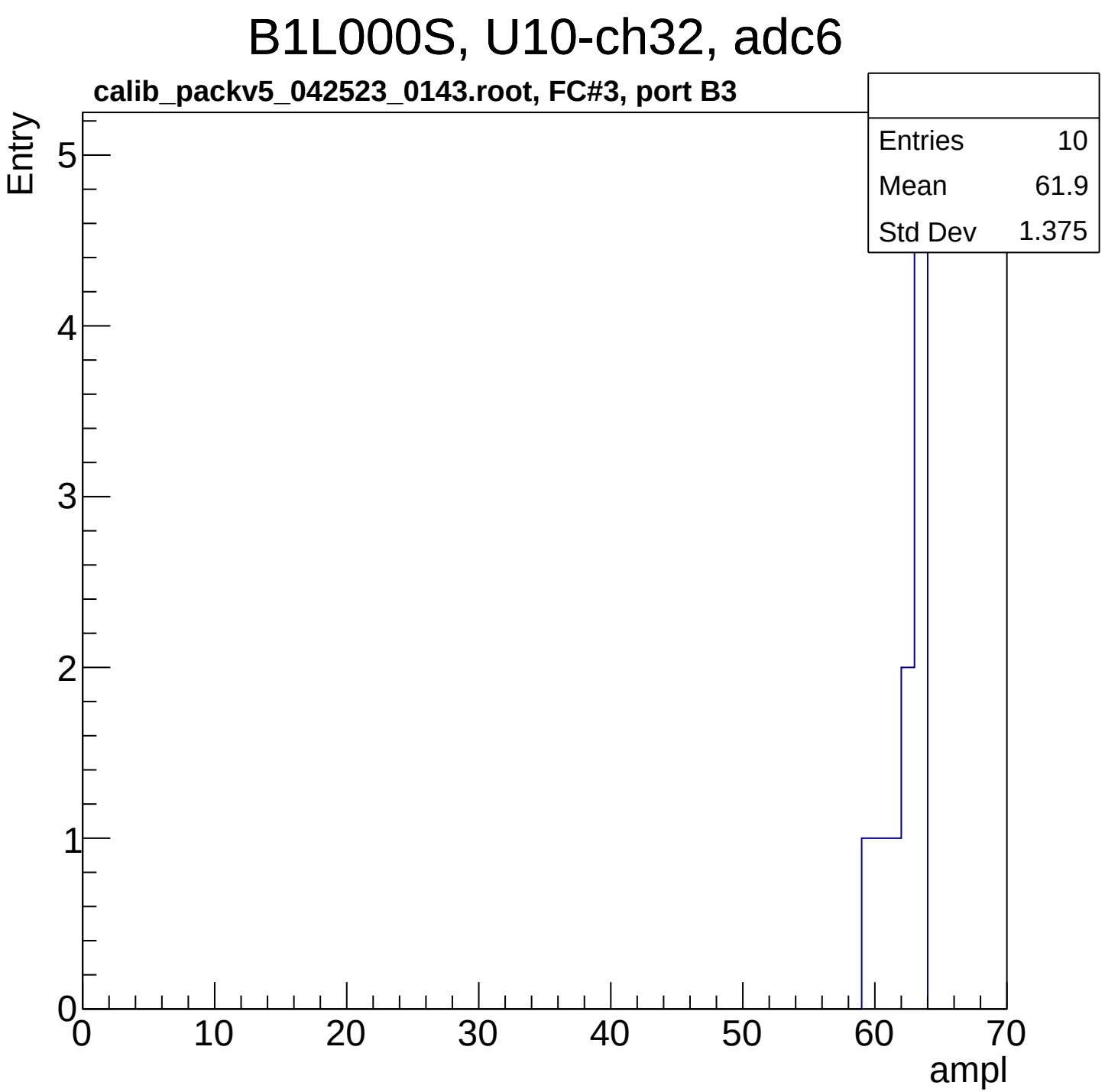
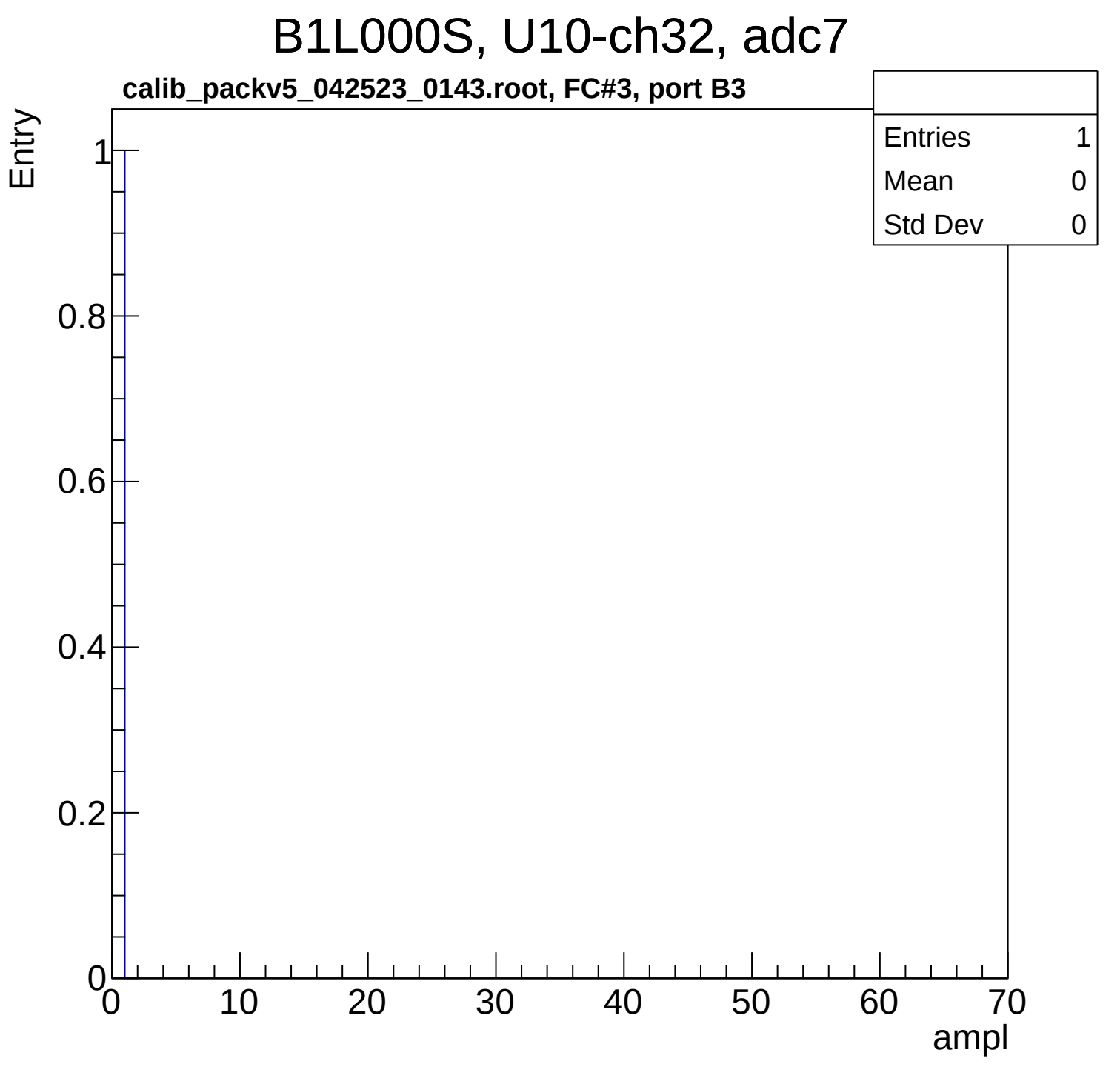
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch32, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.9
Std Dev 1.375
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch32, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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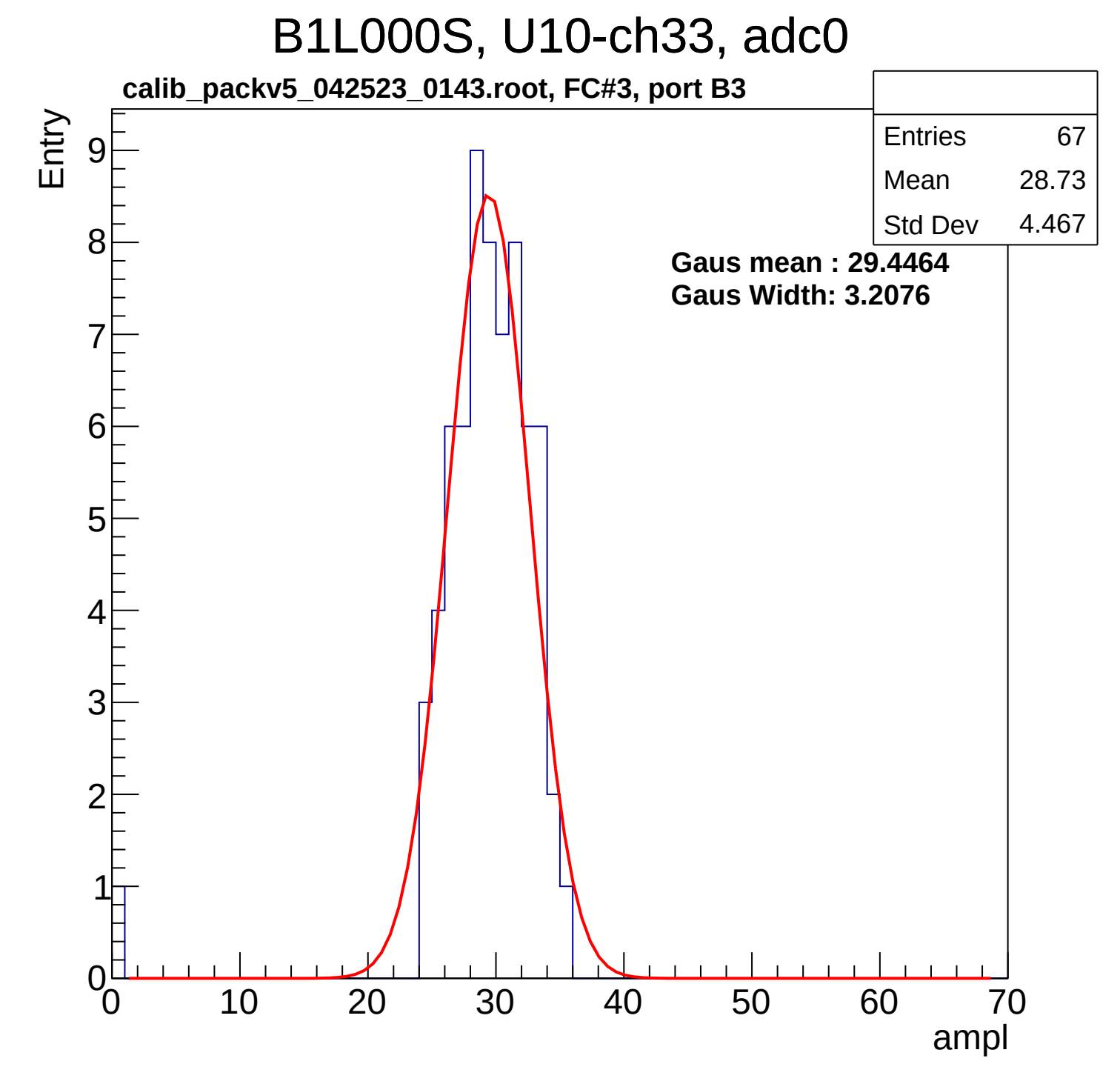
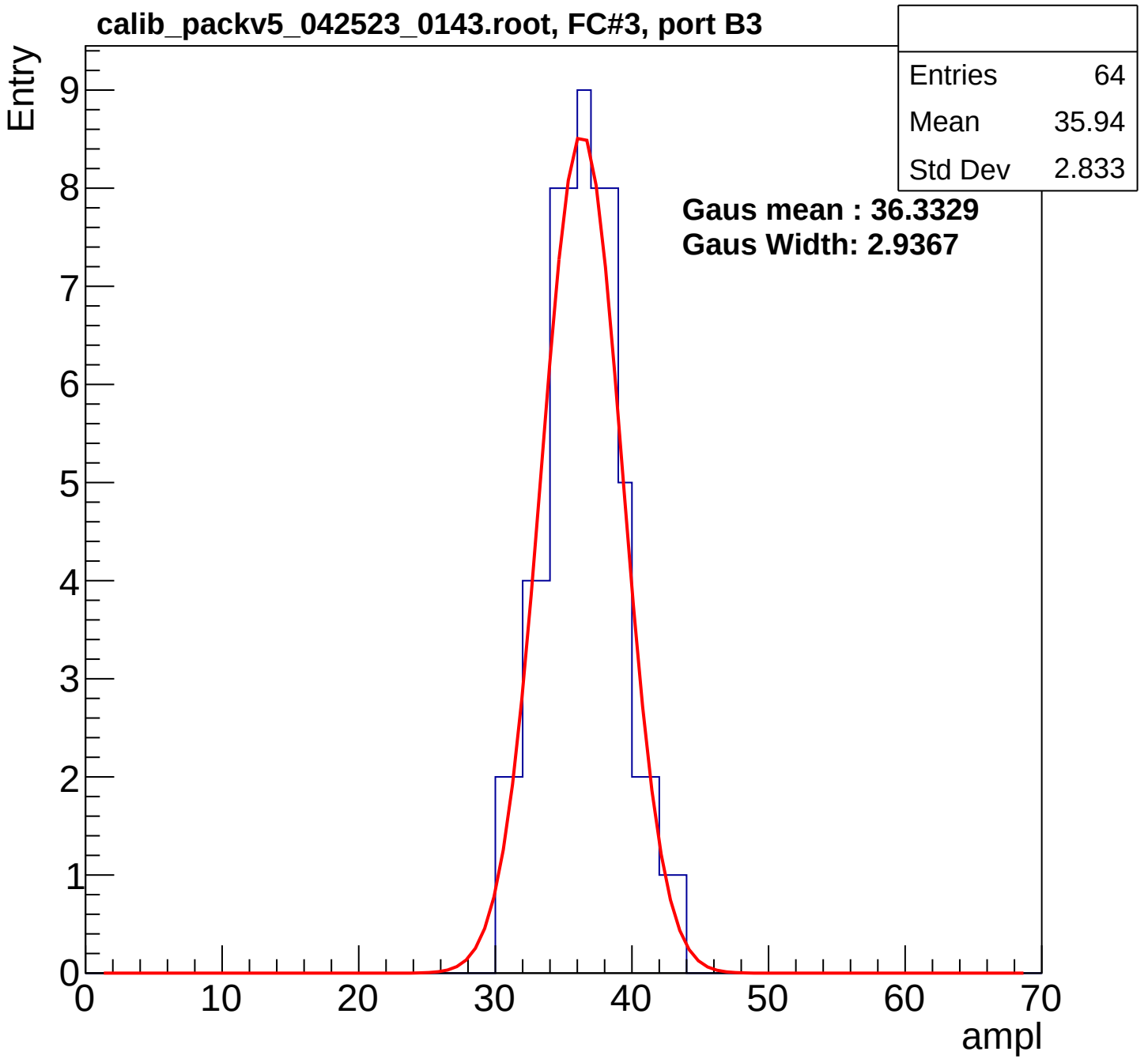
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch33, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 28.73
Std Dev 4.467
Gaus mean : 29.4464
Gaus Width: 3.2076
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch33, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.94
Std Dev 2.833
Gaus mean : 36.3329
Gaus Width: 2.9367
Entry
ampl
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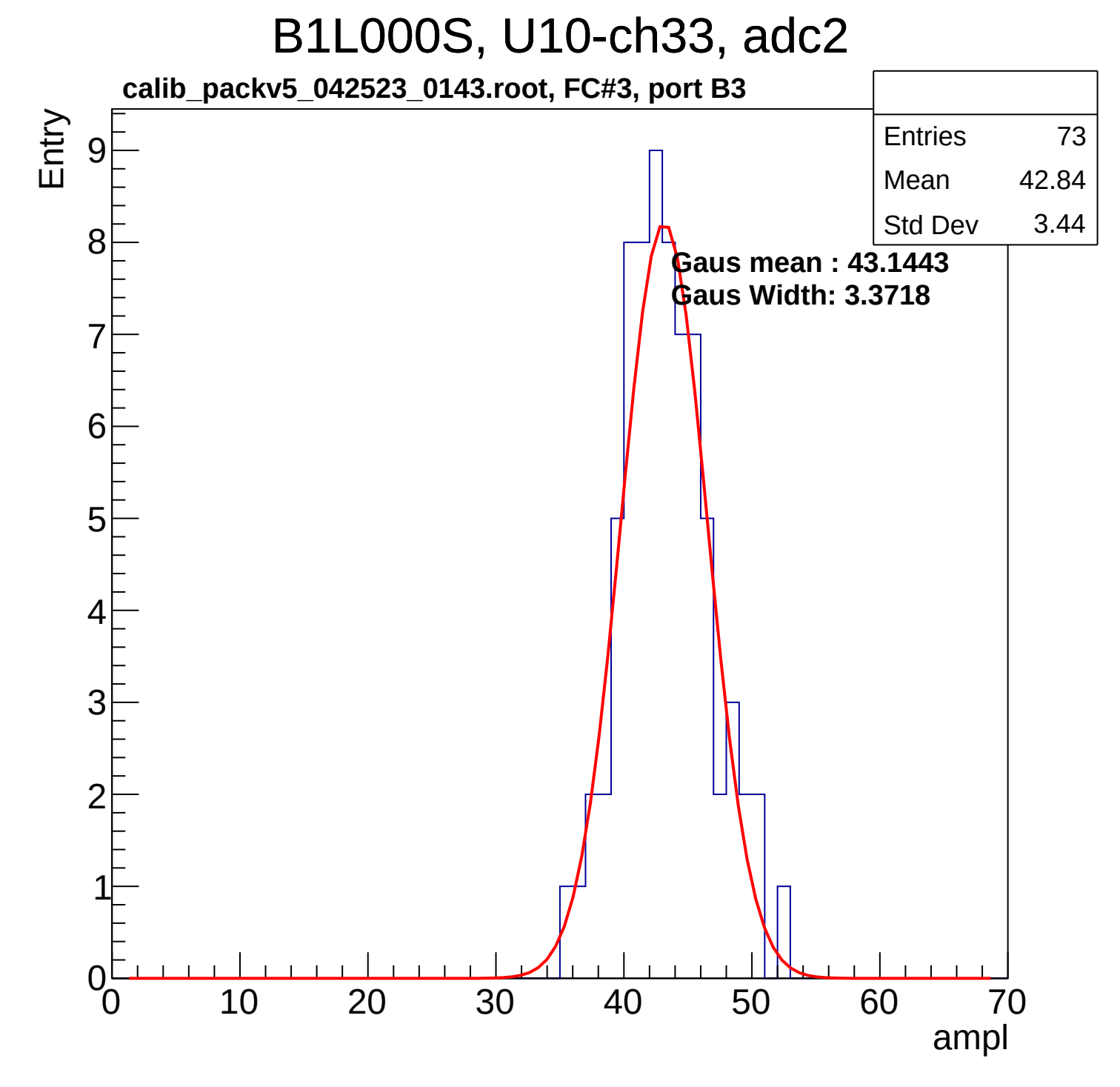
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U10-ch33, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 42.84
Std Dev 3.44
Gaus mean : 43.1443
Gaus Width: 3.3718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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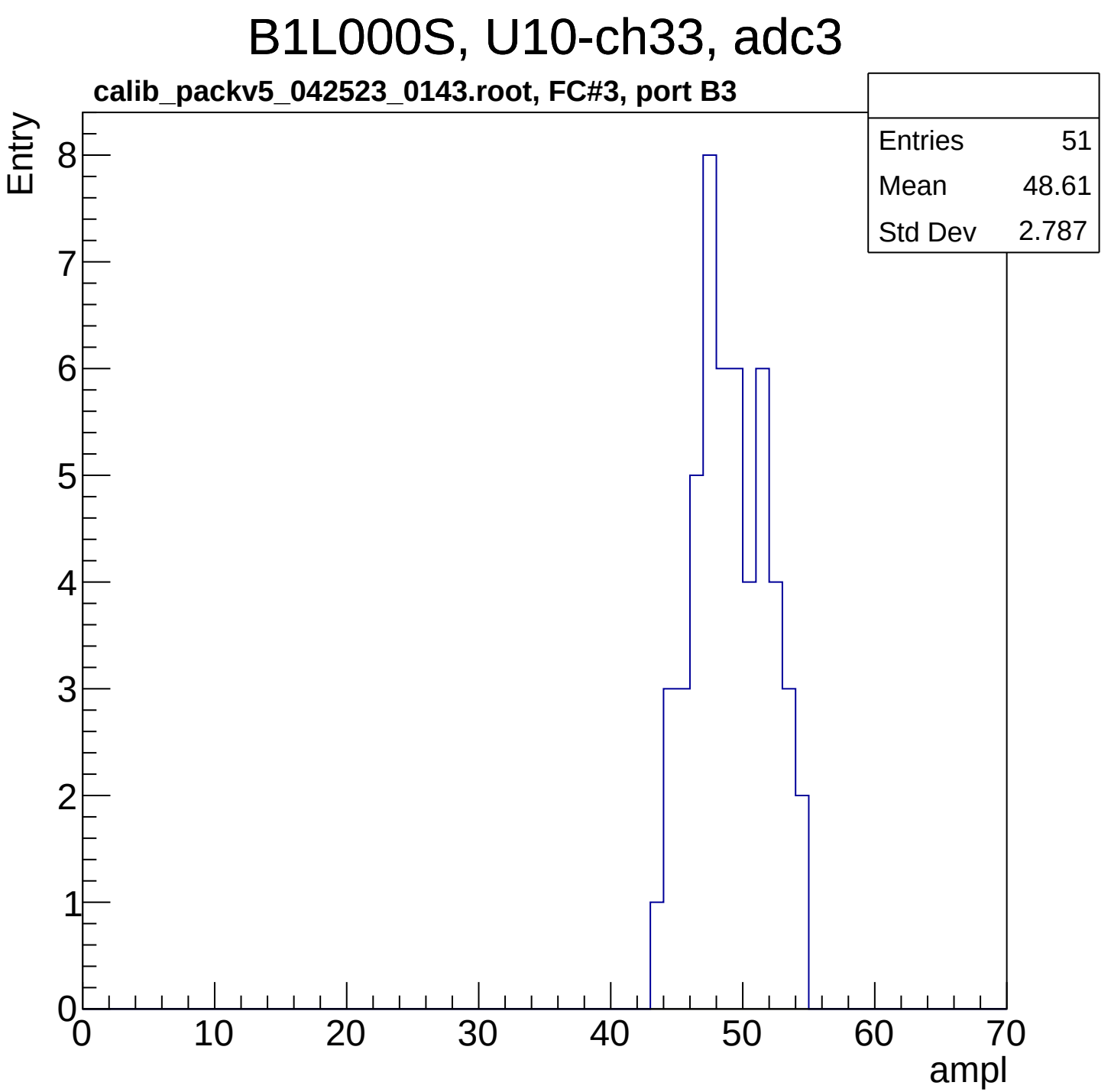
B1L000S, U10-ch33, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 48.61
Std Dev 2.787
Entry
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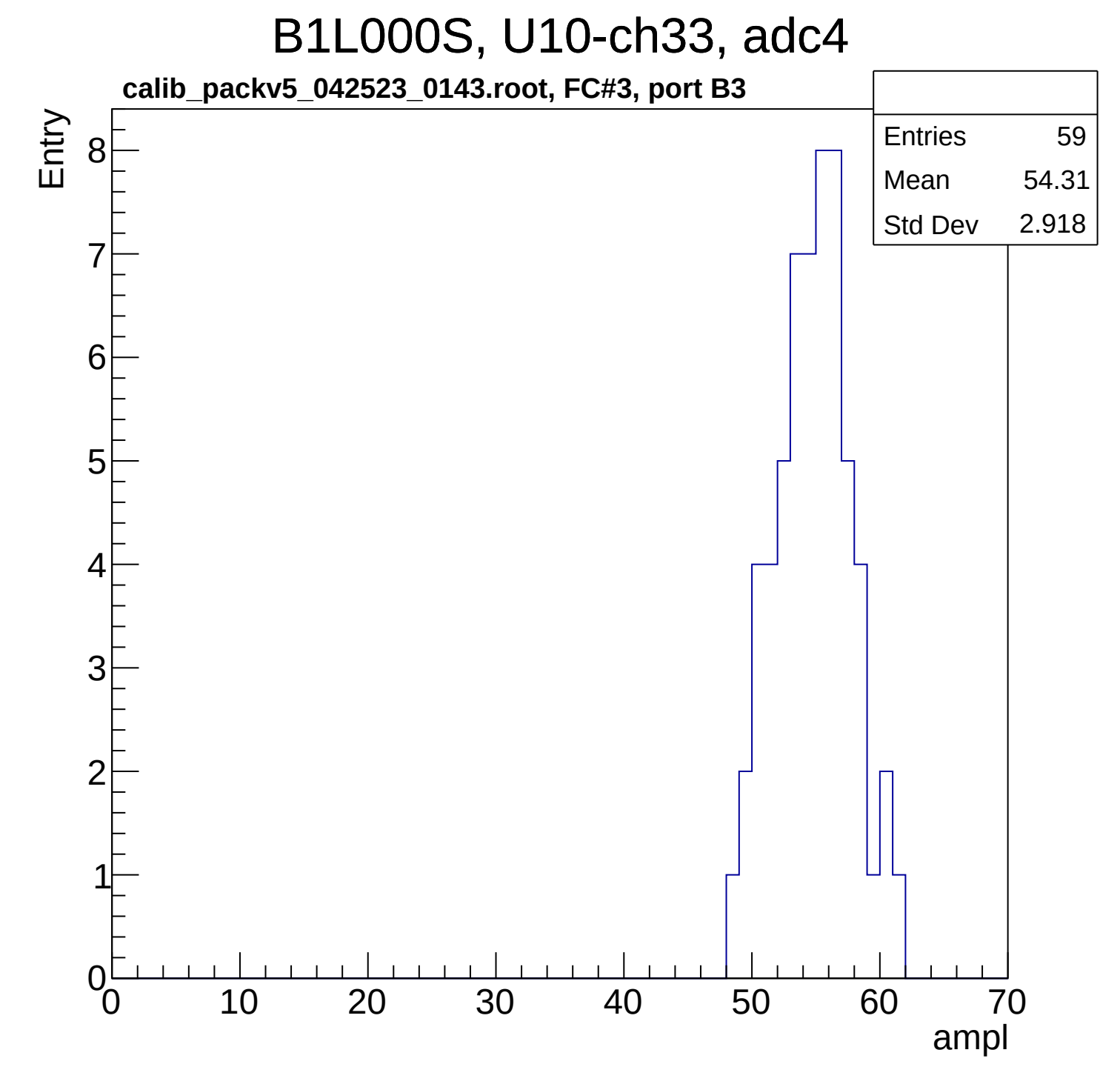
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch33, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 54.31
Std Dev 2.918
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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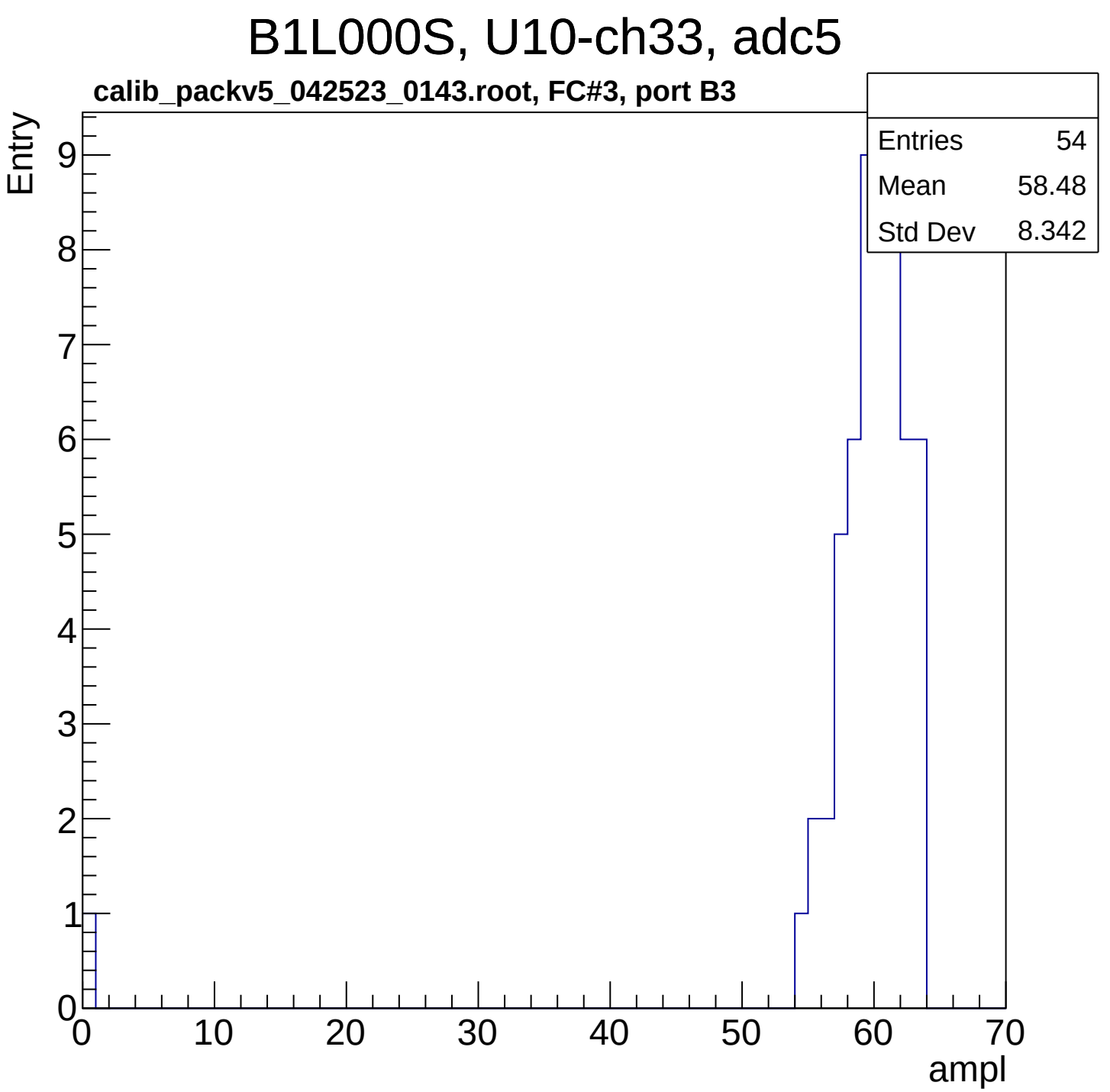
40
50
60
70


B1L000S, U10-ch33, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.48
Std Dev 8.342
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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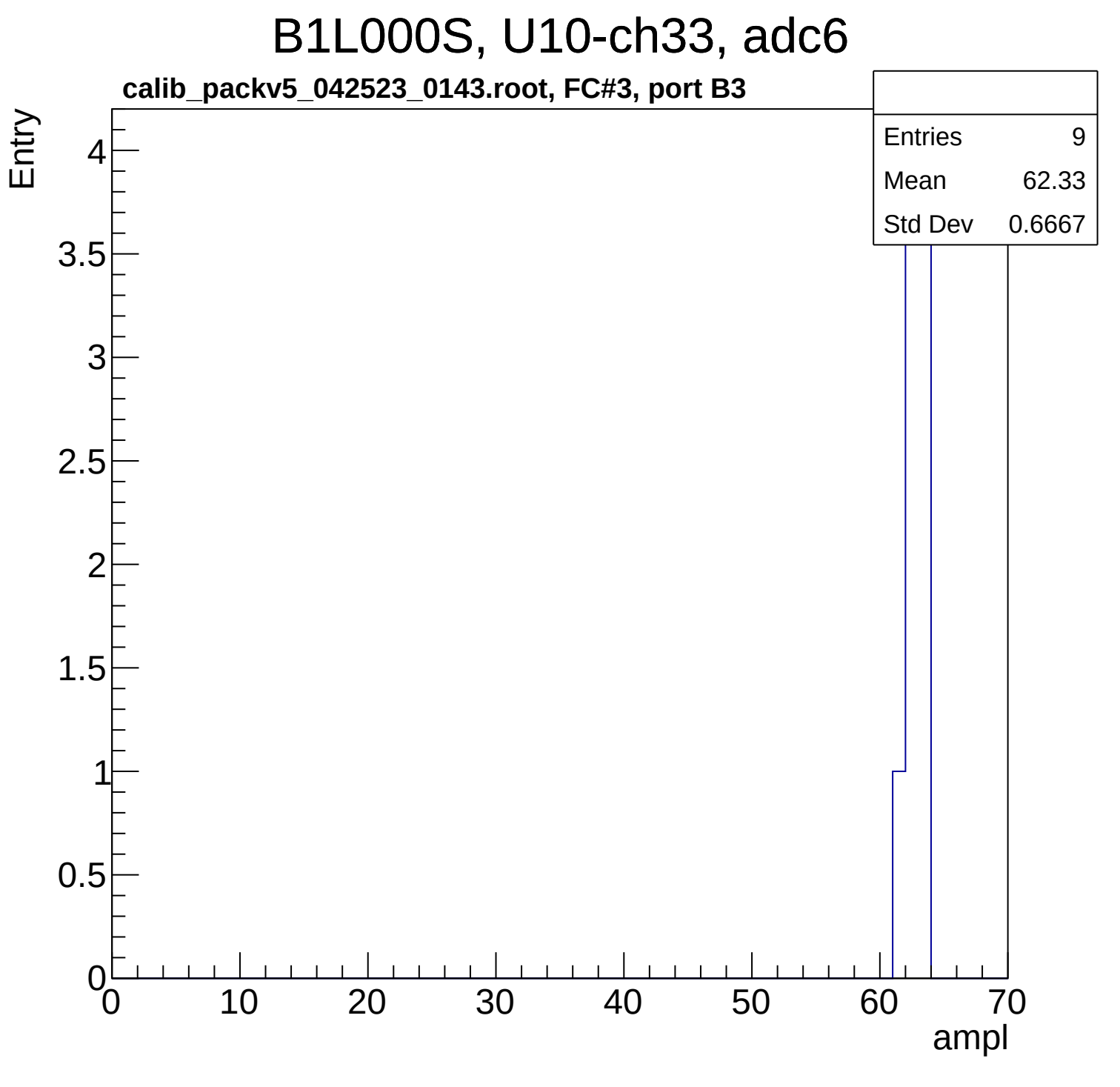
60
70


B1L000S, U10-ch33, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 62.33
Std Dev 0.6667
Entry
ampl
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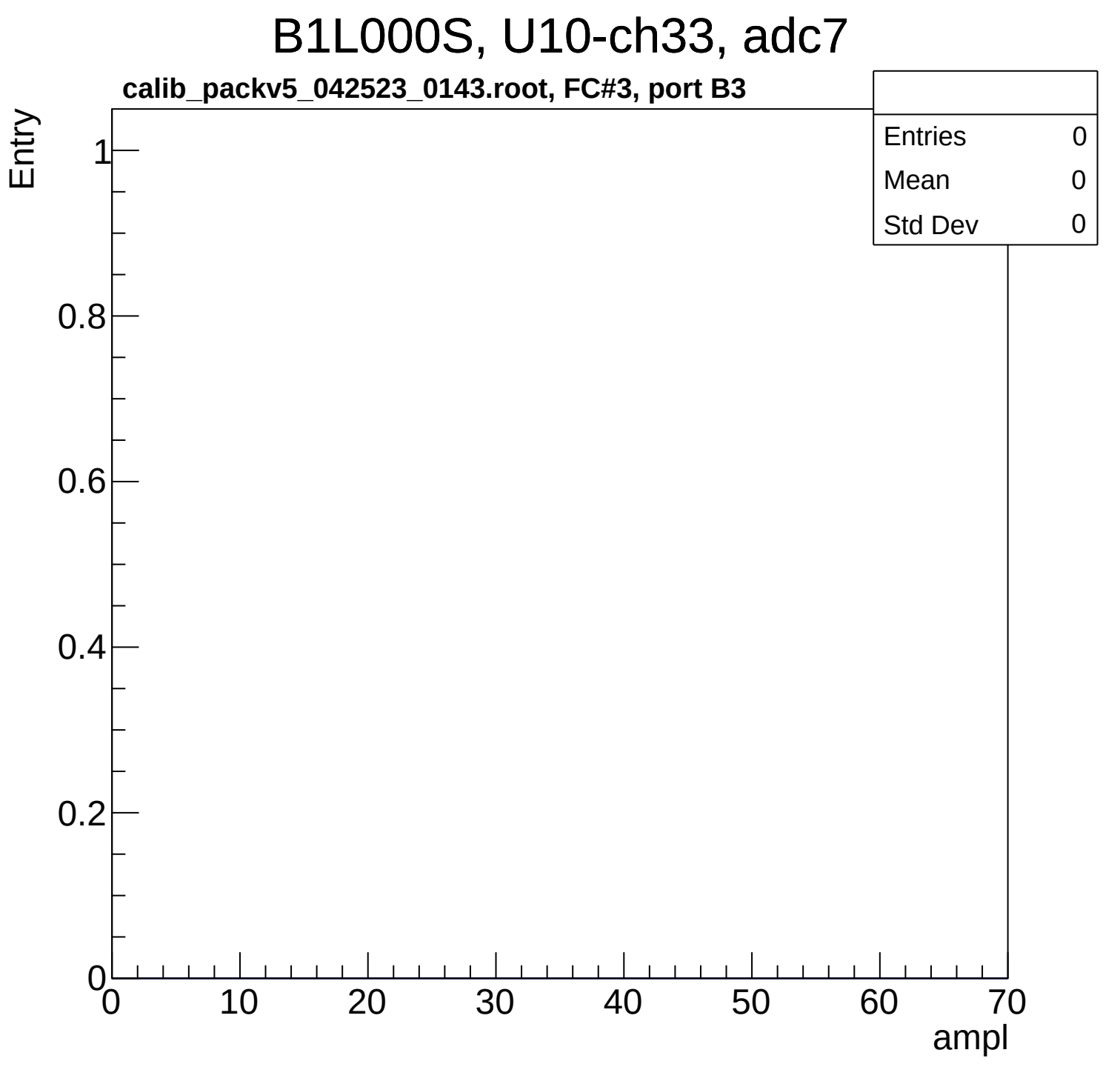
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch33, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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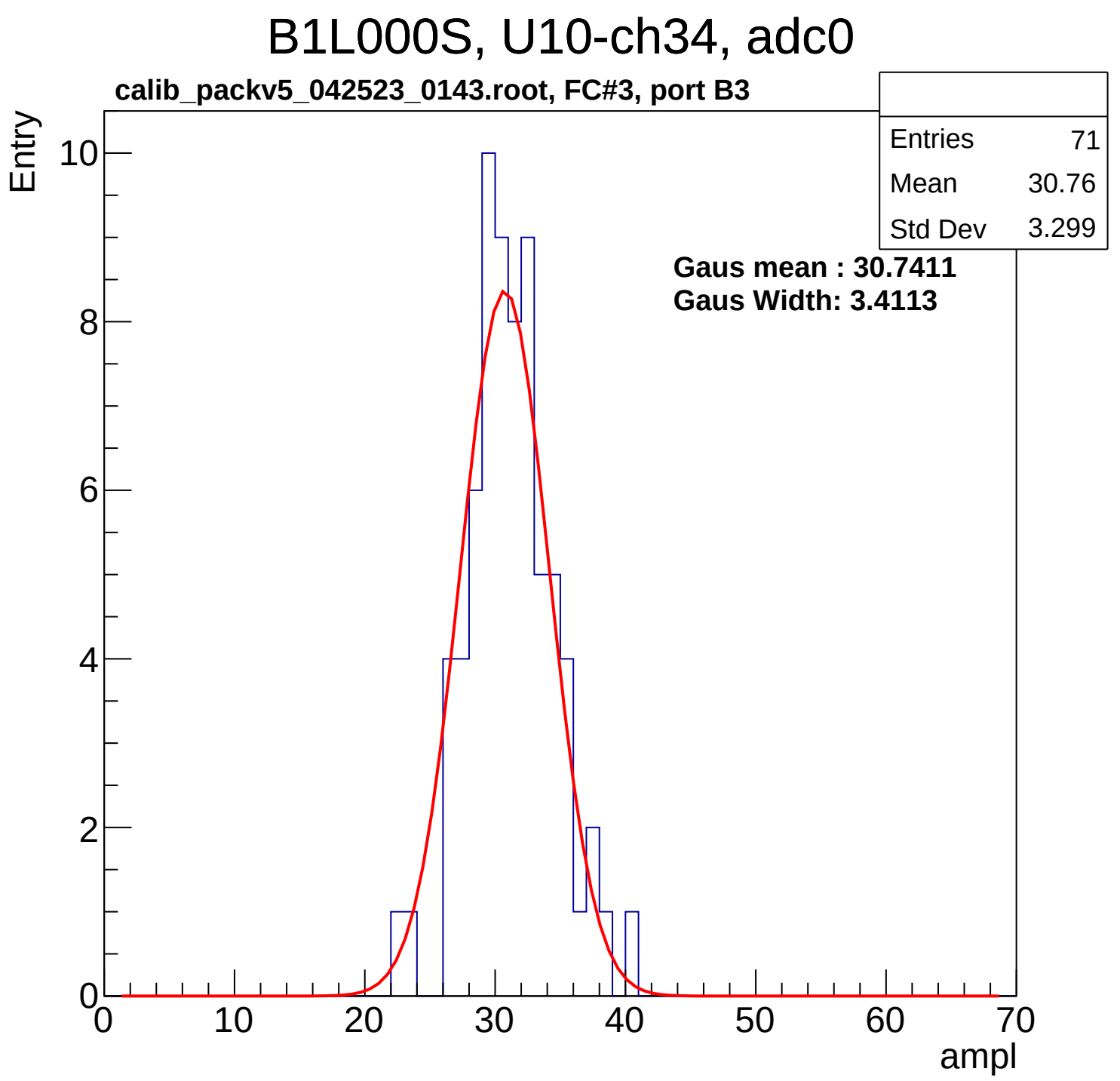
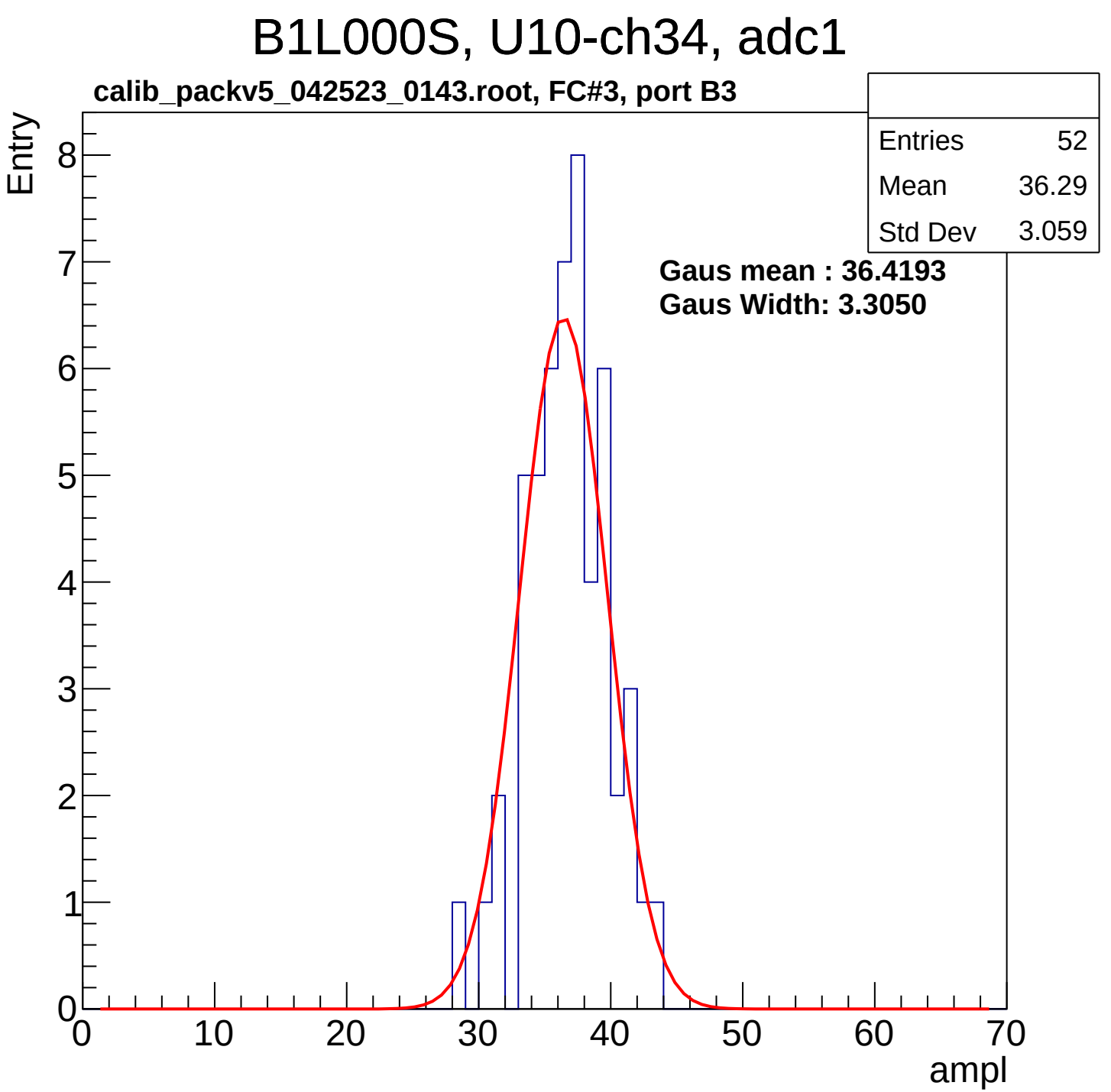
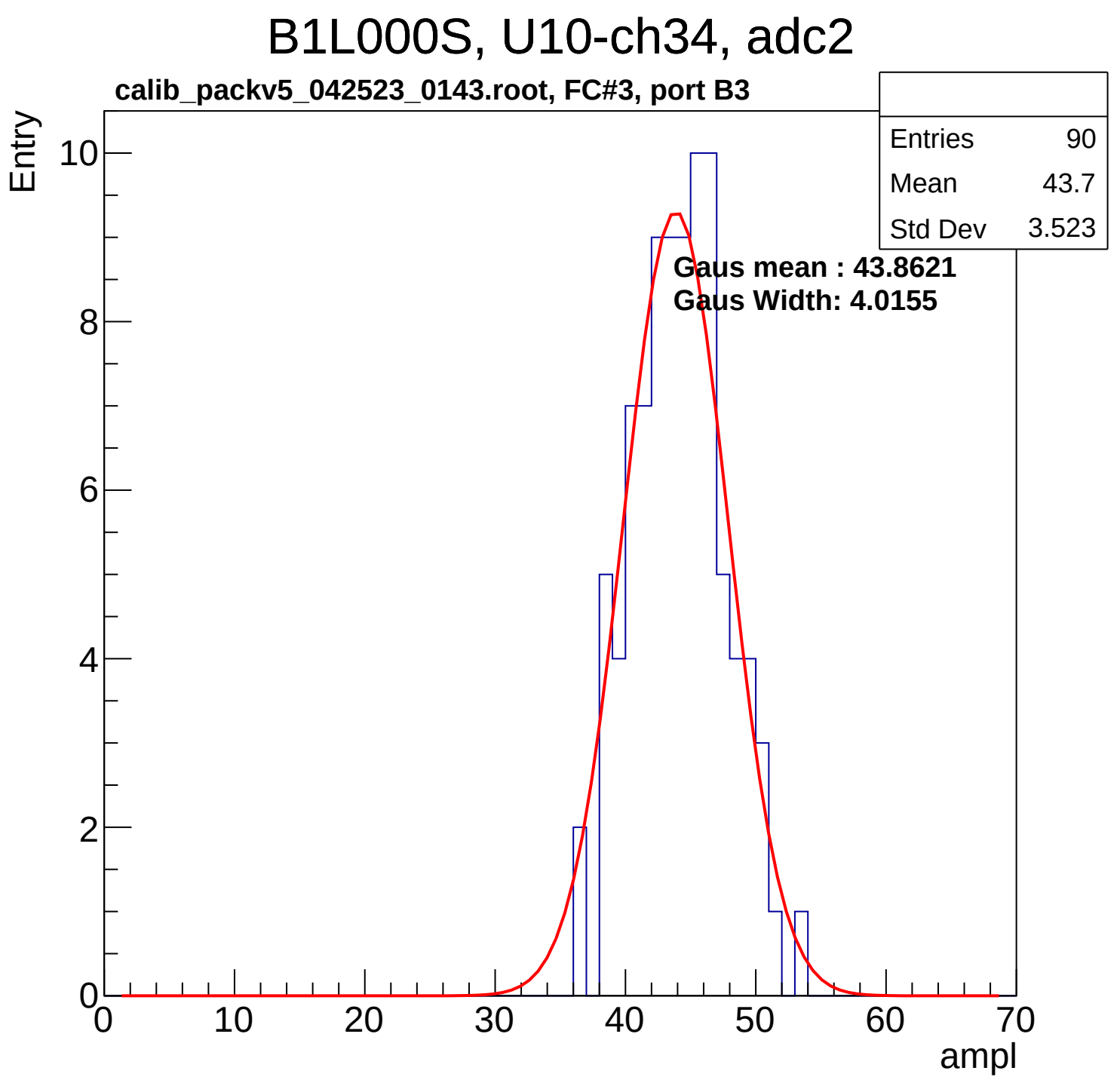
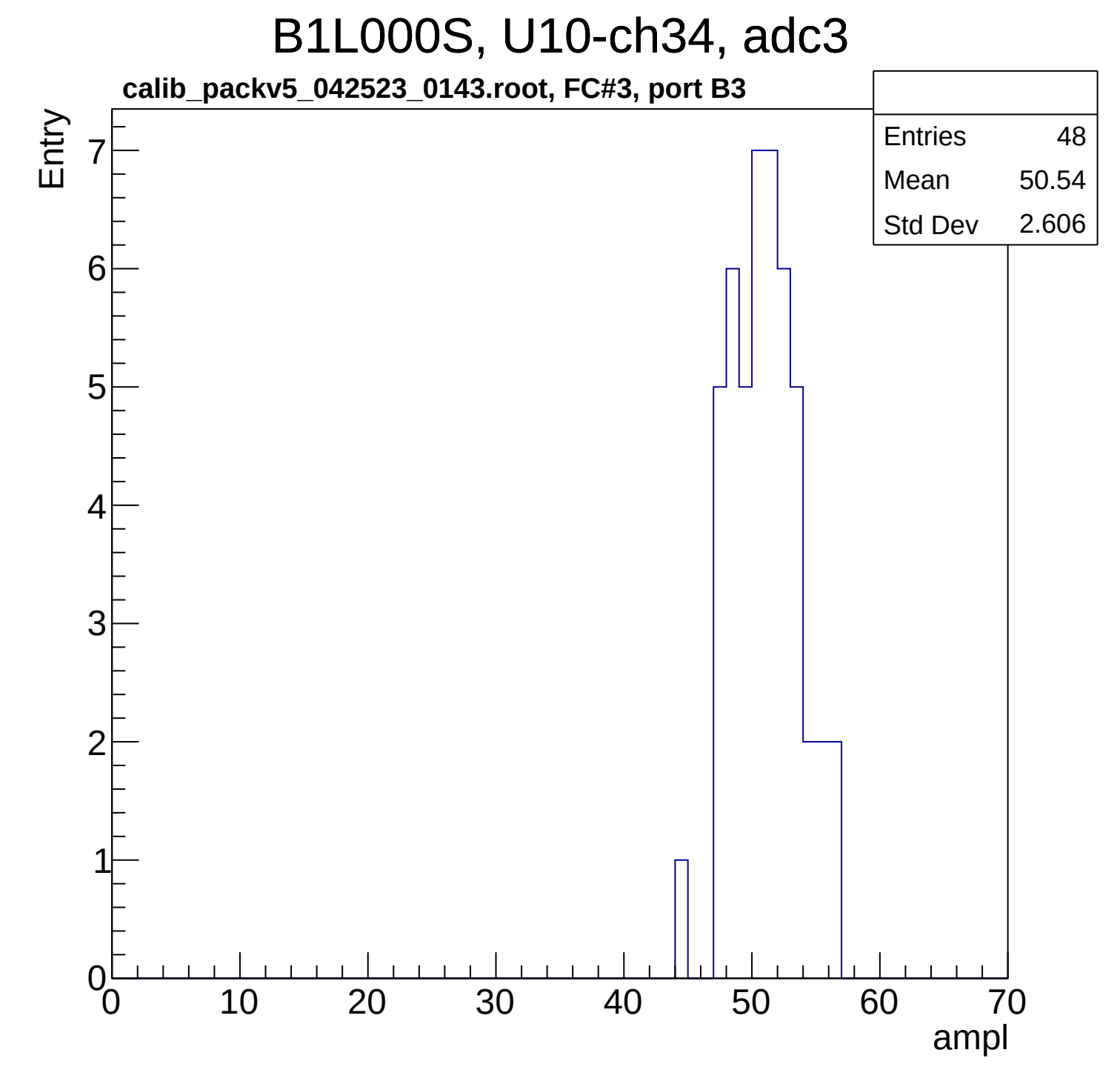
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch34, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 30.76
Std Dev 3.299
Gaus mean : 30.7411
Gaus Width: 3.4113
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch34, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 36.29
Std Dev 3.059
Gaus mean : 36.4193
Gaus Width: 3.3050
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch34, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 90
Mean 43.7
Std Dev 3.523
Gaus mean : 43.8621
Gaus Width: 4.0155
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch34, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 50.54
Std Dev 2.606
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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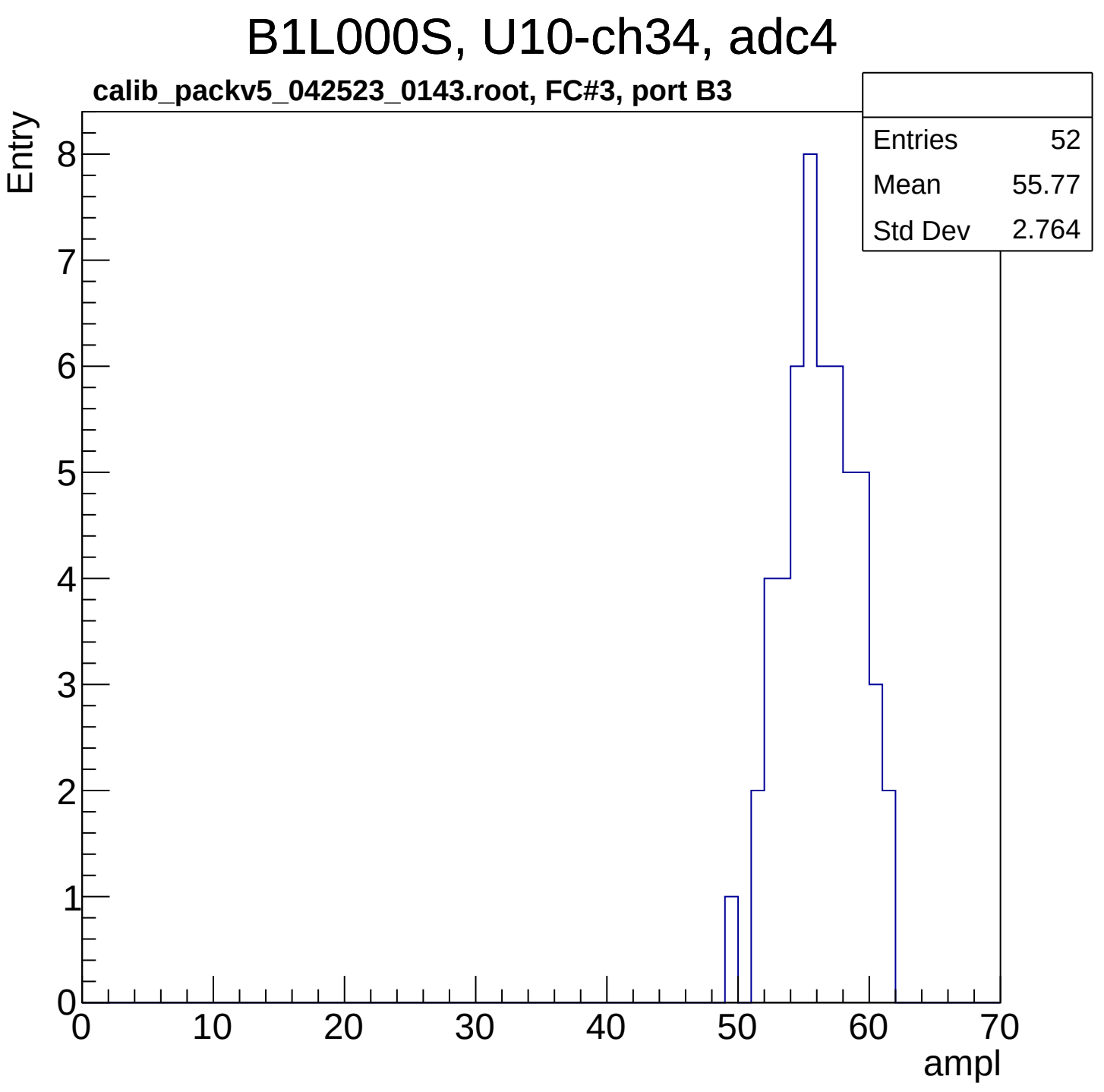
B1L000S, U10-ch34, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 55.77
Std Dev 2.764
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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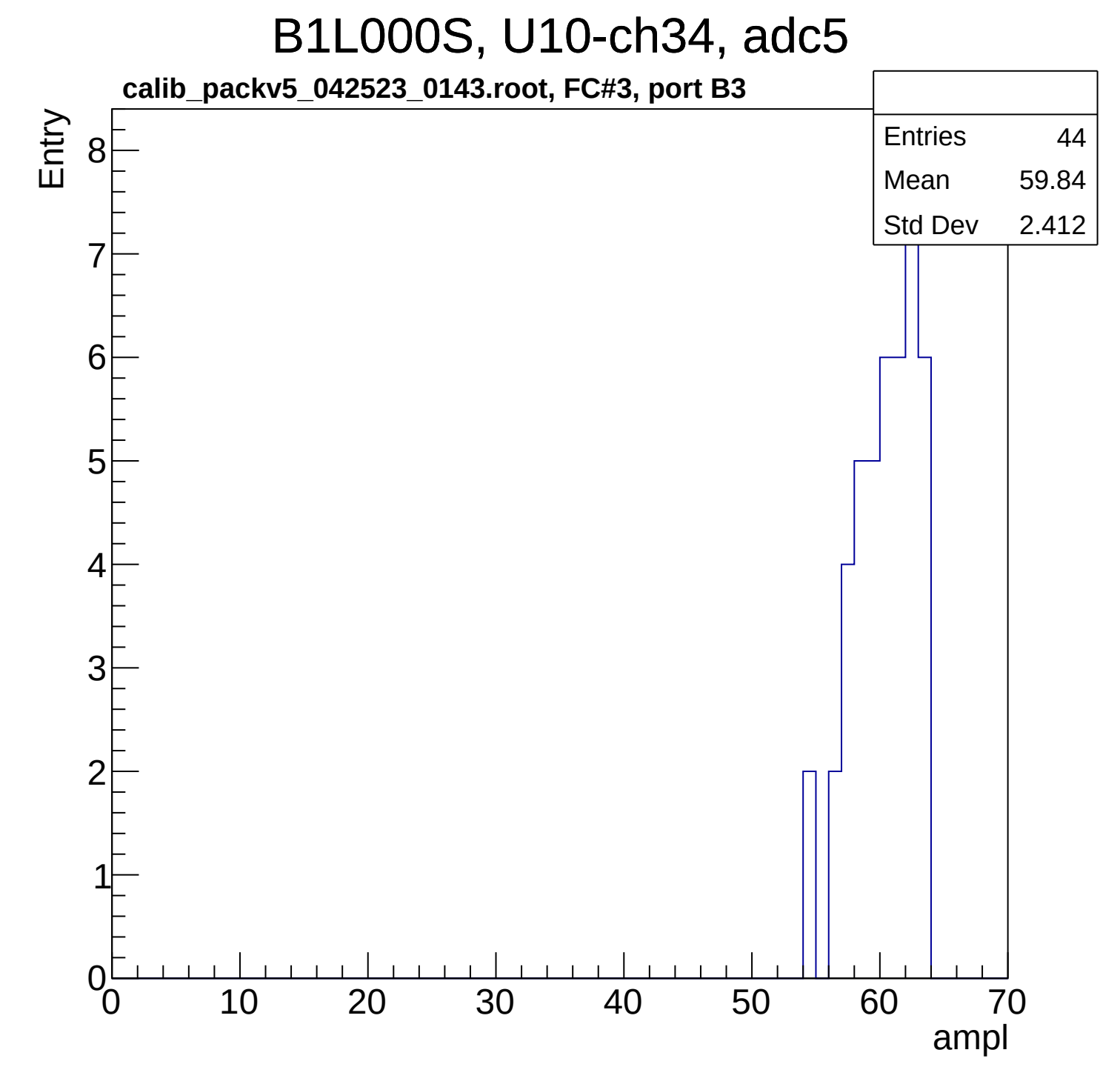
B1L000S, U10-ch34, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.84
Std Dev 2.412
Entry
ampl
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2
3
4
5
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7
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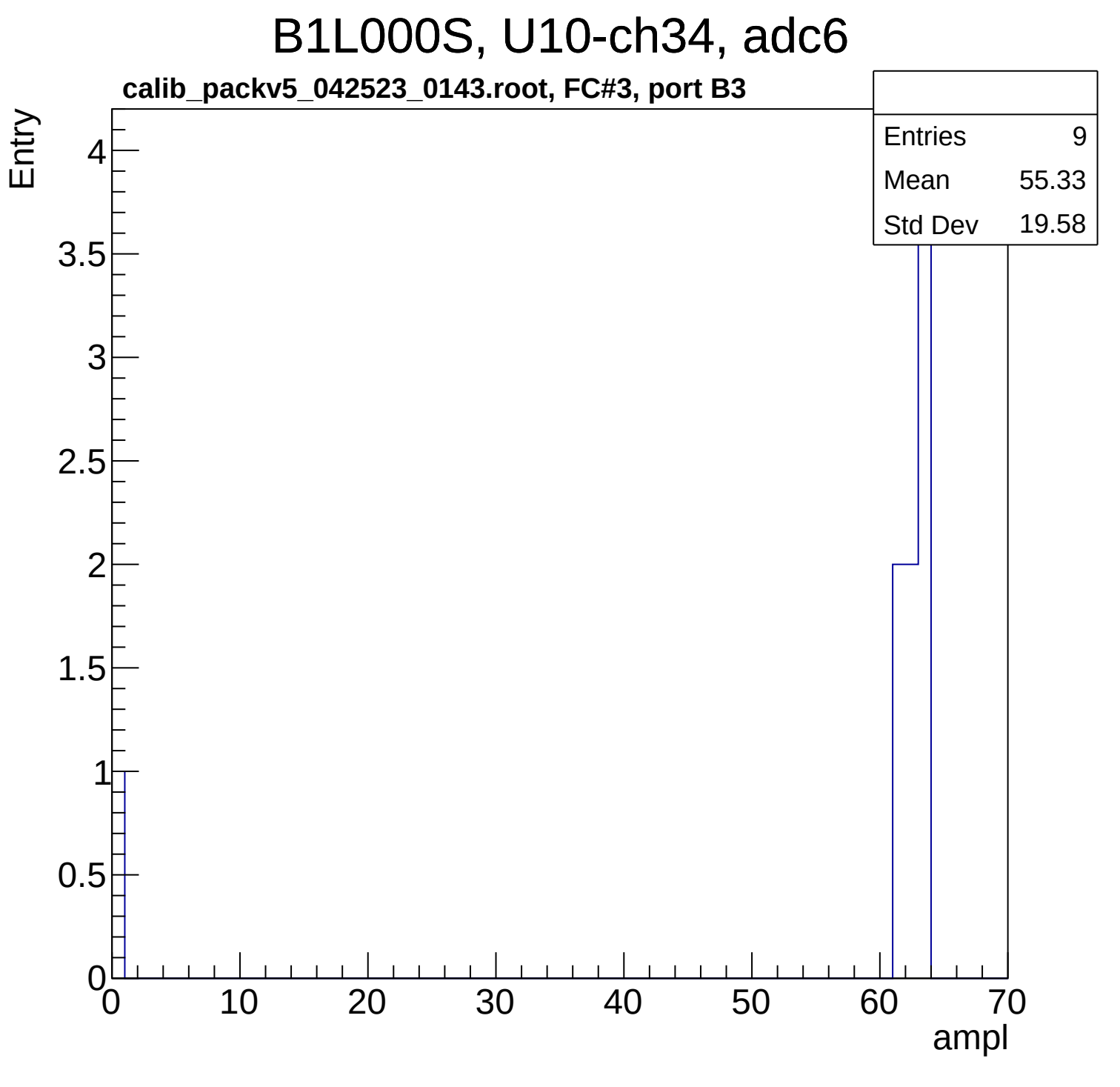
B1L000S, U10-ch34, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
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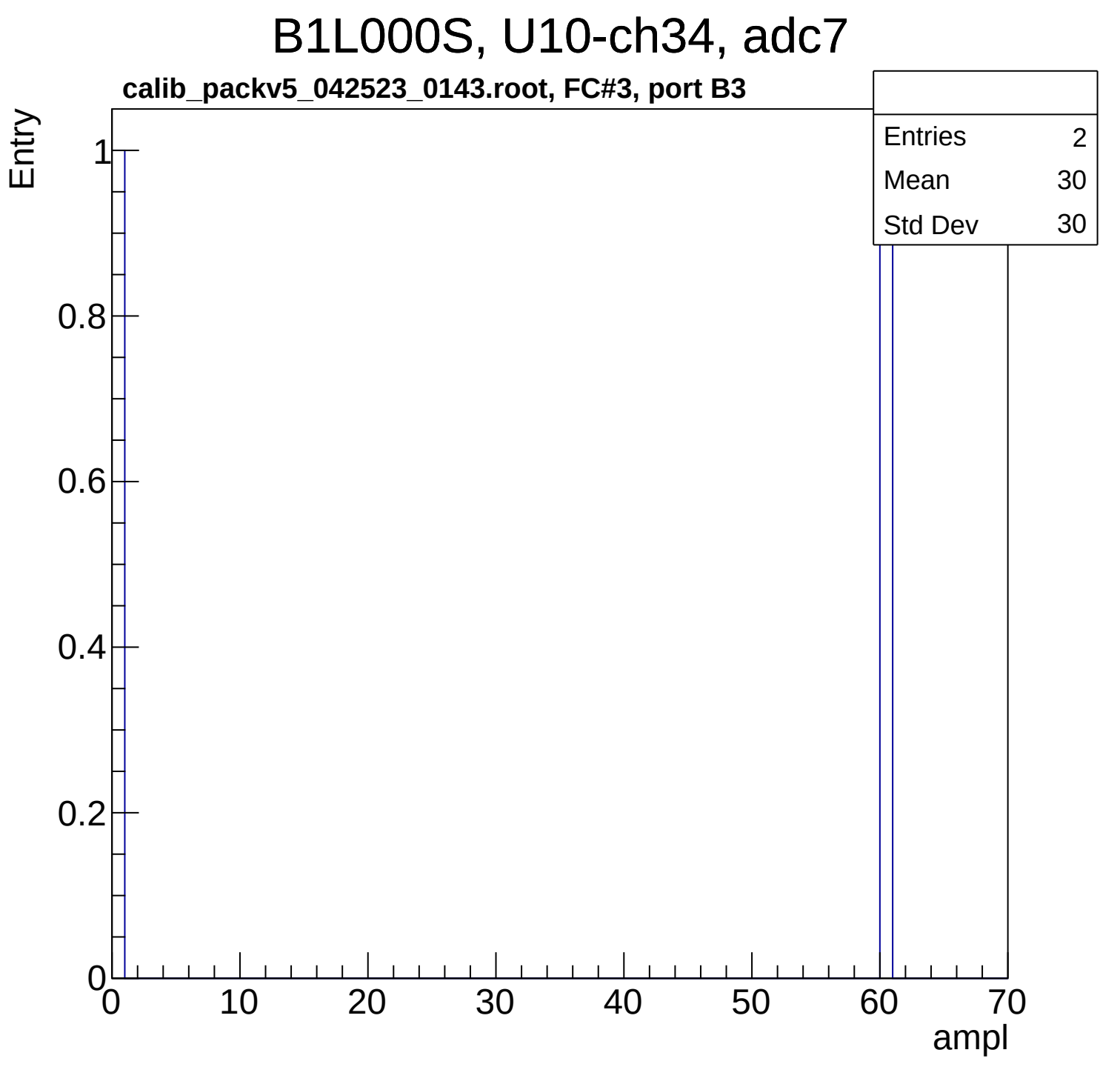
B1L000S, U10-ch34, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 30
Std Dev 30
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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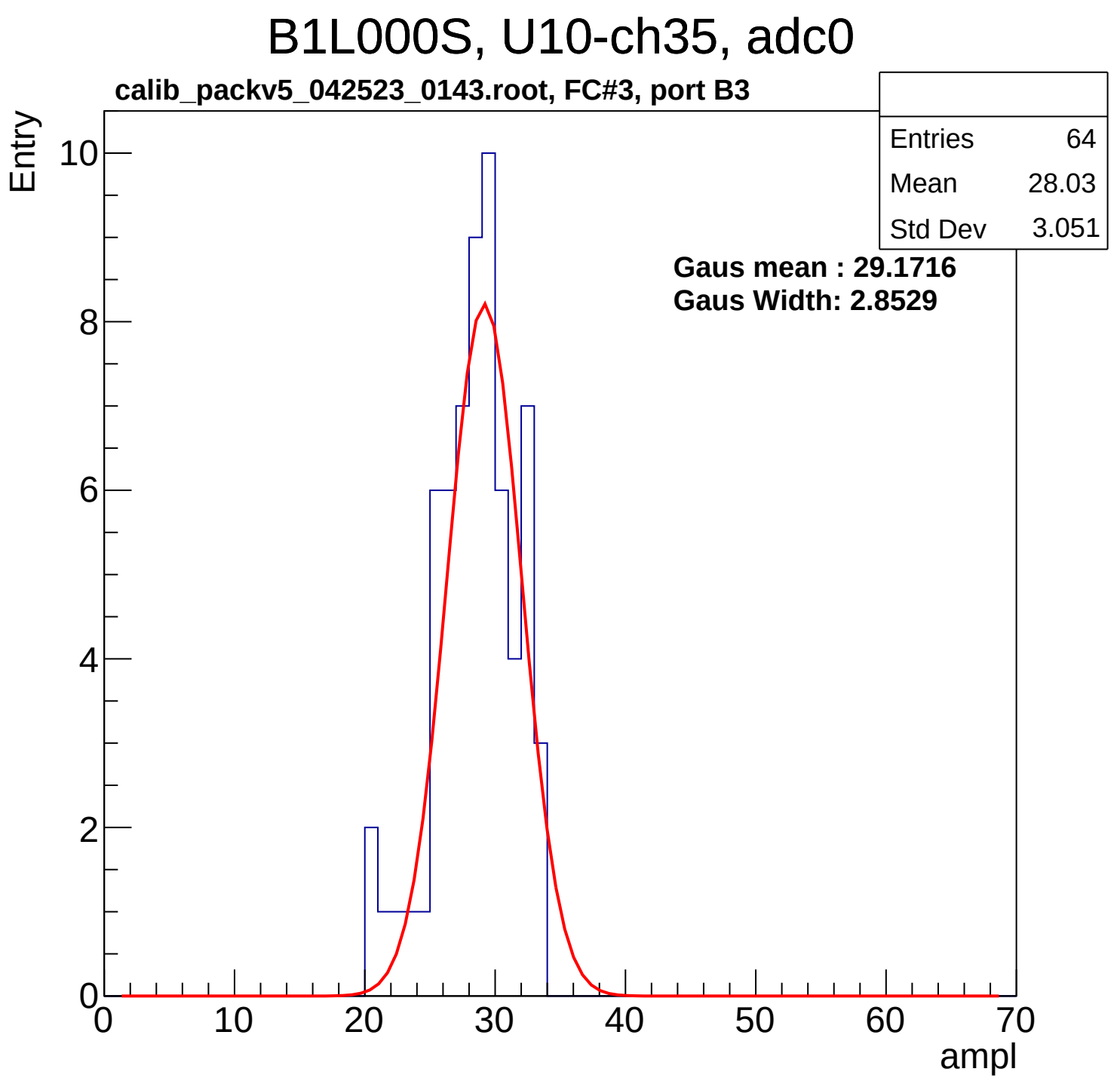
70
ampl


B1L000S, U10-ch35, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.03
Std Dev 3.051
Gaus mean : 29.1716
Gaus Width: 2.8529
Entry
ampl
0
2
4
6
8
10
0
10
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60
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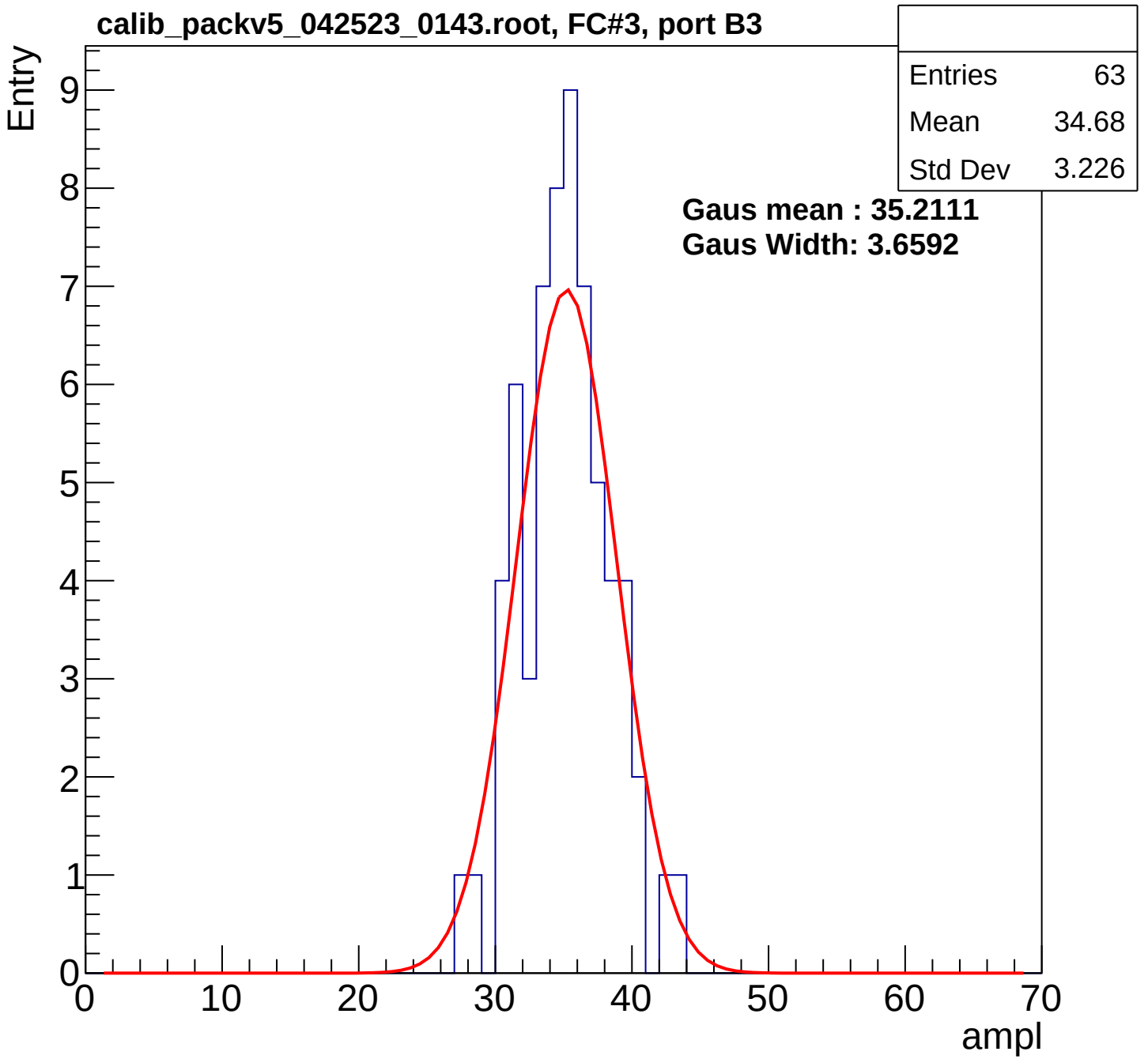
B1L000S, U10-ch35, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.68
Std Dev 3.226
Gaus mean : 35.2111
Gaus Width: 3.6592
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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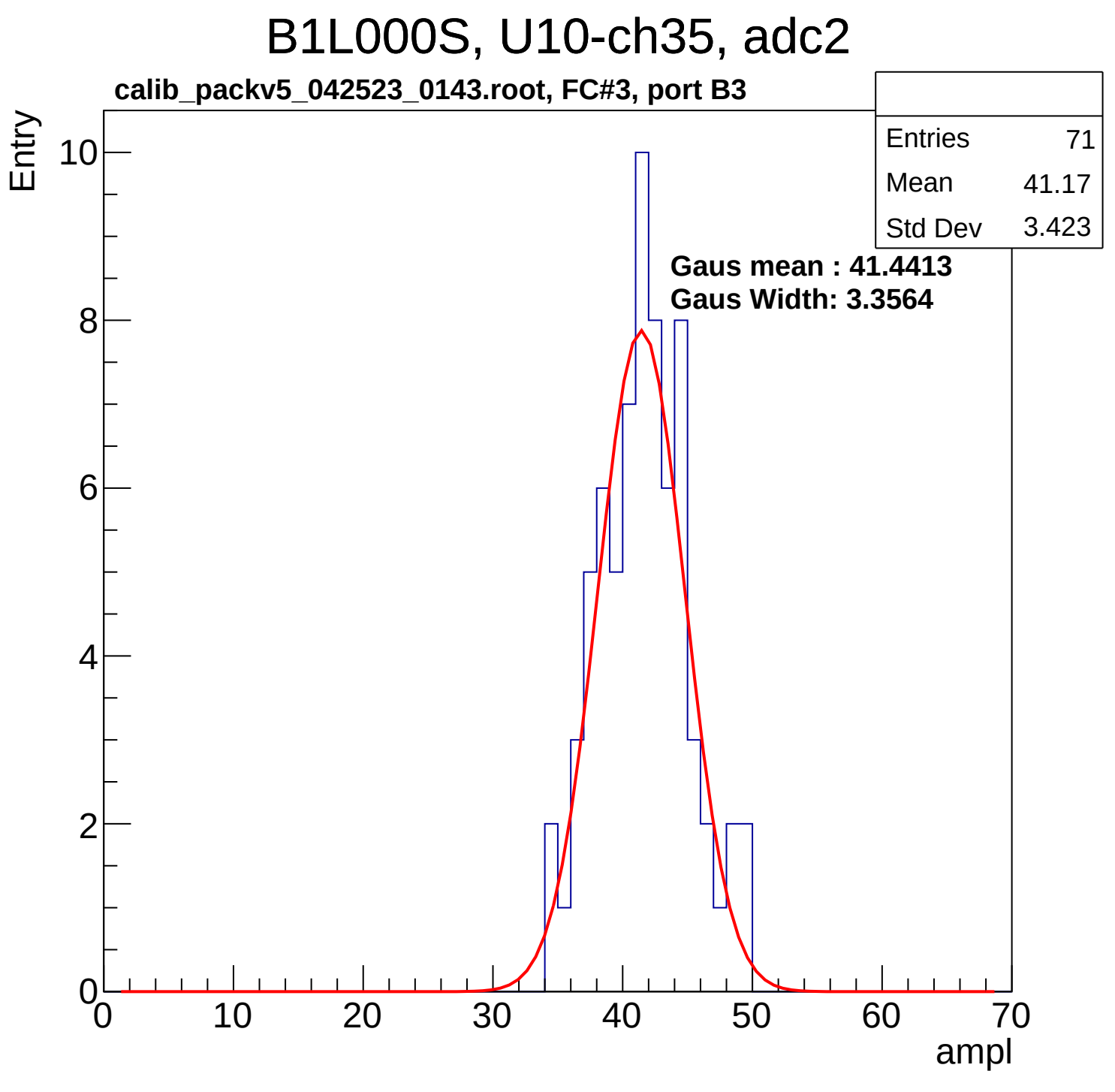
B1L000S, U10-ch35, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 41.17
Std Dev 3.423
Gaus mean : 41.4413
Gaus Width: 3.3564
Entry
ampl
0
2
4
6
8
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0
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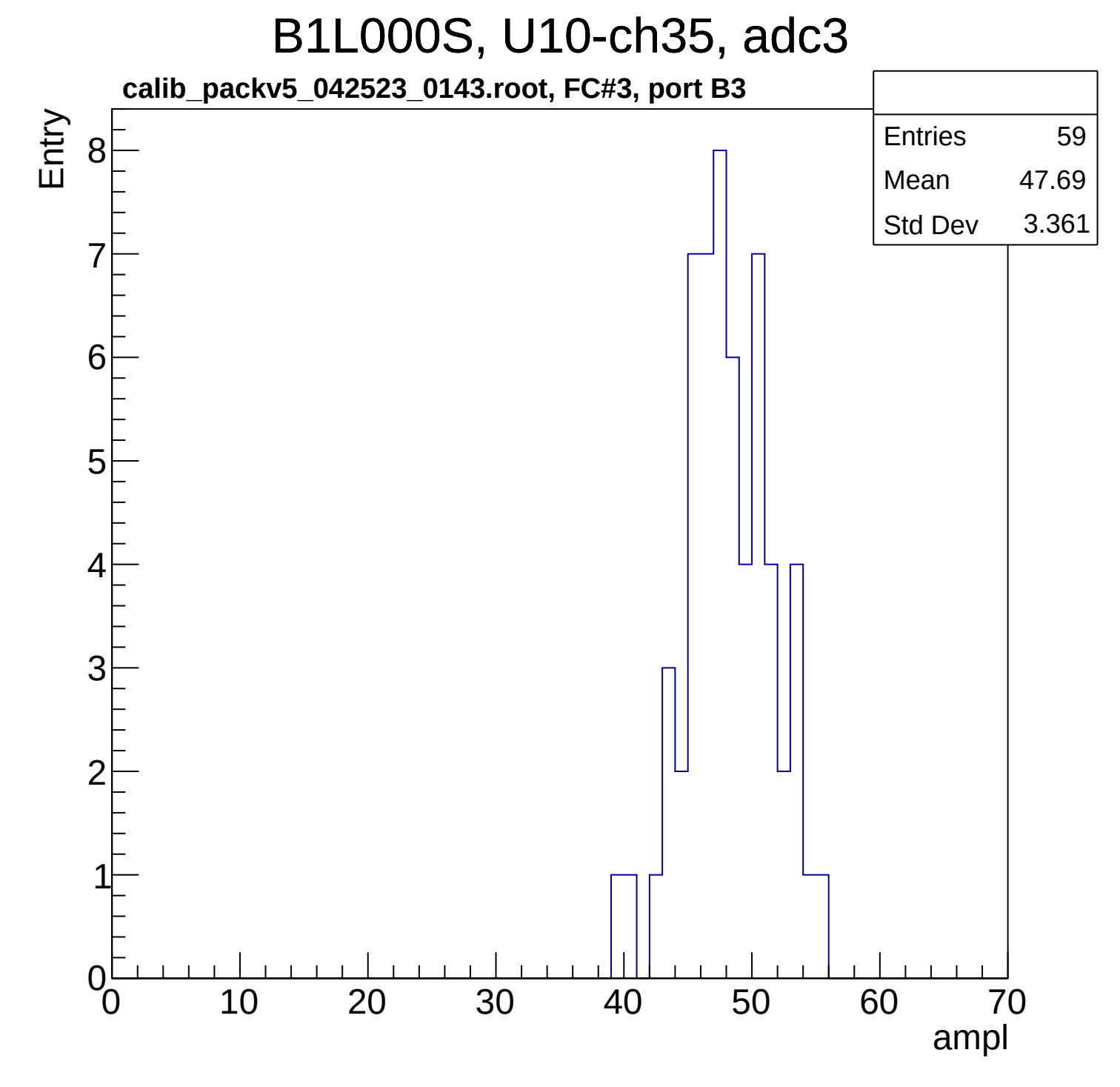
B1L000S, U10-ch35, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 47.69
Std Dev 3.361
Entry
ampl
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1
2
3
4
5
6
7
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0
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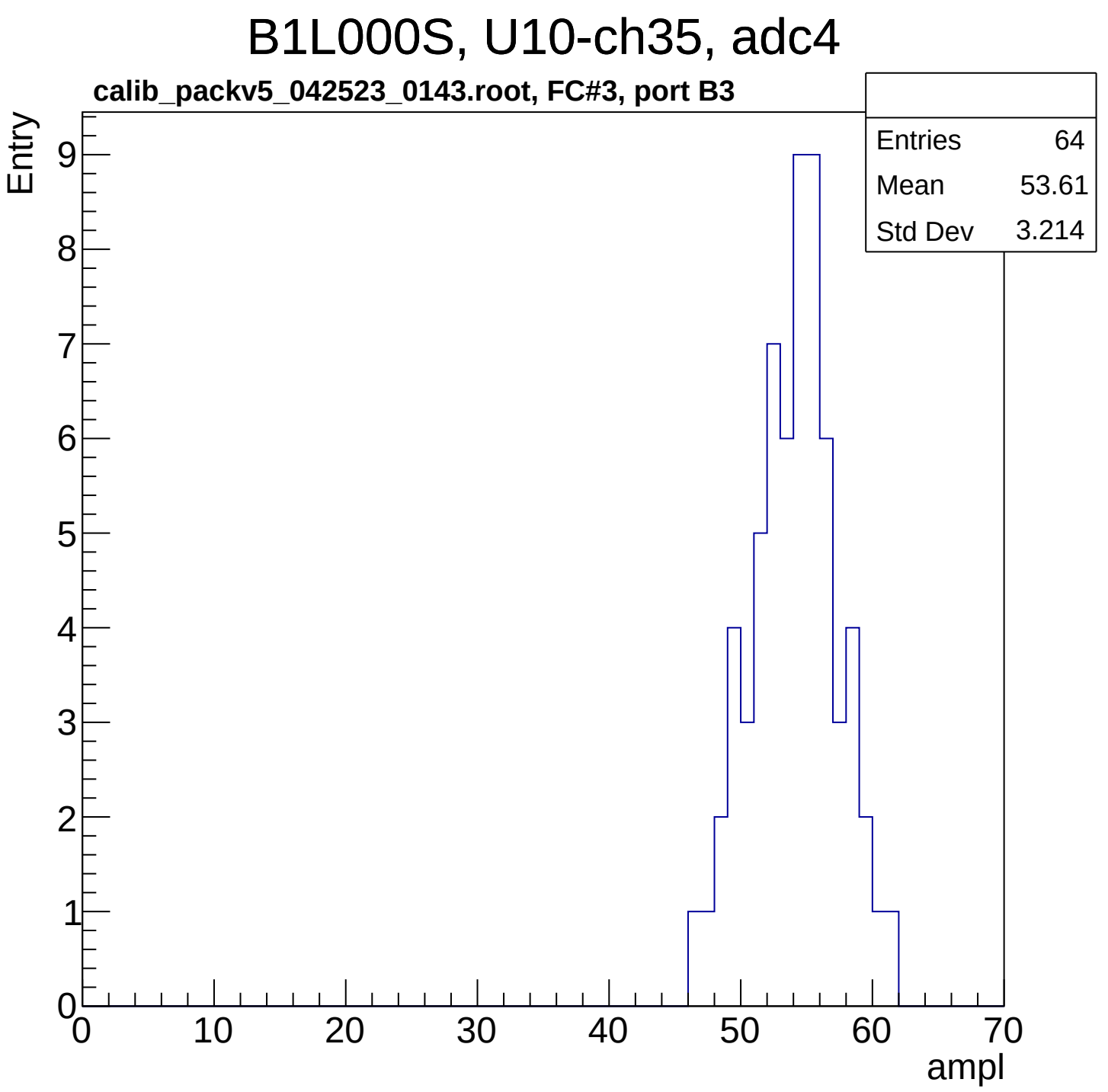
B1L000S, U10-ch35, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 53.61
Std Dev 3.214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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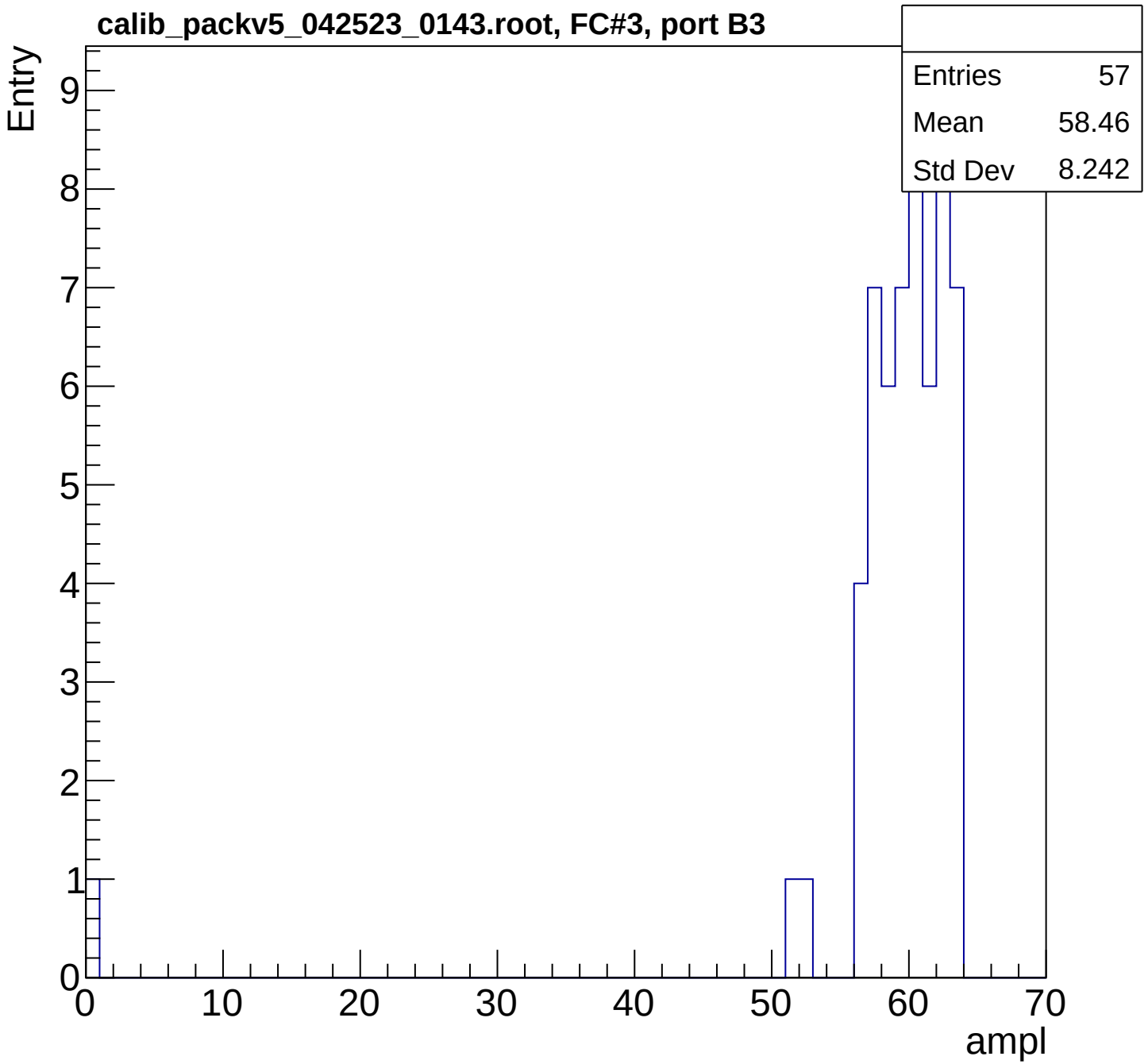
B1L000S, U10-ch35, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.46
Std Dev 8.242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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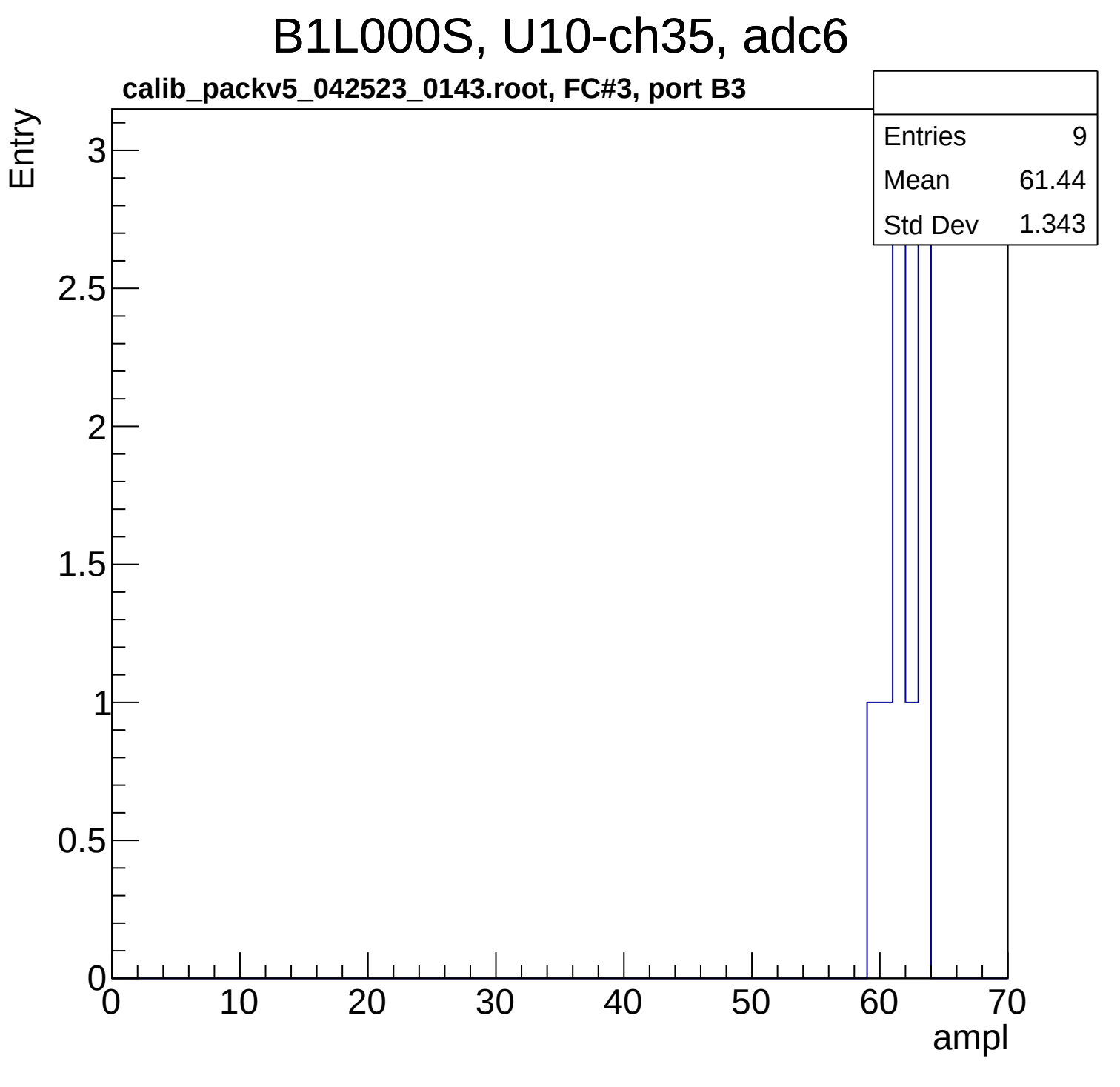
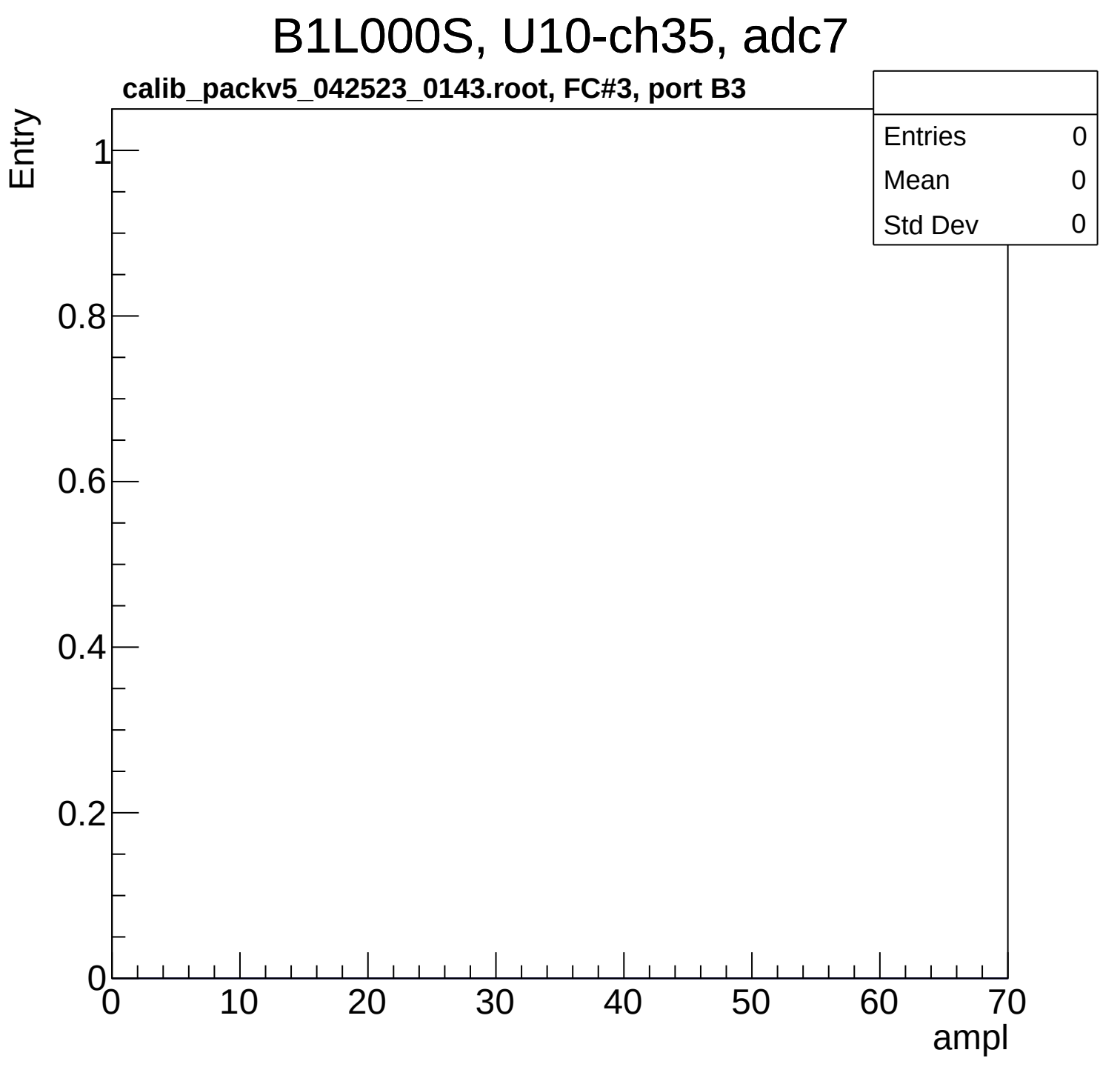
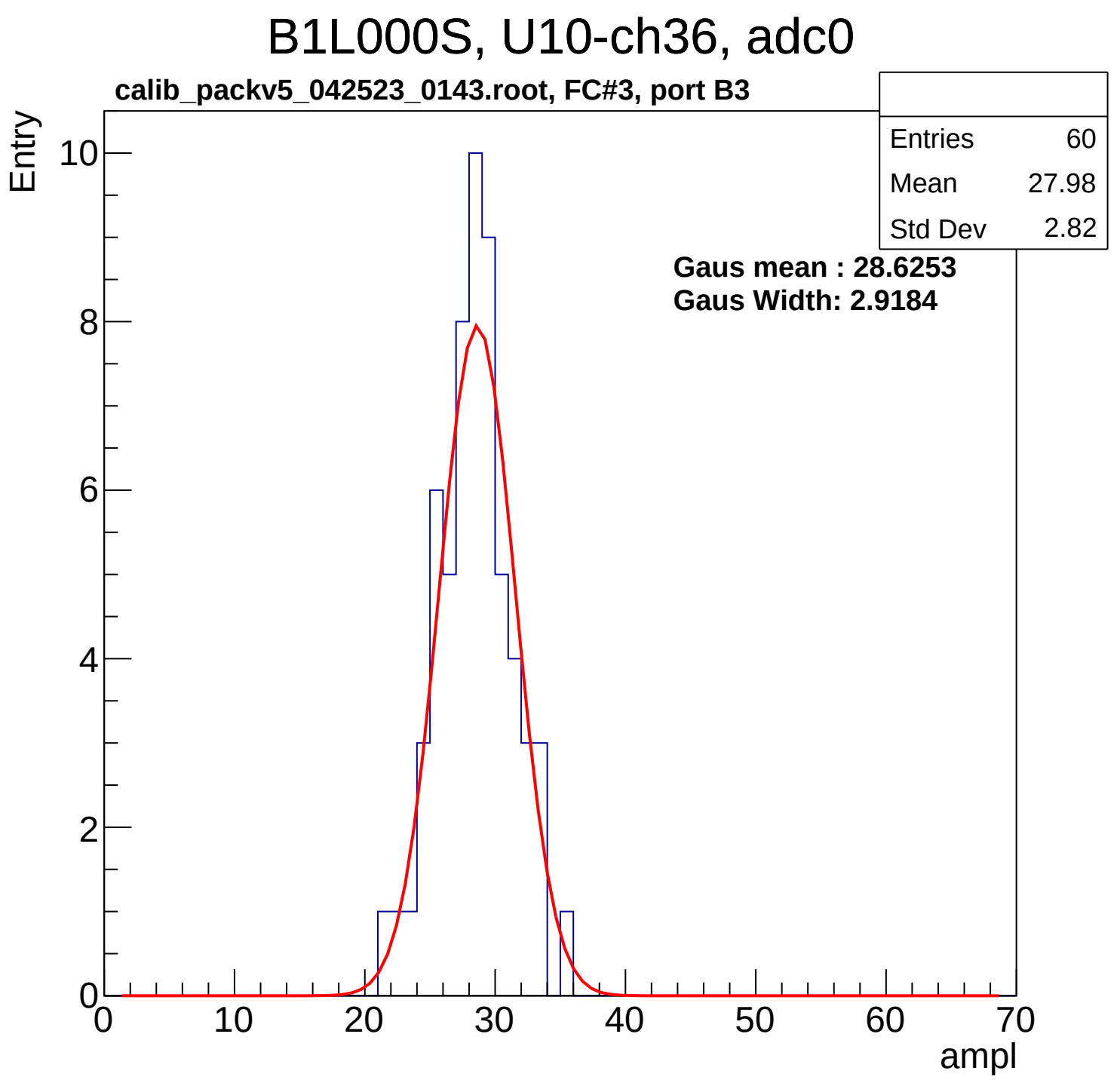
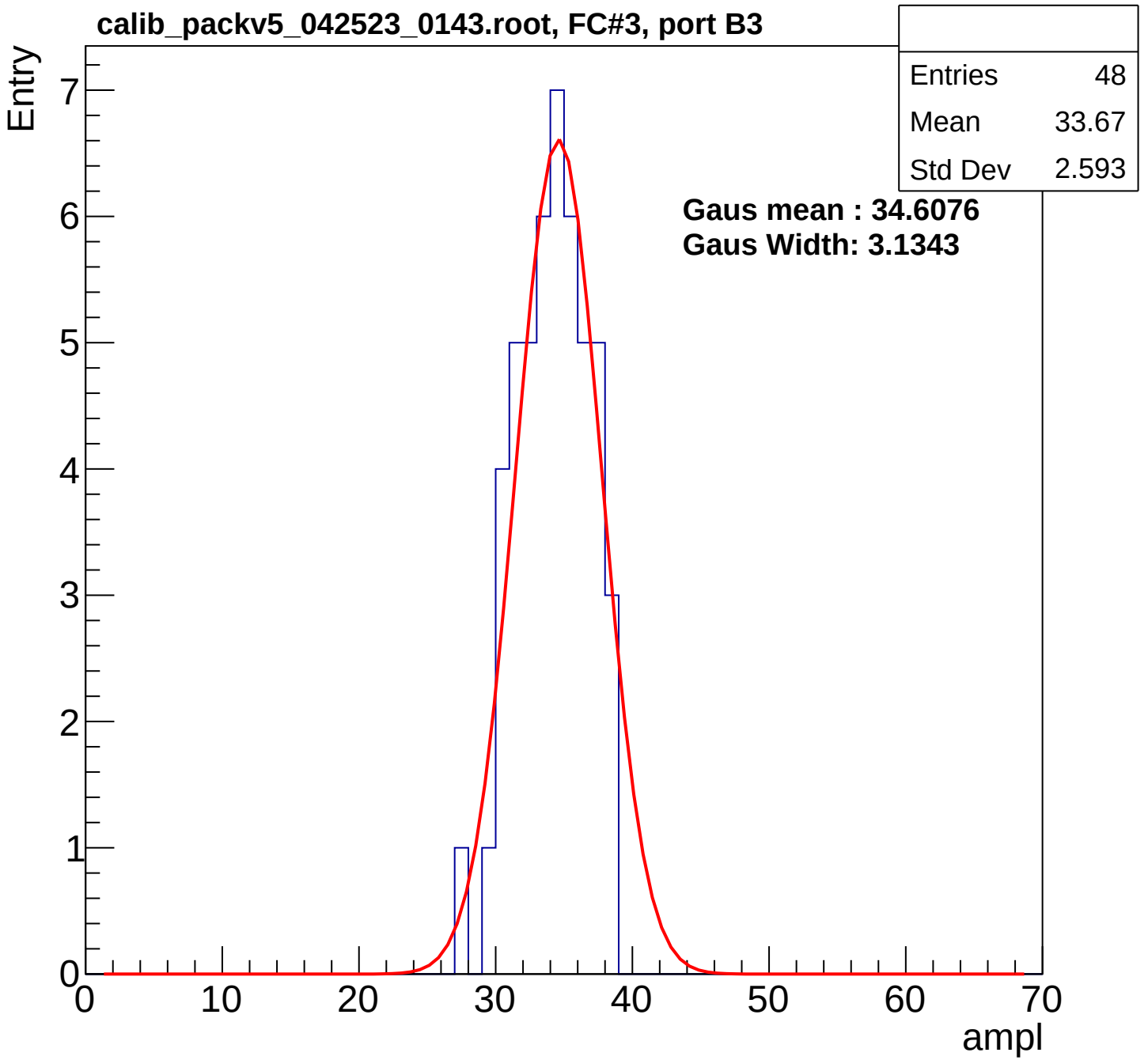
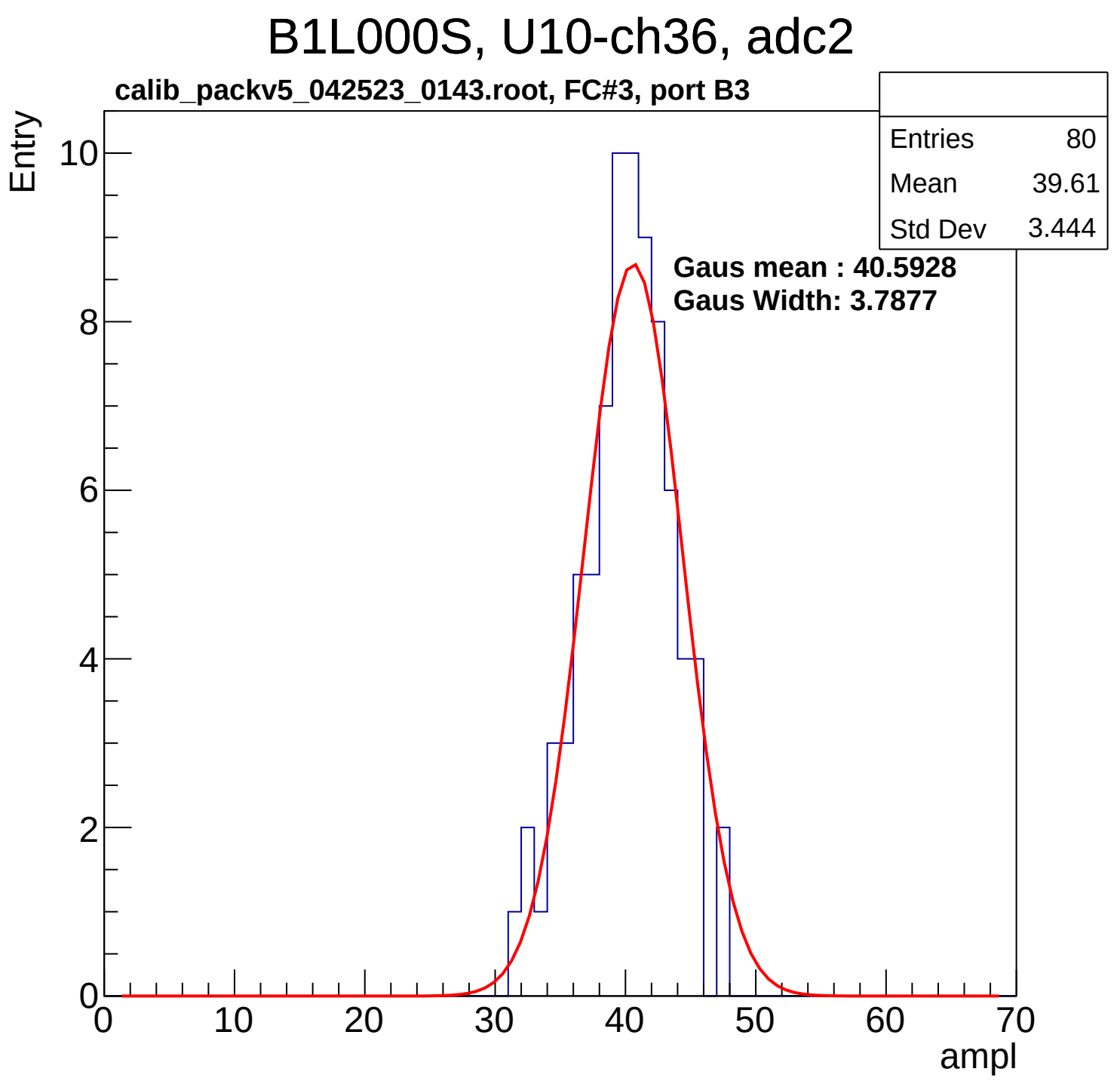

B1L000S, U10-ch35, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 61.44
Std Dev 1.343
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch35, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch36, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 27.98
Std Dev 2.82
Gaus mean : 28.6253
Gaus Width: 2.9184
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch36, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 33.67
Std Dev 2.593
Gaus mean : 34.6076
Gaus Width: 3.1343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch36, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 39.61
Std Dev 3.444
Gaus mean : 40.5928
Gaus Width: 3.7877
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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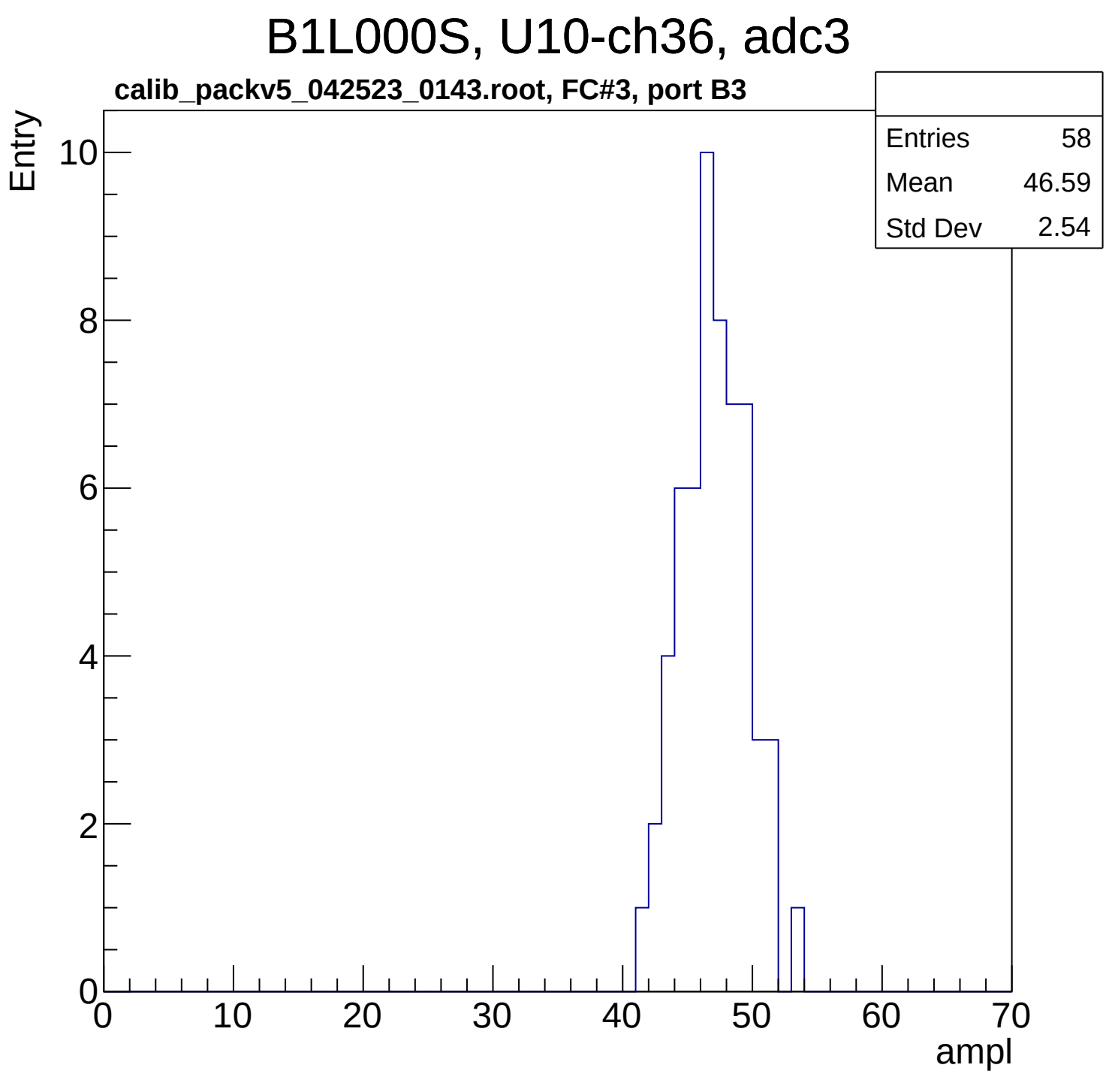
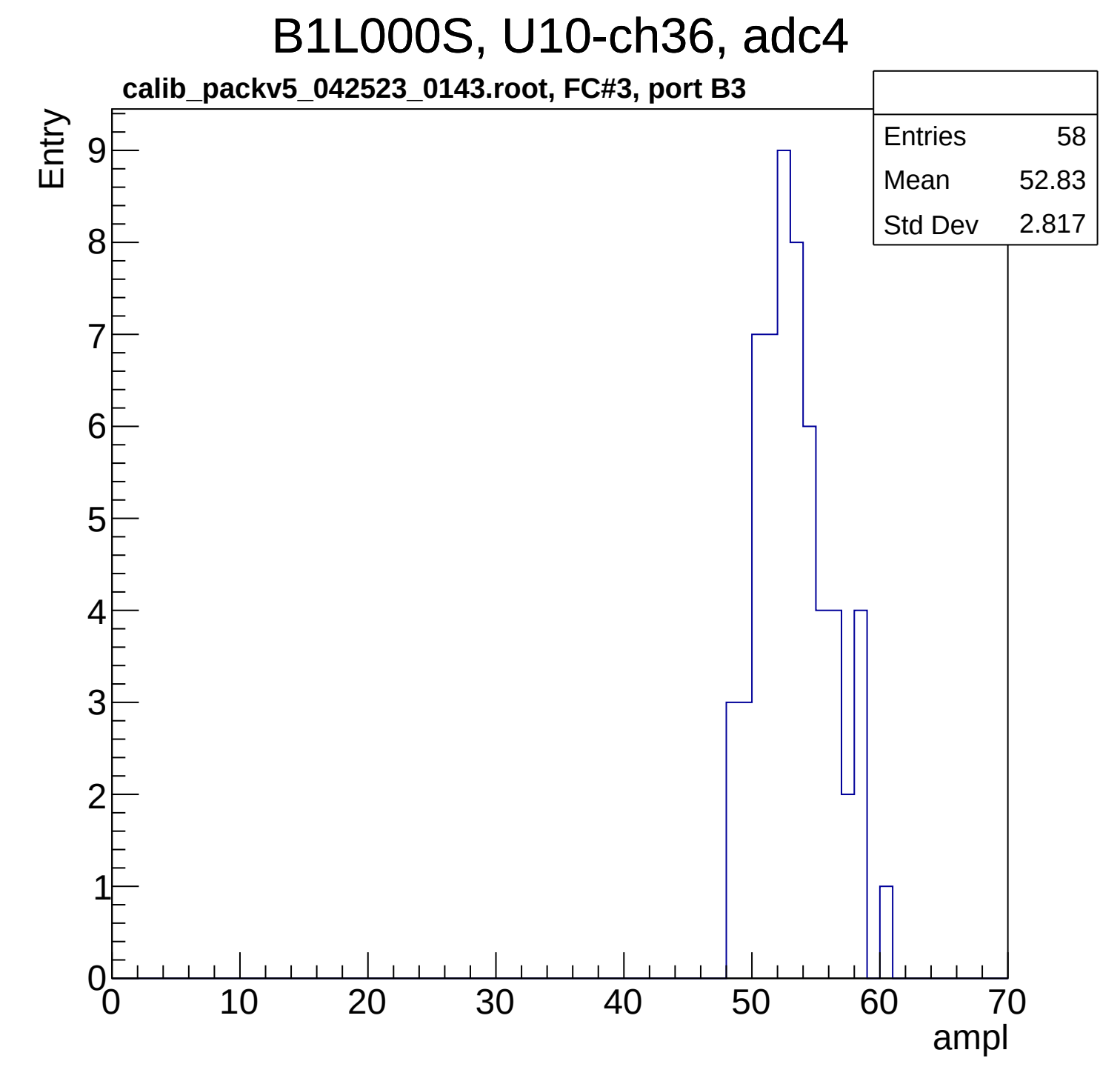
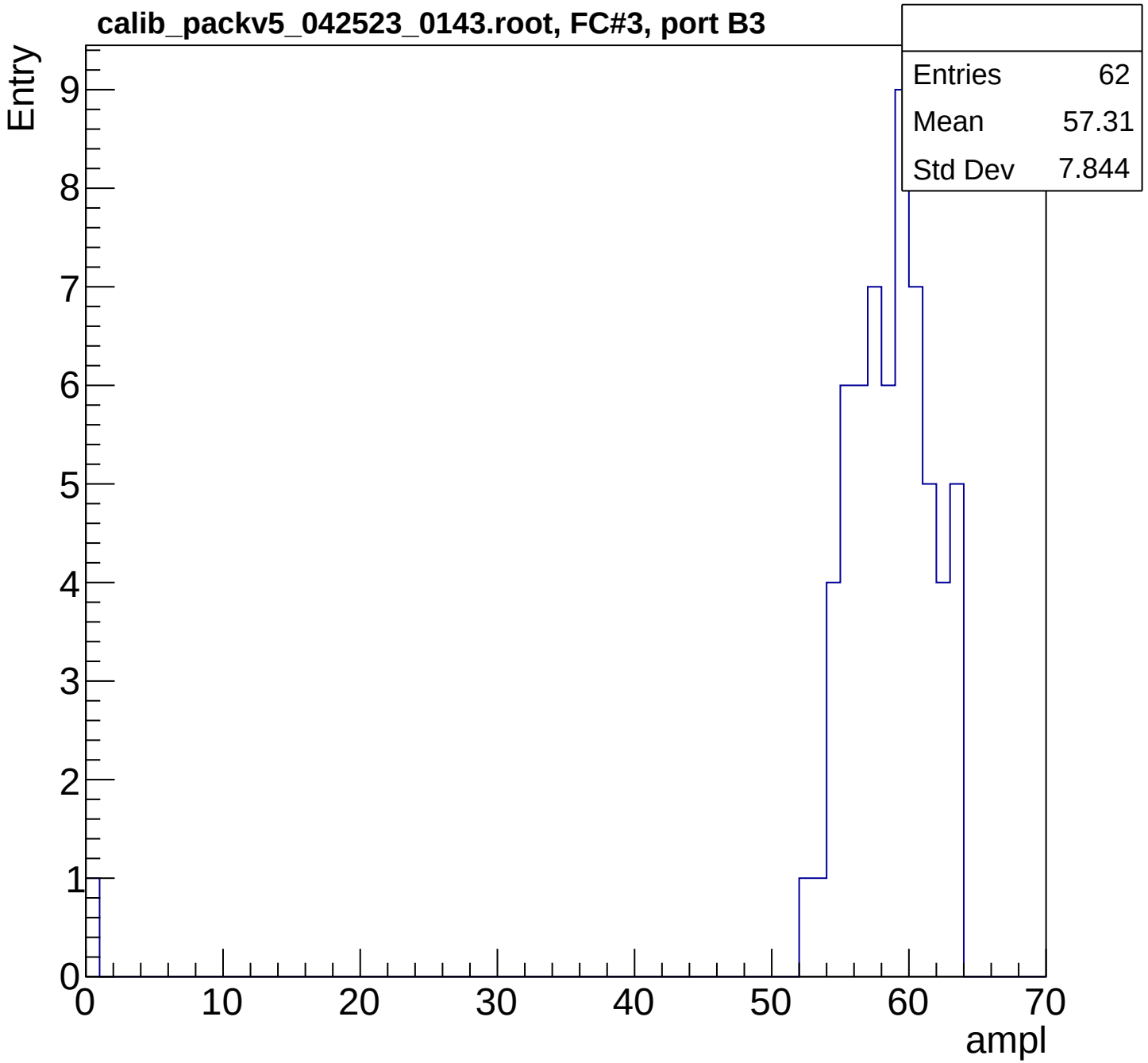

B1L000S, U10-ch36, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 46.59
Std Dev 2.54
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch36, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 52.83
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch36, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.31
Std Dev 7.844
Entry
ampl
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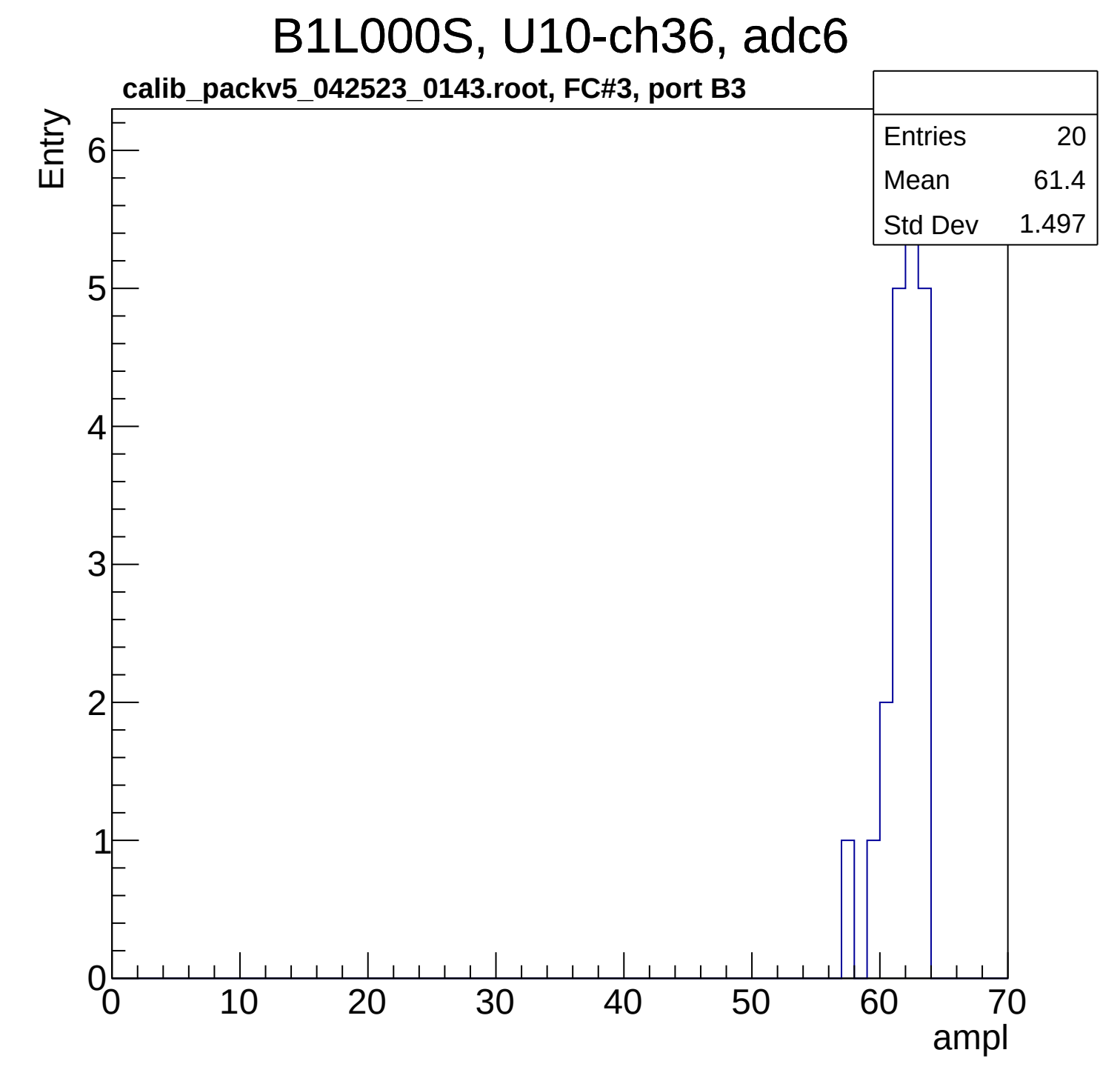
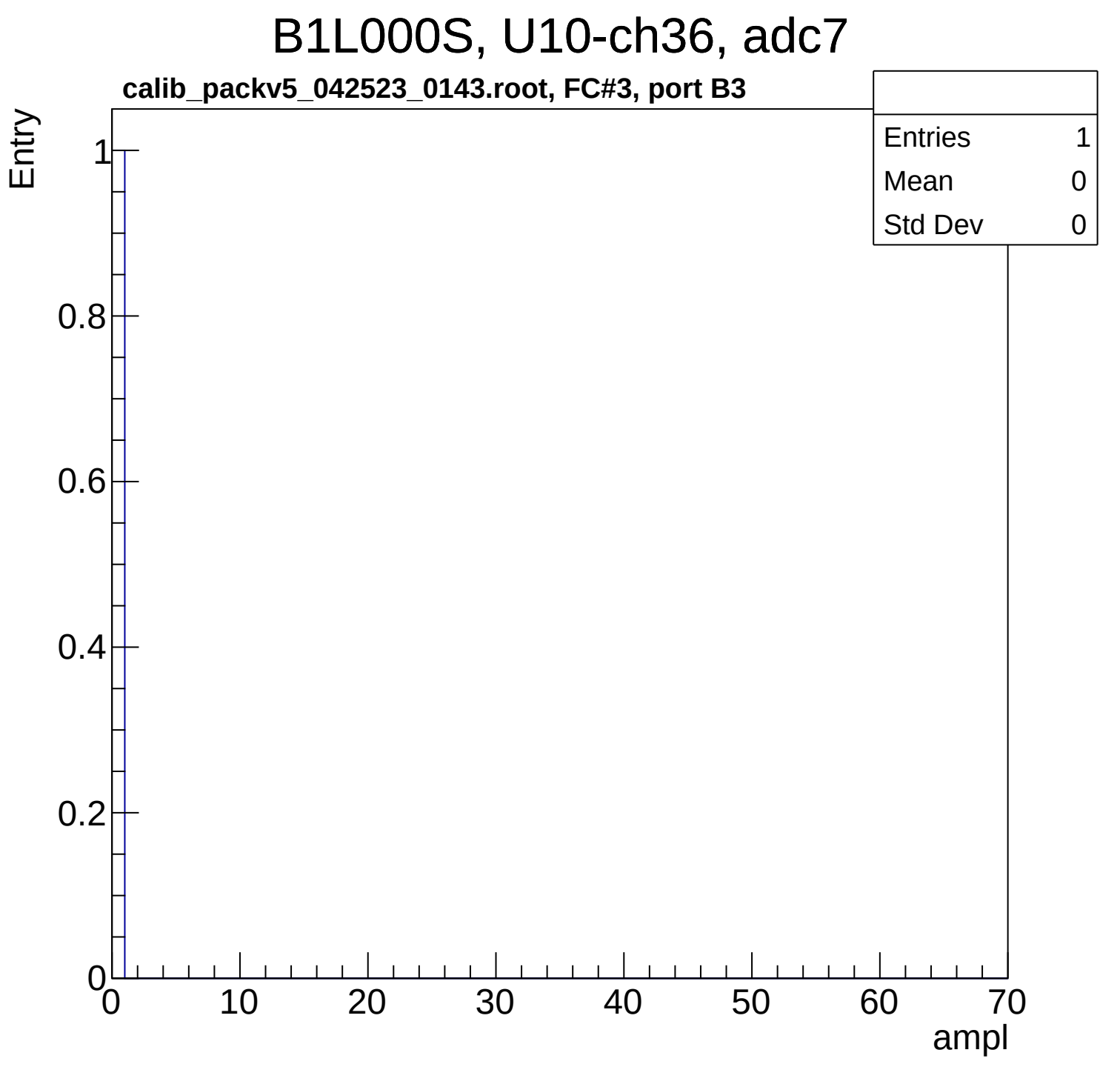
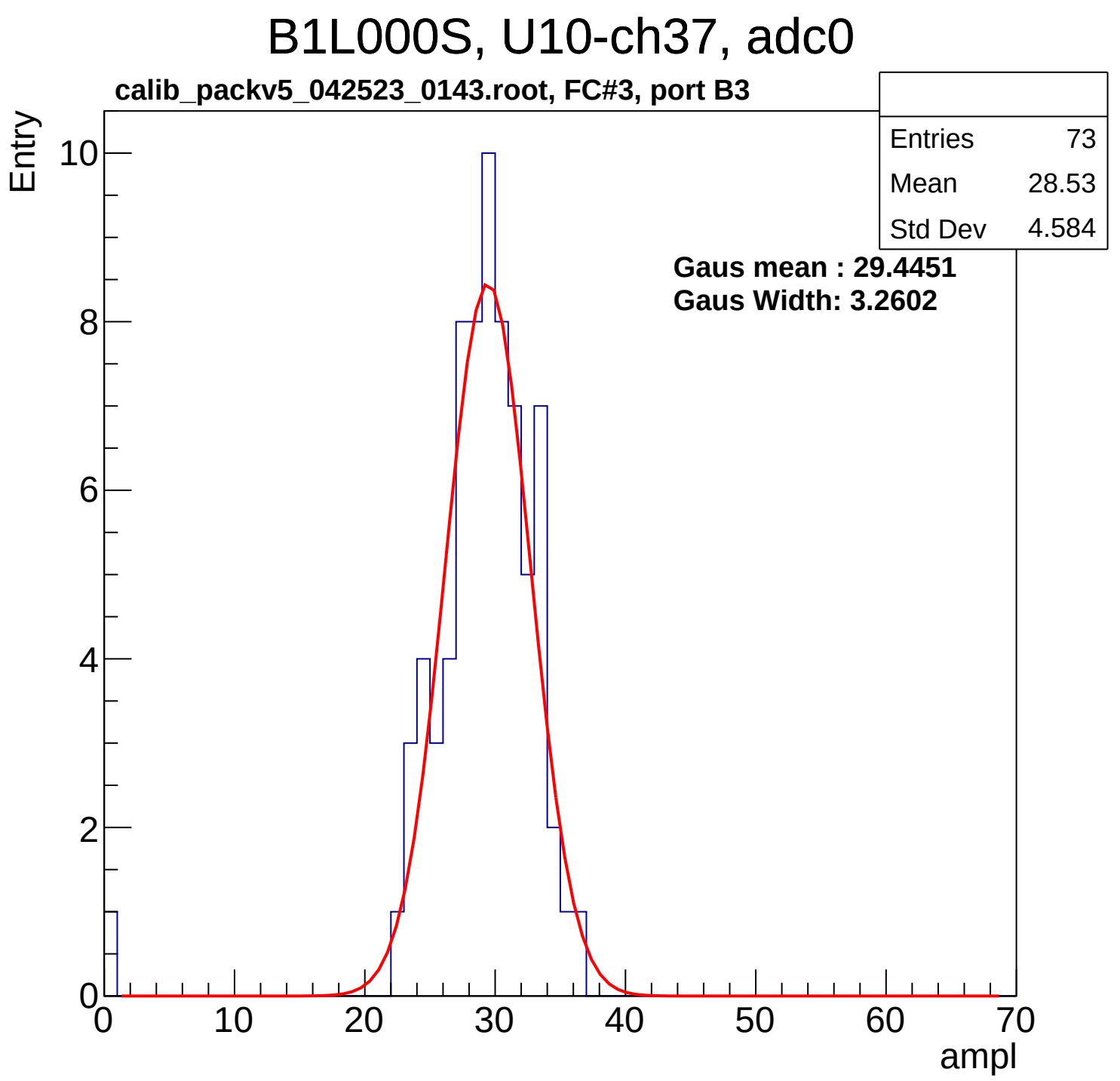
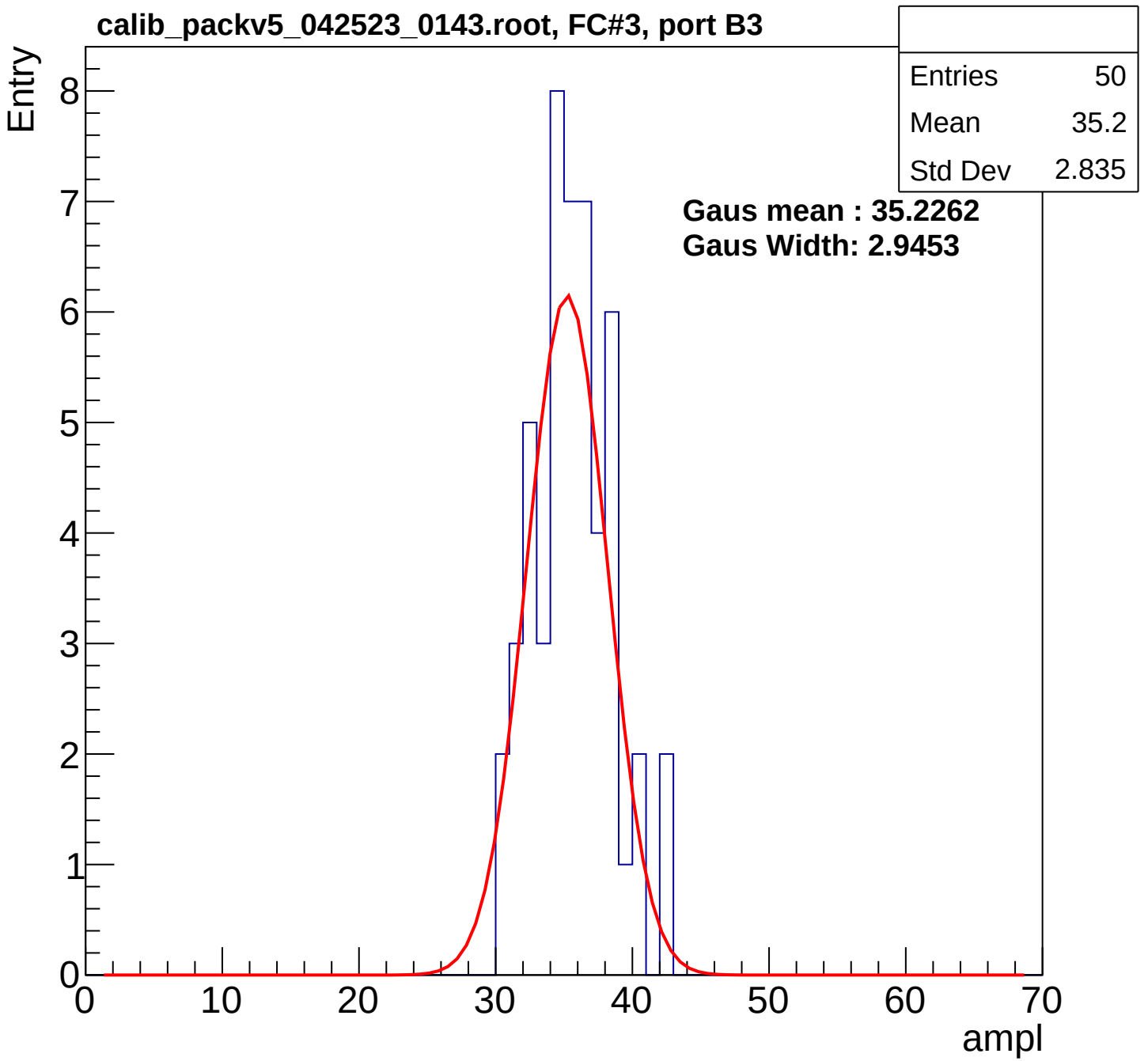
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch36, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.4
Std Dev 1.497
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch36, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch37, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.53
Std Dev 4.584
Gaus mean : 29.4451
Gaus Width: 3.2602
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch37, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 35.2
Std Dev 2.835
Gaus mean : 35.2262
Gaus Width: 2.9453
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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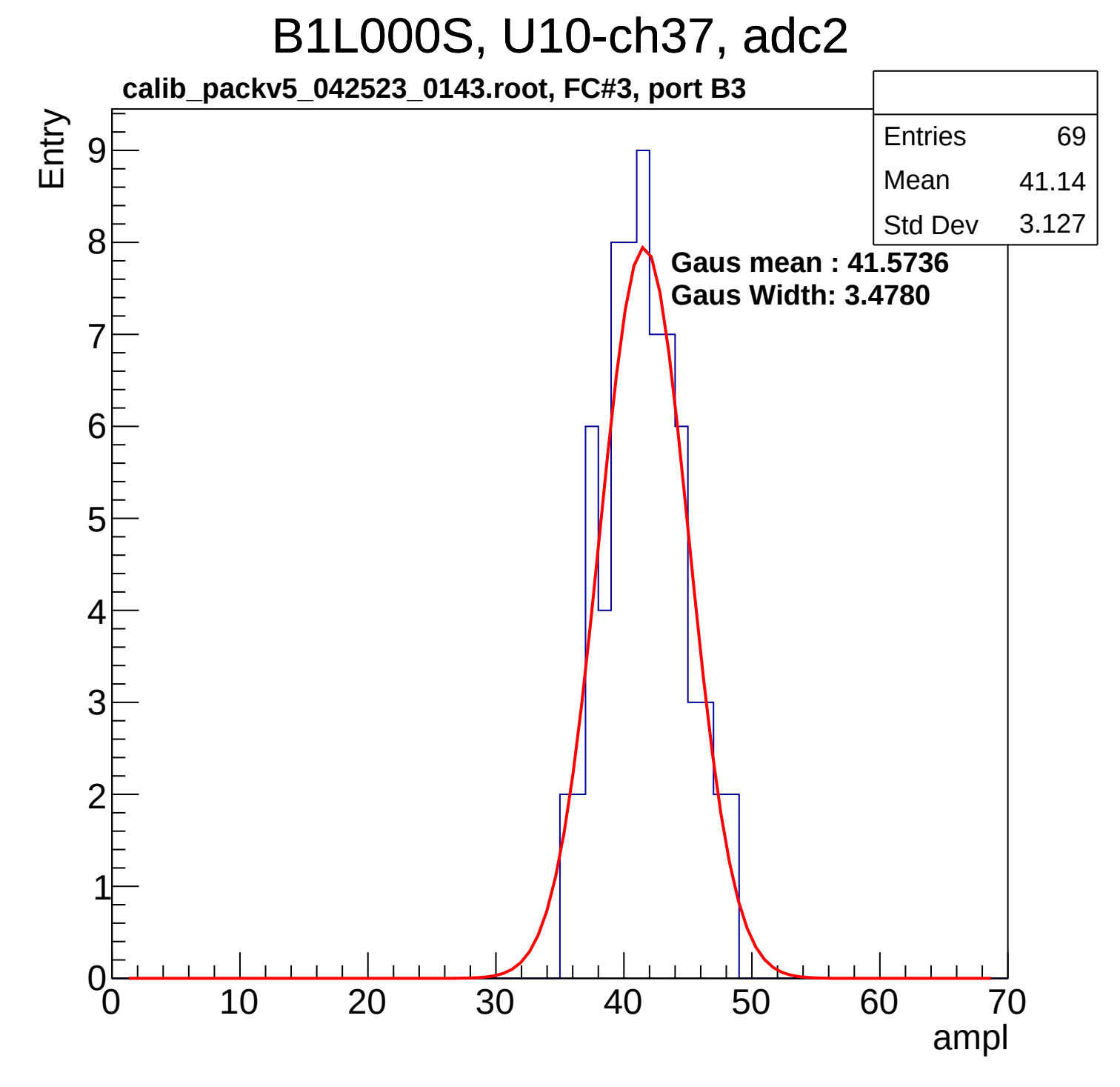
B1L000S, U10-ch37, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.14
Std Dev 3.127
Gaus mean : 41.5736
Gaus Width: 3.4780
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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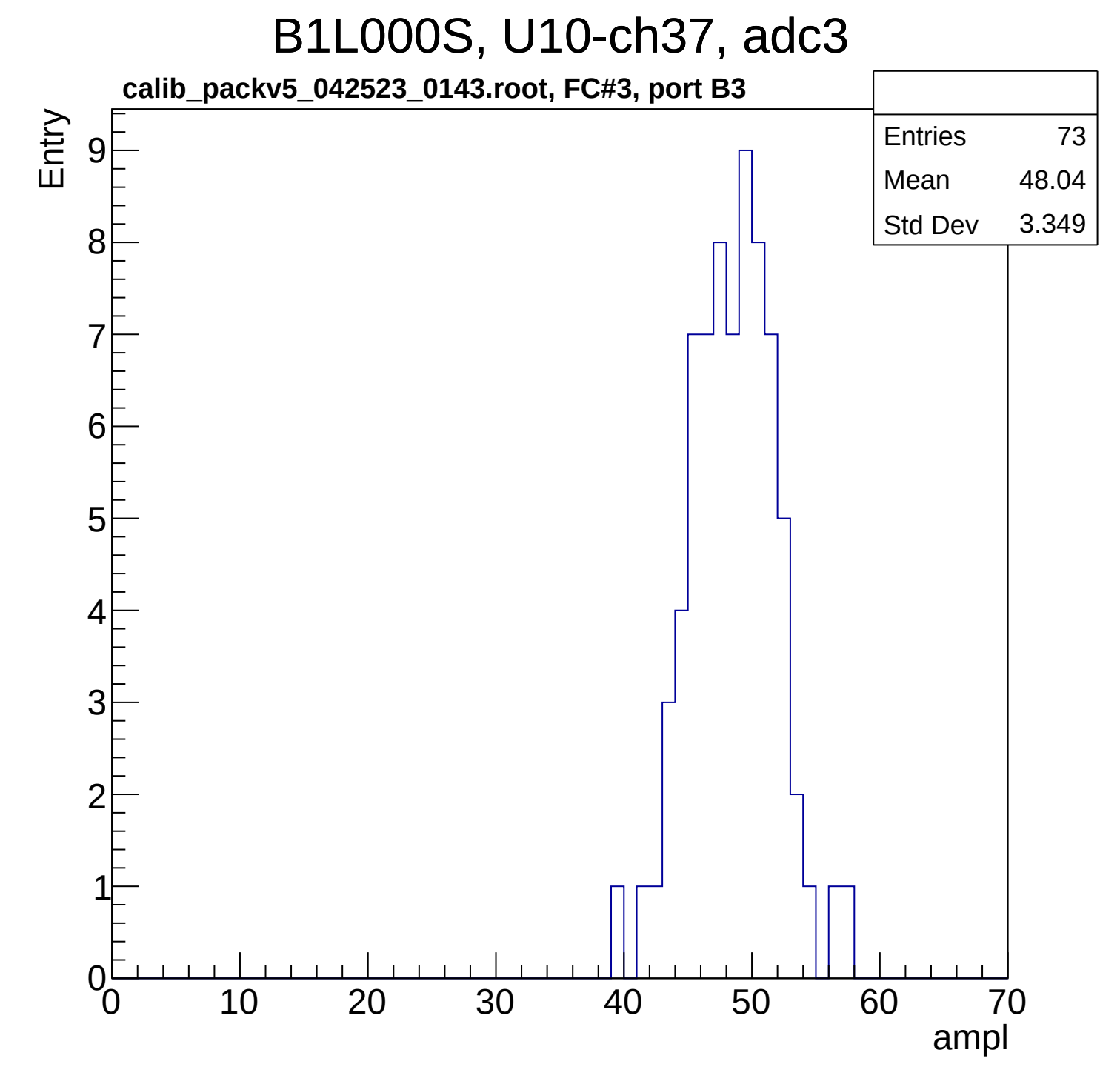
B1L000S, U10-ch37, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 48.04
Std Dev 3.349
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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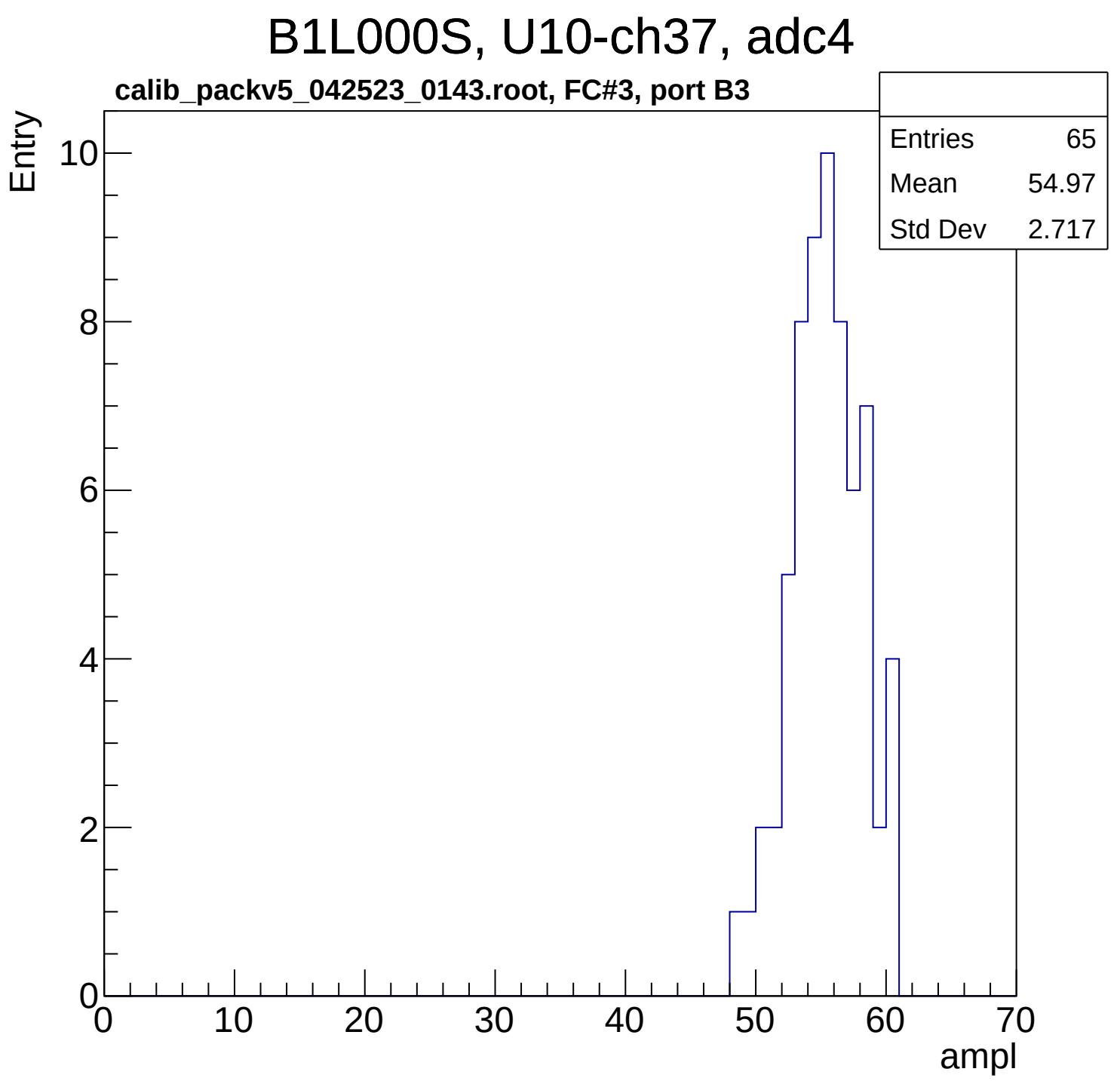
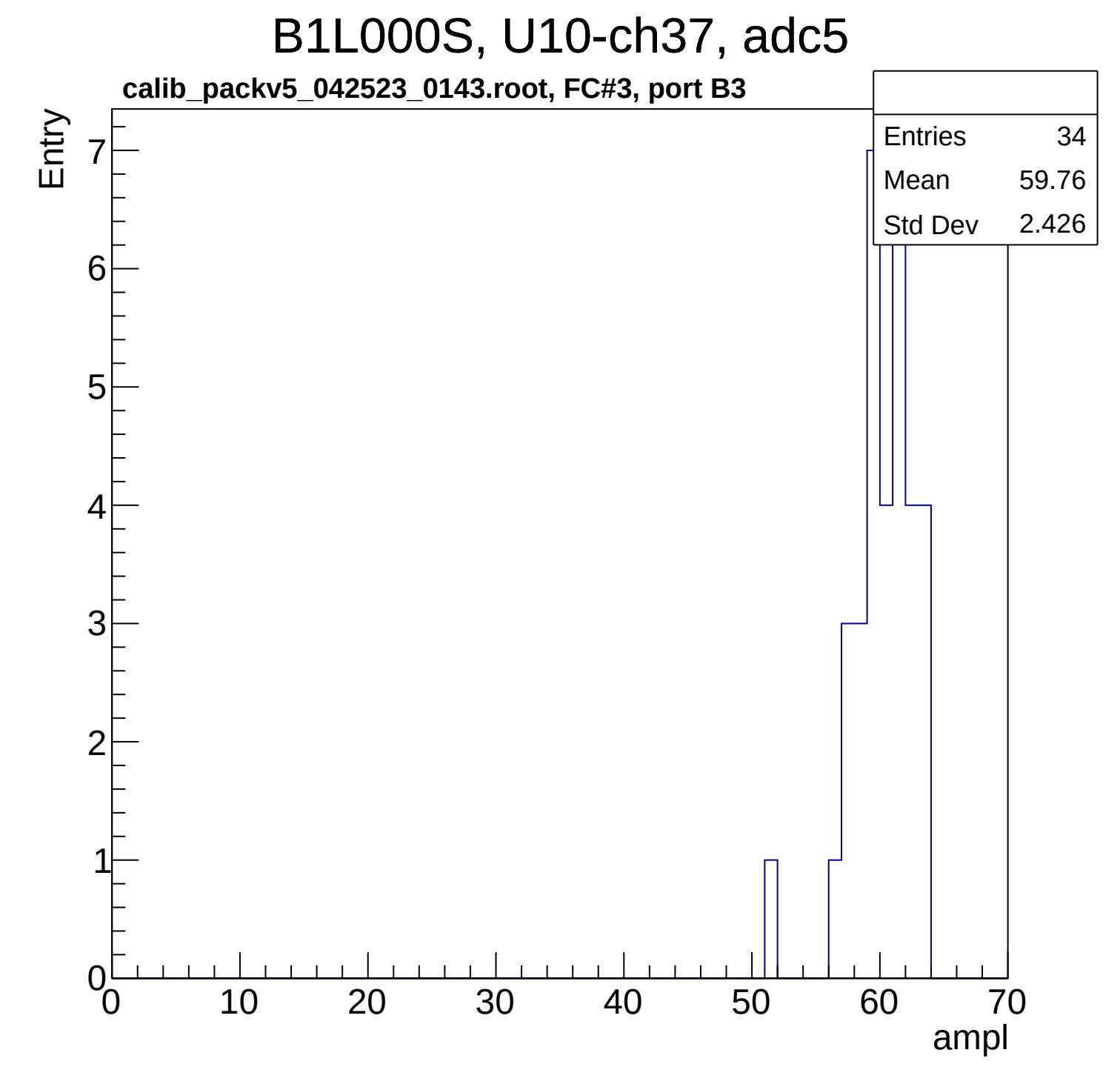
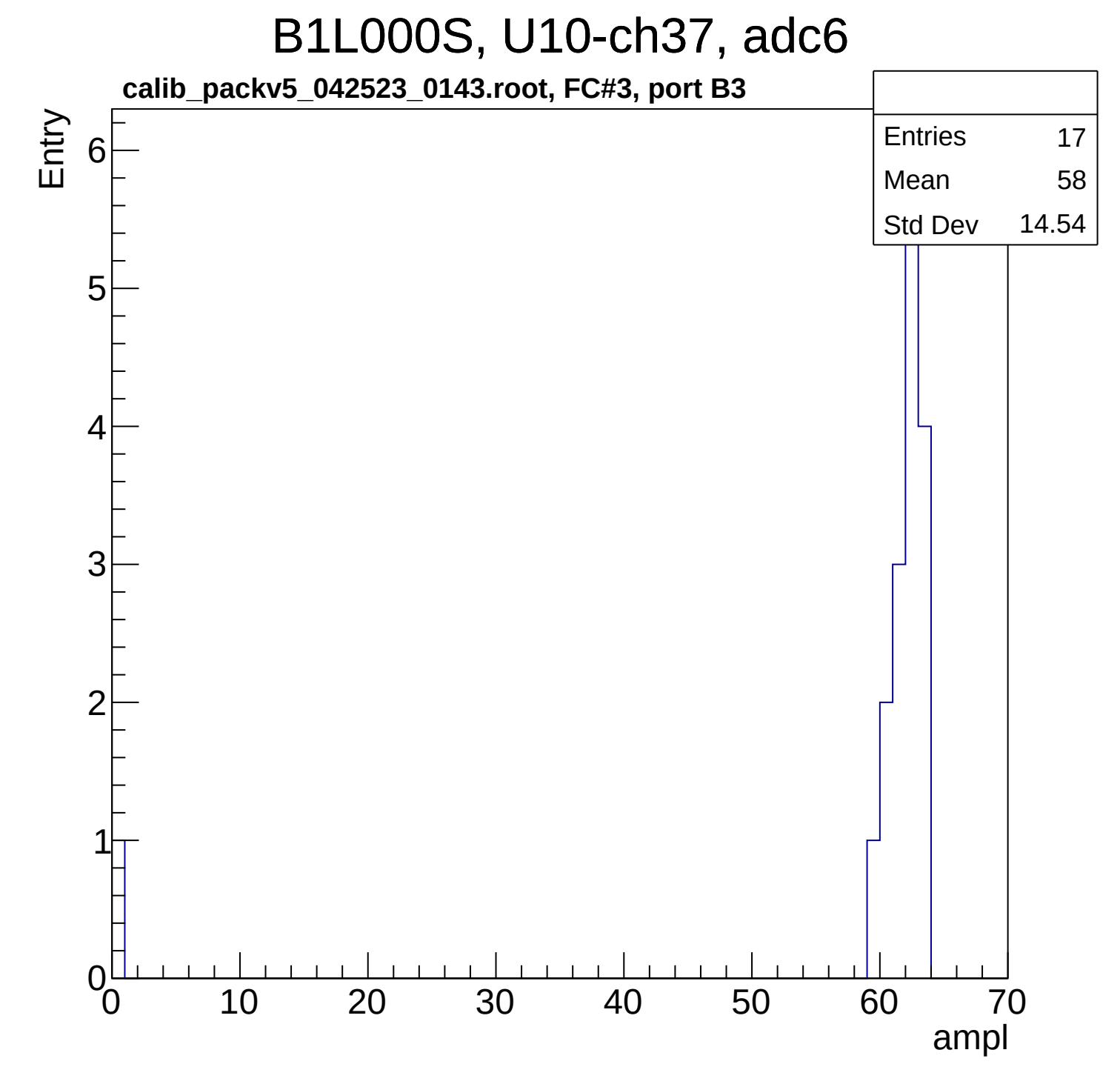

B1L000S, U10-ch37, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 54.97
Std Dev 2.717
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch37, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 34
Mean 59.76
Std Dev 2.426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch37, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 58
Std Dev 14.54
Entry
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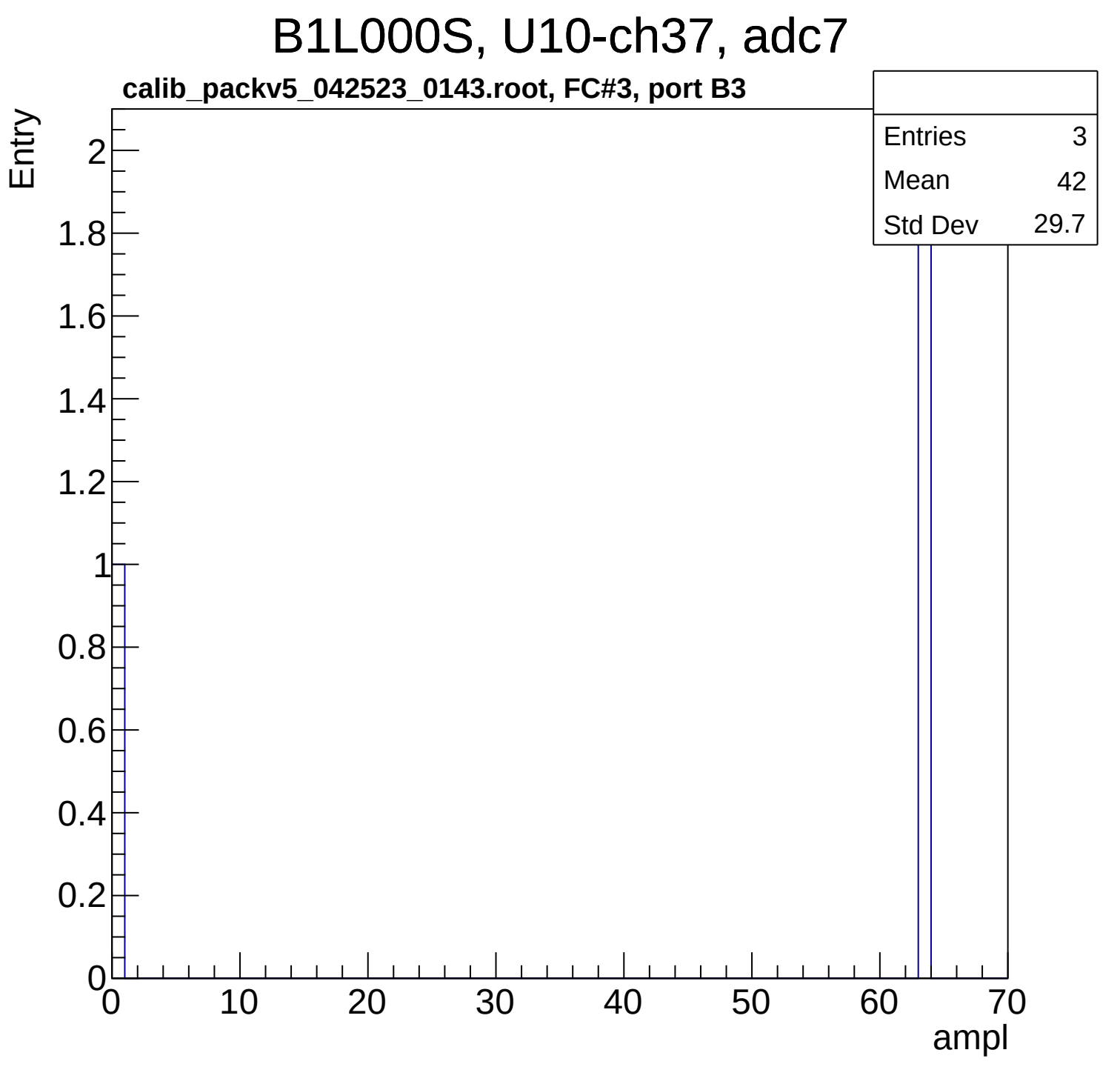
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch37, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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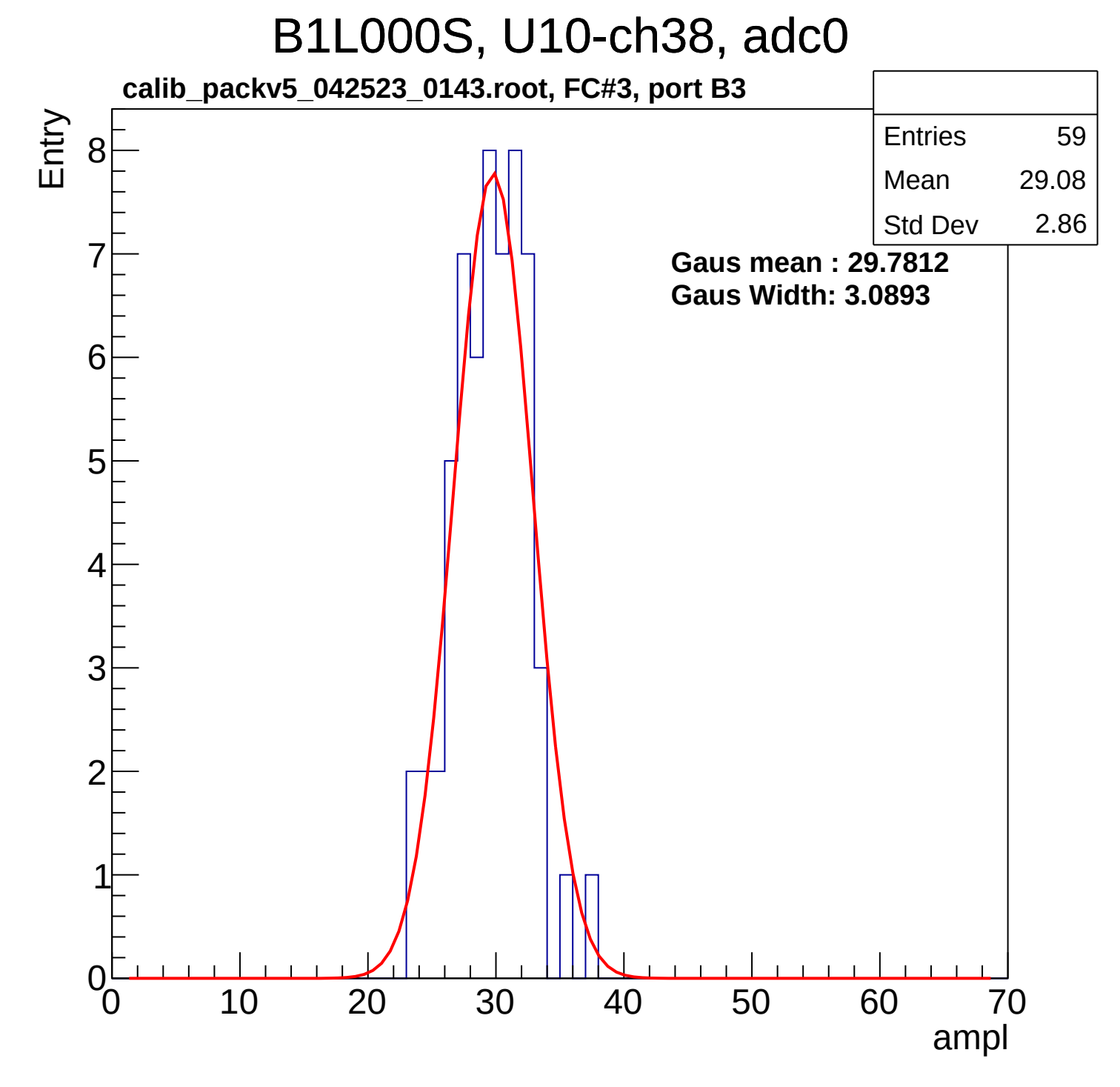
B1L000S, U10-ch38, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 29.08
Std Dev 2.86
Gaus mean : 29.7812
Gaus Width: 3.0893
Entry
ampl
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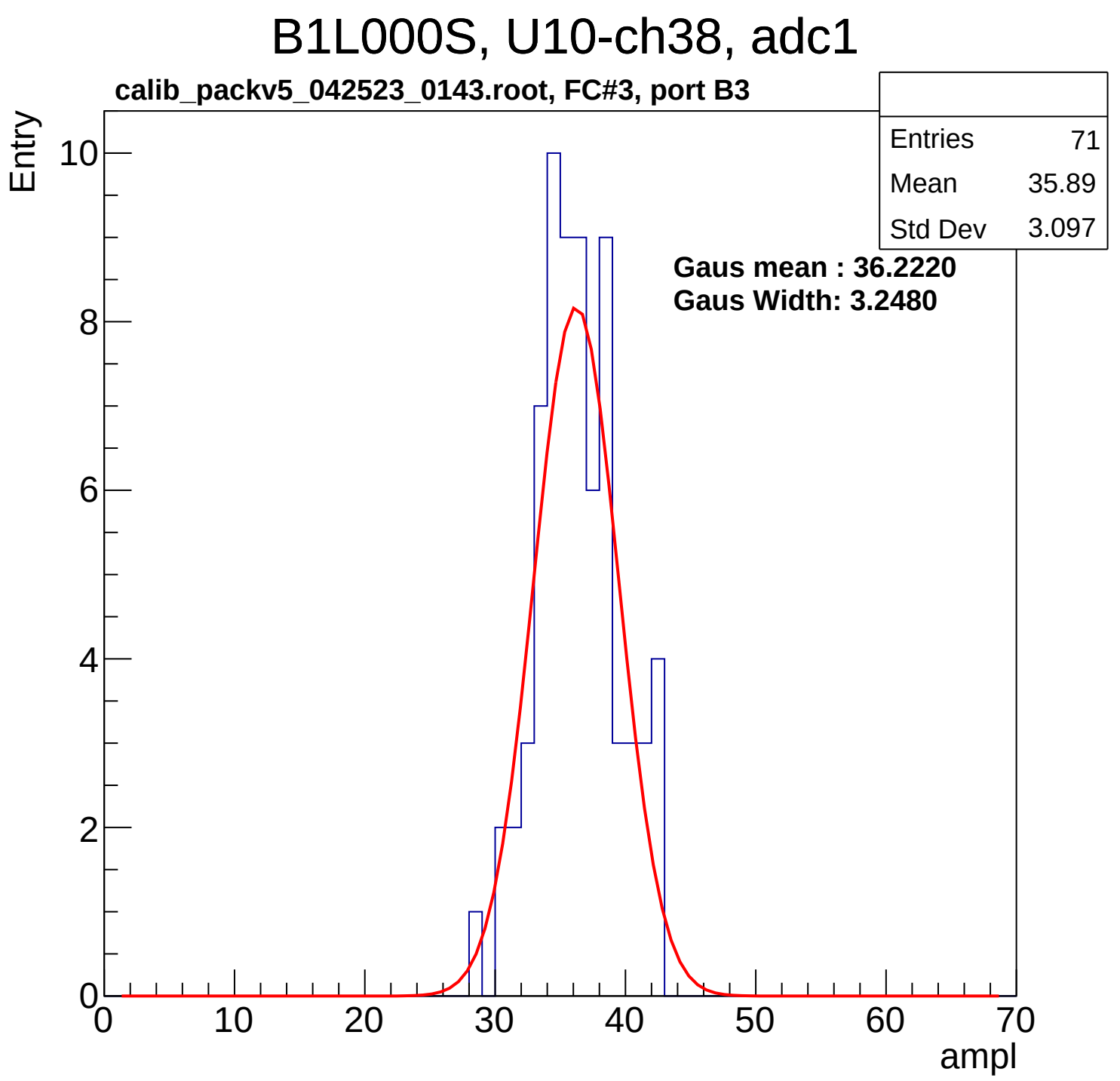
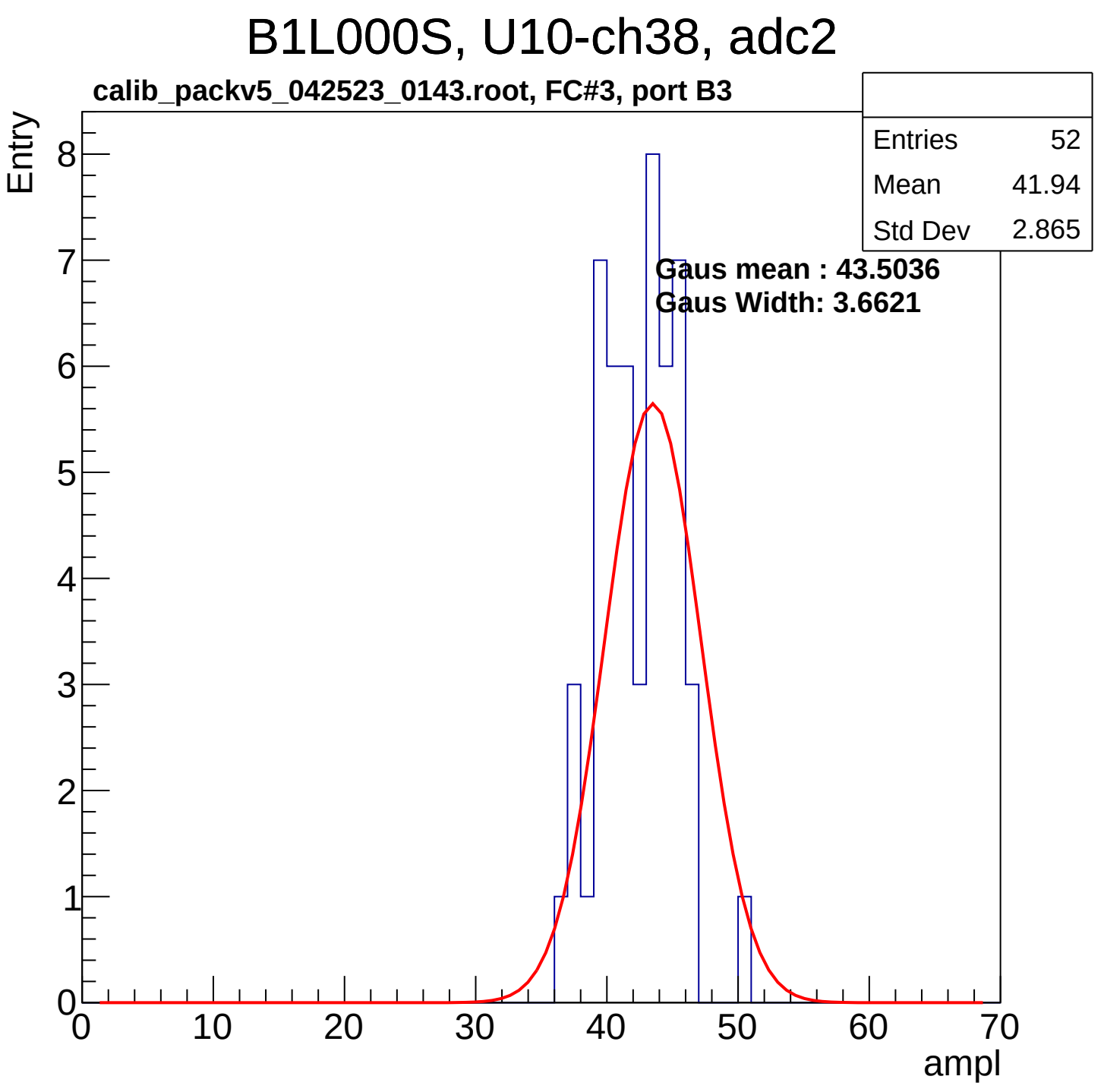
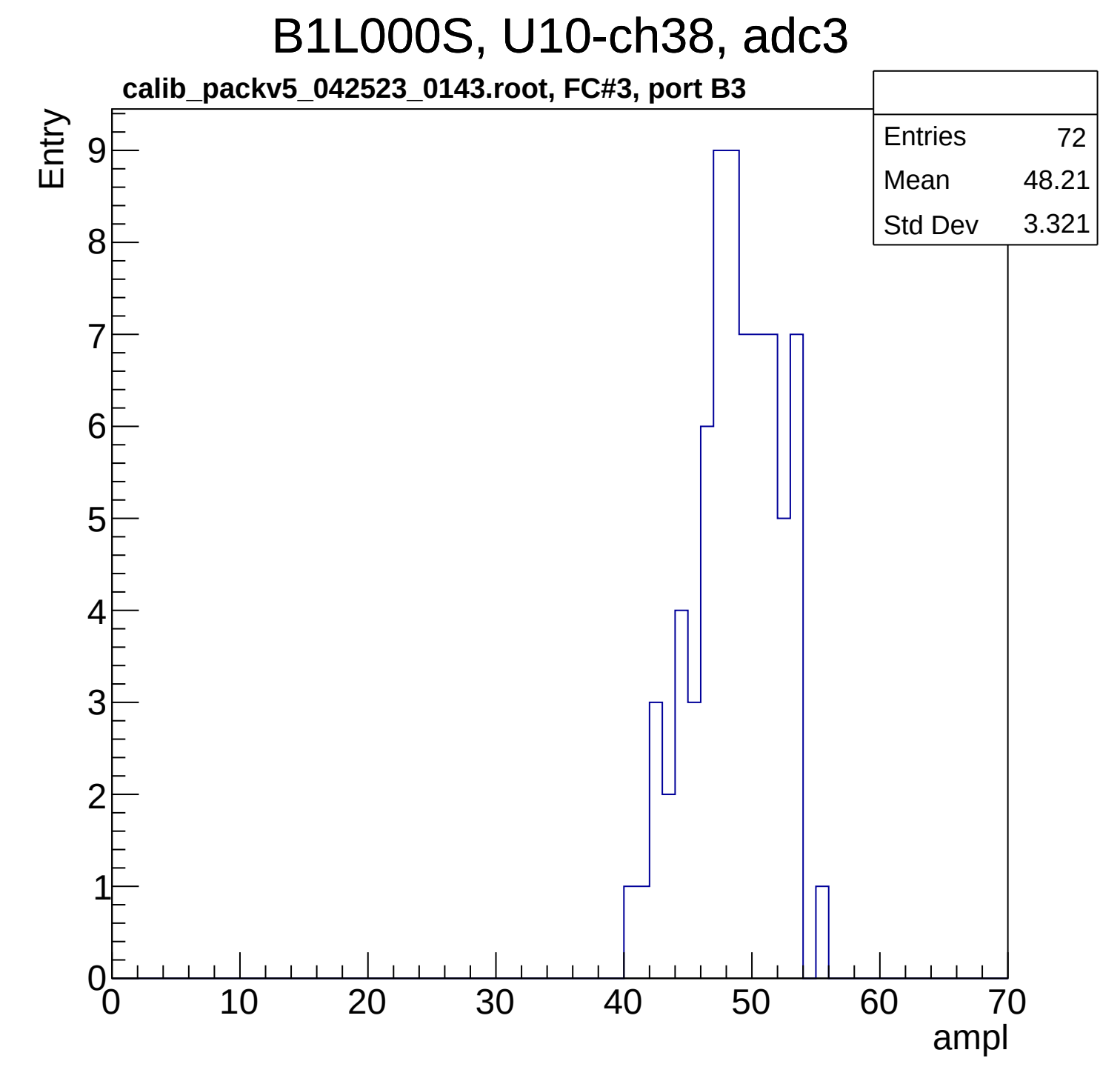
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch38, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.89
Std Dev 3.097
Gaus mean : 36.2220
Gaus Width: 3.2480
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch38, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 41.94
Std Dev 2.865
Gaus mean : 43.5036
Gaus Width: 3.6621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch38, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.21
Std Dev 3.321
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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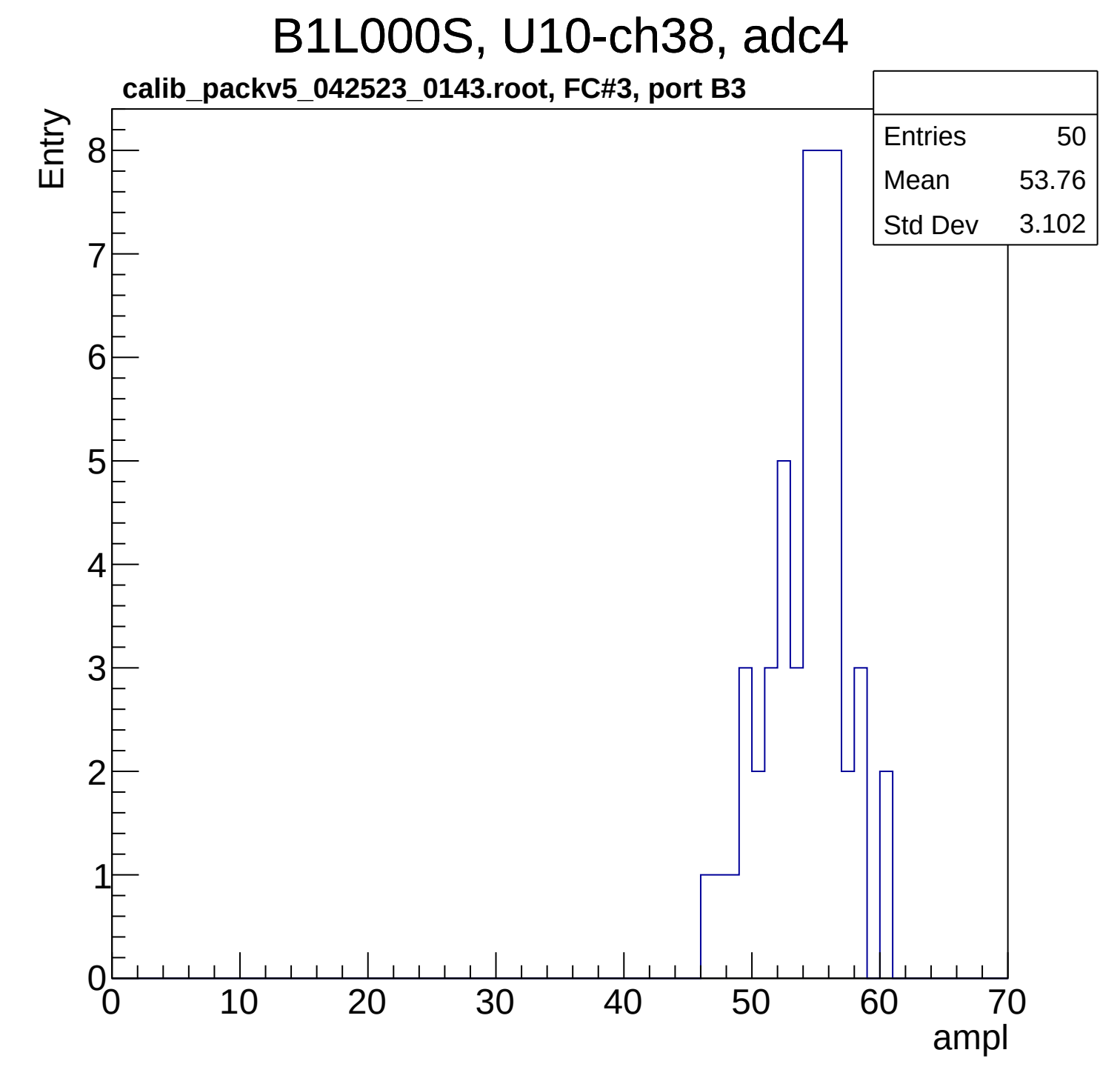
B1L000S, U10-ch38, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 53.76
Std Dev 3.102
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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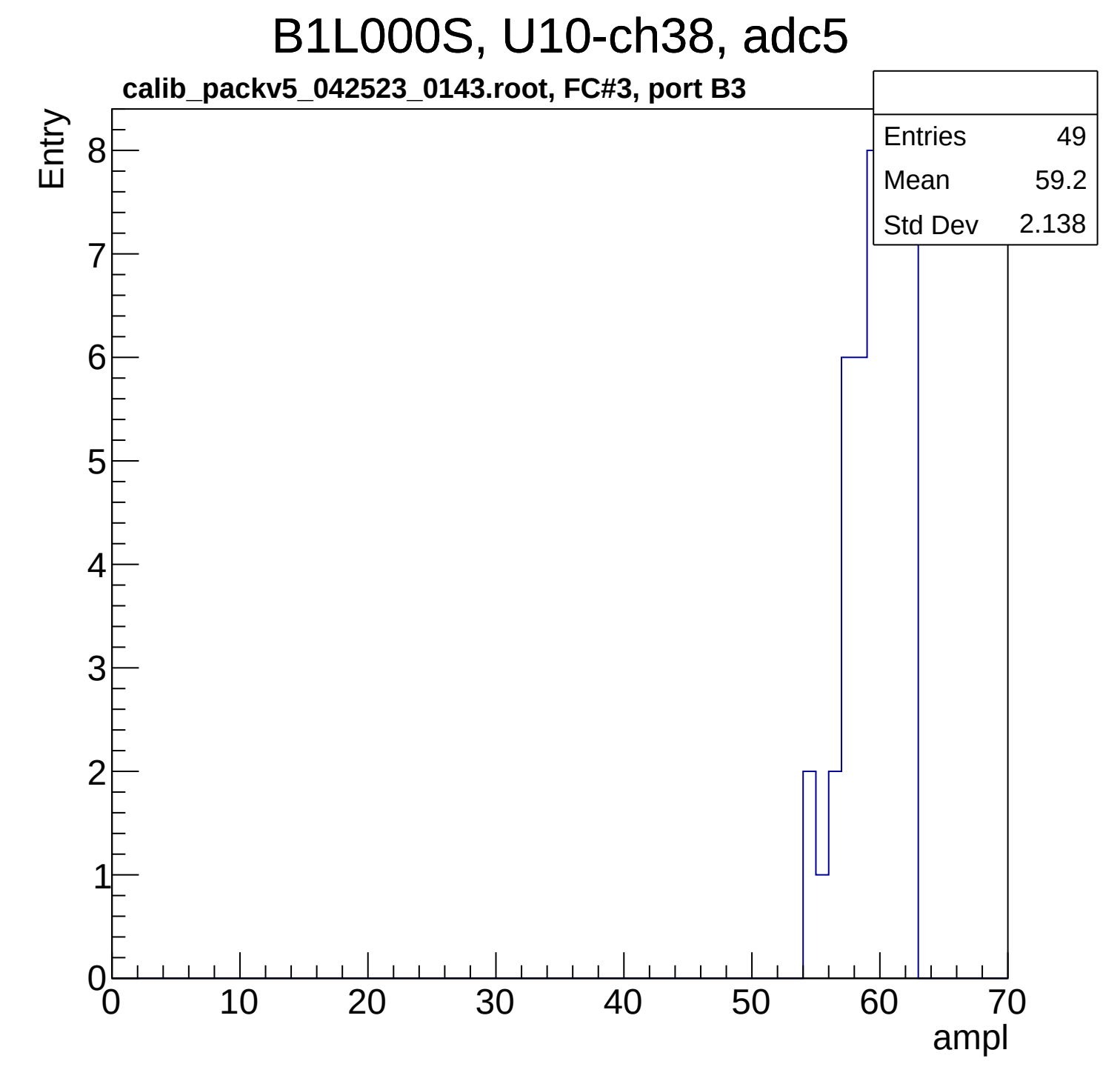
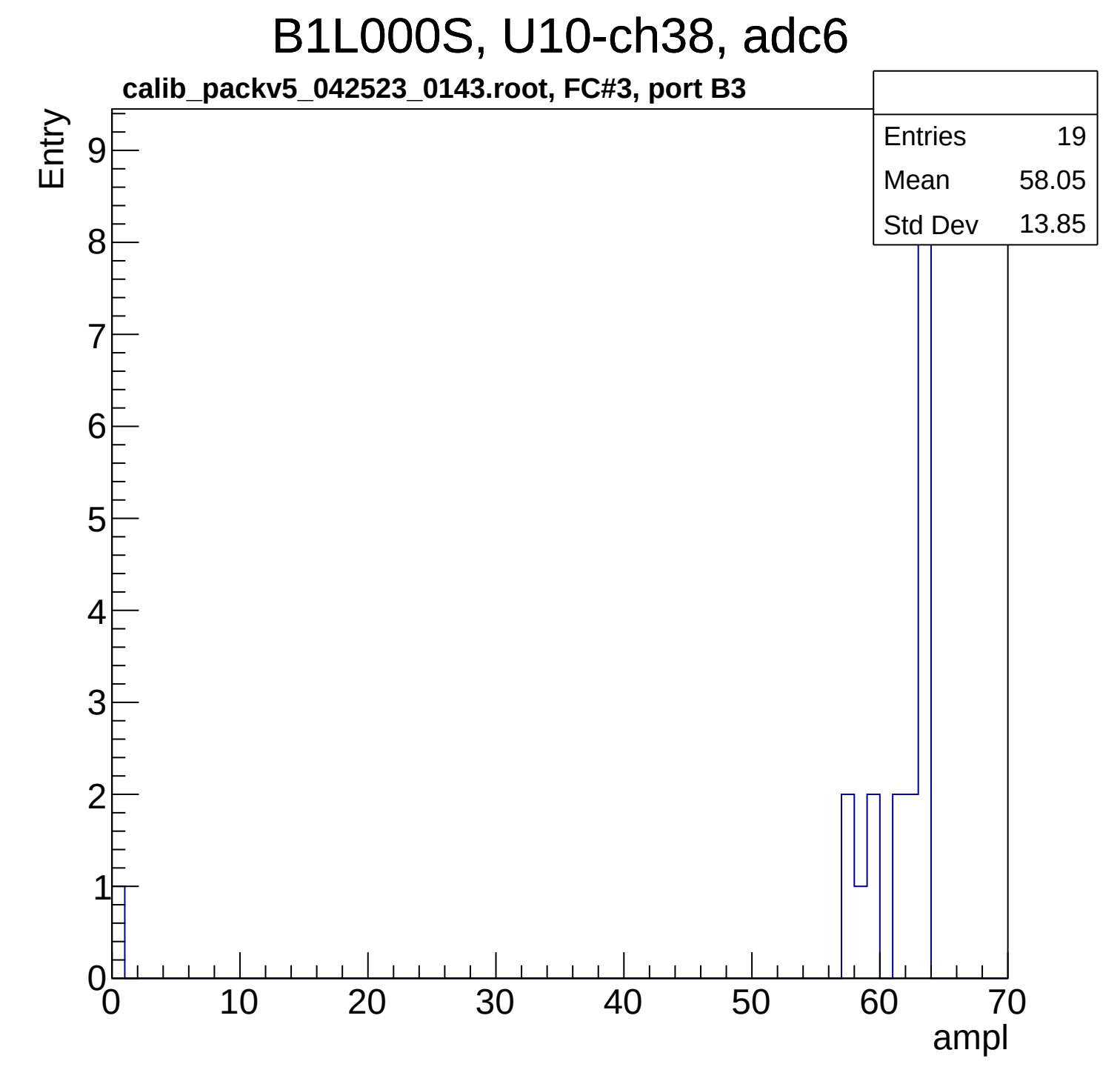

B1L000S, U10-ch38, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 59.2
Std Dev 2.138
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch38, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.05
Std Dev 13.85
Entry
ampl
0
1
2
3
4
5
6
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0
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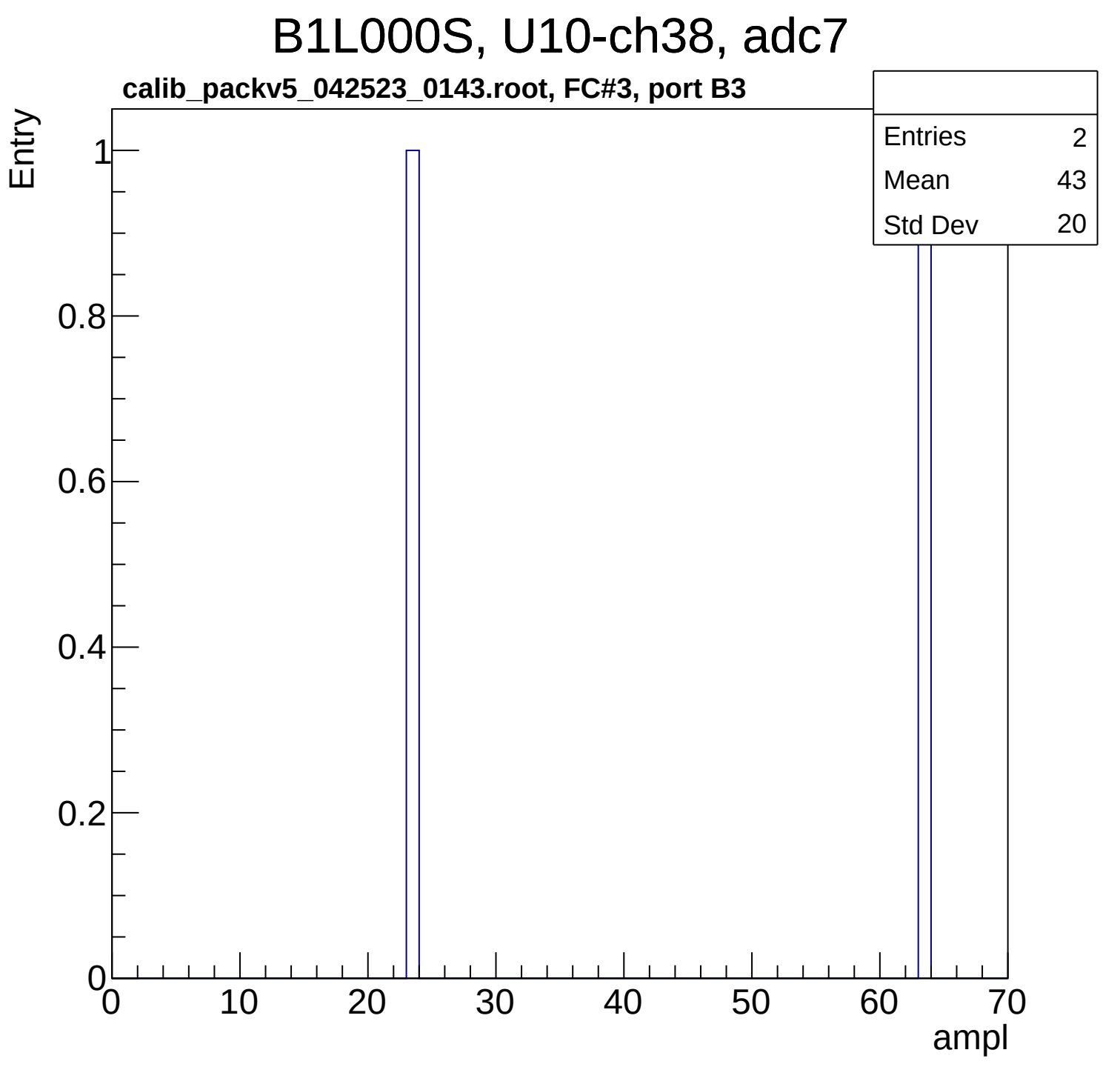
B1L000S, U10-ch38, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 43
Std Dev 20
Entry
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0.8
0.6
0.4
0.2
0
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10
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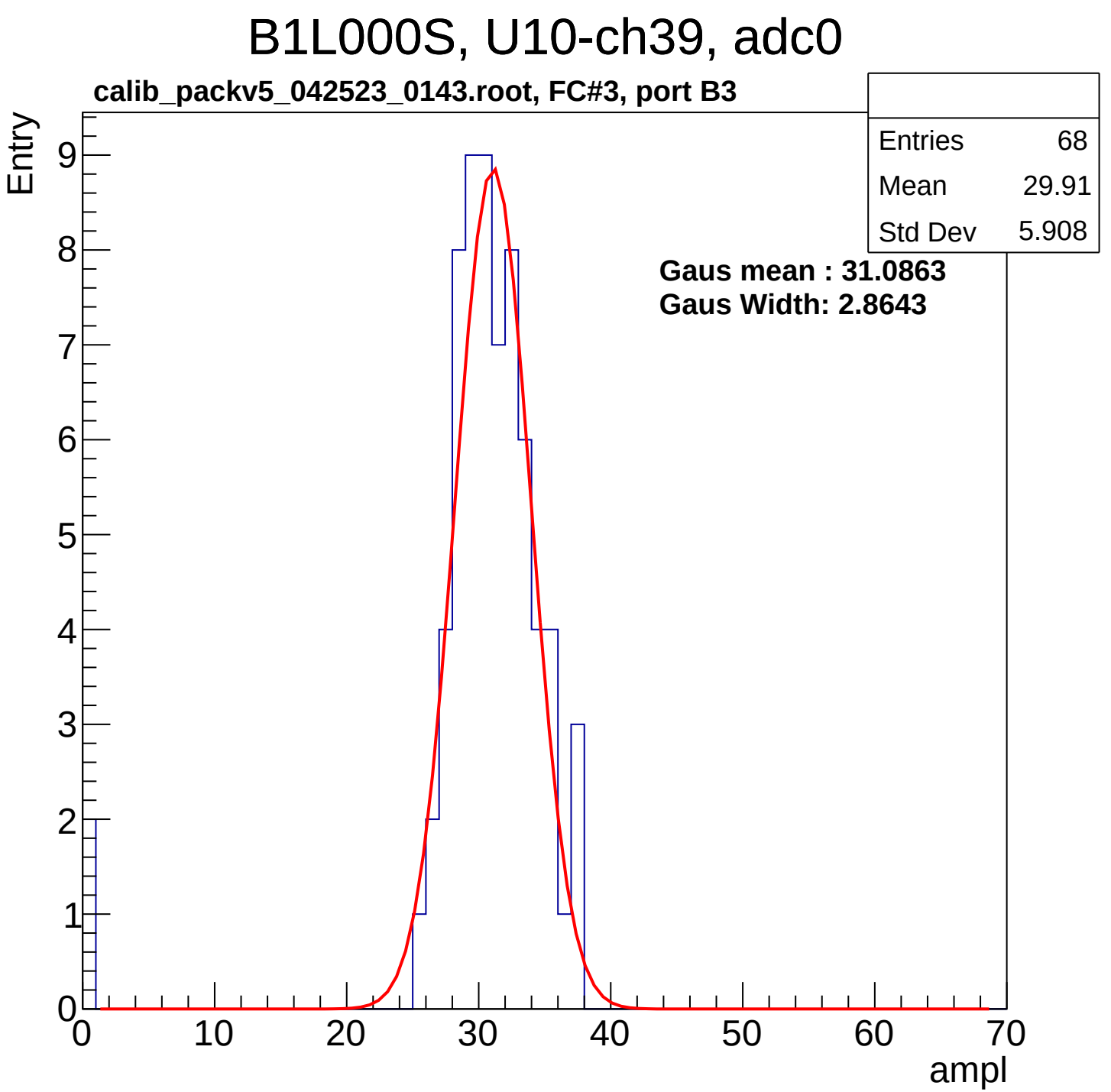
50
60
70
ampl


B1L000S, U10-ch39, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 29.91
Std Dev 5.908
Gaus mean : 31.0863
Gaus Width: 2.8643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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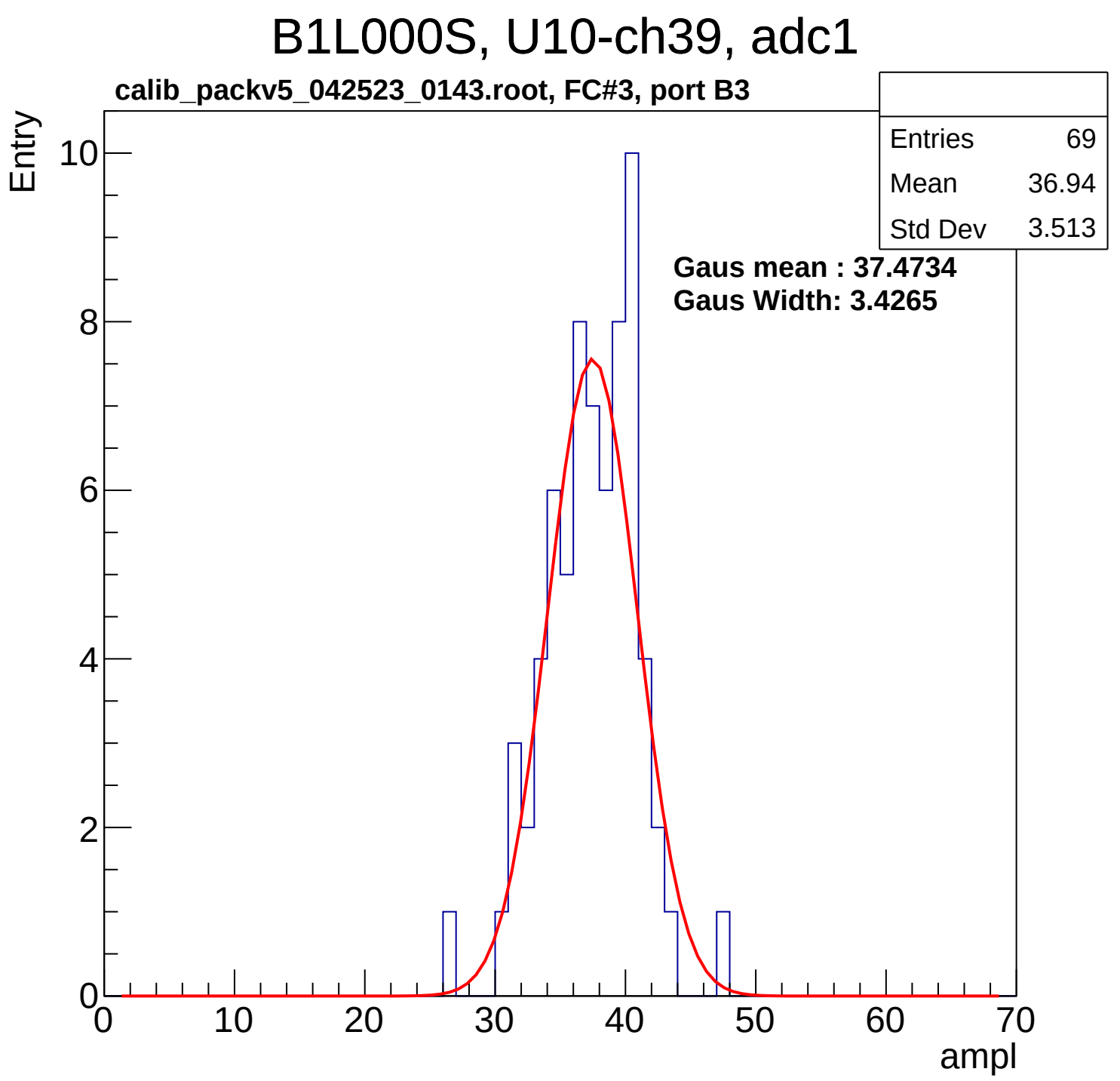
B1L000S, U10-ch39, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 36.94
Std Dev 3.513
Gaus mean : 37.4734
Gaus Width: 3.4265
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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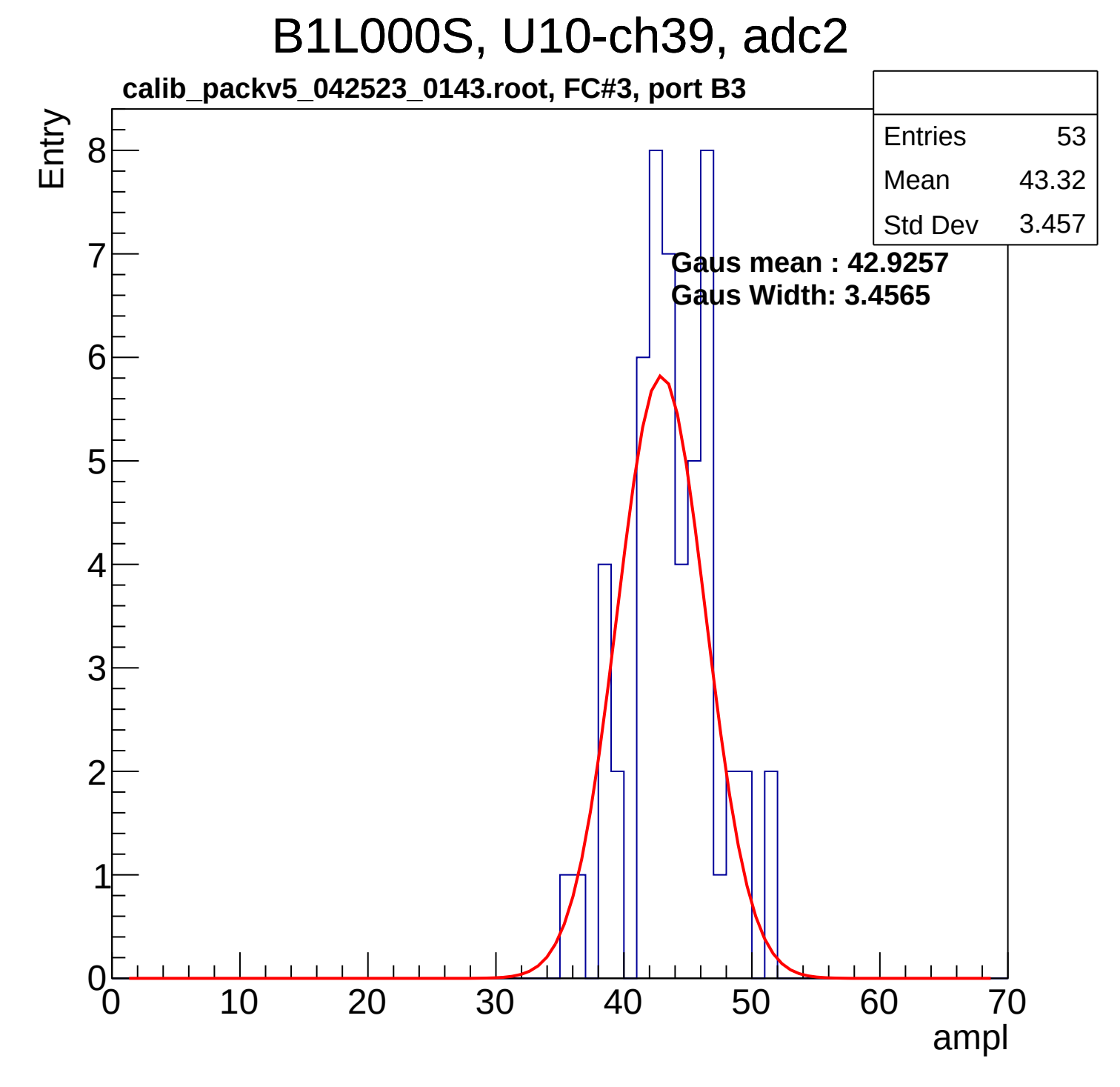
B1L000S, U10-ch39, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 43.32
Std Dev 3.457
Gaus mean : 42.9257
Gaus Width: 3.4565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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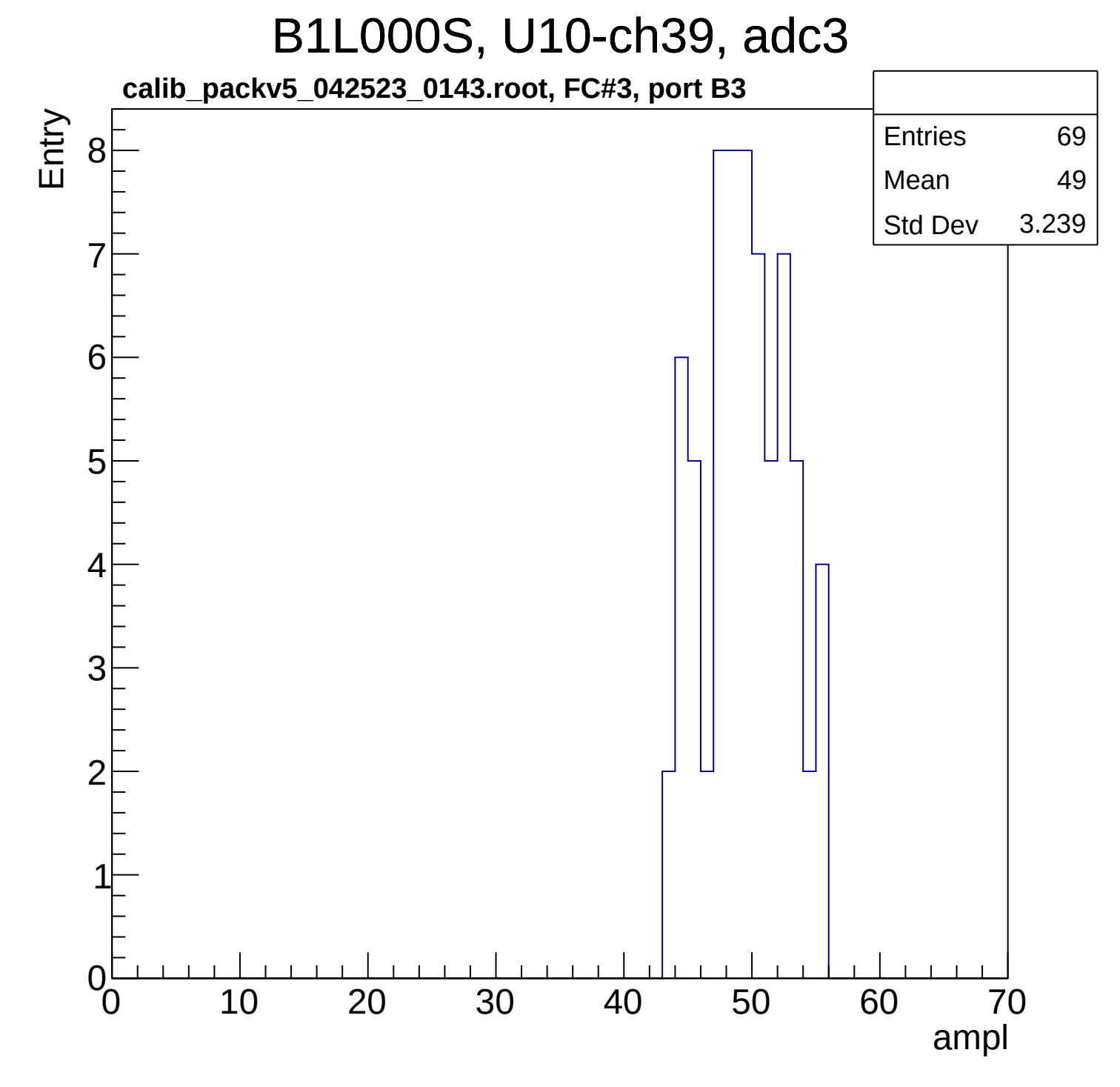
B1L000S, U10-ch39, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 49
Std Dev 3.239
Entry
ampl
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1
2
3
4
5
6
7
8
0
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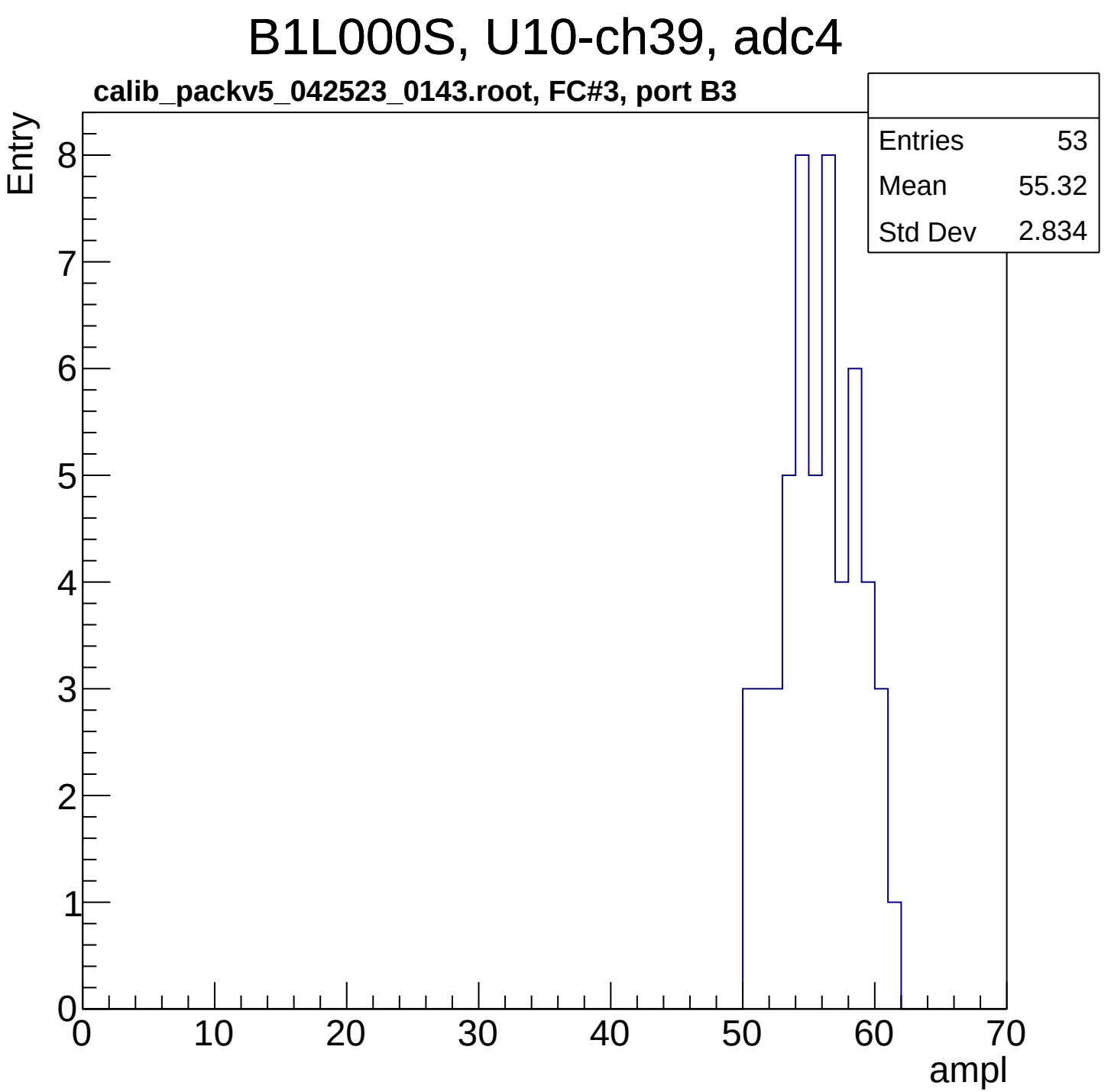
B1L000S, U10-ch39, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 55.32
Std Dev 2.834
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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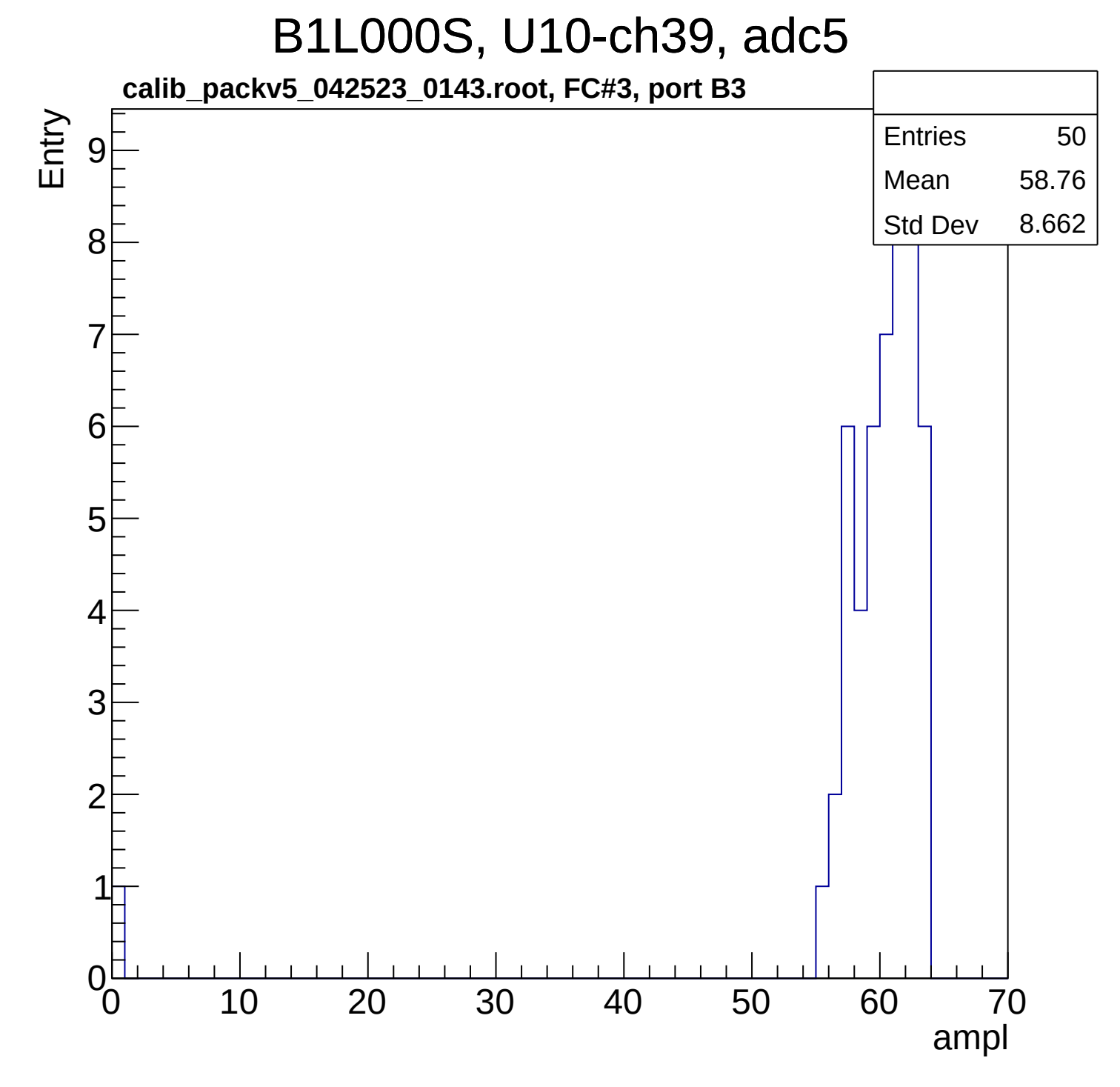
B1L000S, U10-ch39, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.76
Std Dev 8.662
Entry
ampl
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2
3
4
5
6
7
8
9
0
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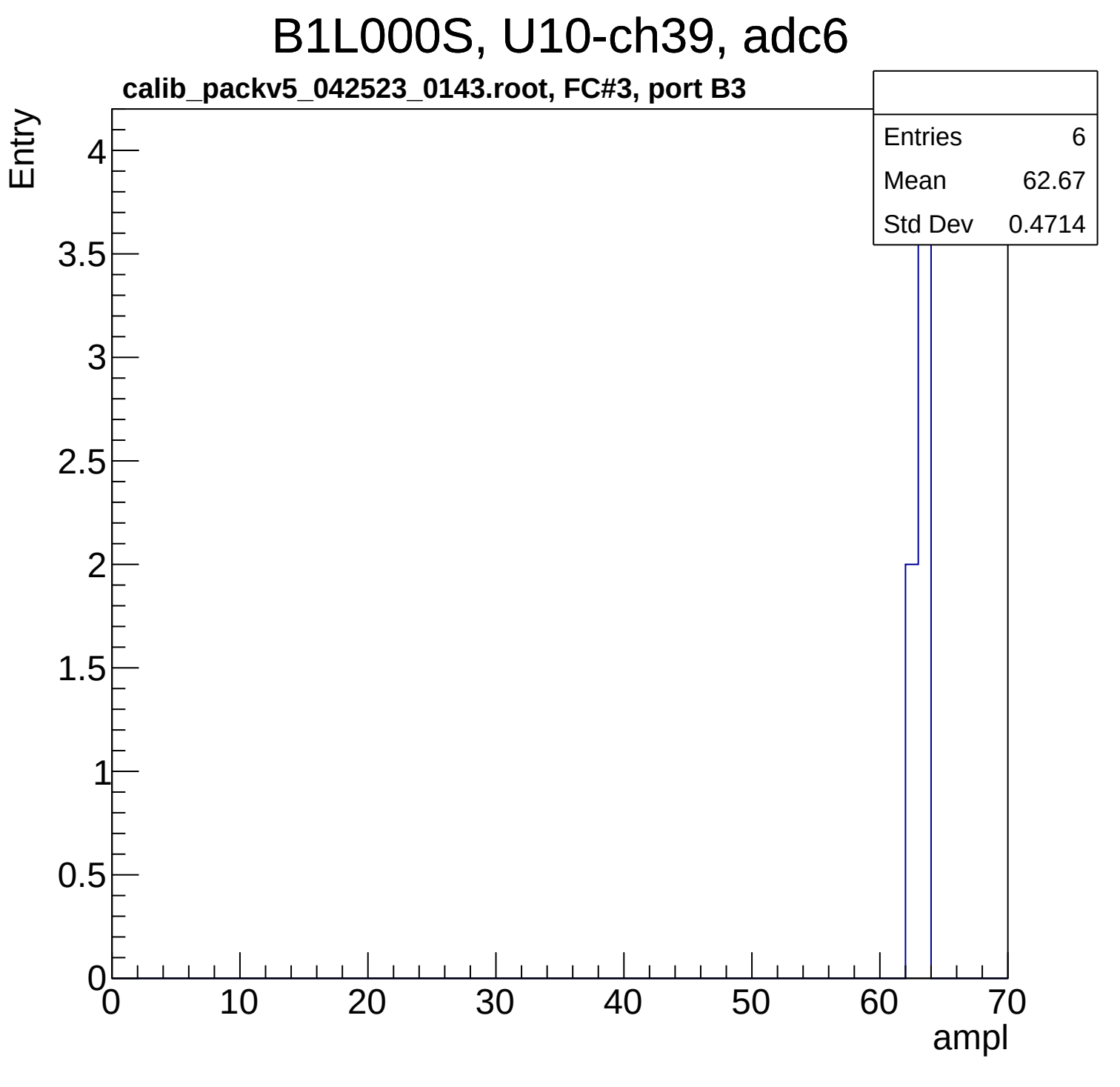
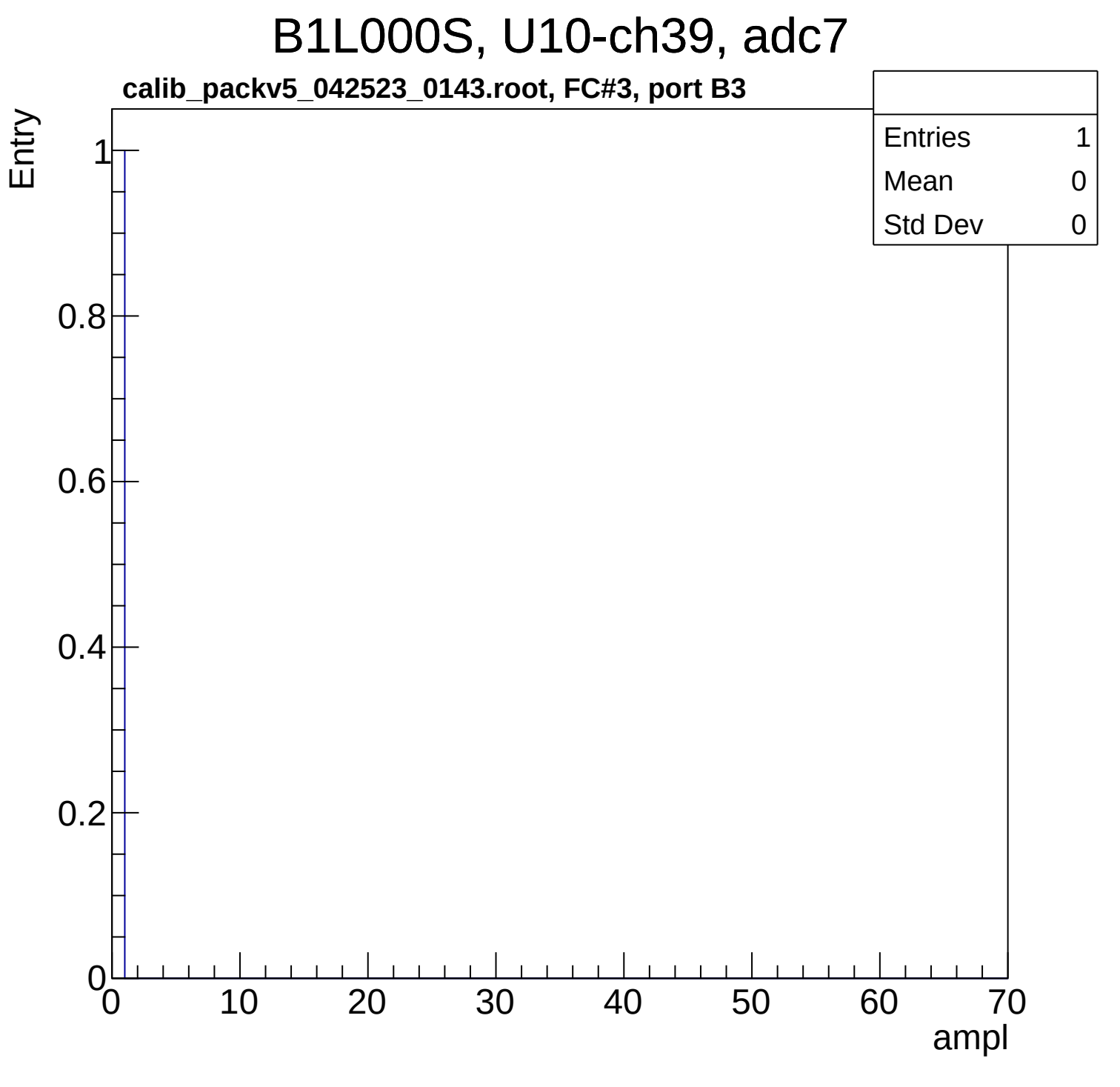
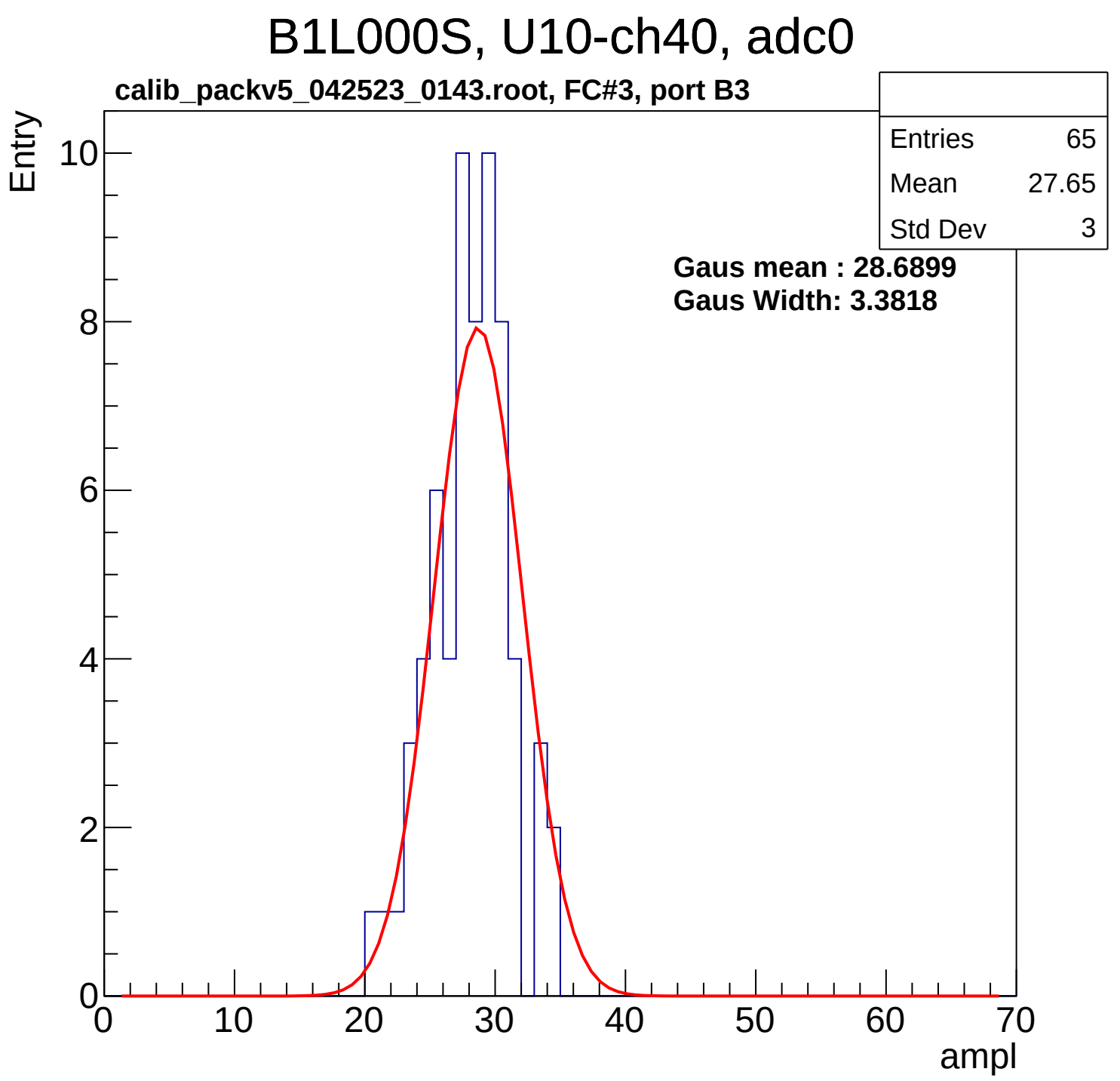
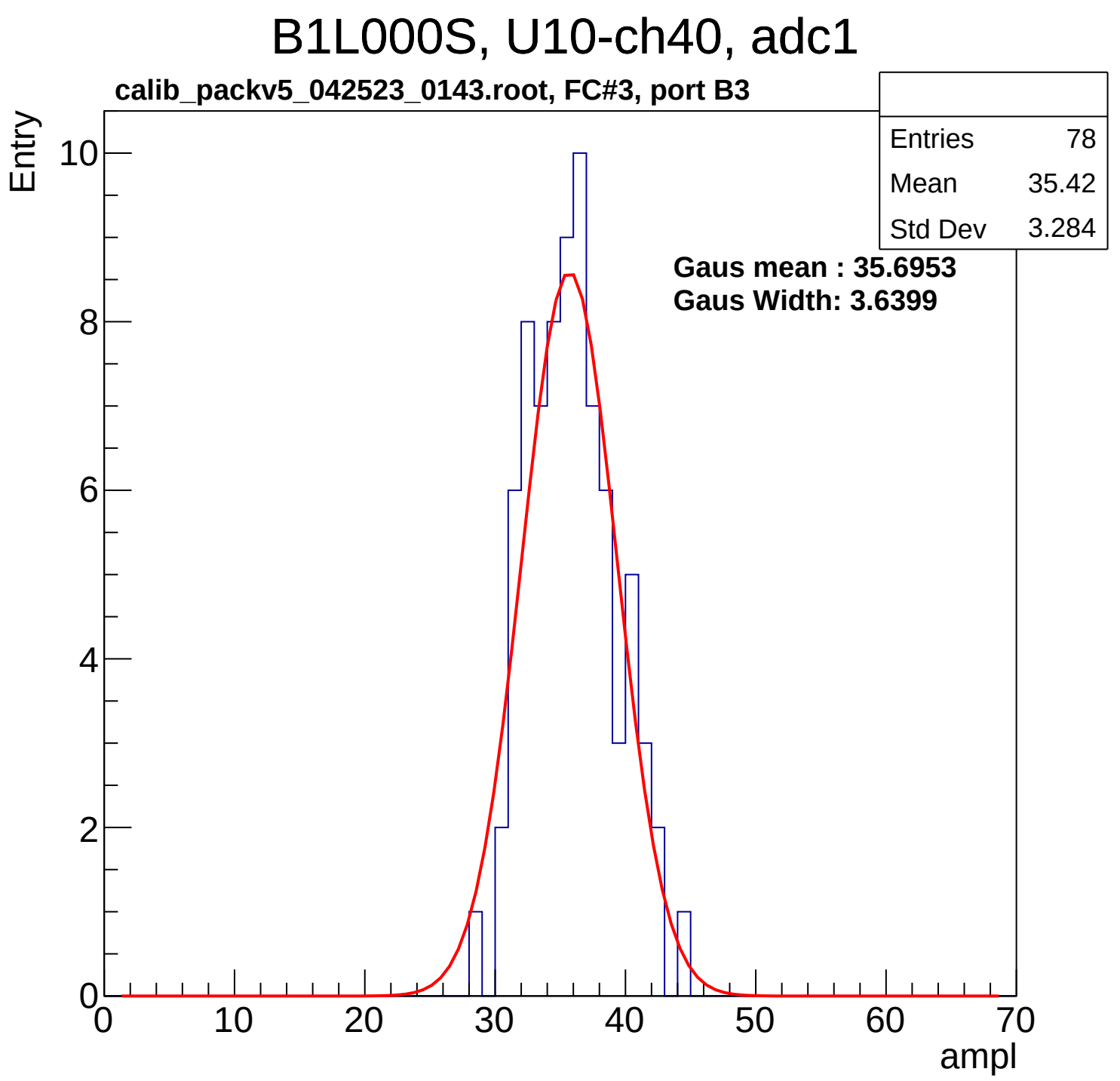

B1L000S, U10-ch39, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch39, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch40, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 27.65
Std Dev 3
Gaus mean : 28.6899
Gaus Width: 3.3818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch40, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 35.42
Std Dev 3.284
Gaus mean : 35.6953
Gaus Width: 3.6399
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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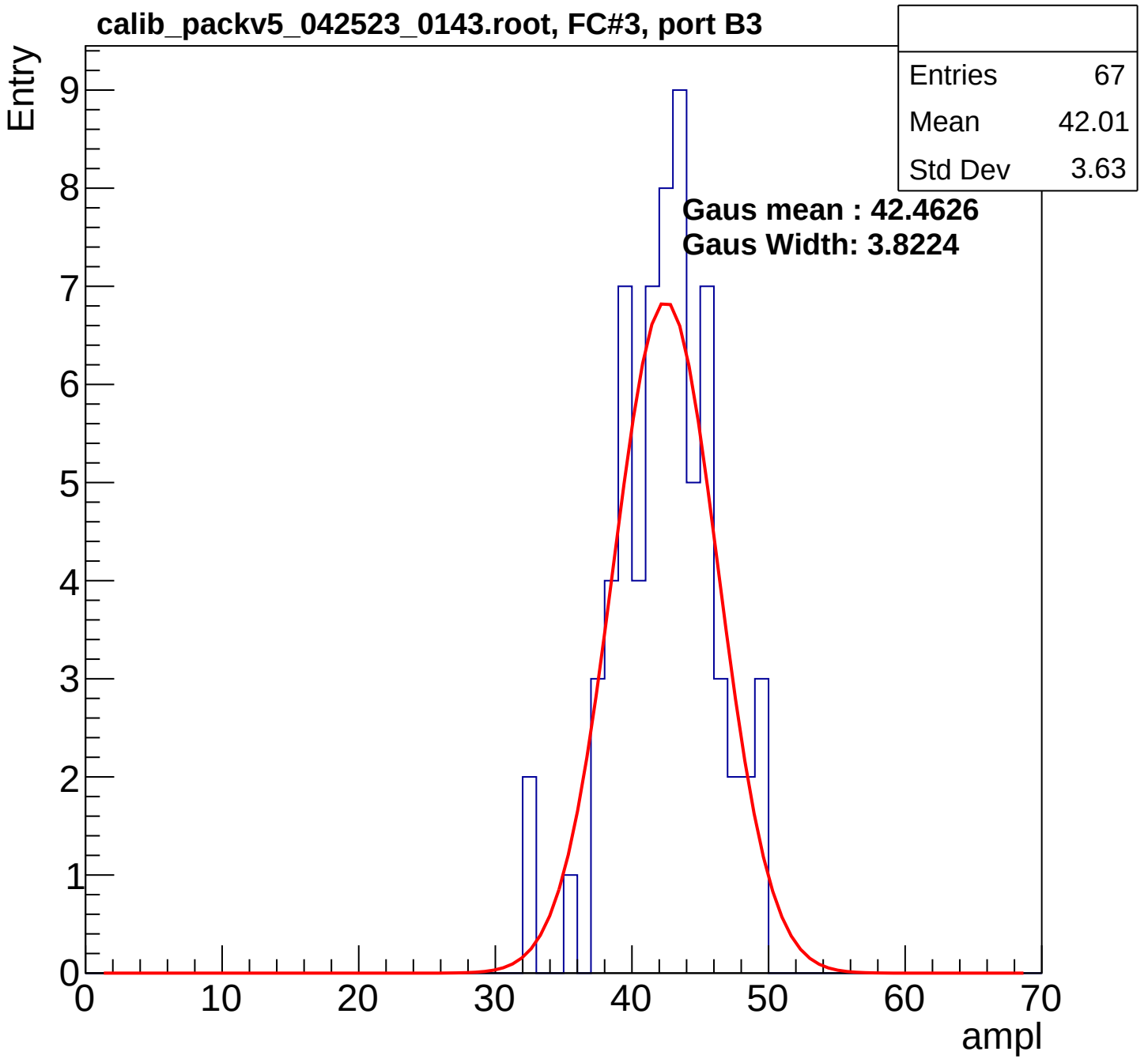
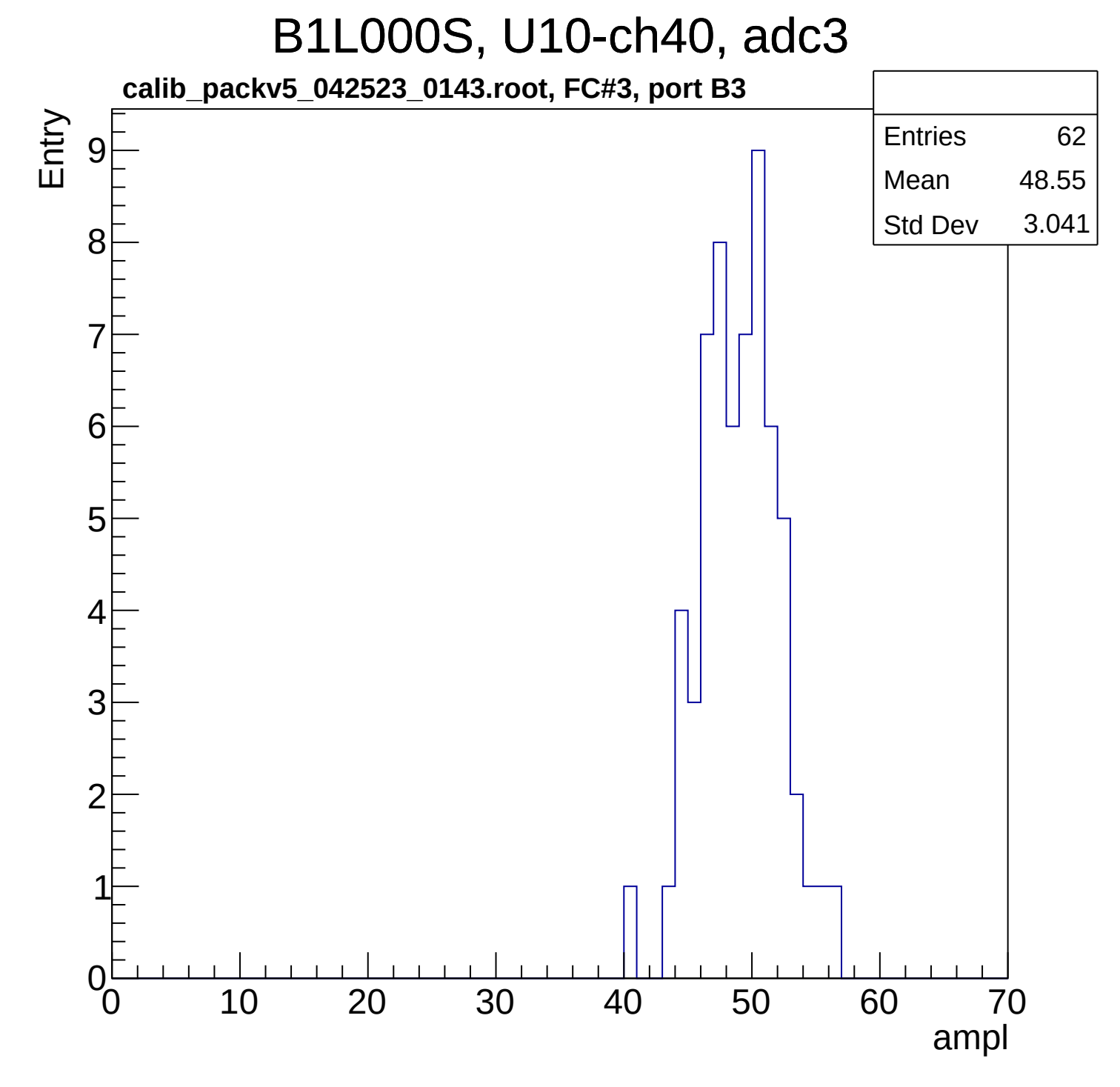

B1L000S, U10-ch40, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 42.01
Std Dev 3.63
Gaus mean : 42.4626
Gaus Width: 3.8224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch40, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 48.55
Std Dev 3.041
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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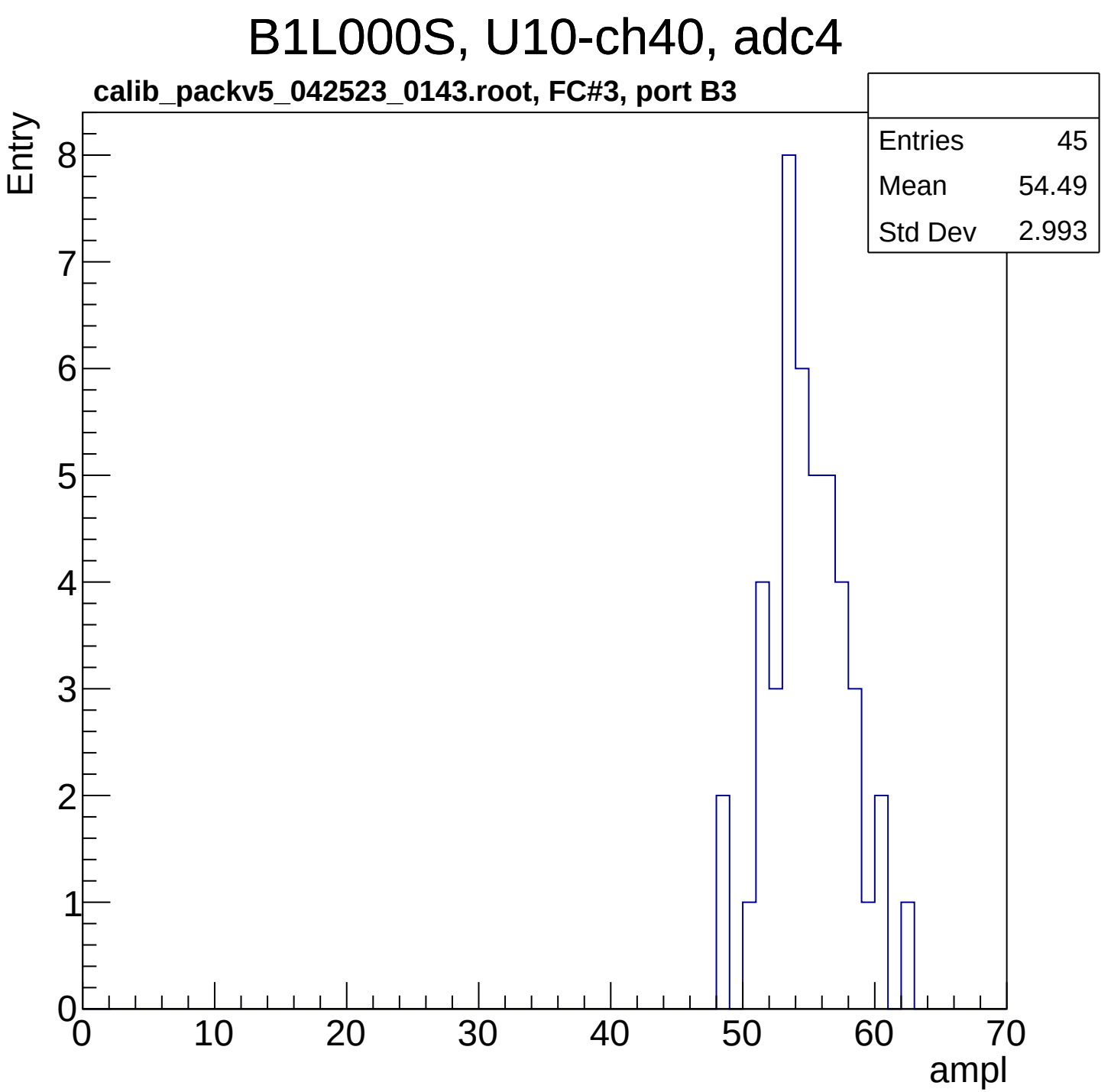
B1L000S, U10-ch40, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 54.49
Std Dev 2.993
Entry
ampl
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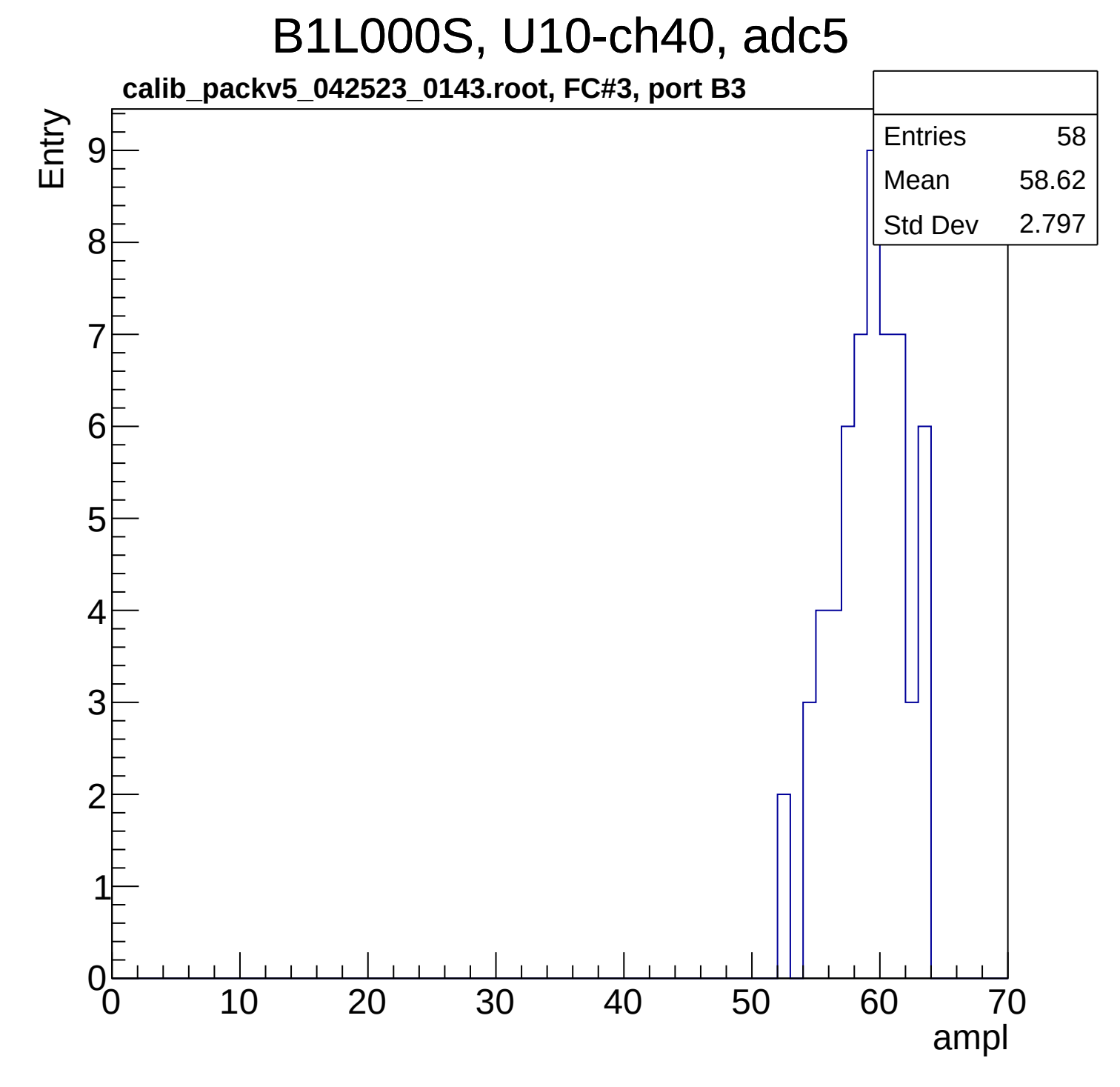
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch40, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.62
Std Dev 2.797
Entry
ampl
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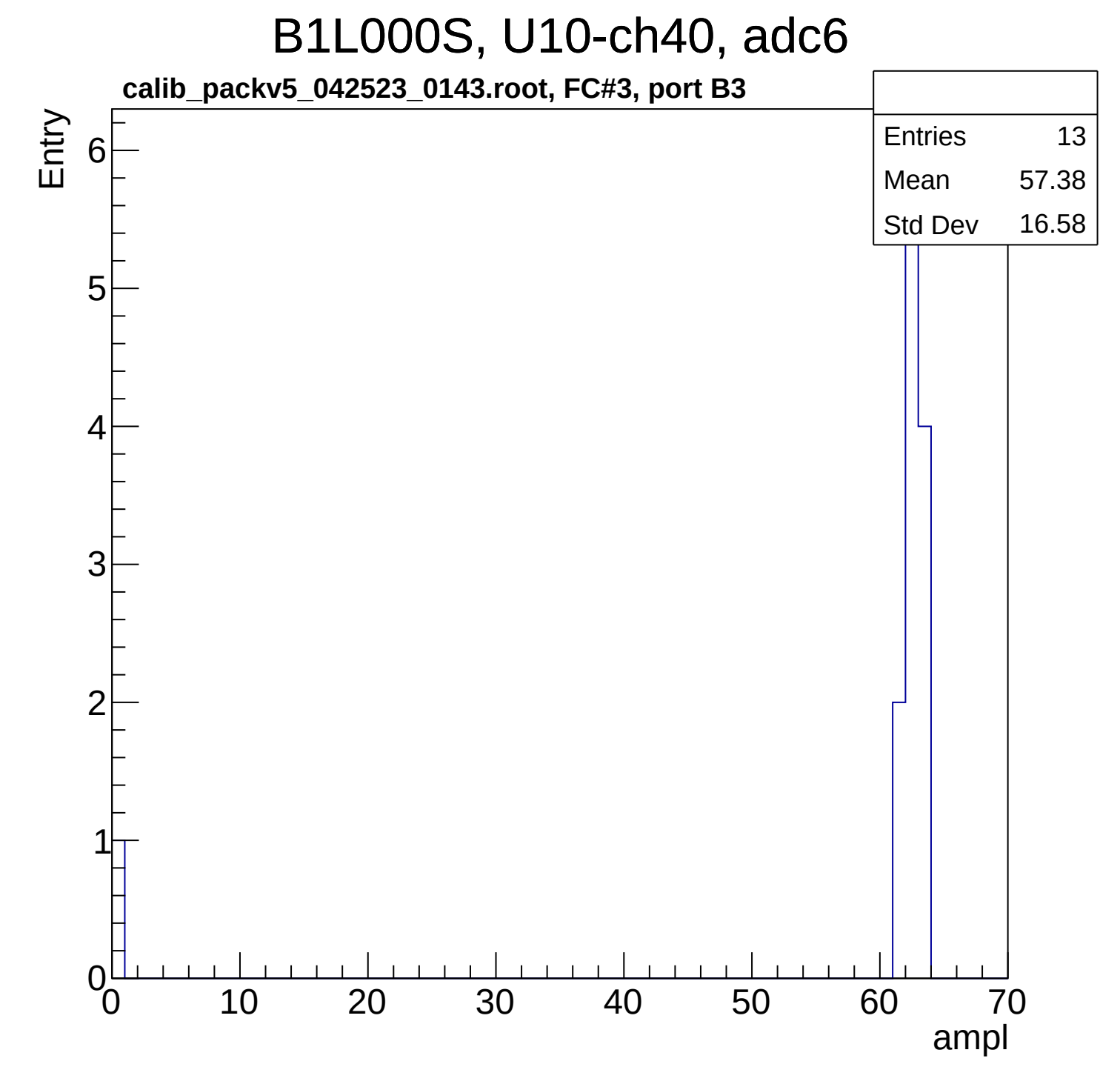
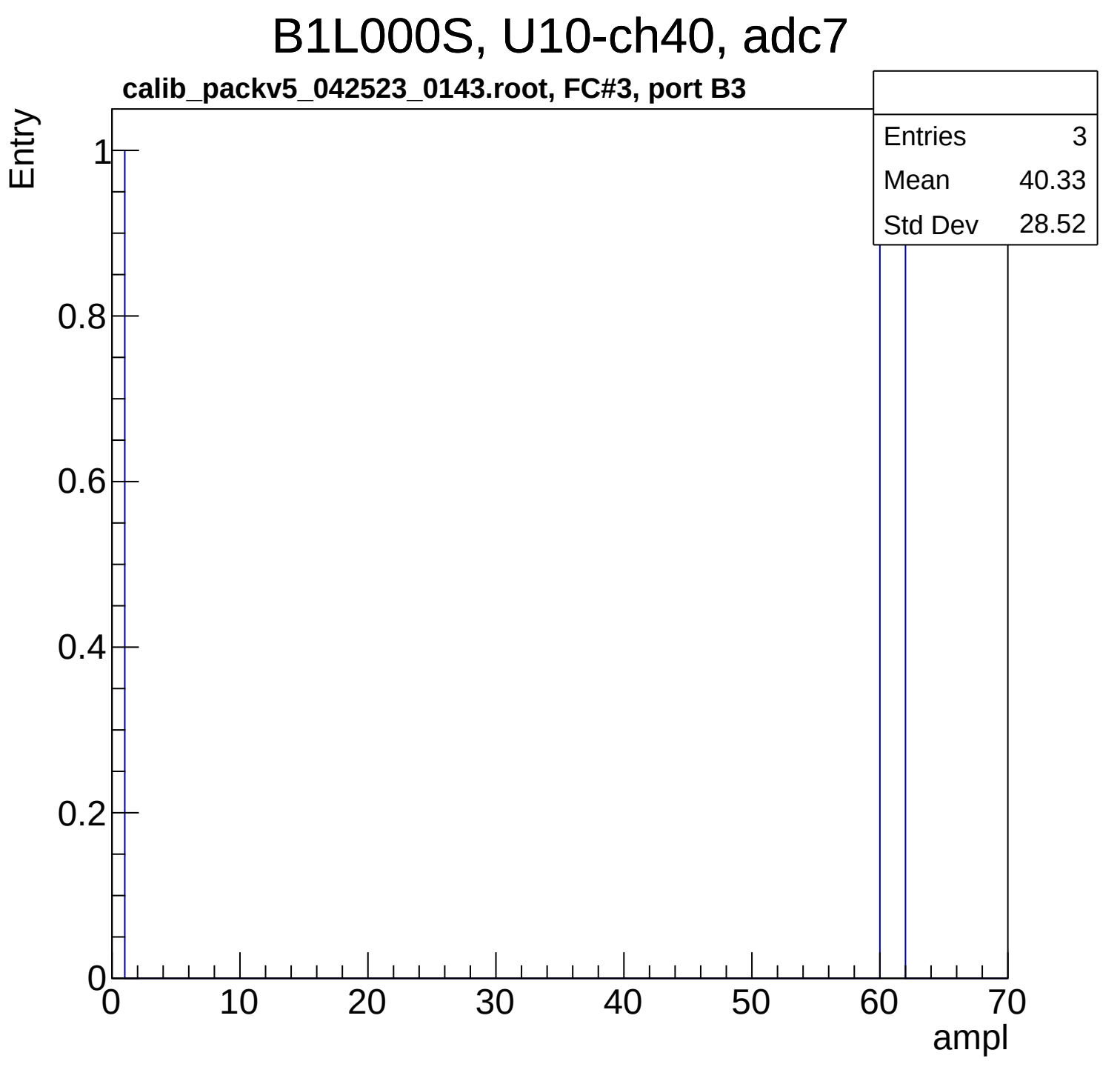
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch40, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.38
Std Dev 16.58
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch40, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 40.33
Std Dev 28.52
Entry
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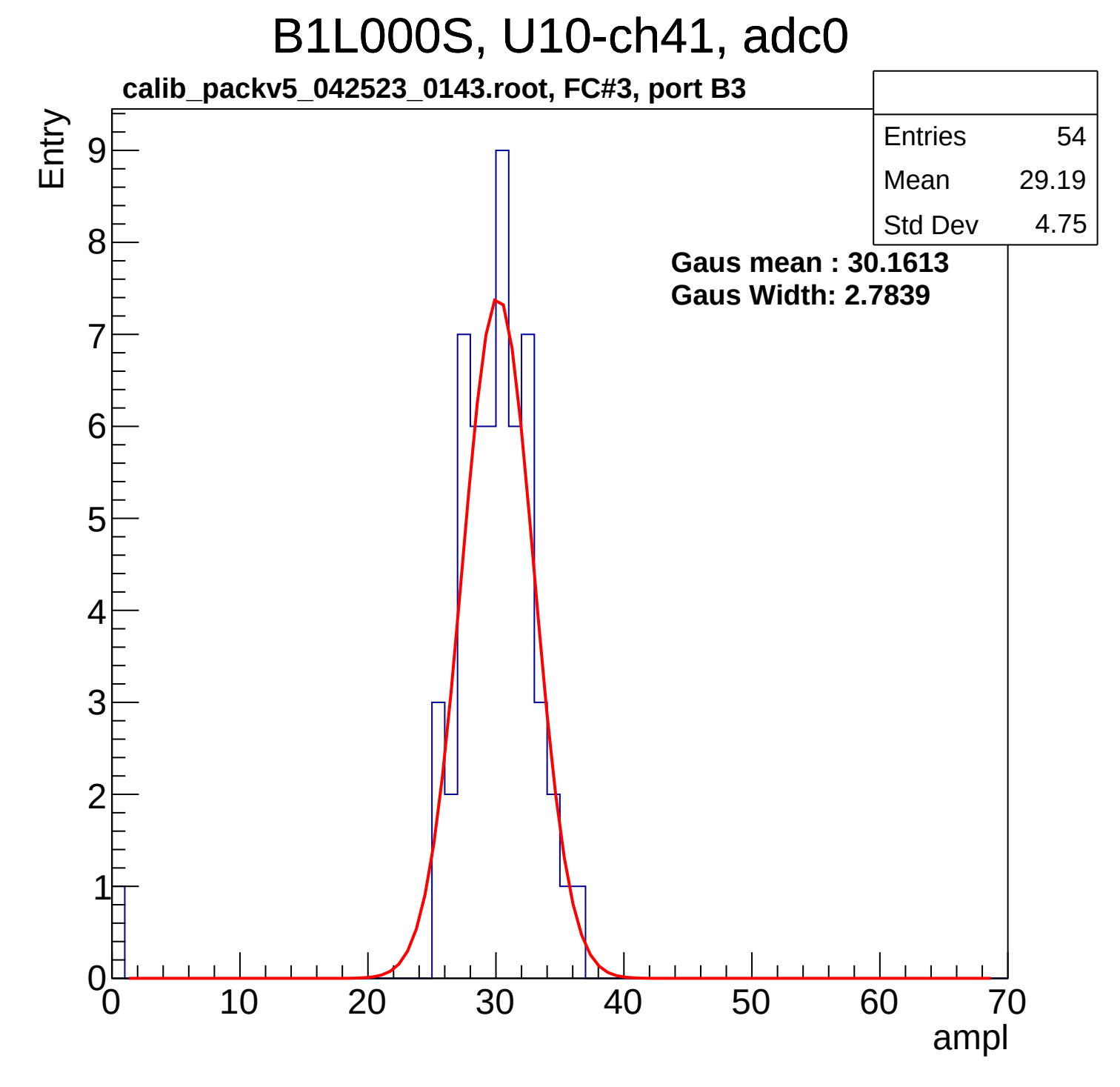
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch41, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 29.19
Std Dev 4.75
Gaus mean : 30.1613
Gaus Width: 2.7839
Entry
ampl
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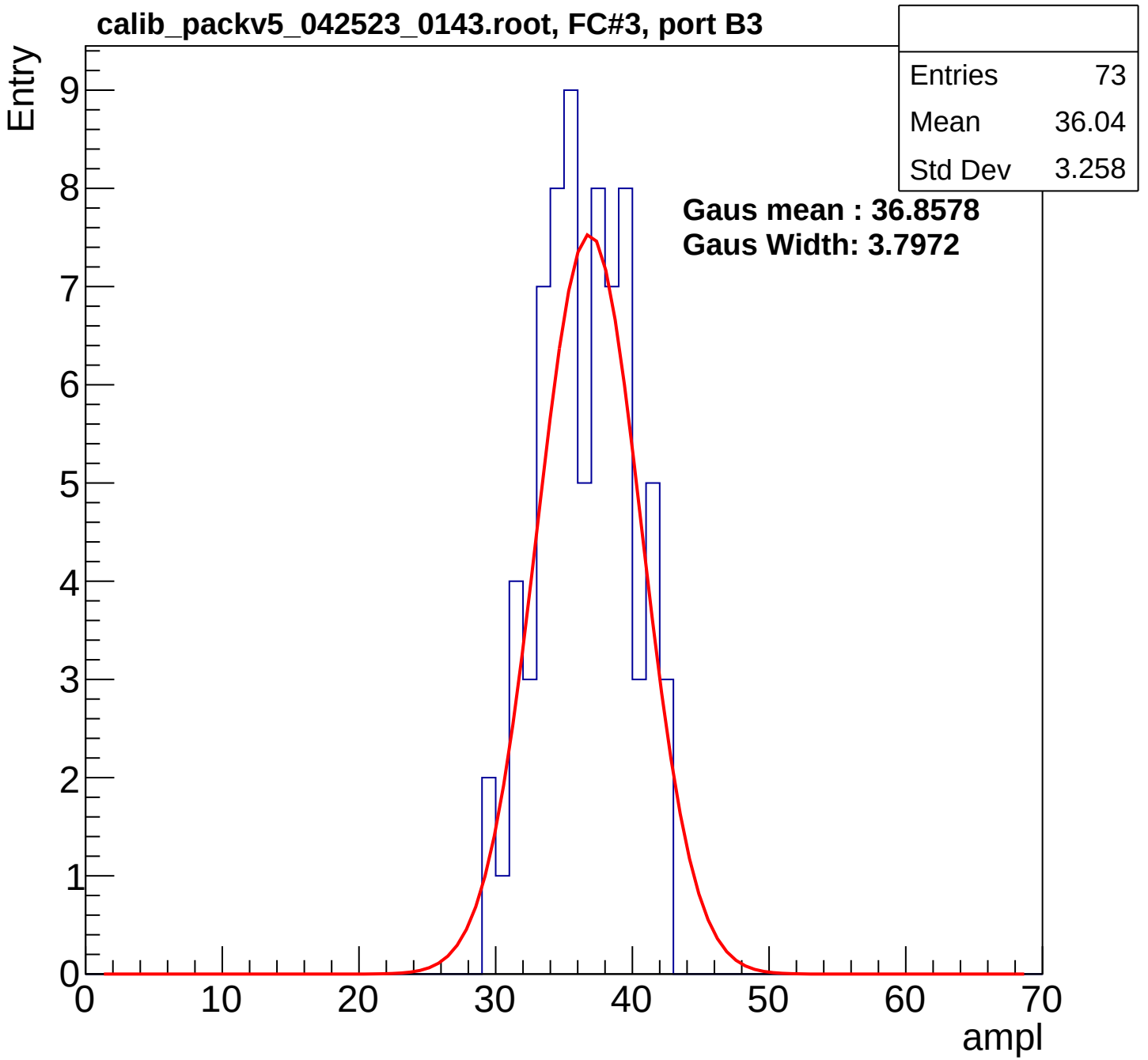
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch41, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 36.04
Std Dev 3.258
Gaus mean : 36.8578
Gaus Width: 3.7972
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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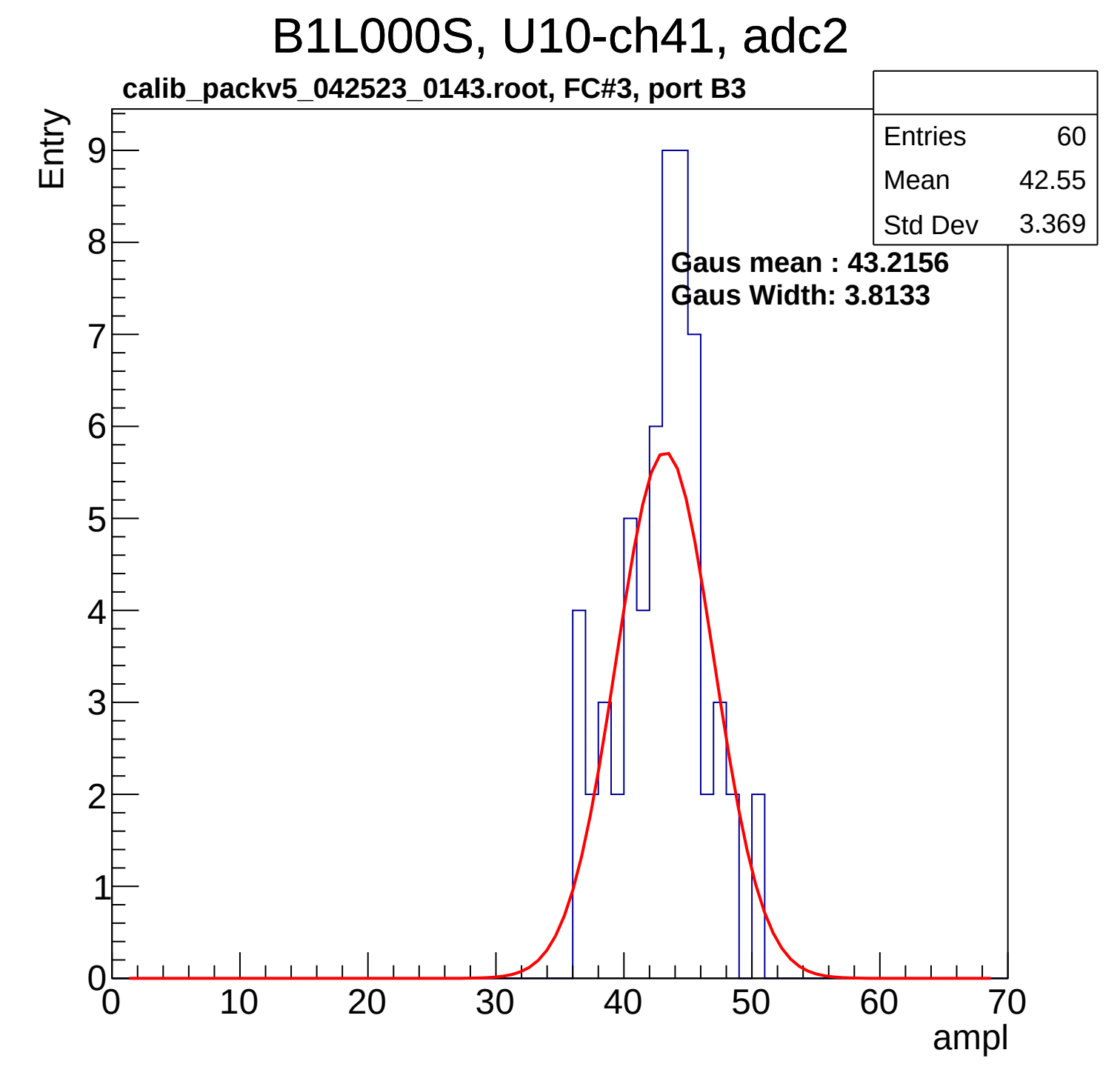
B1L000S, U10-ch41, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 42.55
Std Dev 3.369
Gaus mean : 43.2156
Gaus Width: 3.8133
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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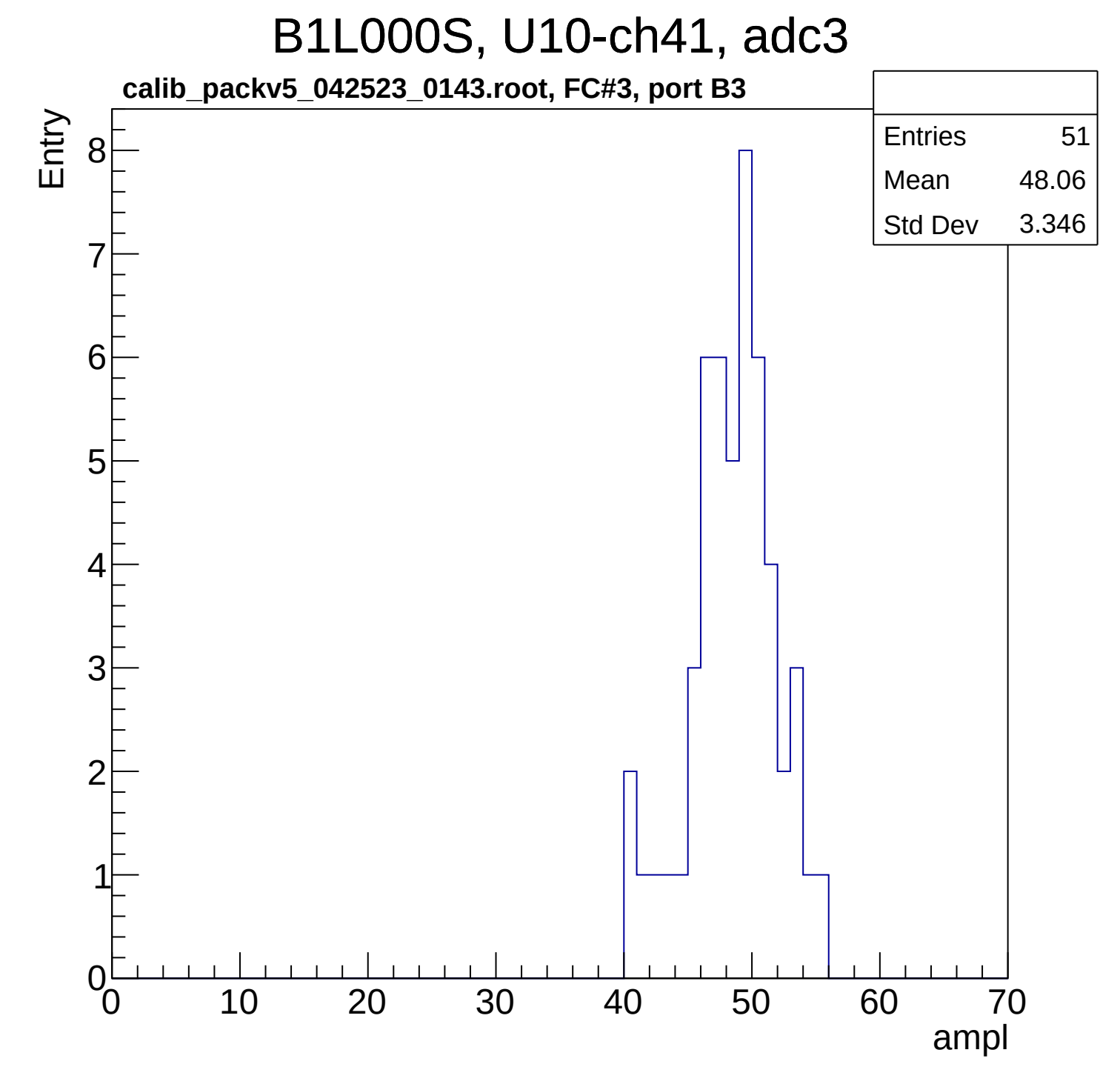
B1L000S, U10-ch41, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 48.06
Std Dev 3.346
Entry
ampl
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3
4
5
6
7
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0
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60
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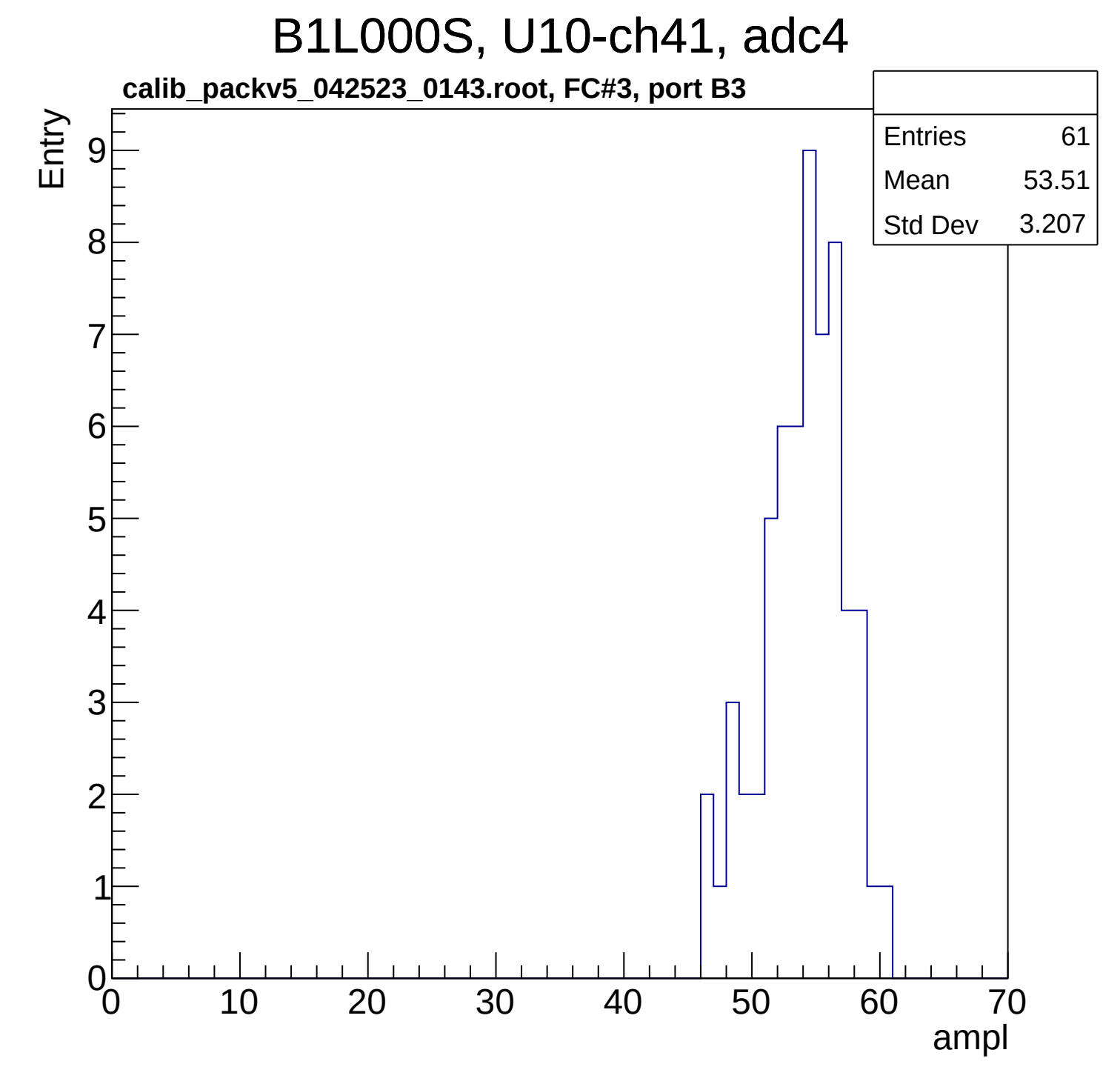
B1L000S, U10-ch41, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 53.51
Std Dev 3.207
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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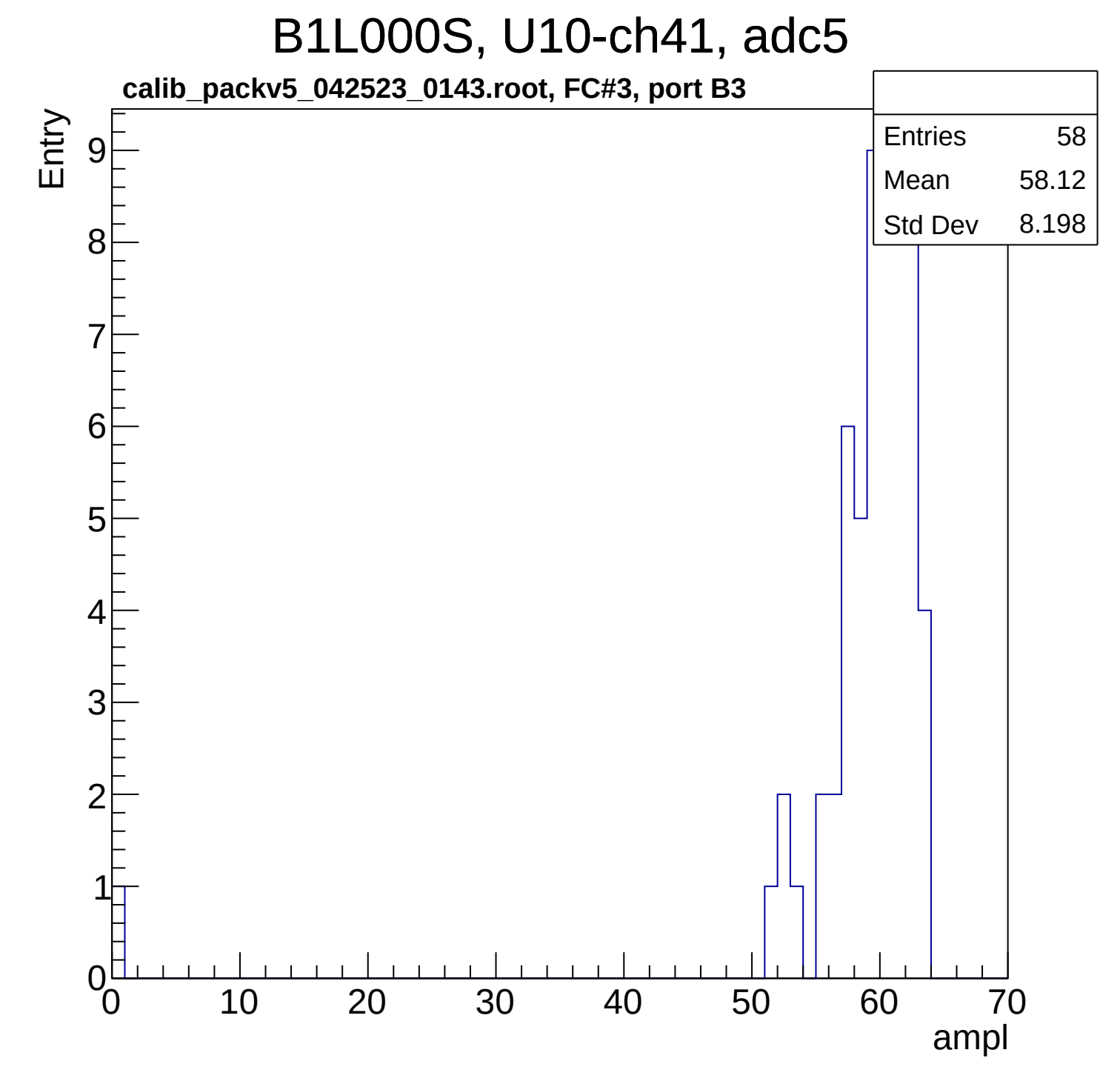
B1L000S, U10-ch41, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.12
Std Dev 8.198
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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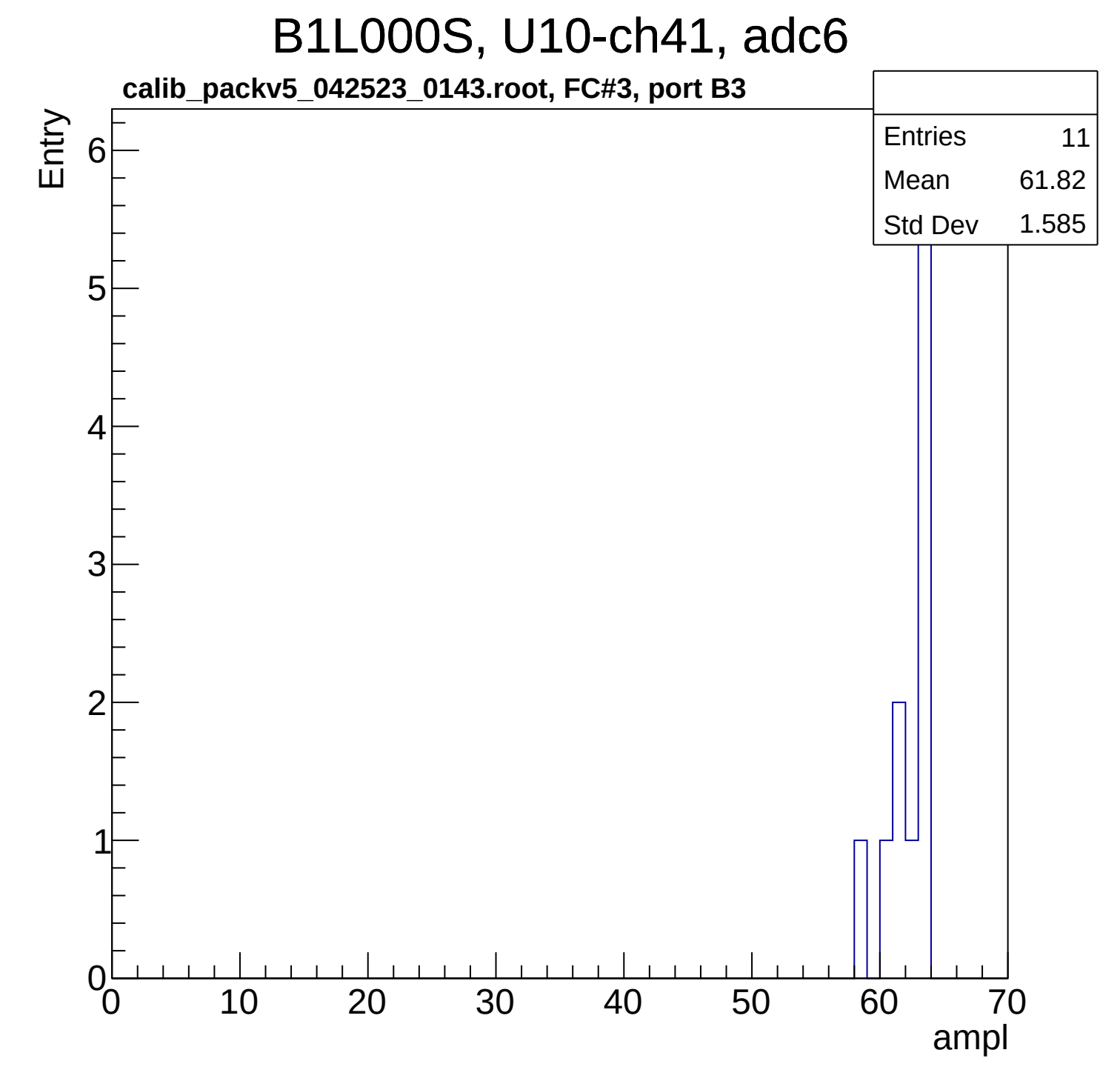
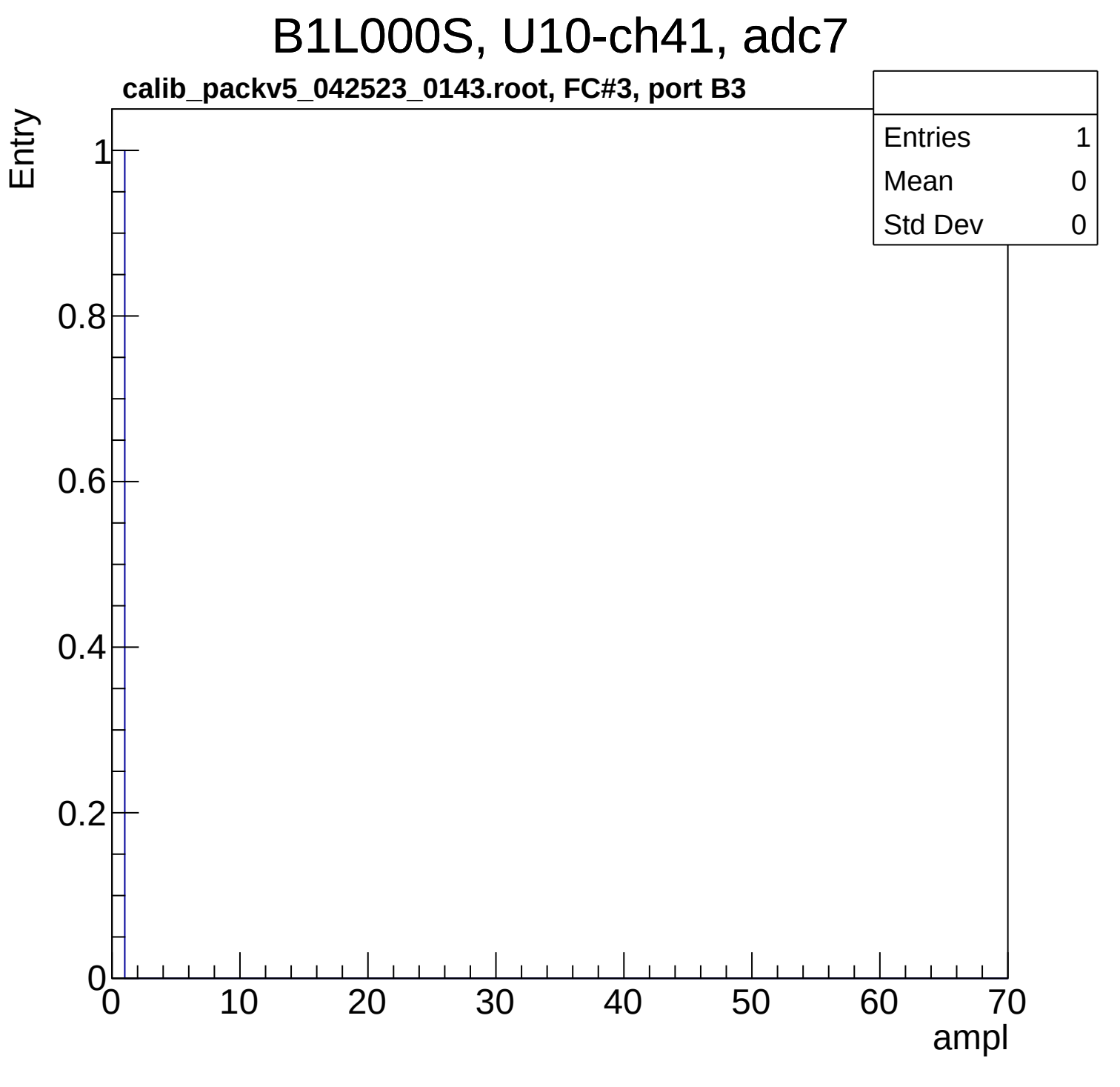

B1L000S, U10-ch41, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.82
Std Dev 1.585
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch41, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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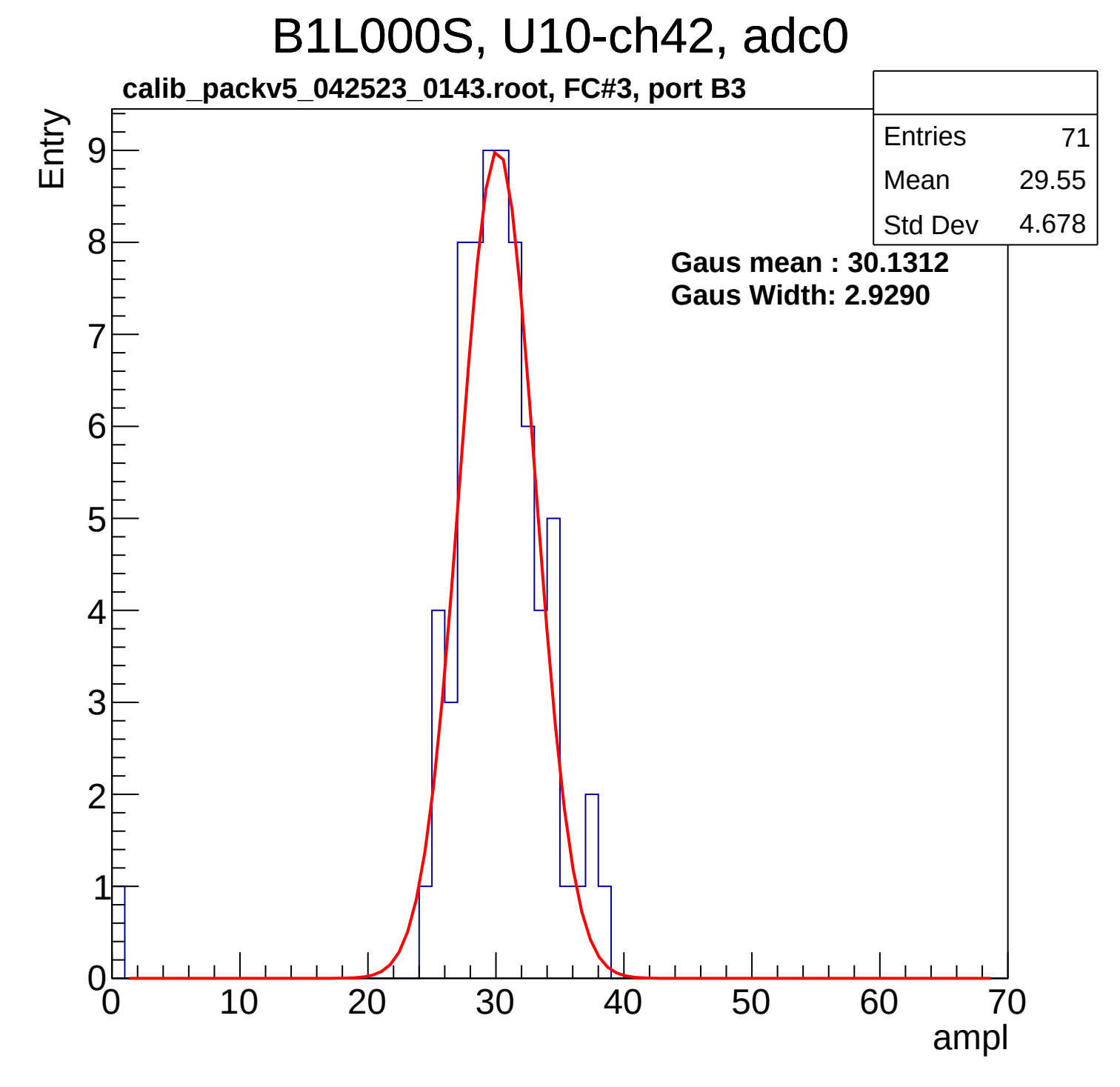
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch42, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 29.55
Std Dev 4.678
Gaus mean : 30.1312
Gaus Width: 2.9290
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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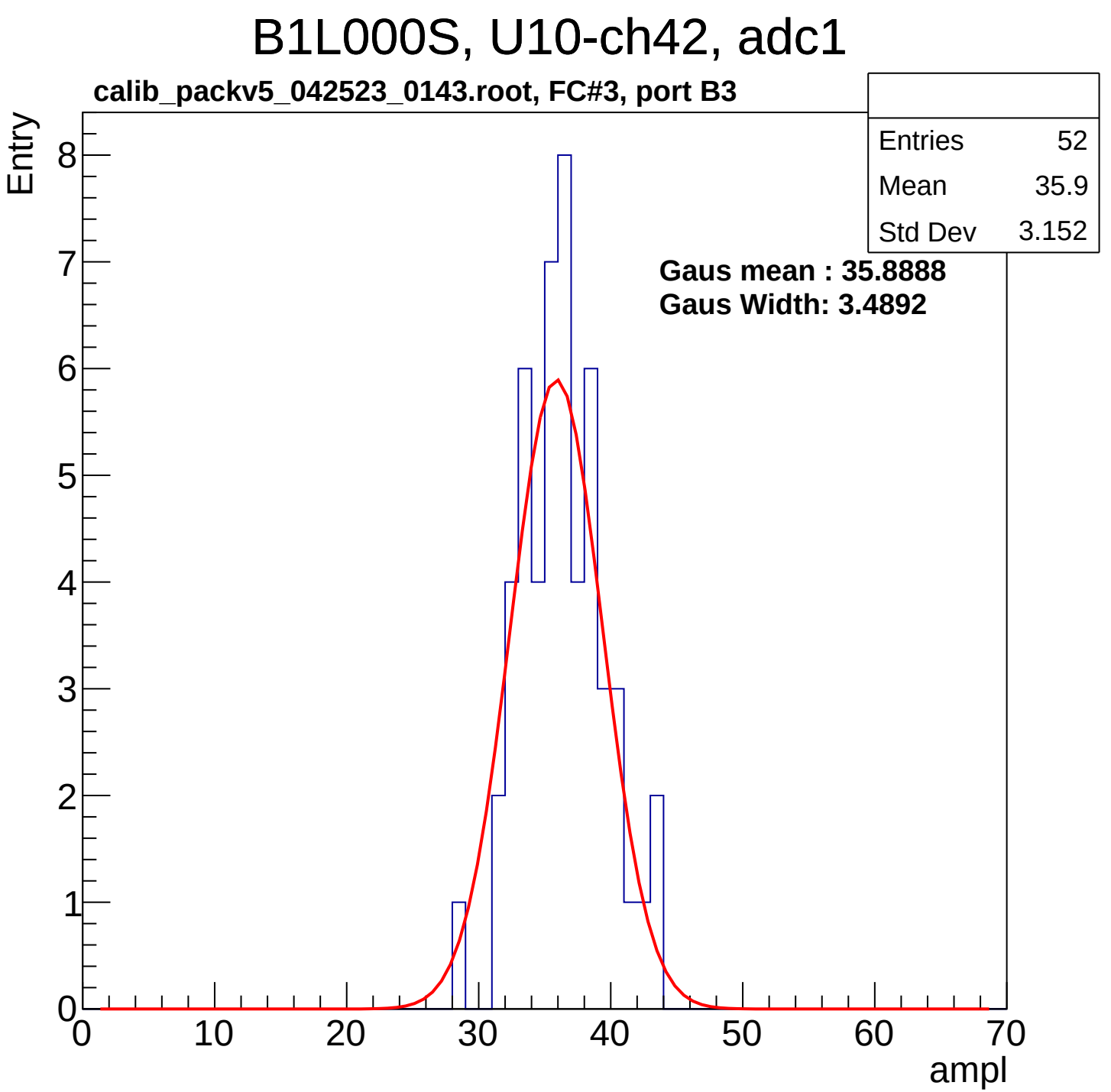
B1L000S, U10-ch42, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 35.9
Std Dev 3.152
Gaus mean : 35.8888
Gaus Width: 3.4892
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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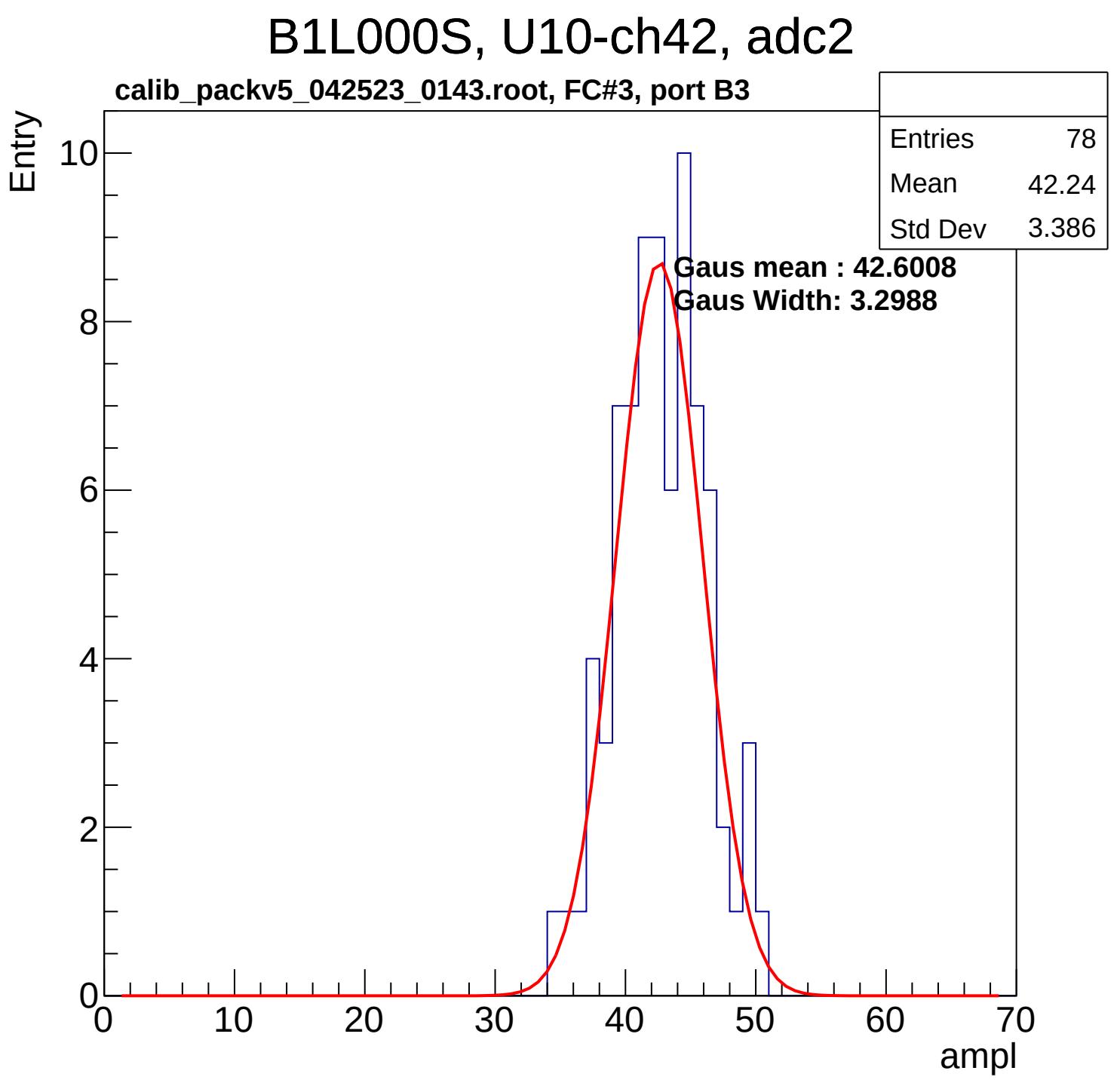
B1L000S, U10-ch42, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 42.24
Std Dev 3.386
Gaus mean : 42.6008
Gaus Width: 3.2988
Entry
ampl
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4
6
8
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0
10
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40
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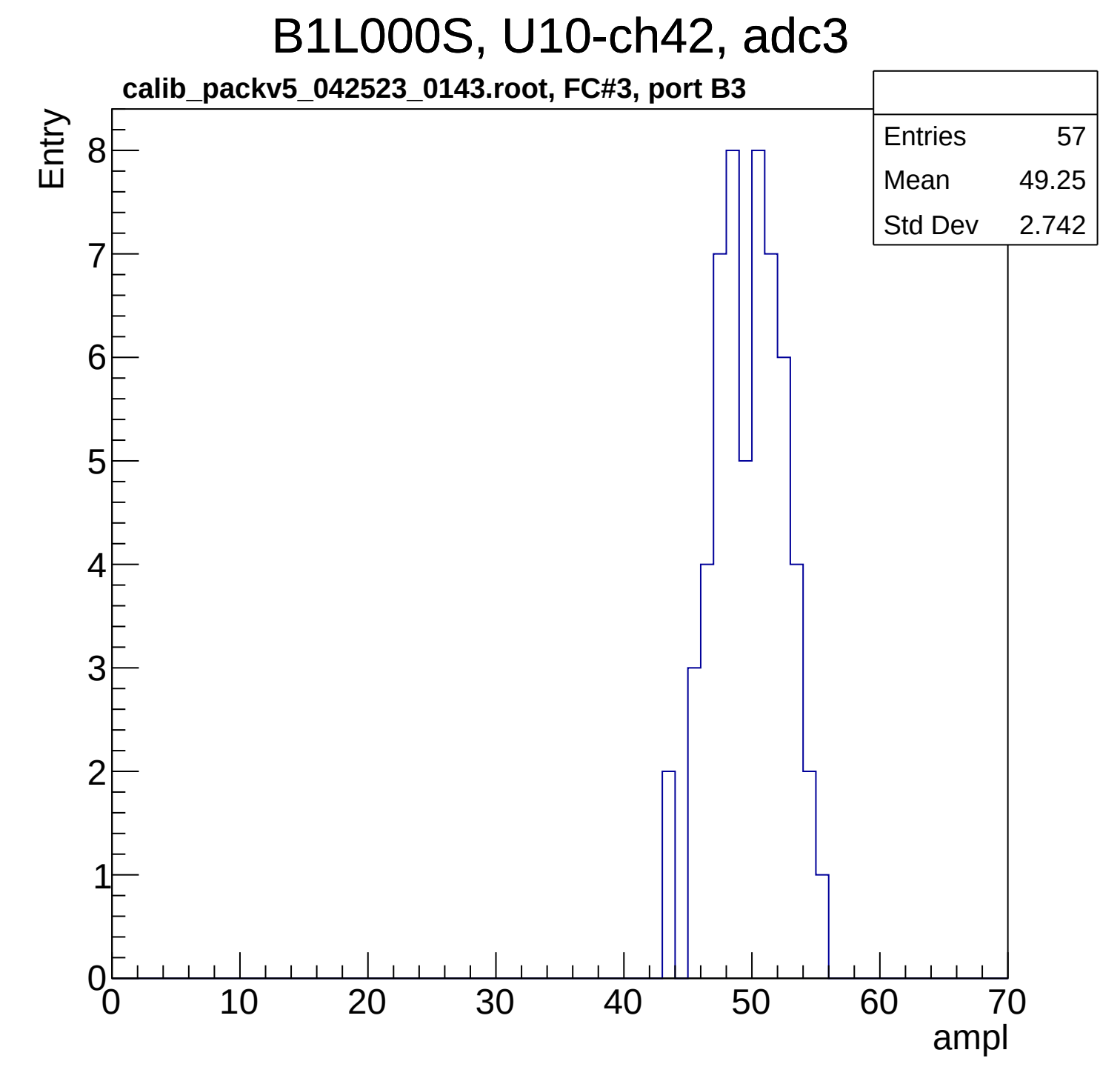
B1L000S, U10-ch42, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 49.25
Std Dev 2.742
Entry
ampl
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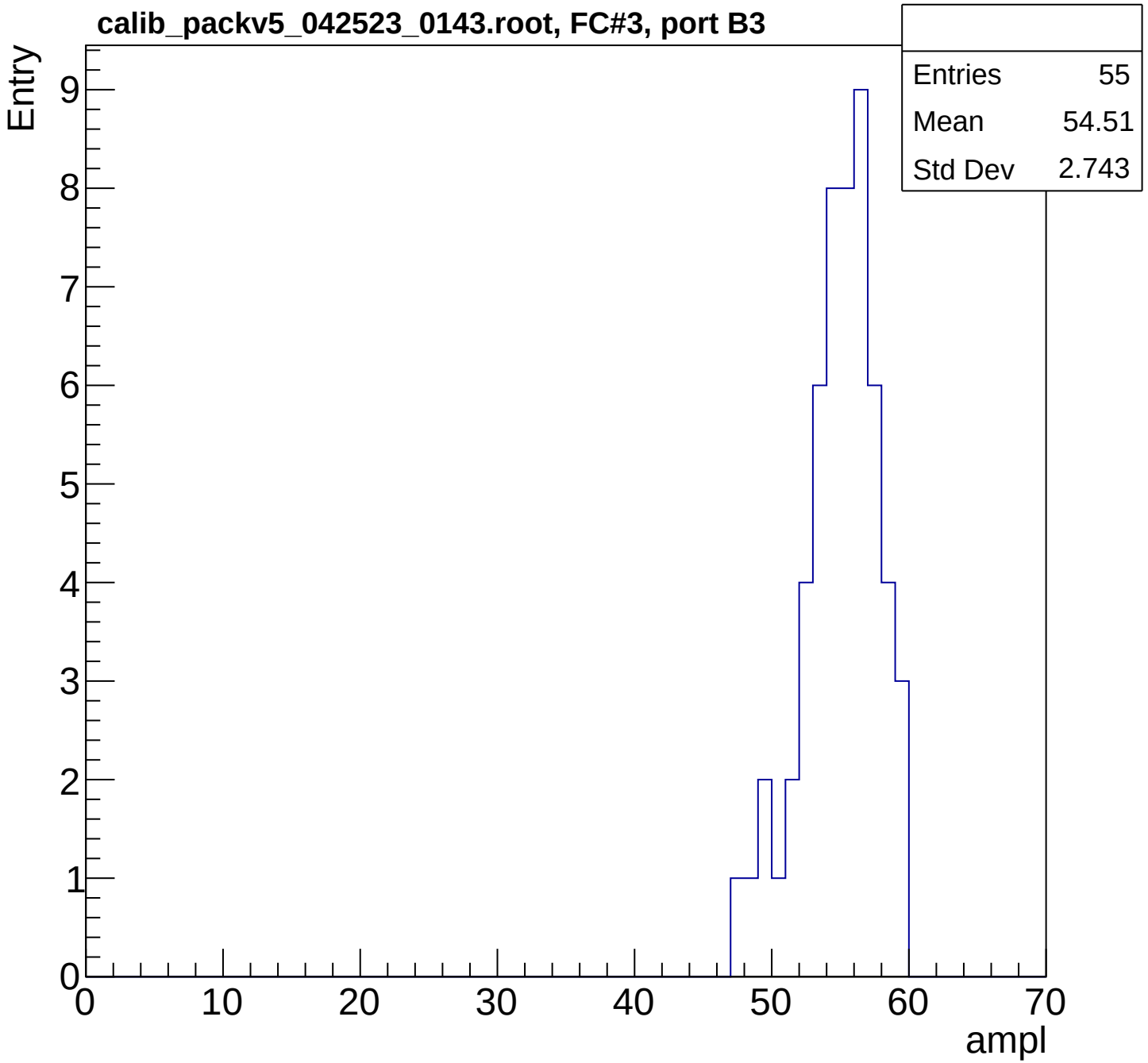
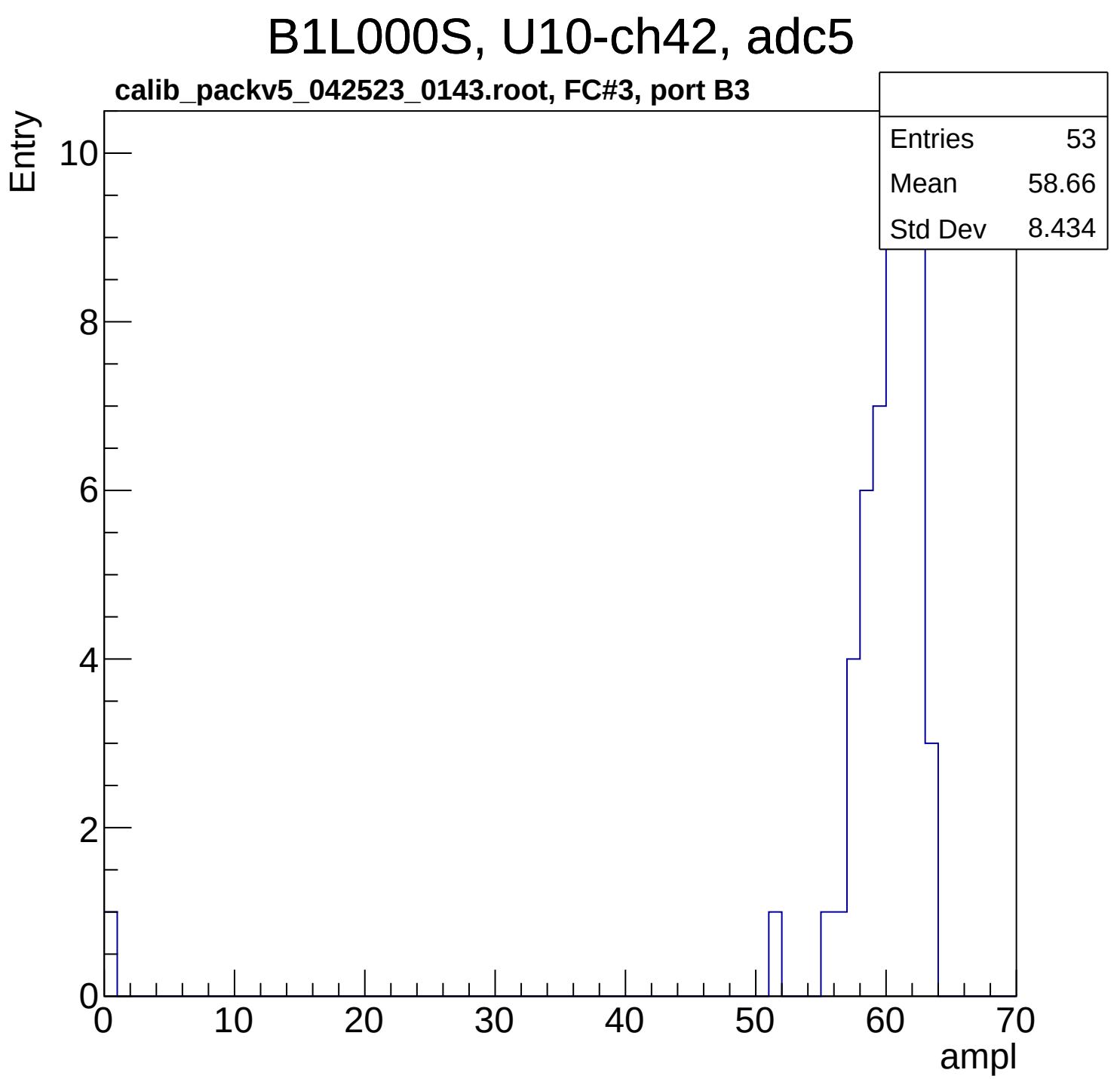

B1L000S, U10-ch42, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 54.51
Std Dev 2.743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch42, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.66
Std Dev 8.434
Entry
0
2
4
6
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10
0
10
20
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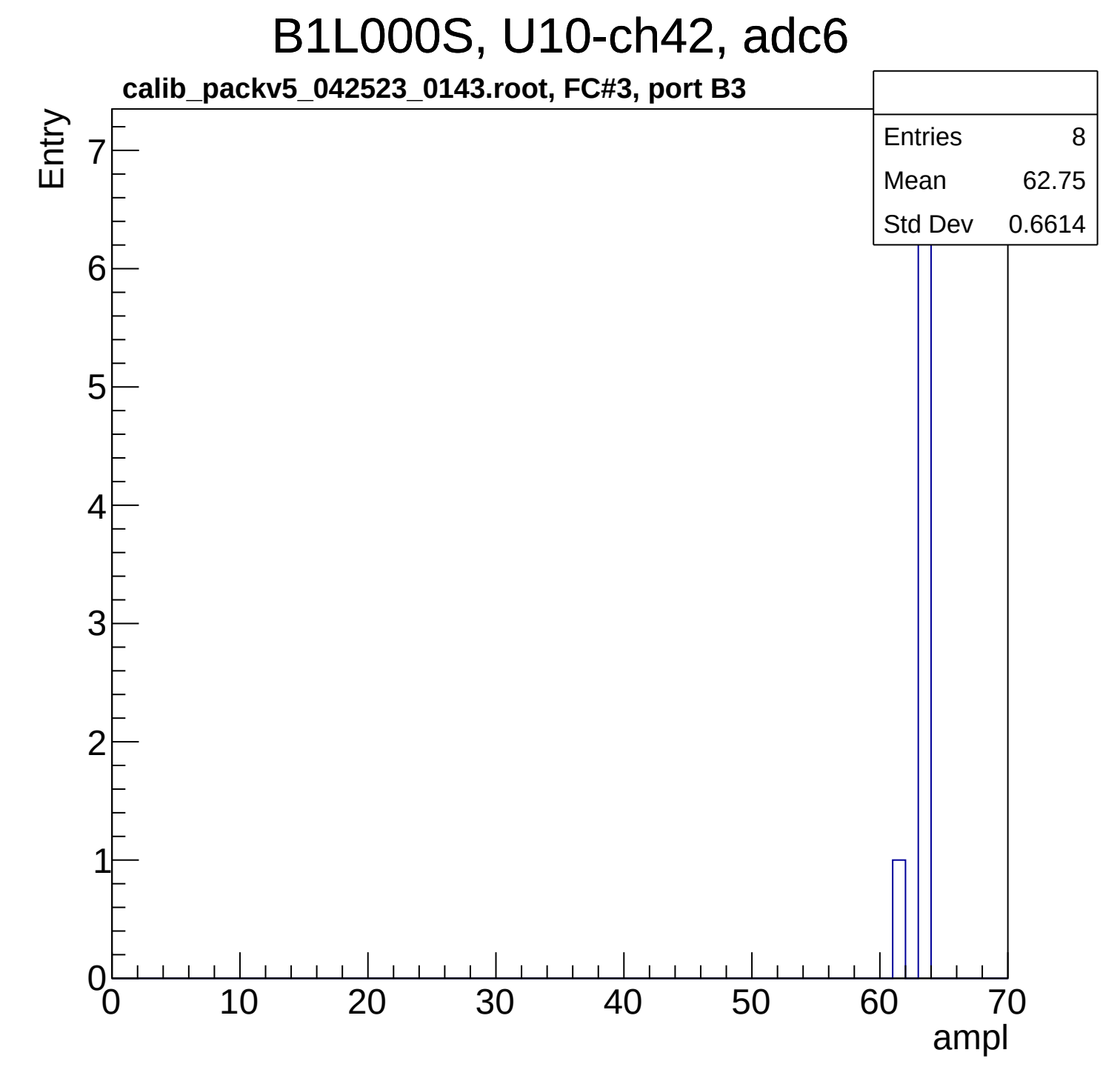
60
70
ampl


B1L000S, U10-ch42, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.75
Std Dev 0.6614
Entry
ampl
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1
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3
4
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7
0
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40
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60
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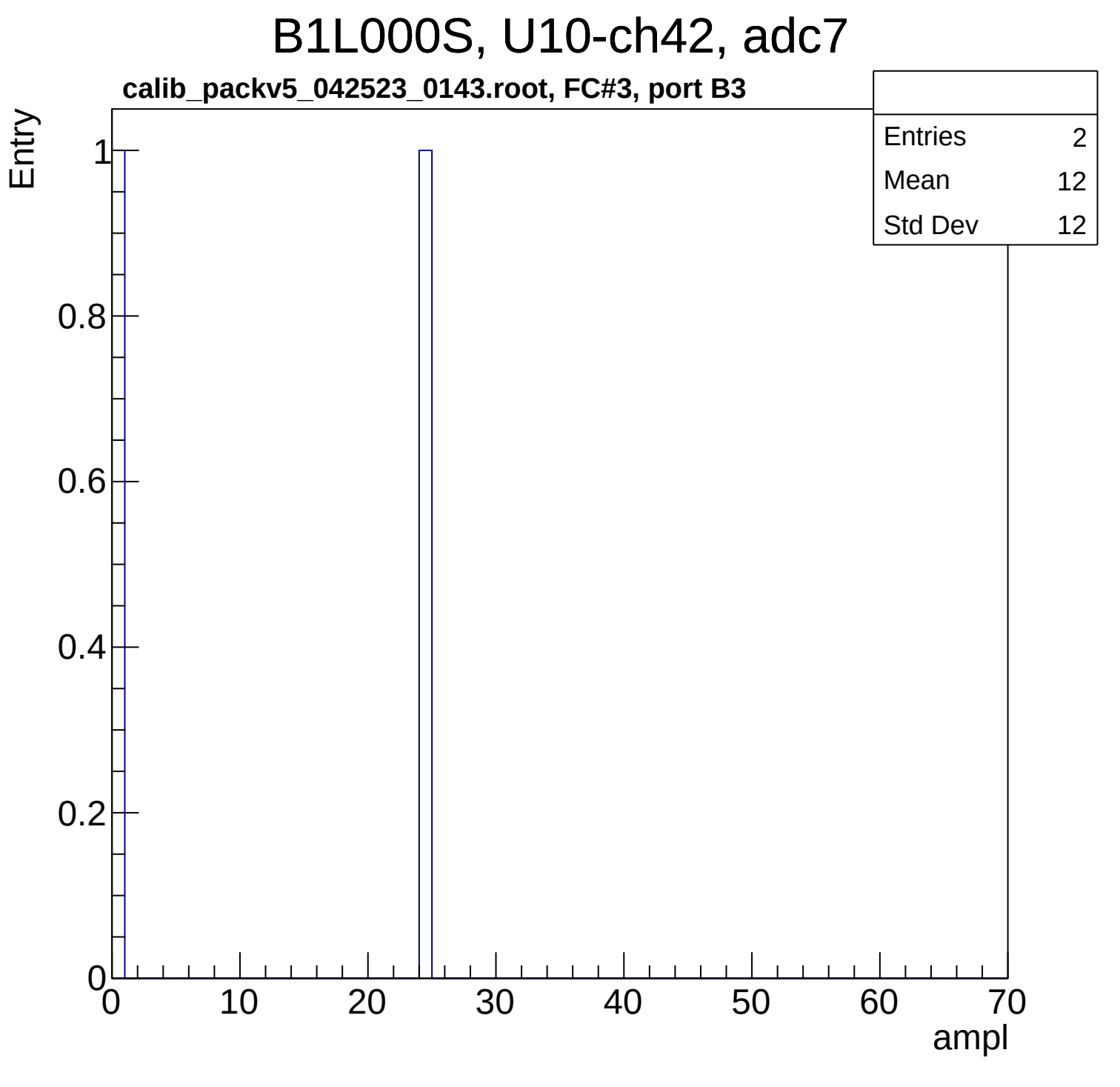
B1L000S, U10-ch42, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 12
Std Dev 12
Entry
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0.8
0.6
0.4
0.2
0
0
10
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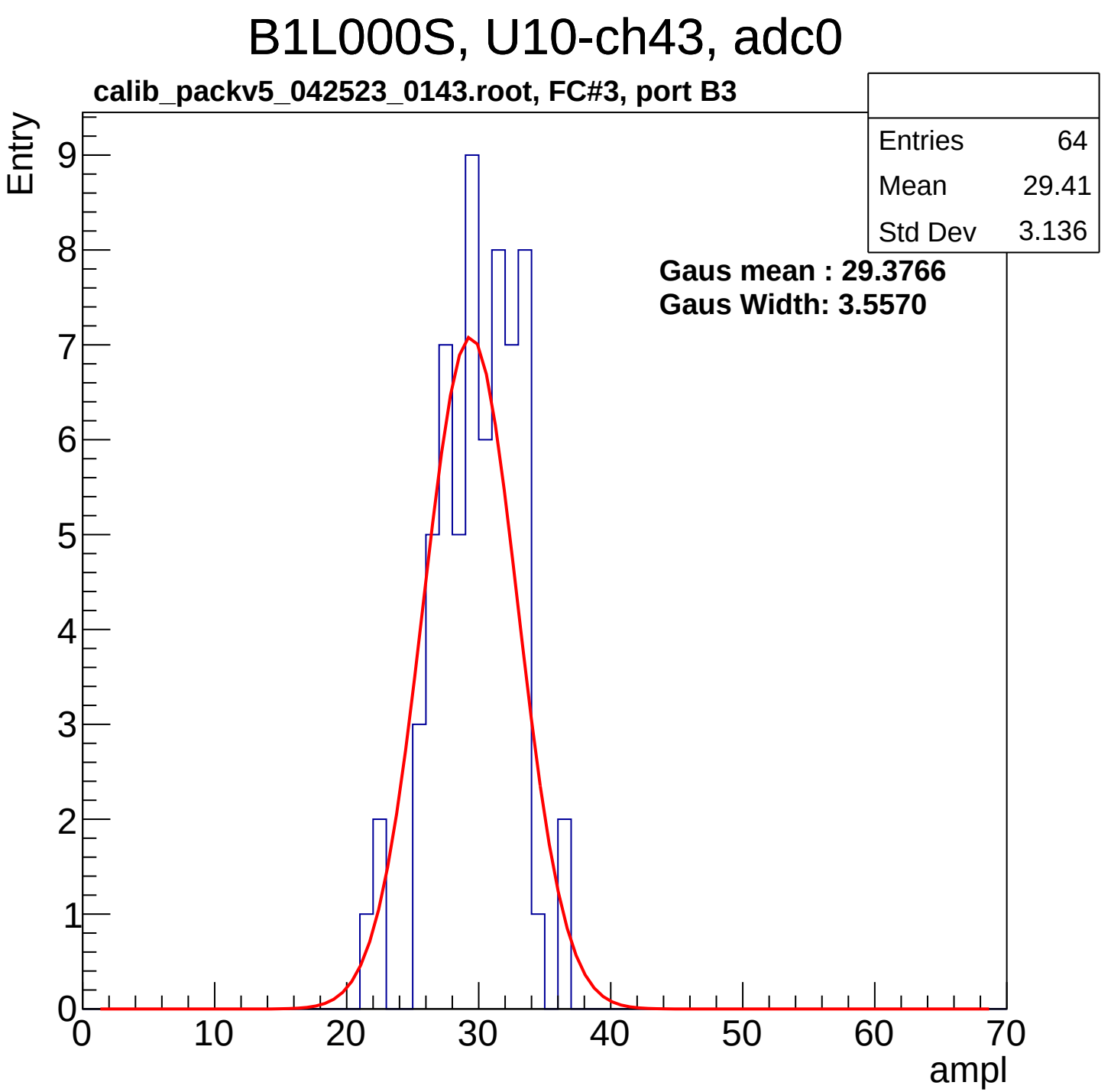
50
60
70
ampl


B1L000S, U10-ch43, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 29.41
Std Dev 3.136
Gaus mean : 29.3766
Gaus Width: 3.5570
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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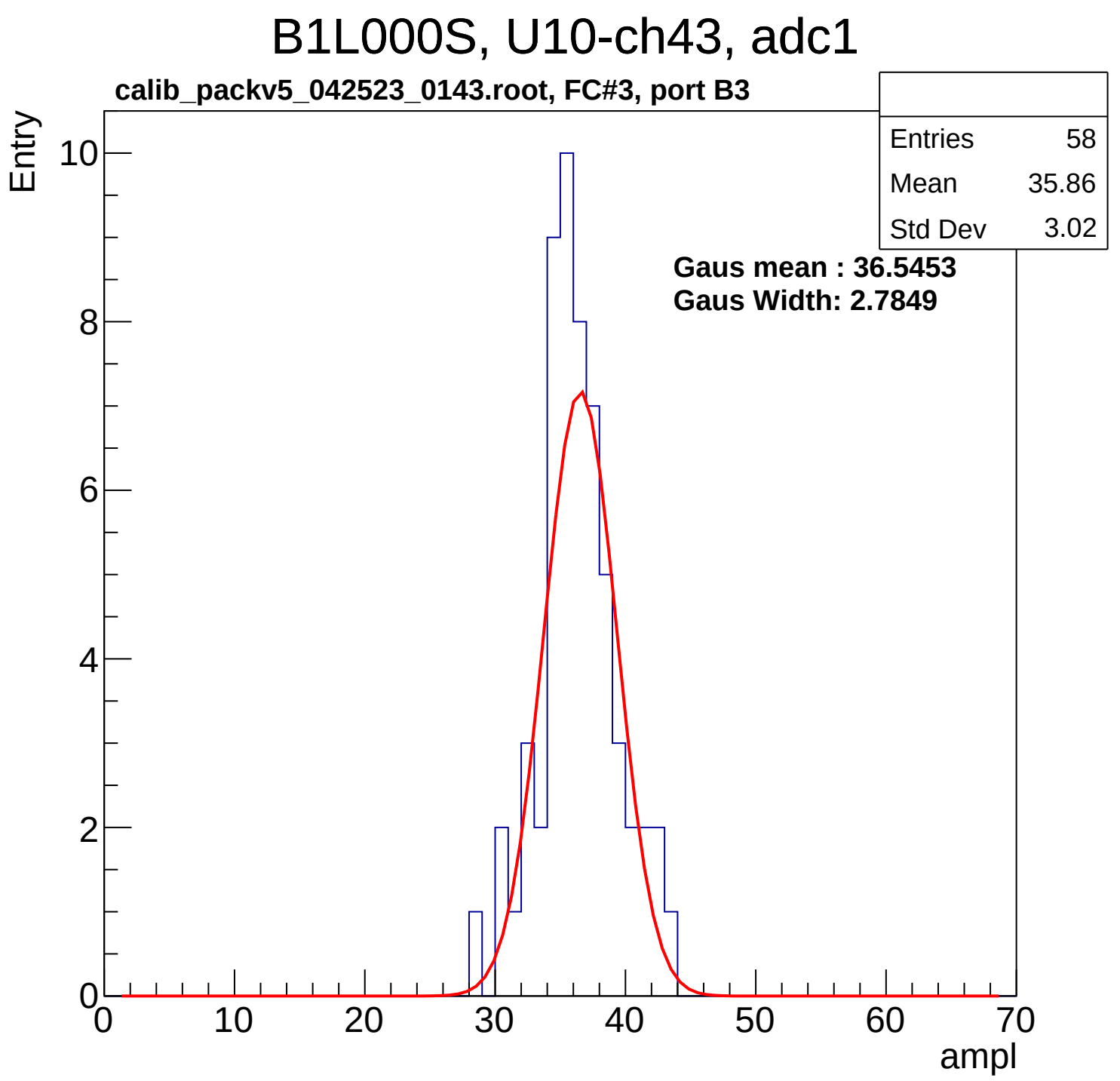
B1L000S, U10-ch43, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.86
Std Dev 3.02
Gaus mean : 36.5453
Gaus Width: 2.7849
Entry
ampl
0
2
4
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0
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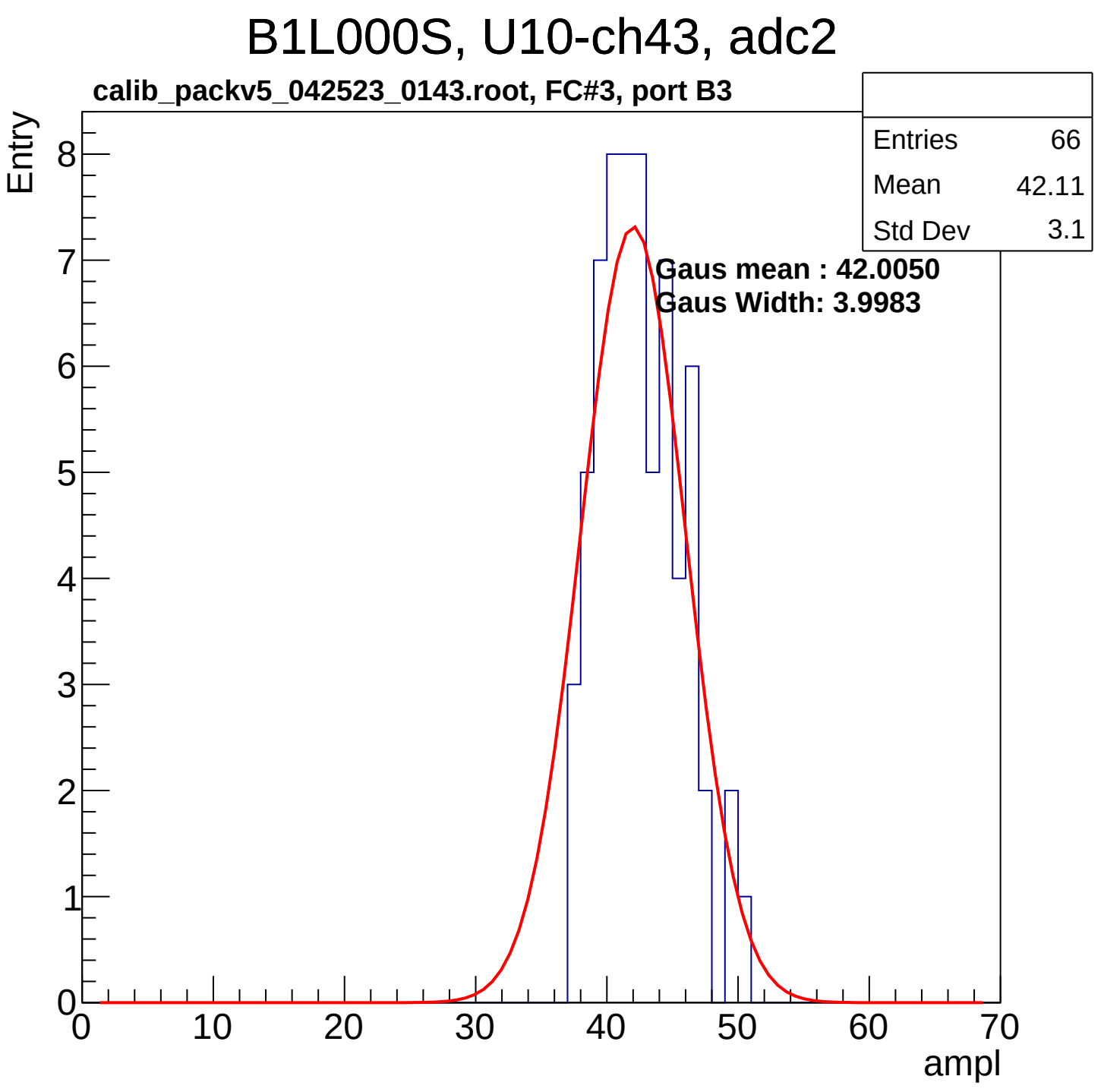
B1L000S, U10-ch43, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 42.11
Std Dev 3.1
Gaus mean : 42.0050
Gaus Width: 3.9983
Entry
ampl
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1
2
3
4
5
6
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0
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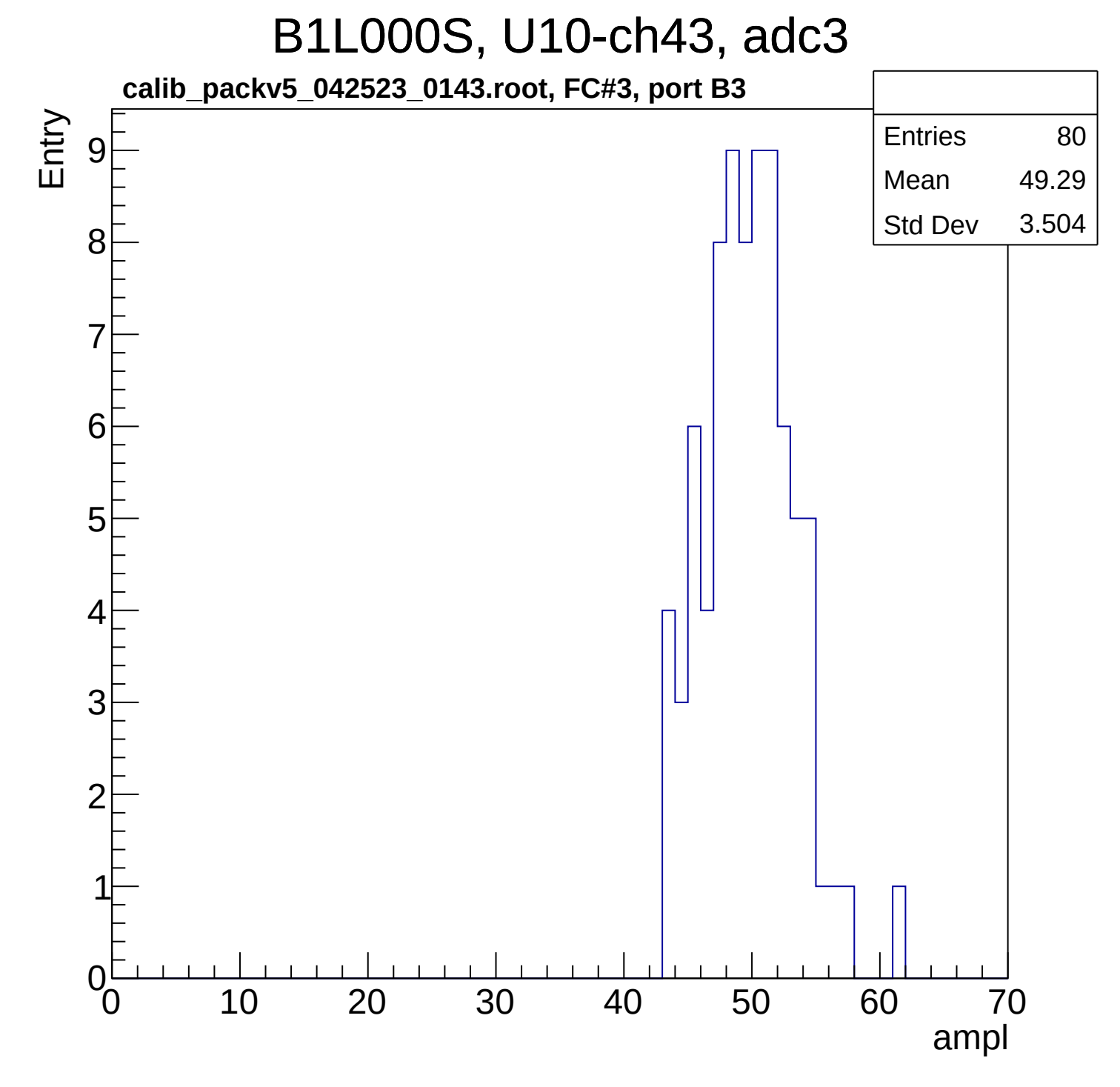
B1L000S, U10-ch43, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 49.29
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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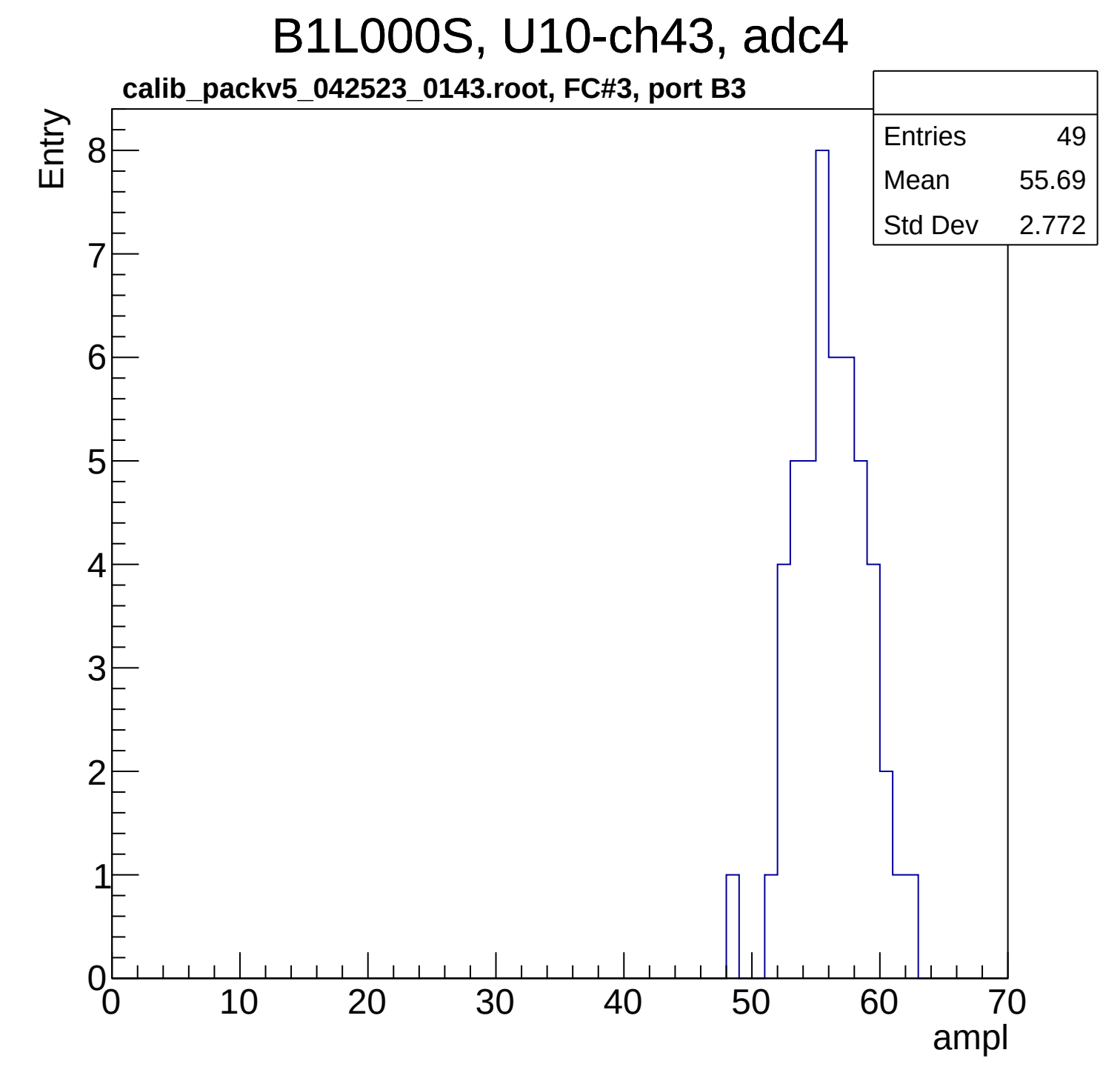
B1L000S, U10-ch43, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 55.69
Std Dev 2.772
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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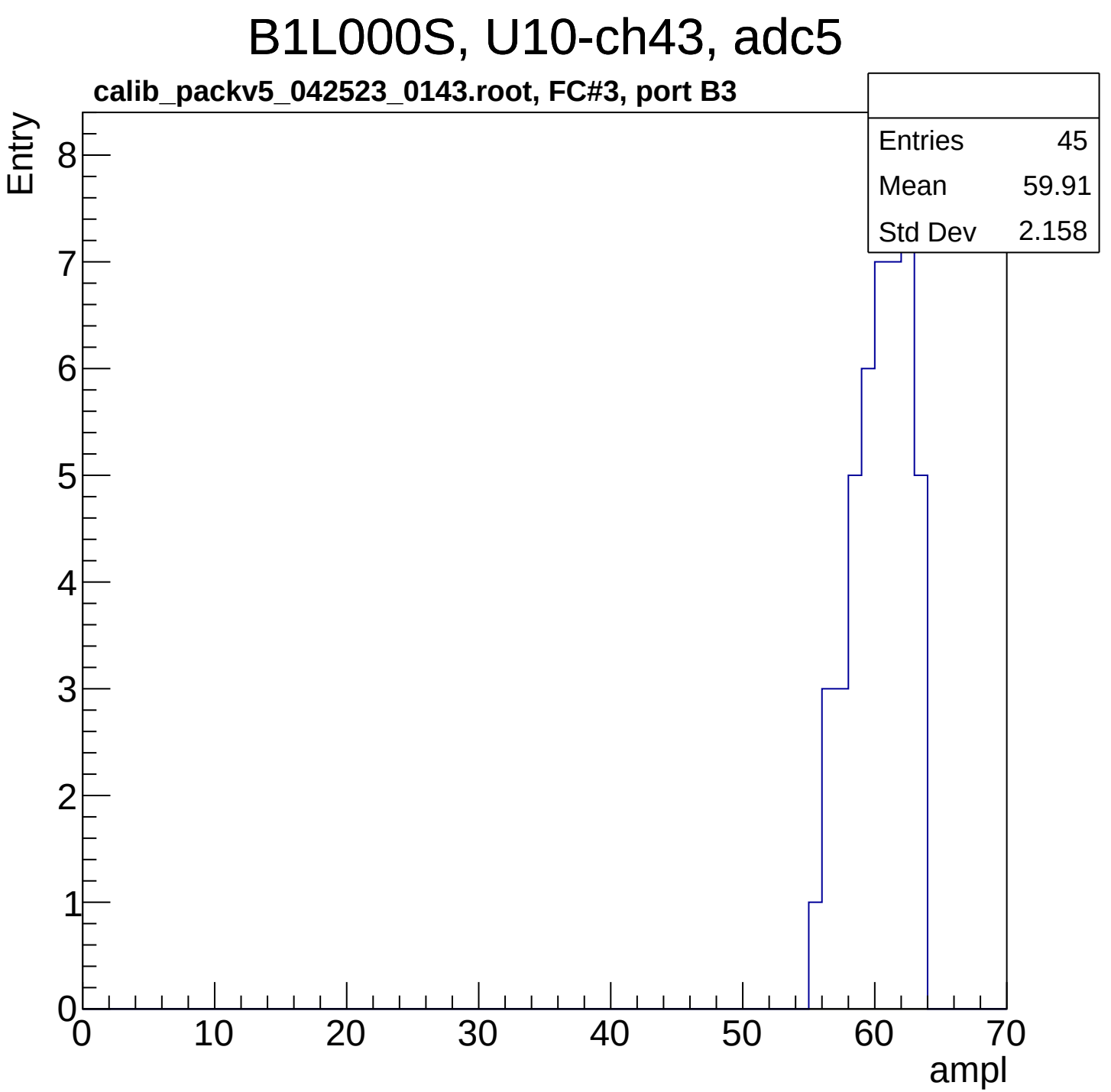
B1L000S, U10-ch43, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 59.91
Std Dev 2.158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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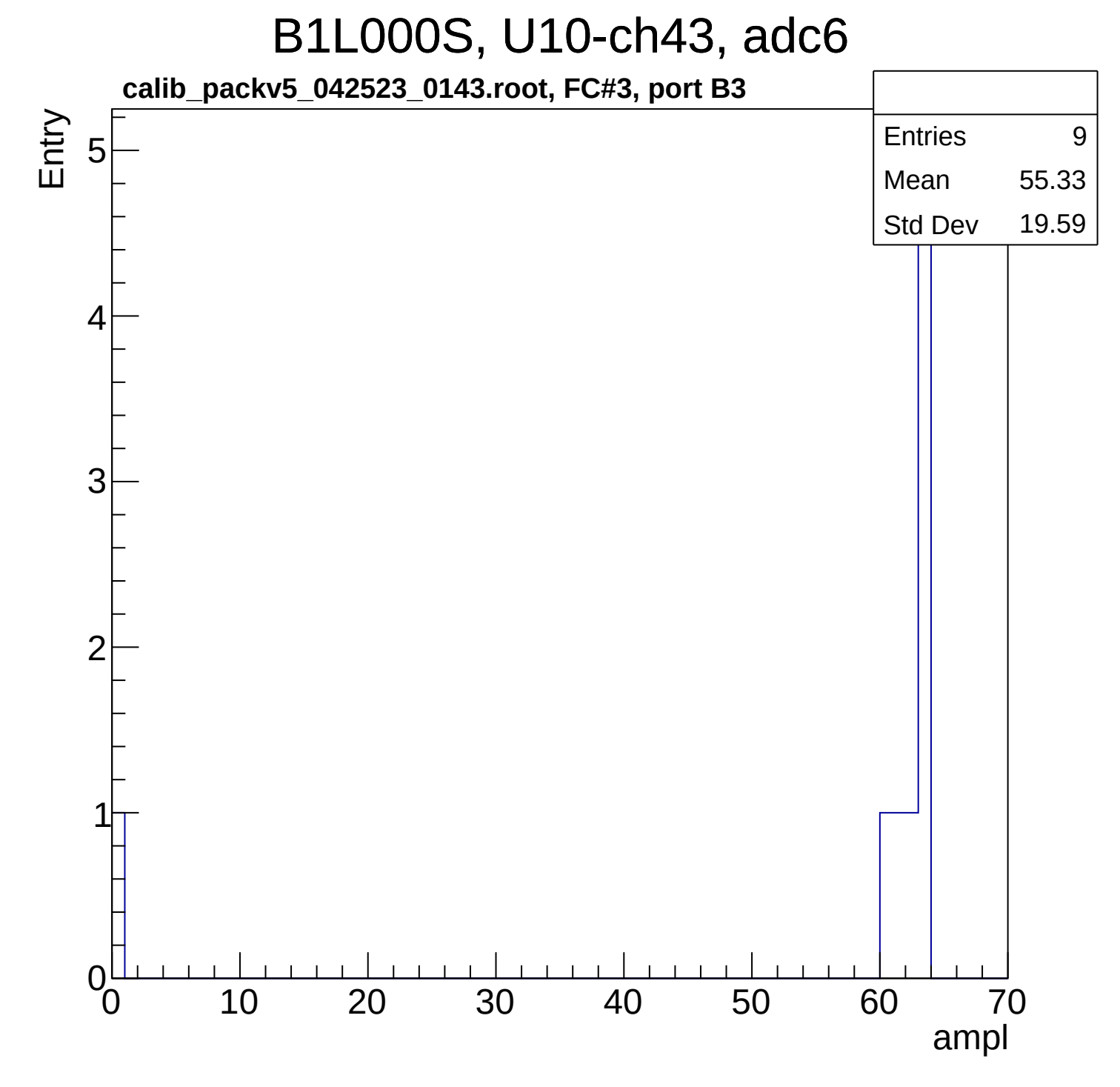
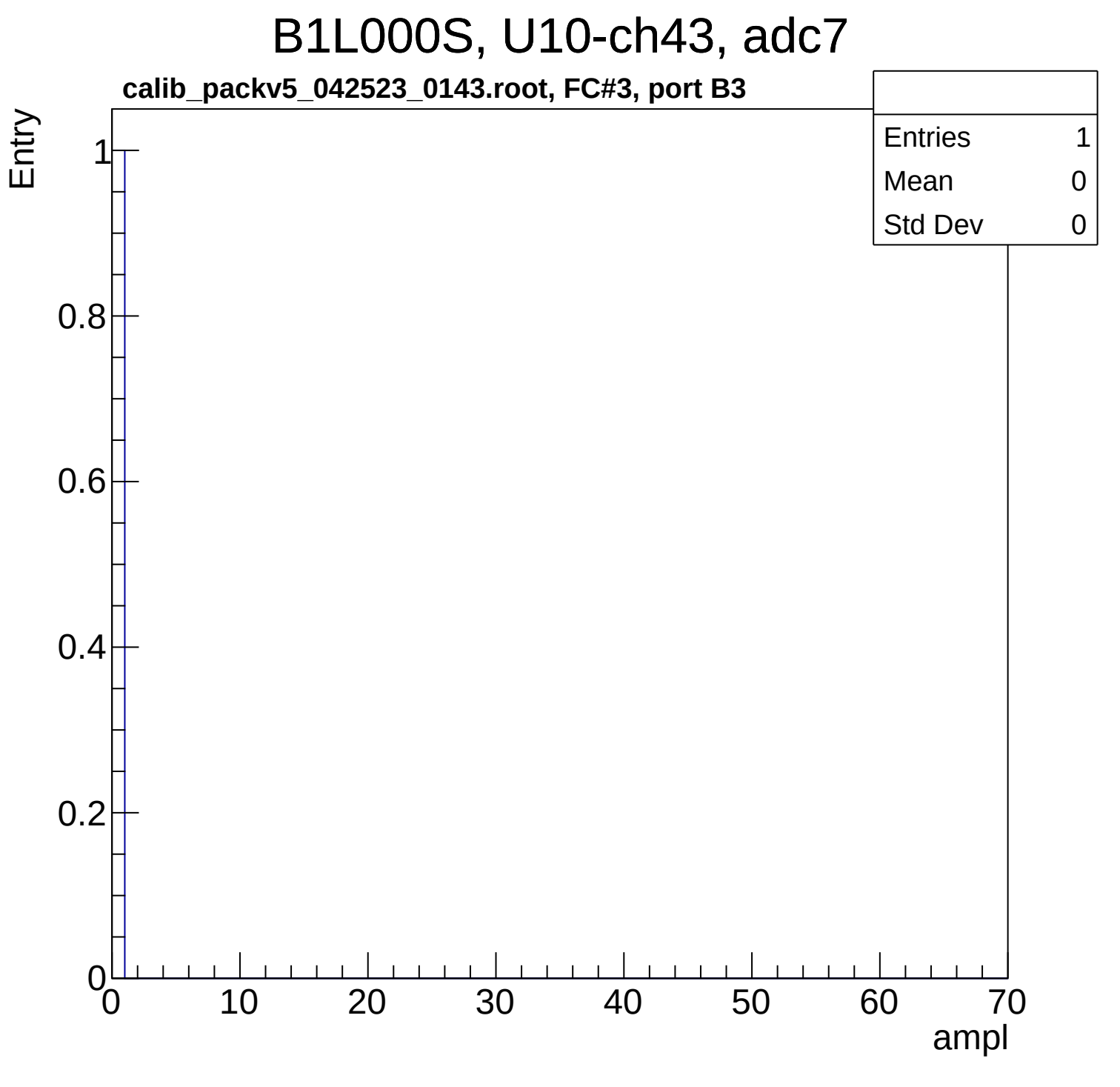
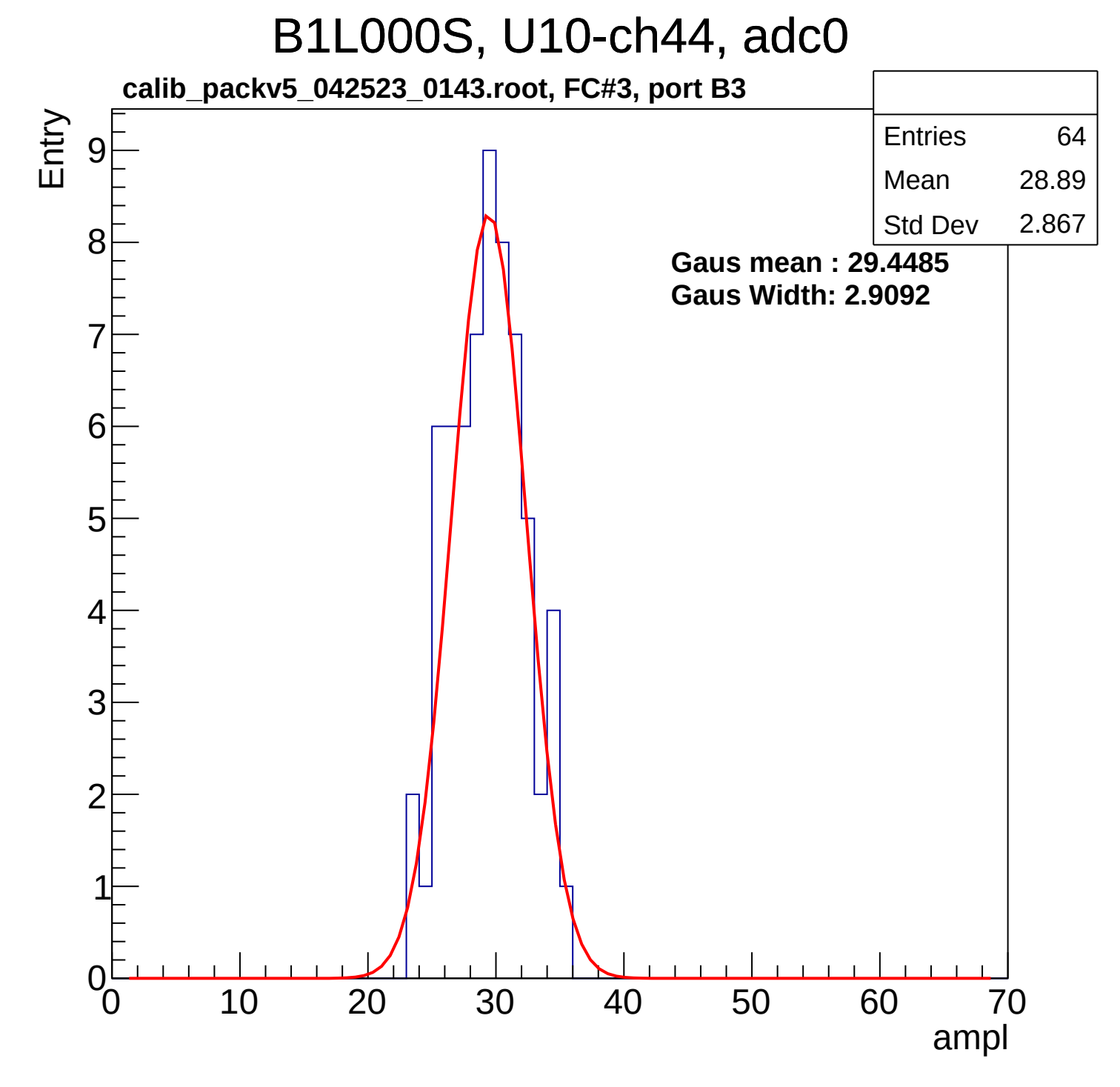

B1L000S, U10-ch43, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 55.33
Std Dev 19.59
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch43, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch44, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.89
Std Dev 2.867
Gaus mean : 29.4485
Gaus Width: 2.9092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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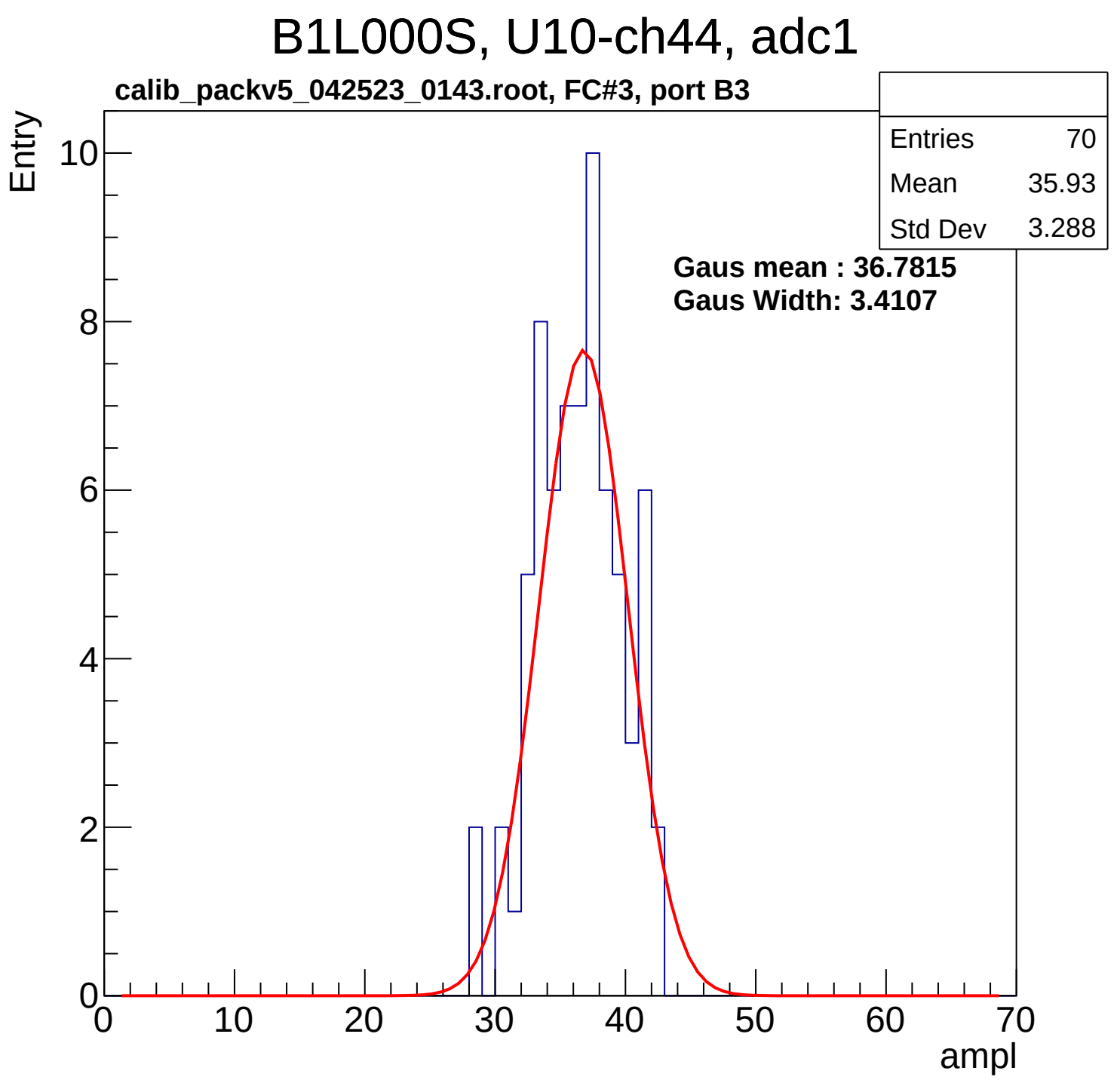
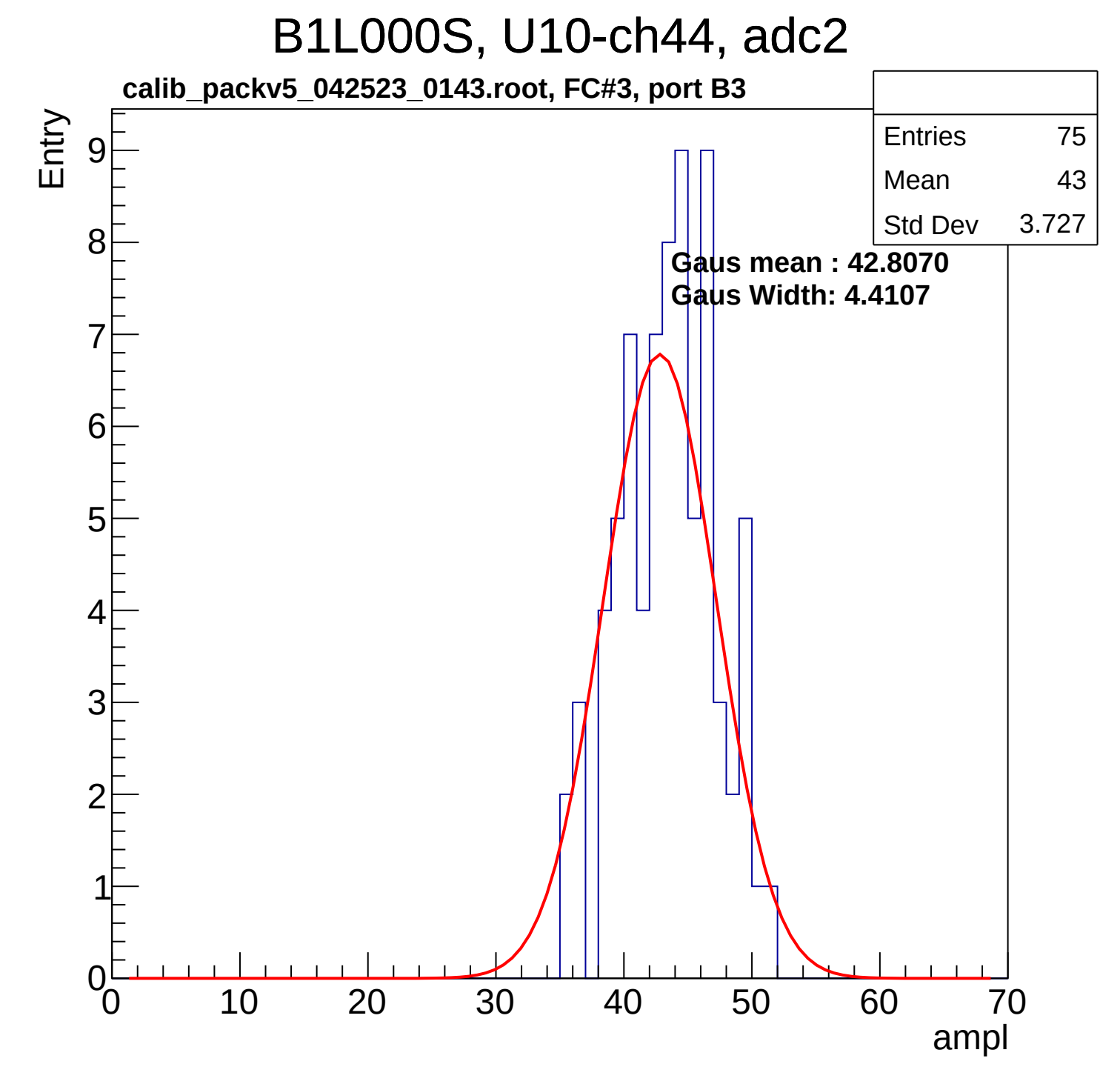

B1L000S, U10-ch44, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 35.93
Std Dev 3.288
Gaus mean : 36.7815
Gaus Width: 3.4107
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch44, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 43
Std Dev 3.727
Gaus mean : 42.8070
Gaus Width: 4.4107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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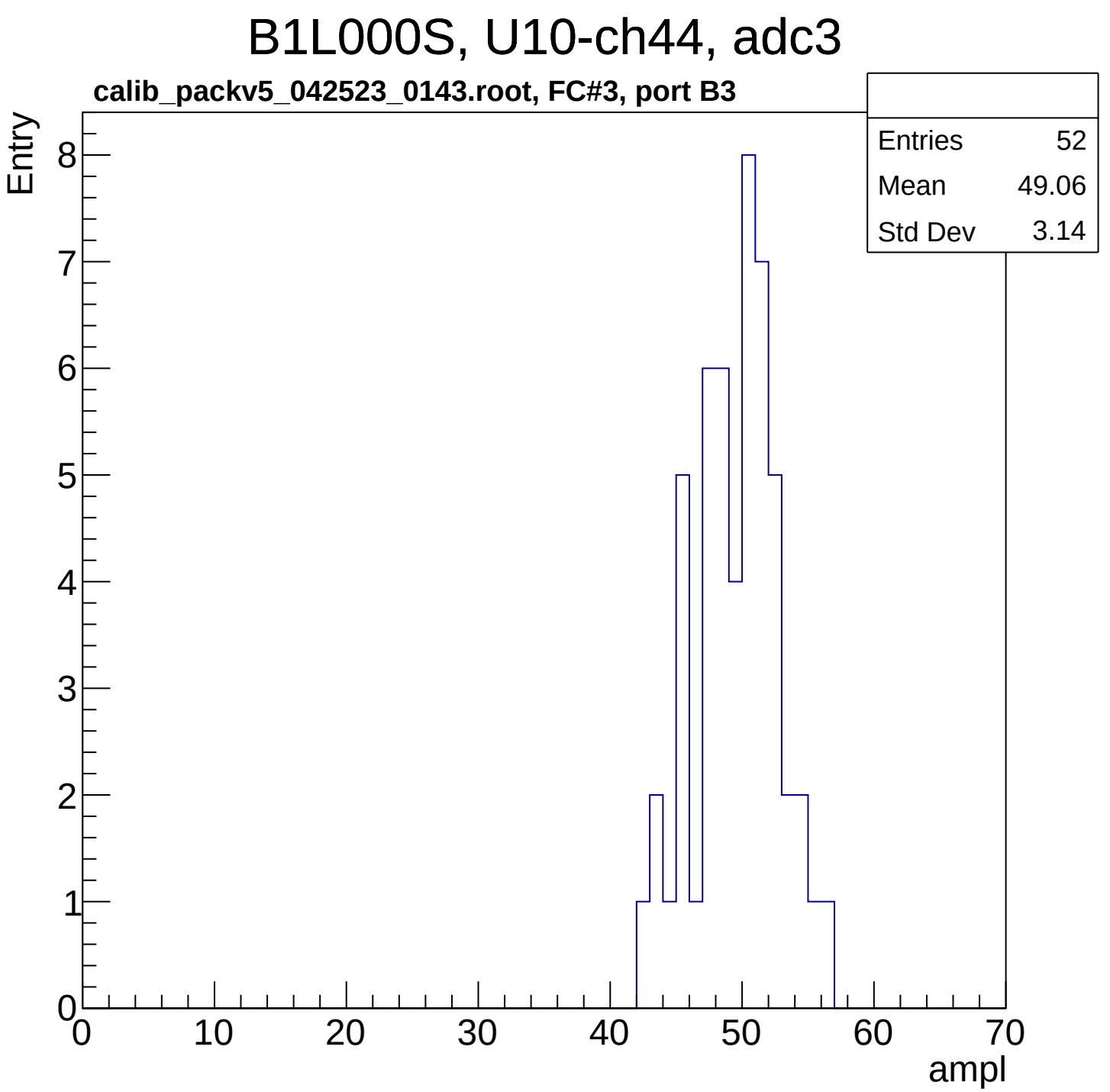
B1L000S, U10-ch44, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 49.06
Std Dev 3.14
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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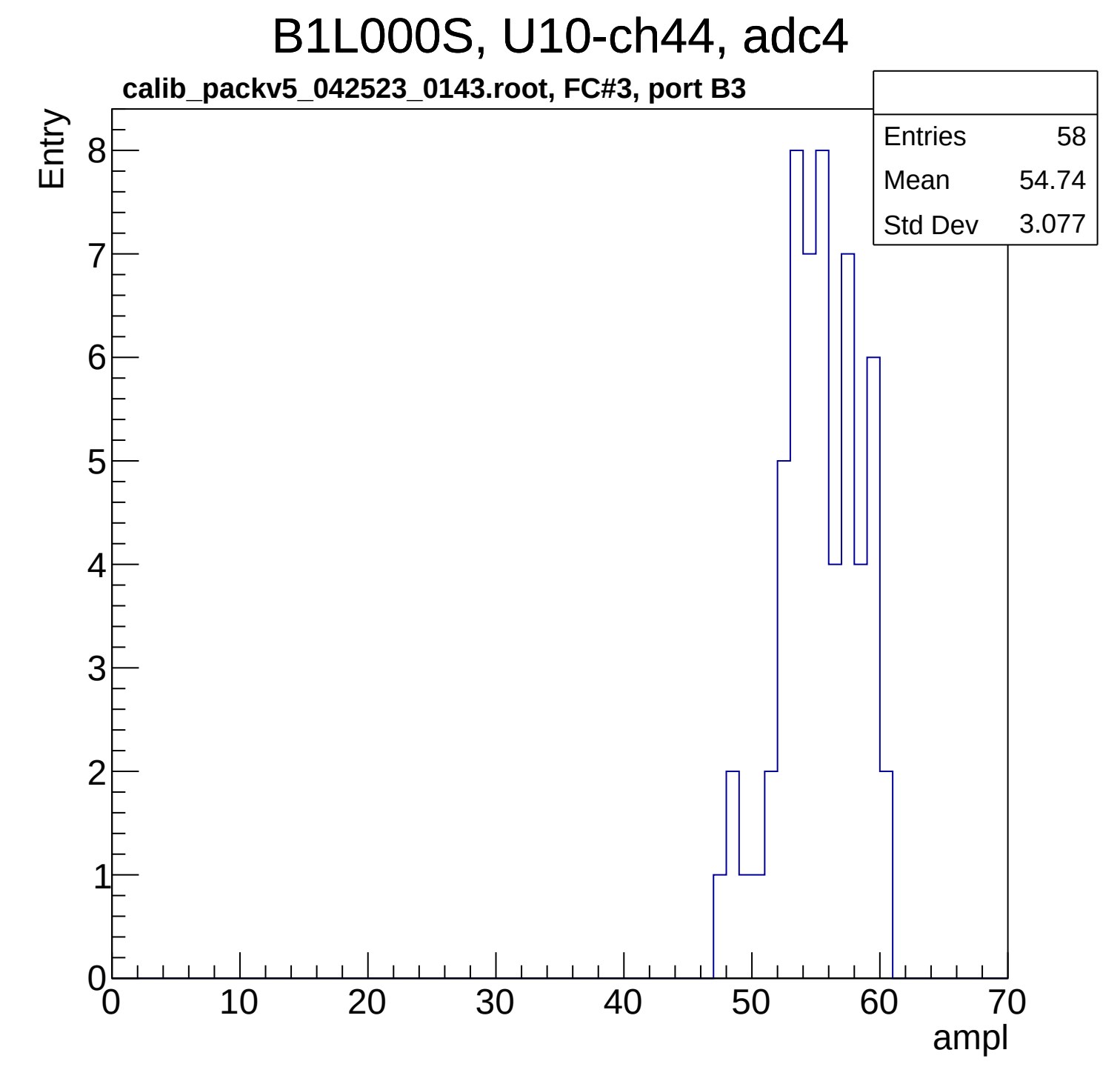
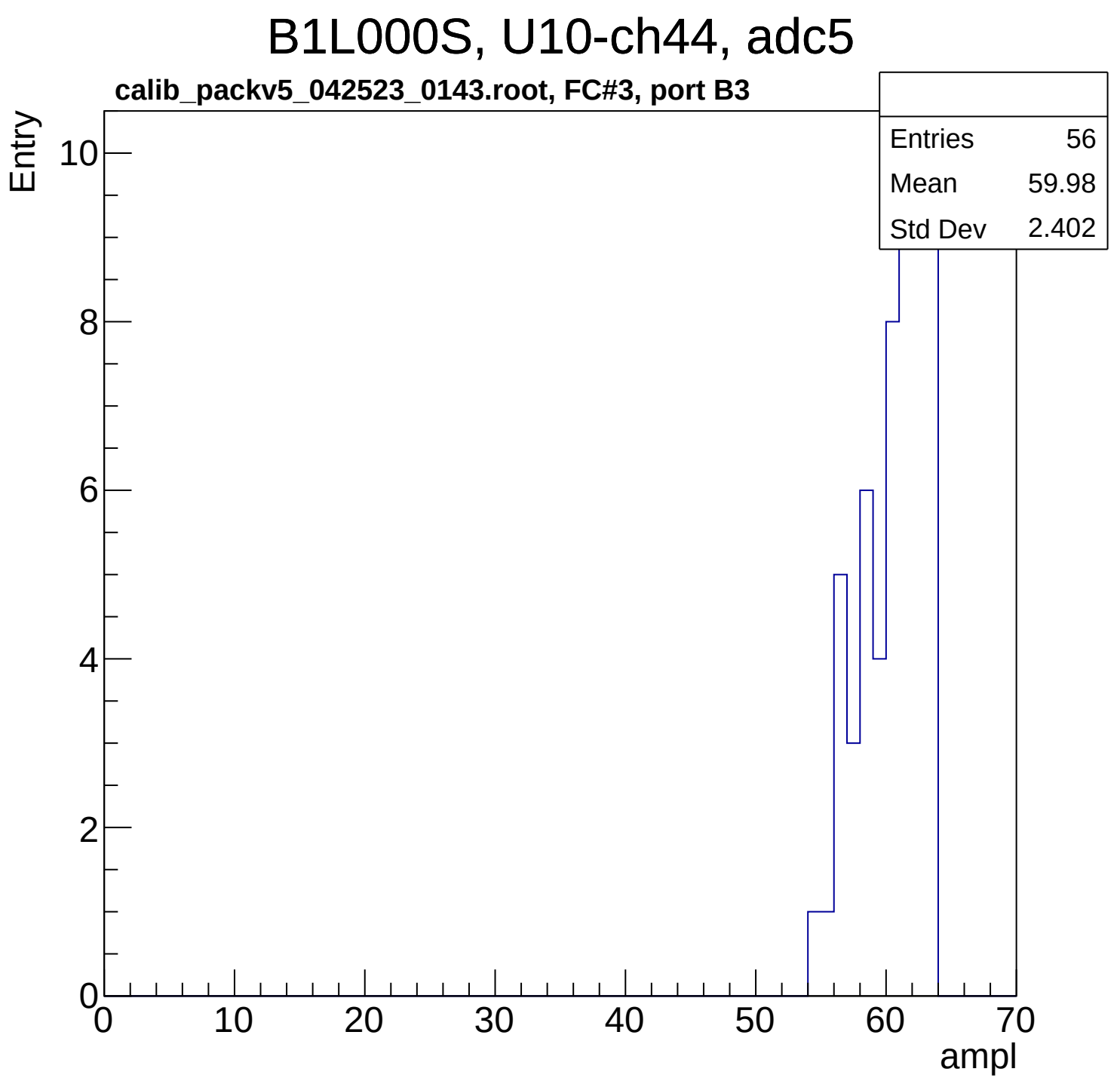
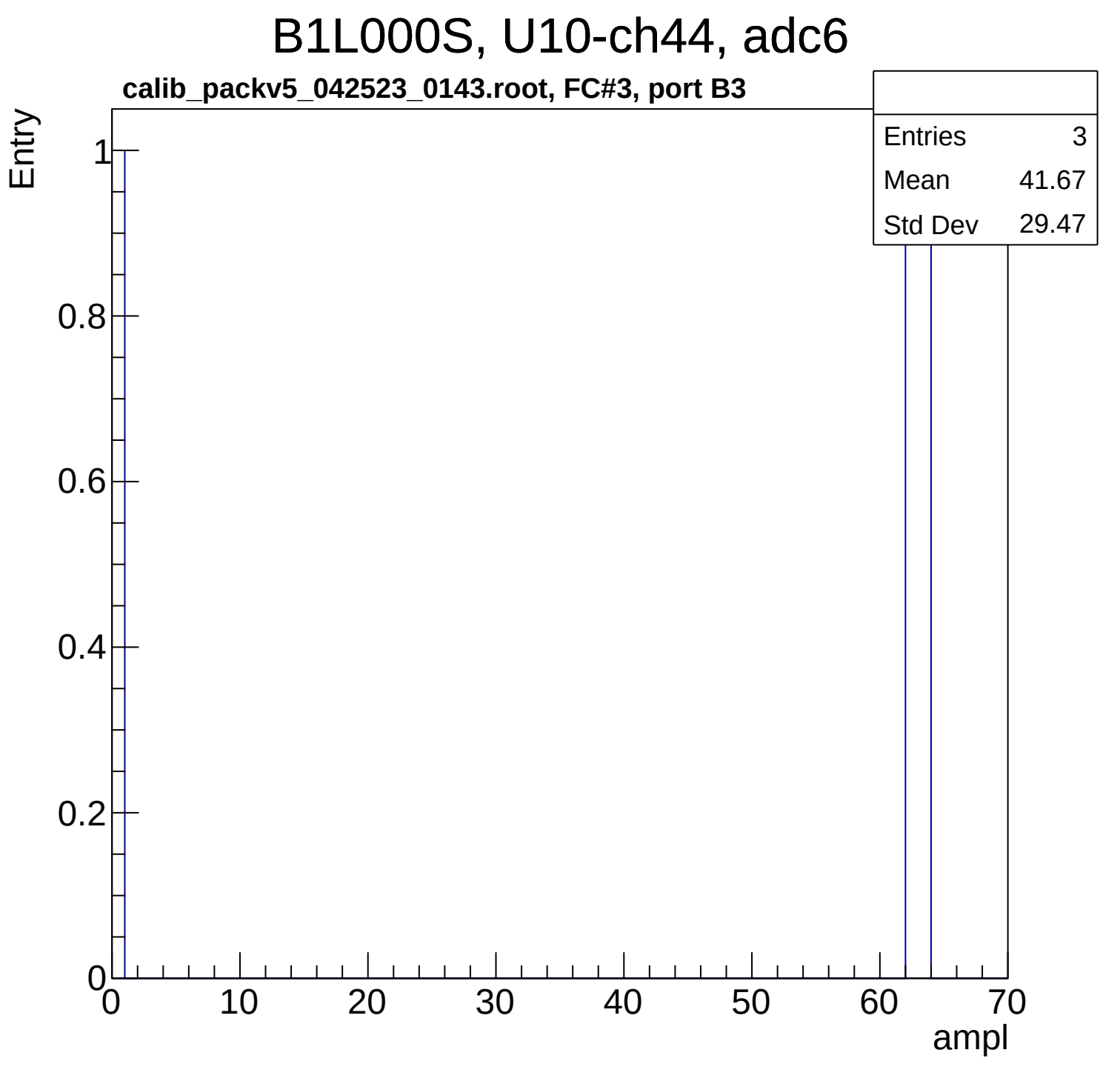
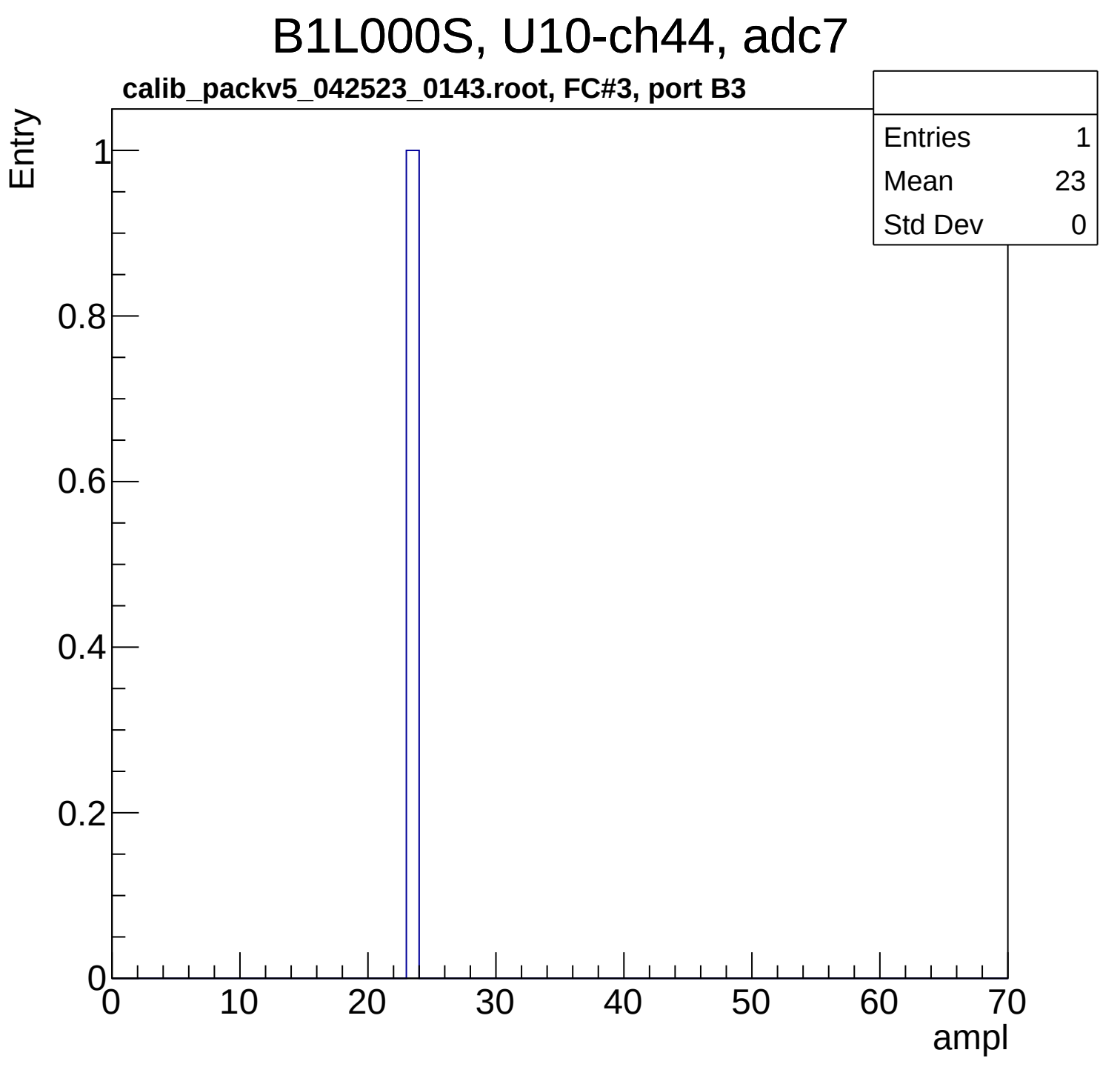

B1L000S, U10-ch44, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.74
Std Dev 3.077
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch44, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 59.98
Std Dev 2.402
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch44, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch44, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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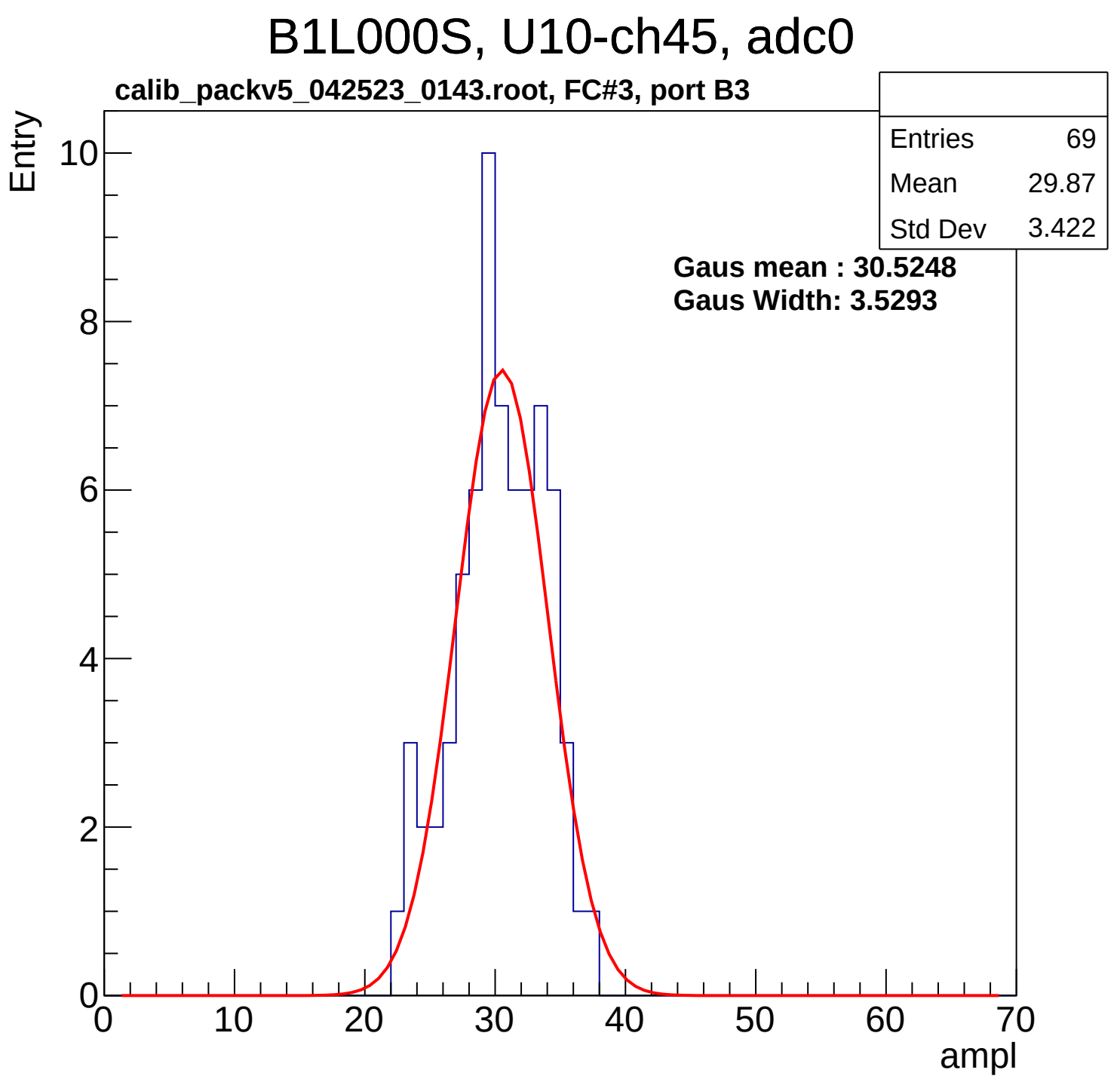
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch45, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 29.87
Std Dev 3.422
Gaus mean : 30.5248
Gaus Width: 3.5293
Entry
ampl
0
2
4
6
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0
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60
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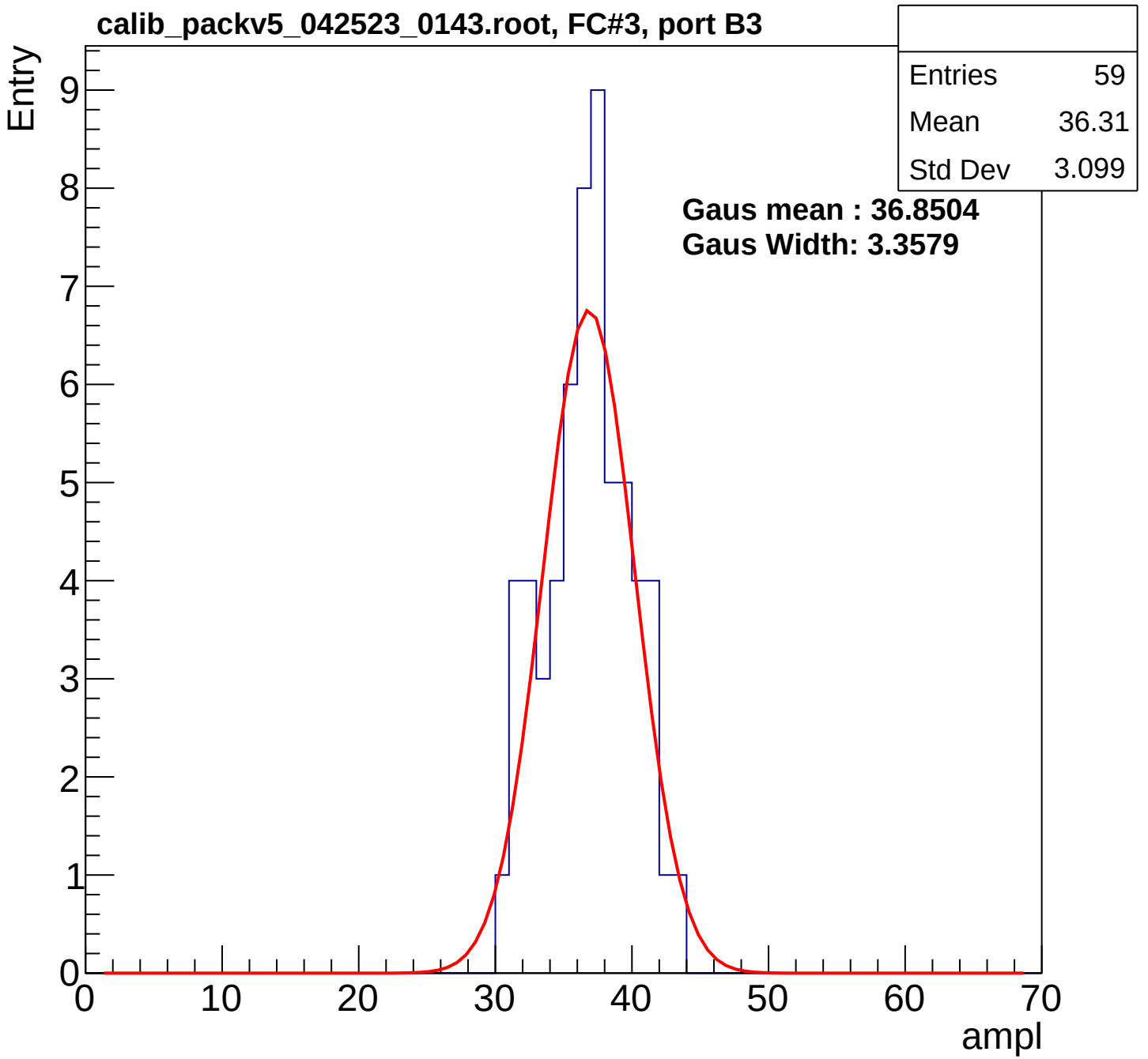
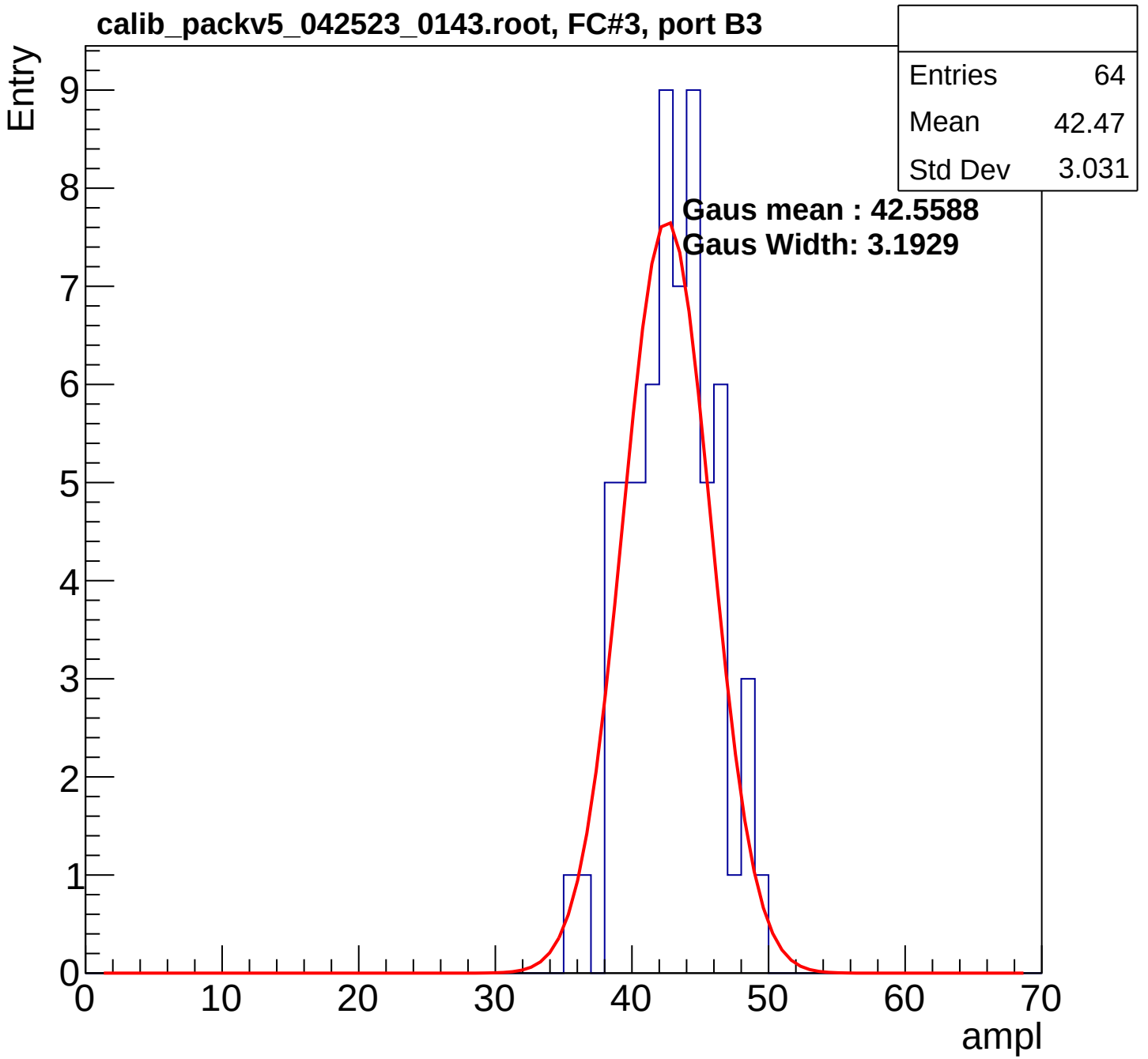

B1L000S, U10-ch45, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 36.31
Std Dev 3.099
Gaus mean : 36.8504
Gaus Width: 3.3579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch45, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.47
Std Dev 3.031
Gaus mean : 42.5588
Gaus Width: 3.1929
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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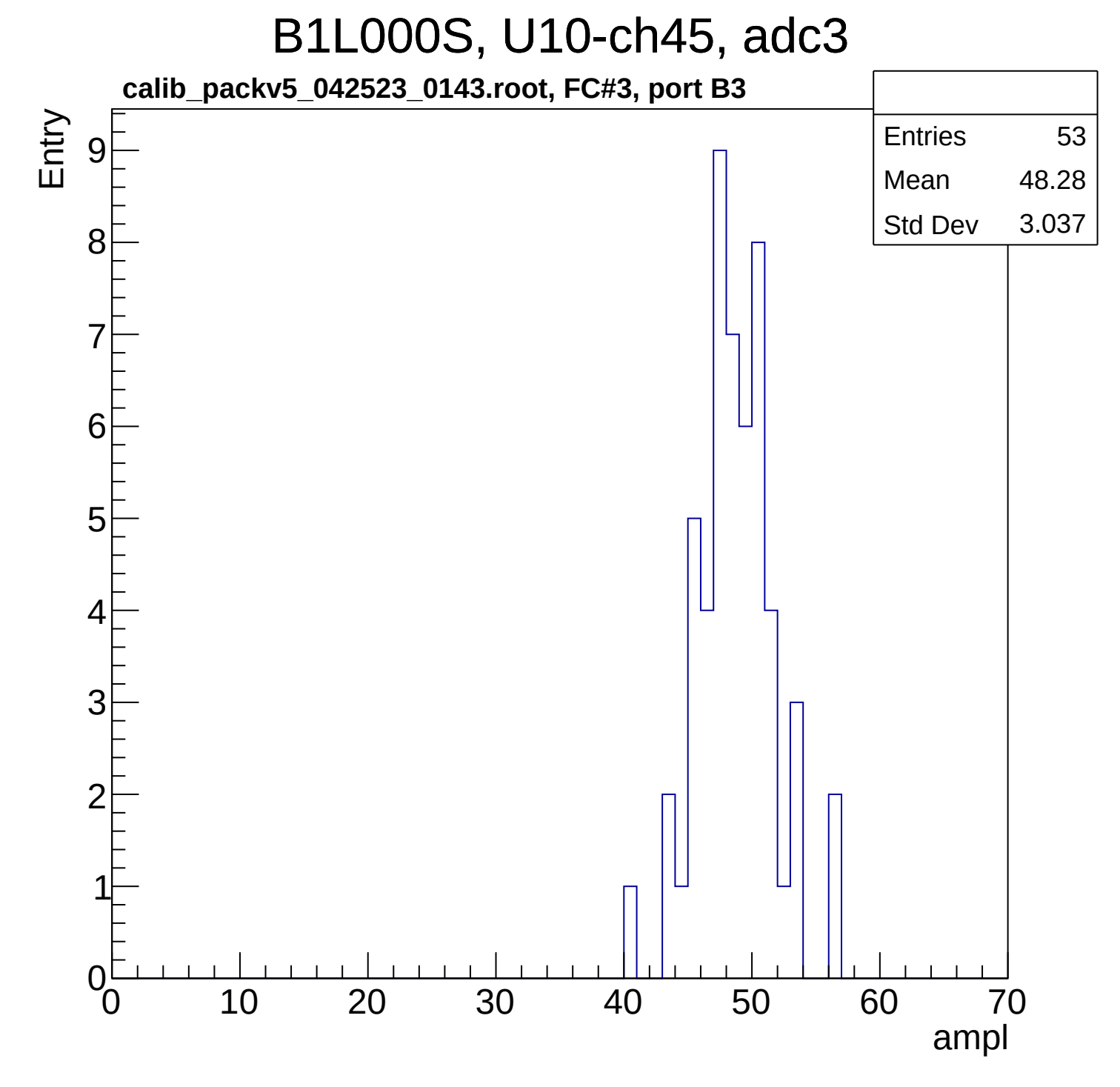
60
70


B1L000S, U10-ch45, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 48.28
Std Dev 3.037
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
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60
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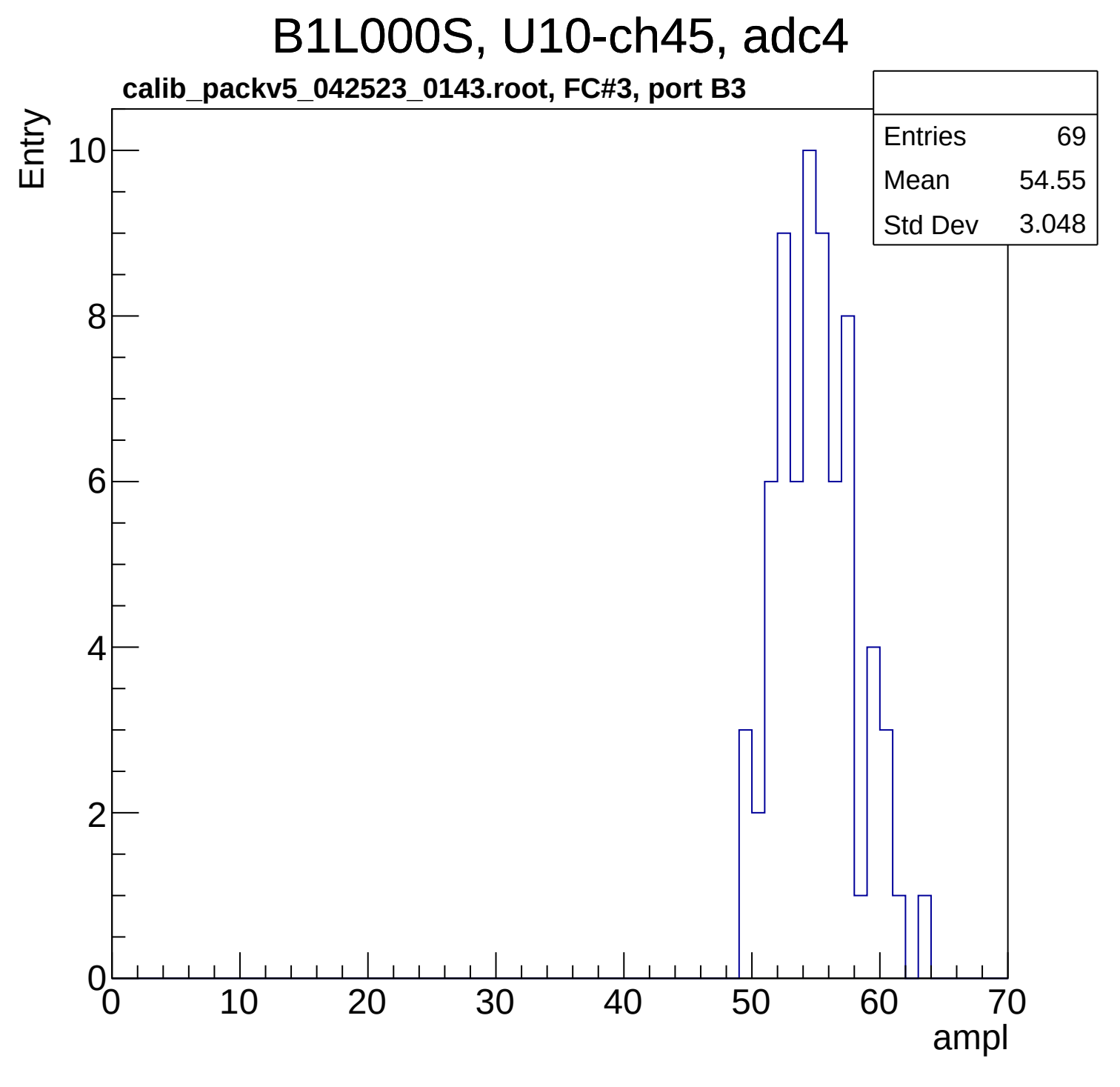
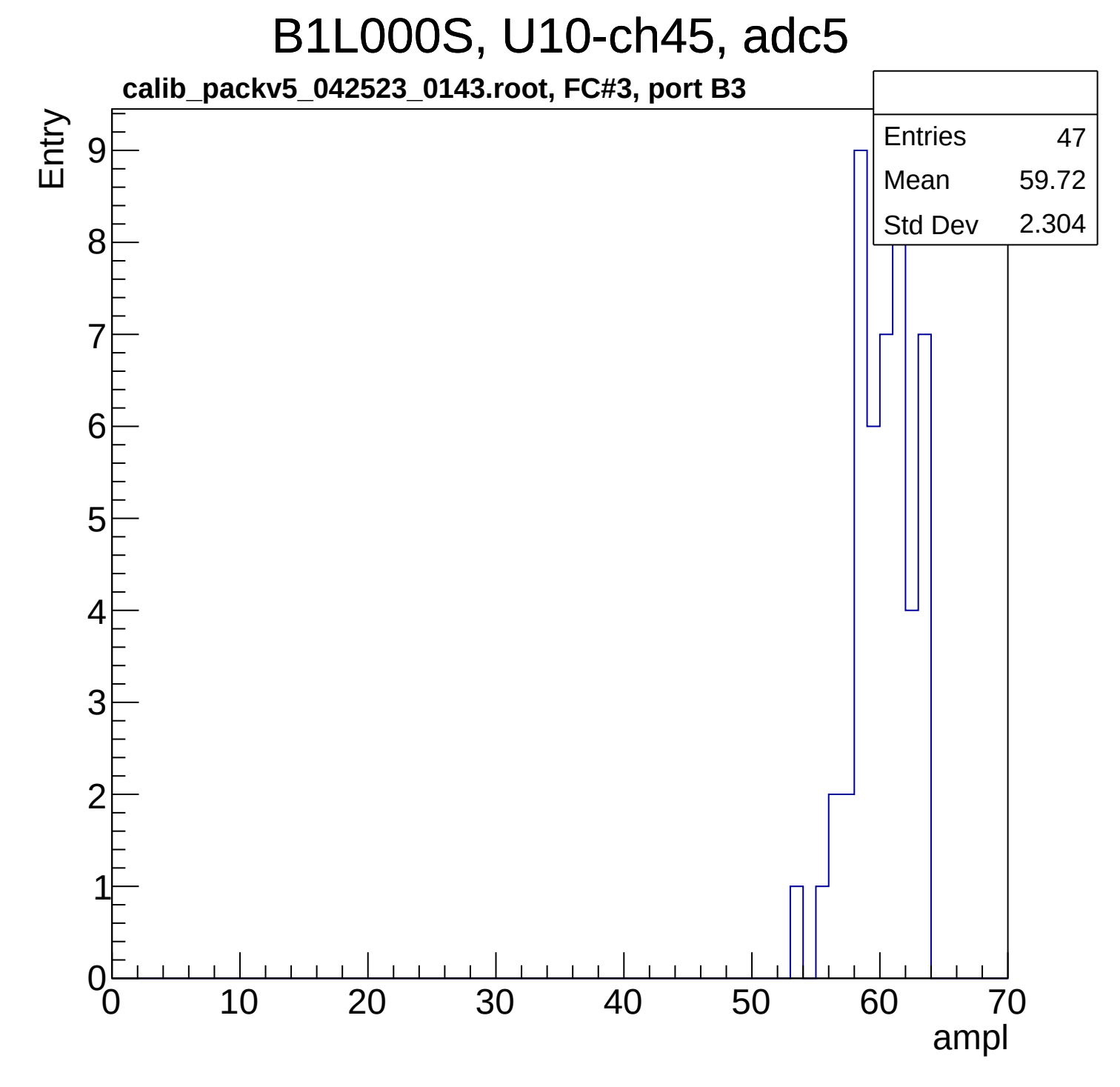

B1L000S, U10-ch45, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 54.55
Std Dev 3.048
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch45, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.72
Std Dev 2.304
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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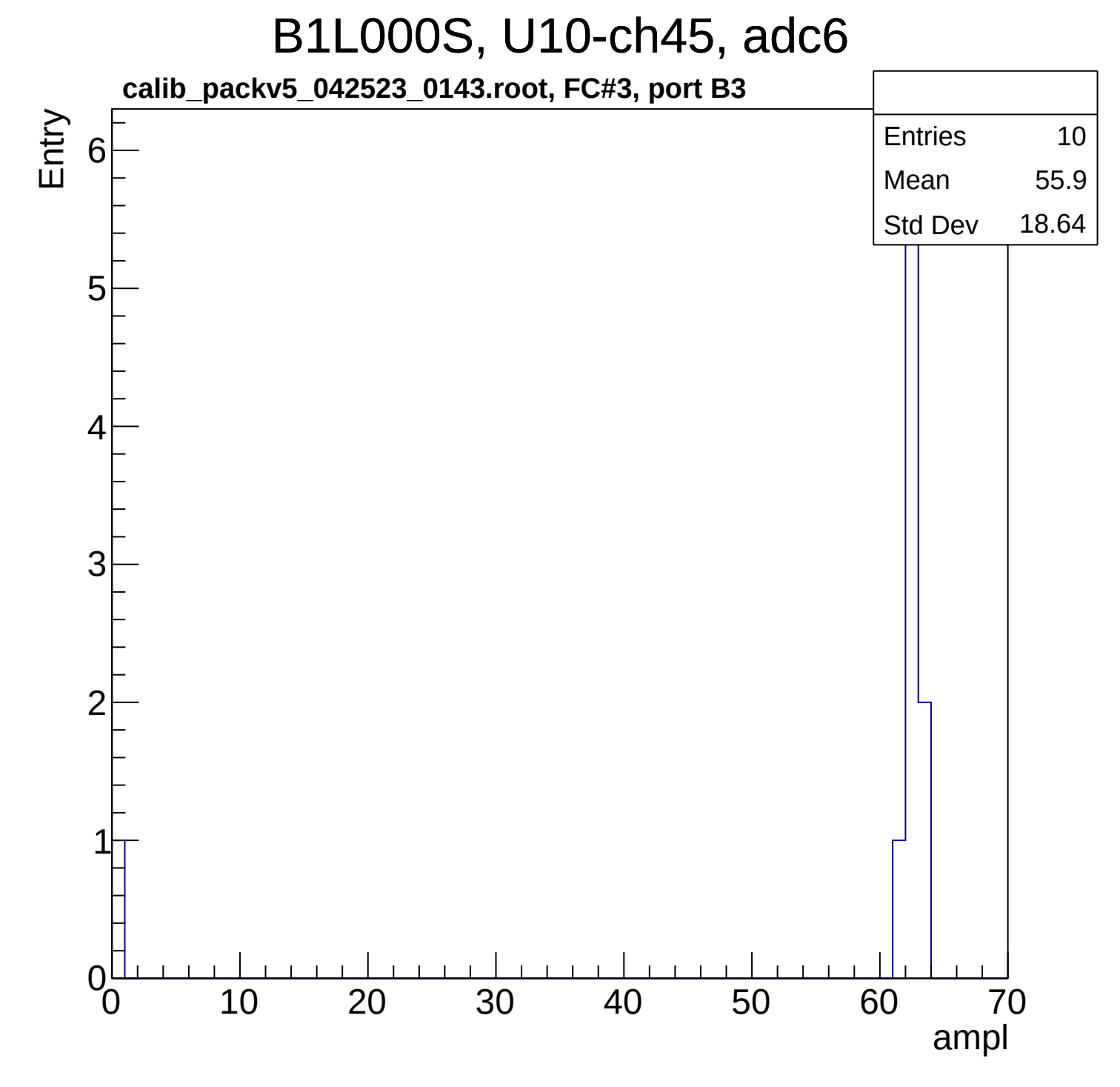
B1L000S, U10-ch45, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.9
Std Dev 18.64
Entry
0
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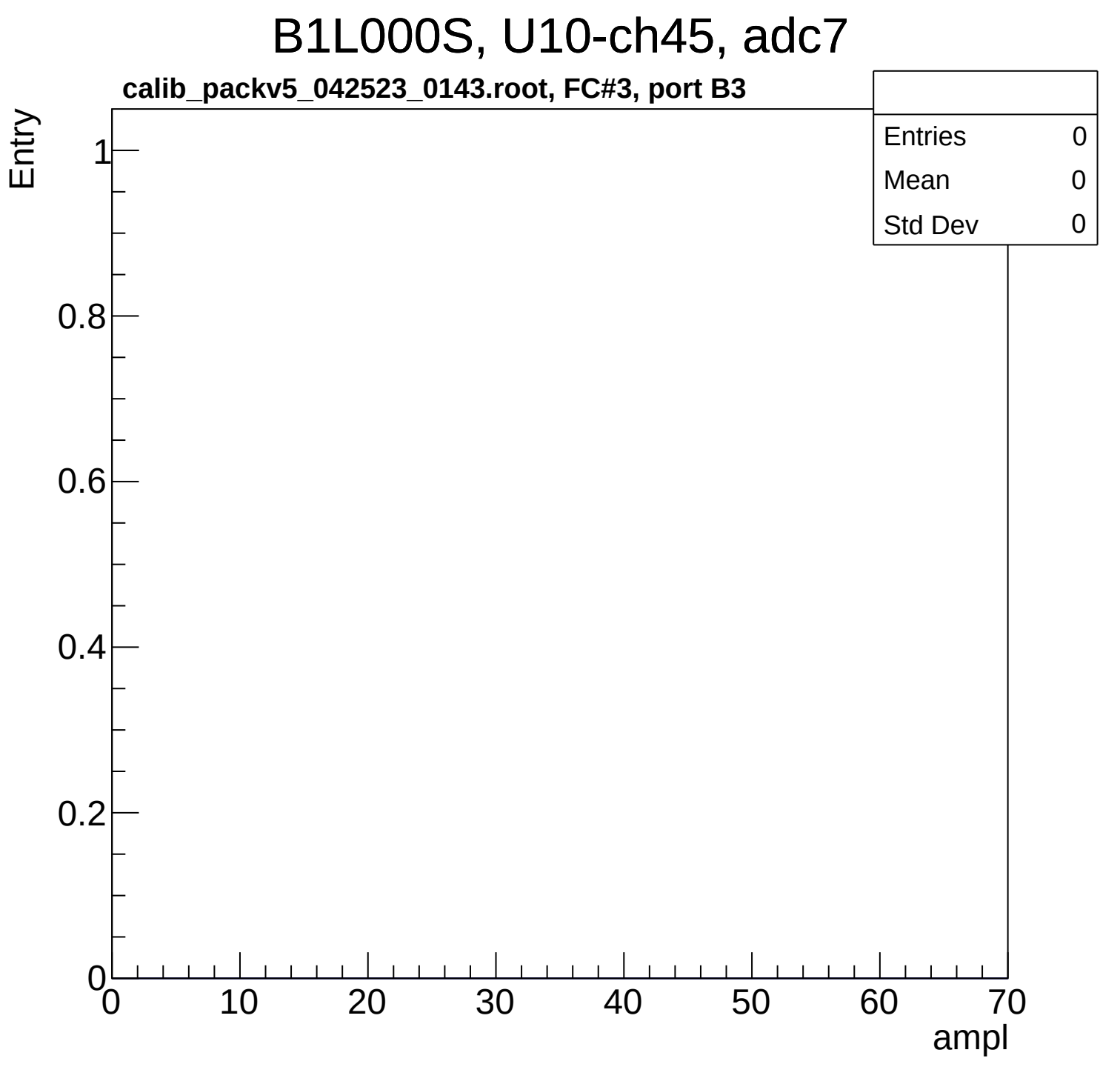
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch45, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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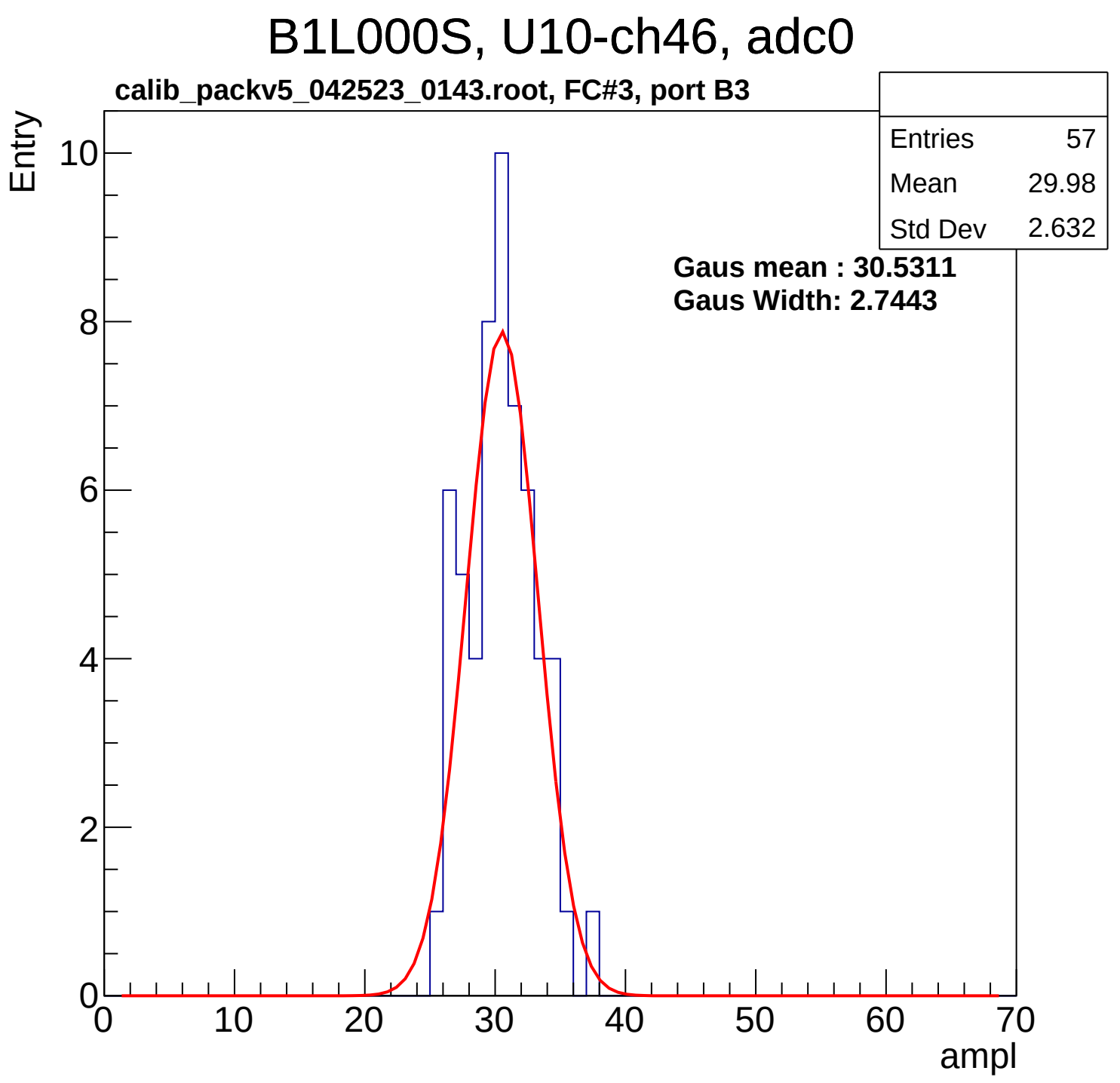
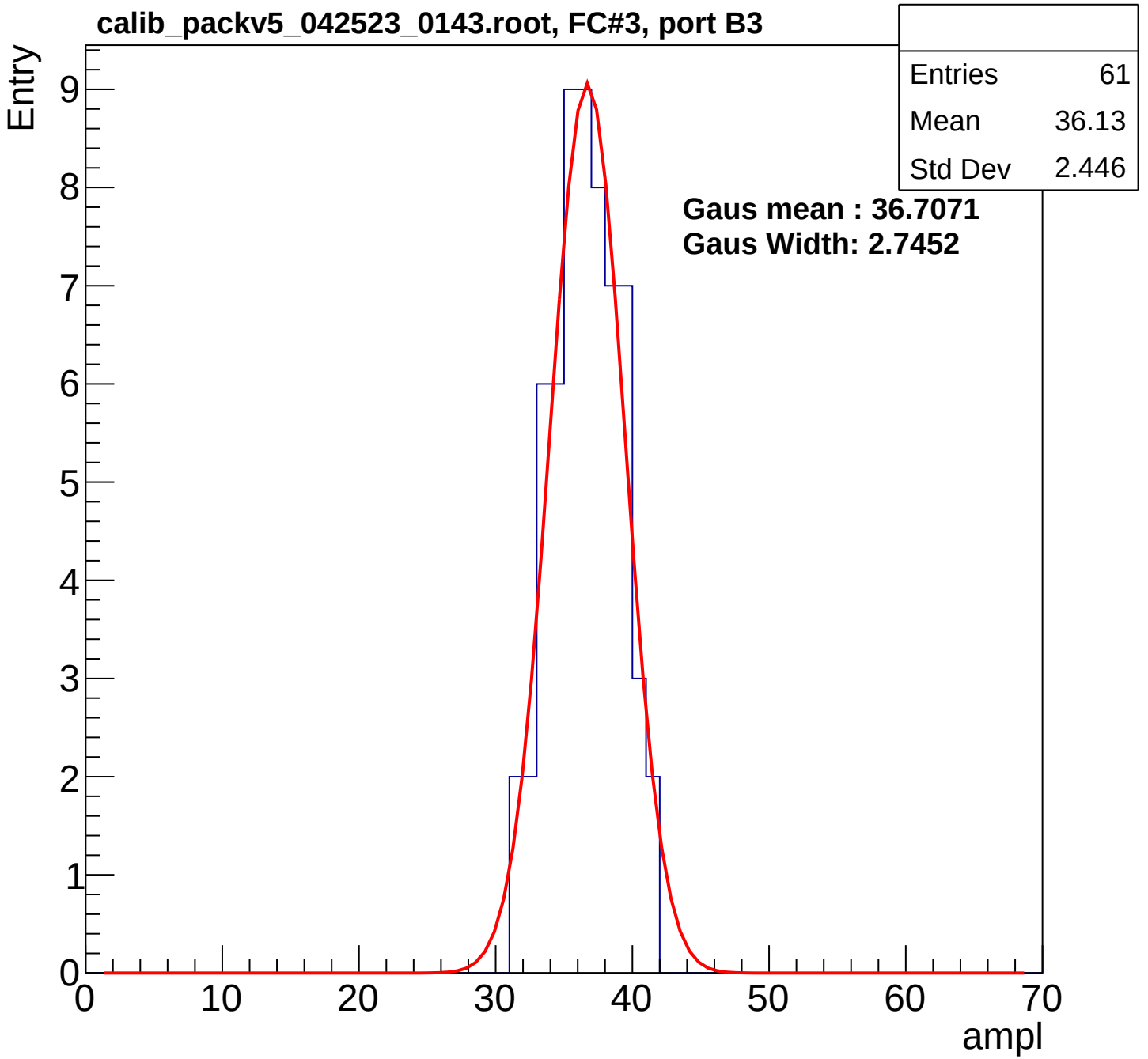
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch46, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 29.98
Std Dev 2.632
Gaus mean : 30.5311
Gaus Width: 2.7443
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch46, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 36.13
Std Dev 2.446
Gaus mean : 36.7071
Gaus Width: 2.7452
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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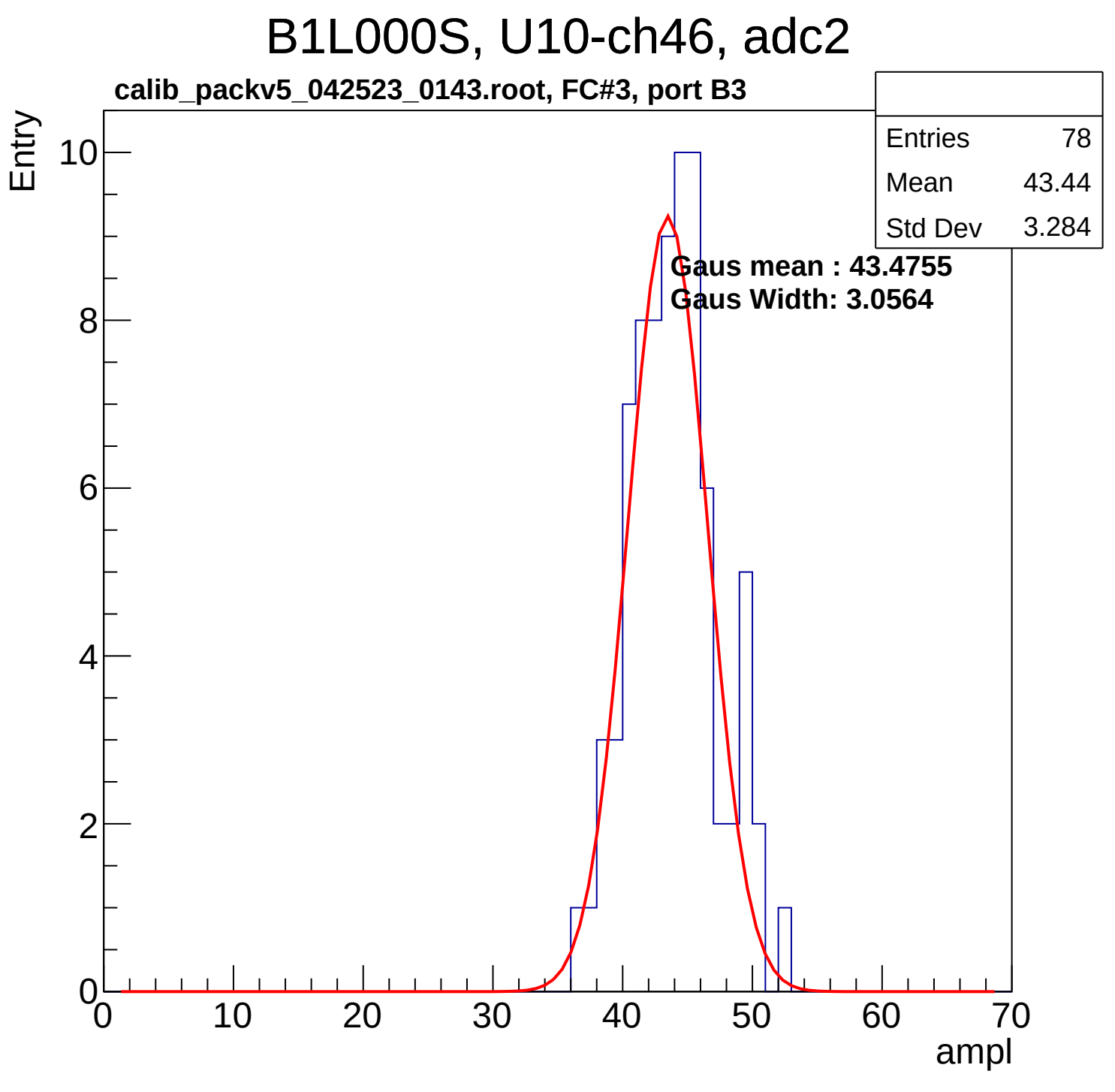
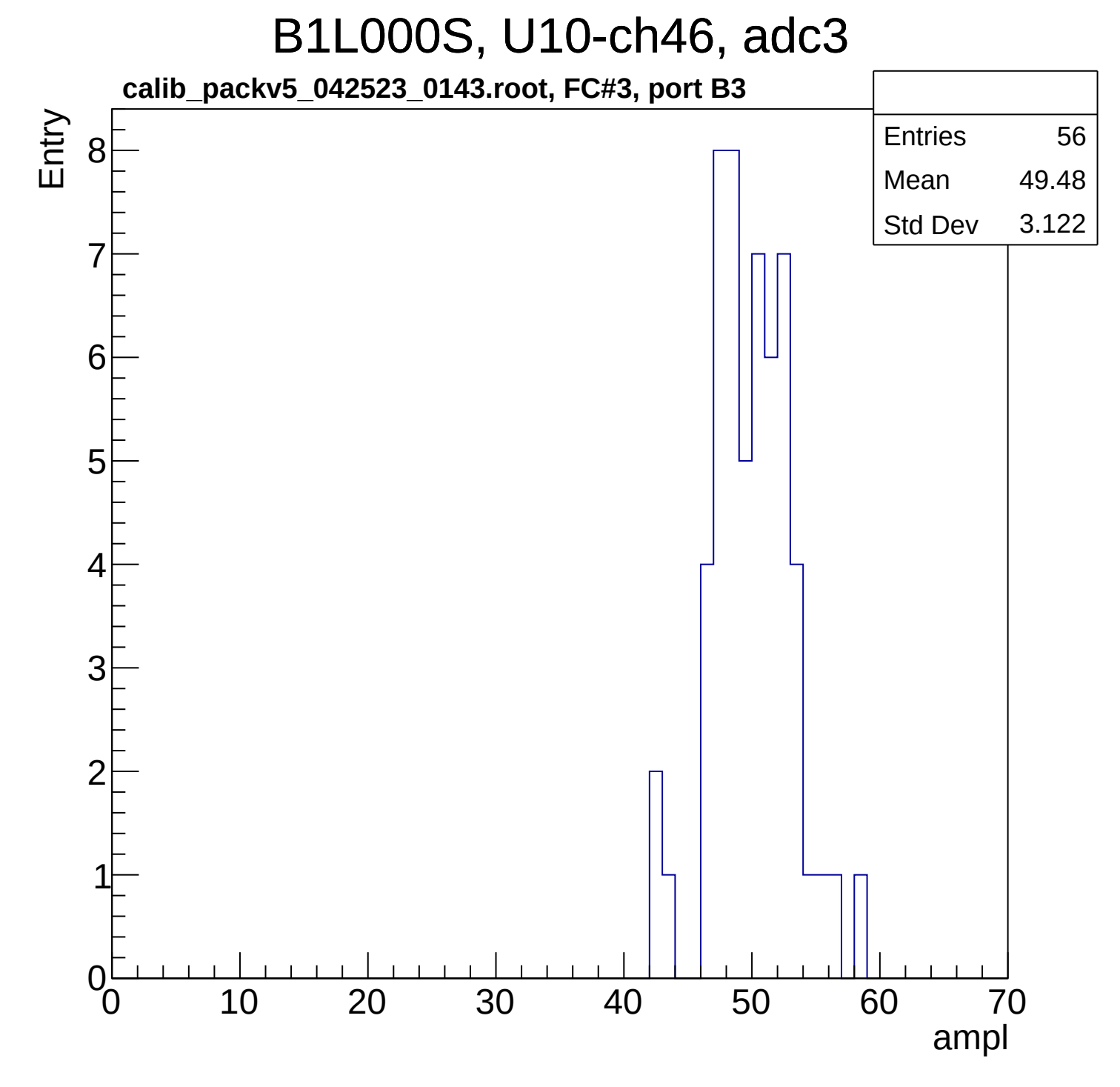
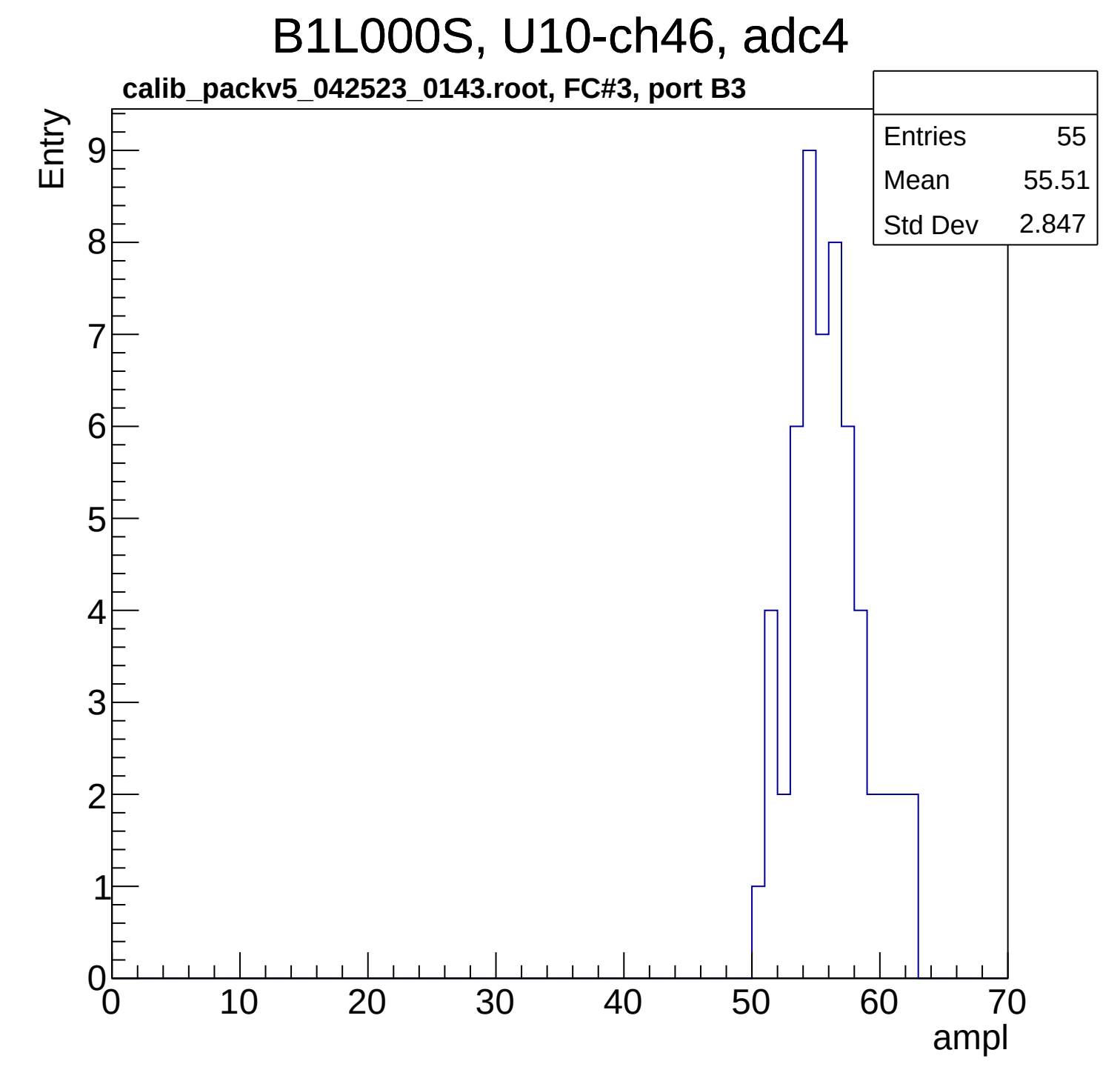

B1L000S, U10-ch46, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 43.44
Std Dev 3.284
Gaus mean : 43.4755
Gaus Width: 3.0564
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch46, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 49.48
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch46, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 55.51
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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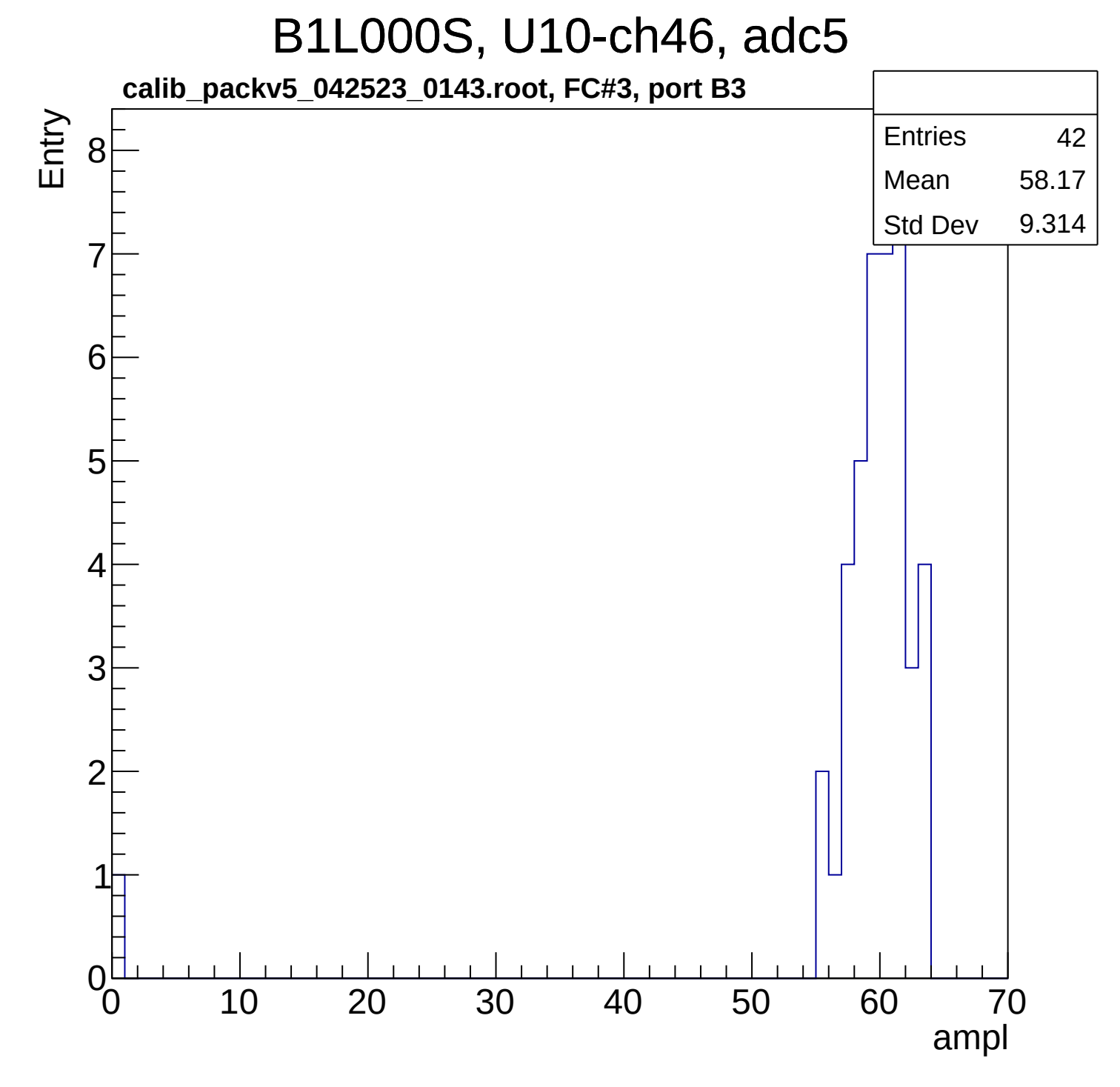
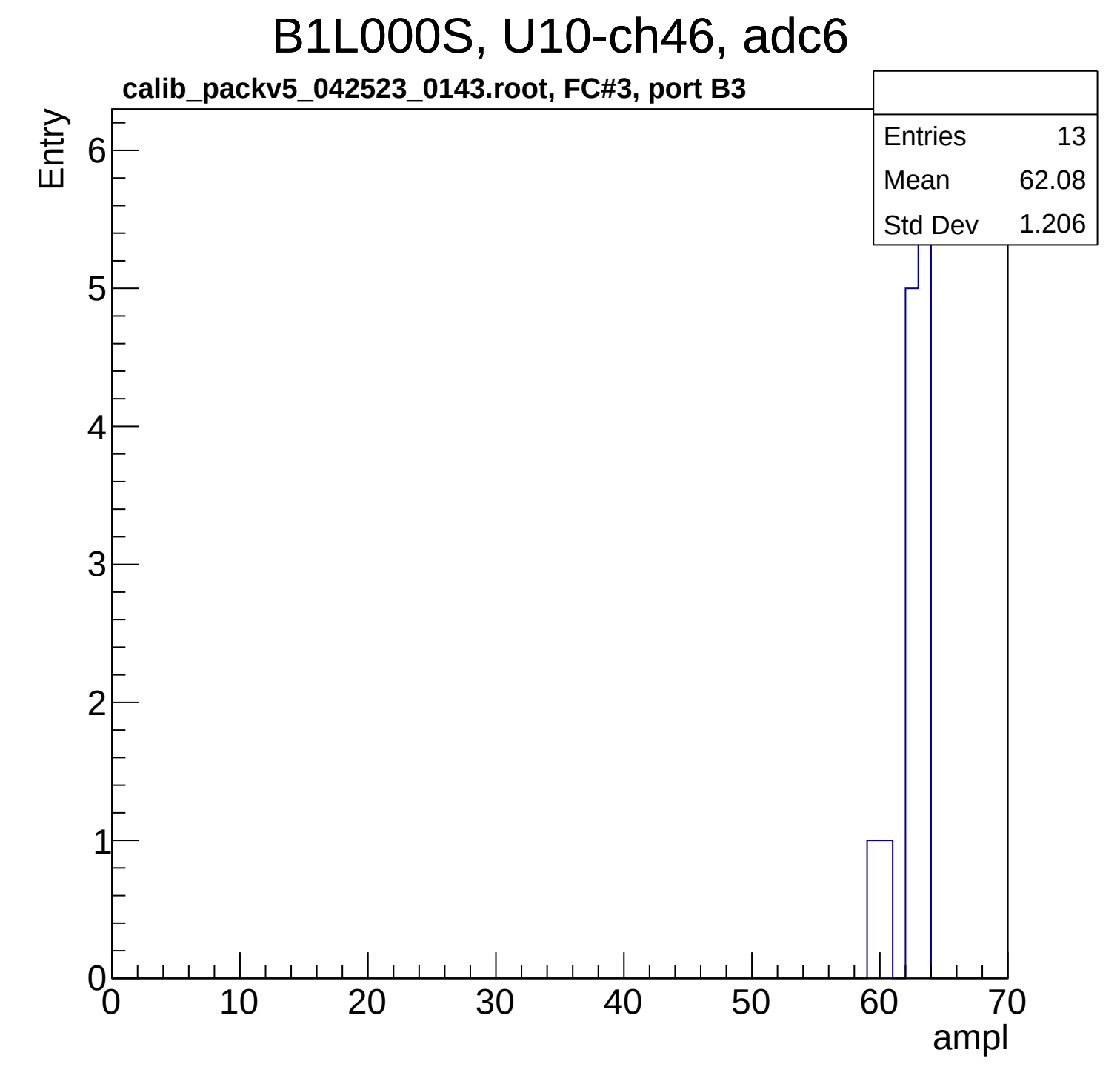
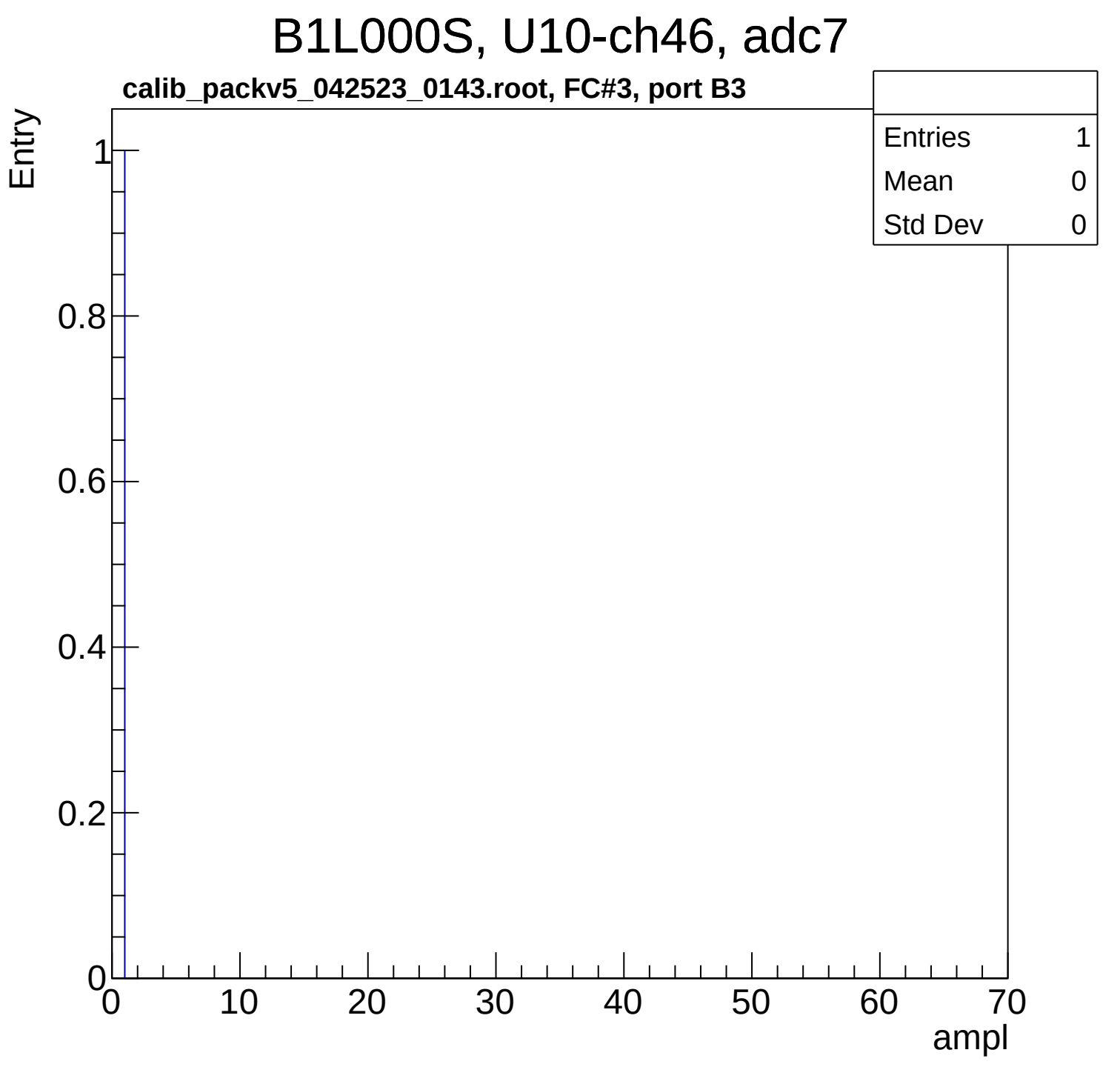
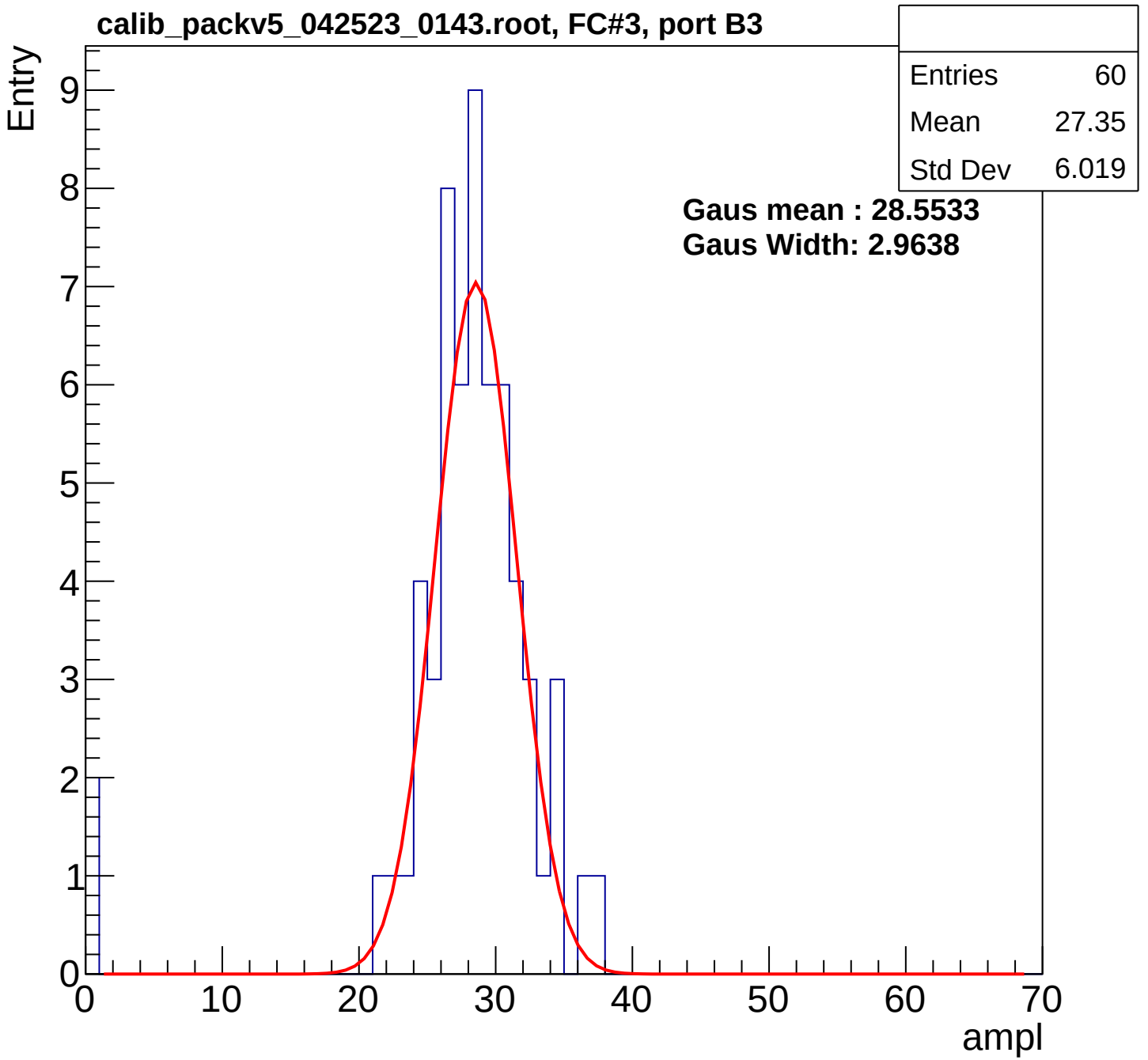

B1L000S, U10-ch46, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 58.17
Std Dev 9.314
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch46, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 62.08
Std Dev 1.206
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch46, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch47, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 27.35
Std Dev 6.019
Gaus mean : 28.5533
Gaus Width: 2.9638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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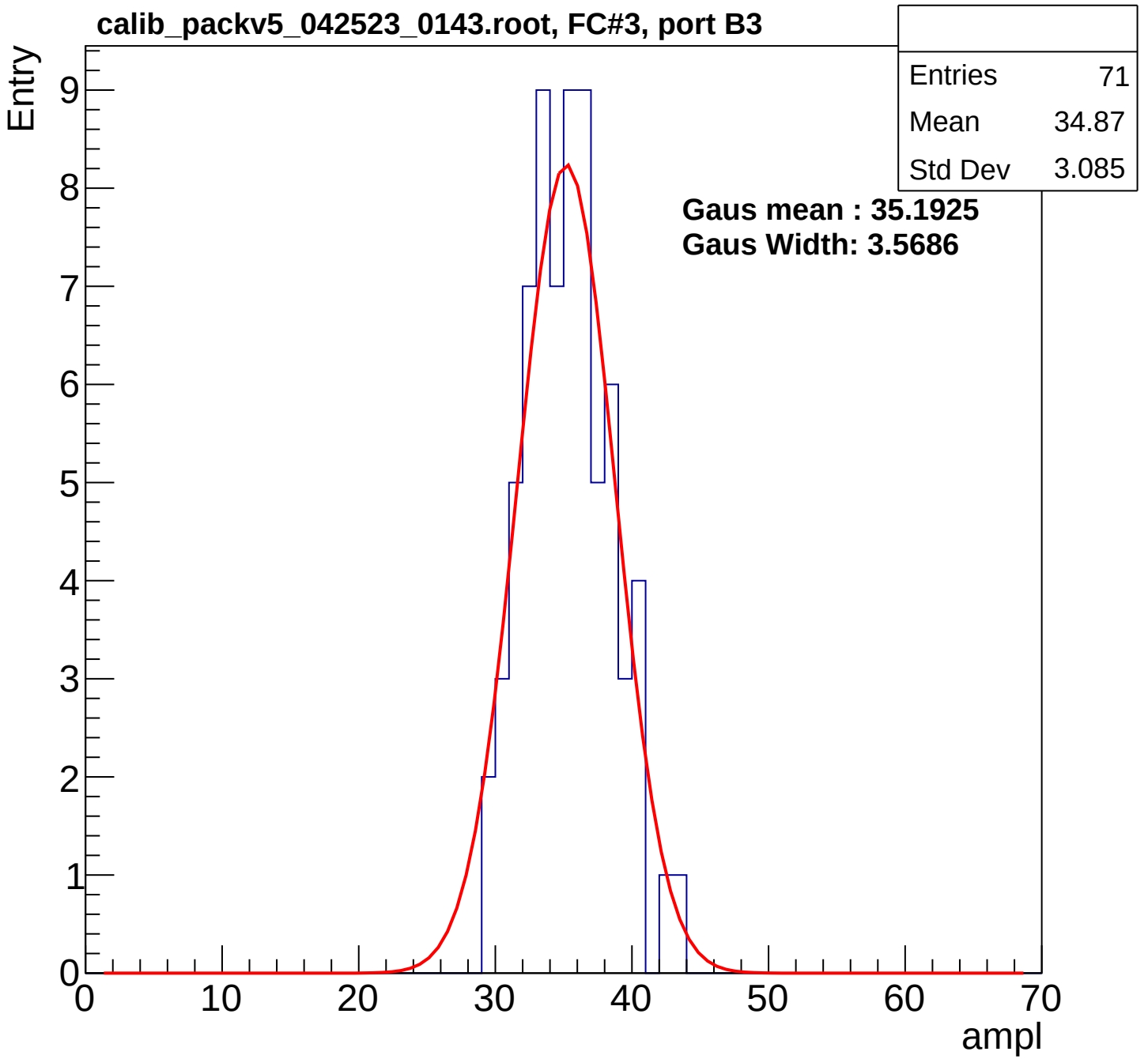
B1L000S, U10-ch47, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 34.87
Std Dev 3.085
Gaus mean : 35.1925
Gaus Width: 3.5686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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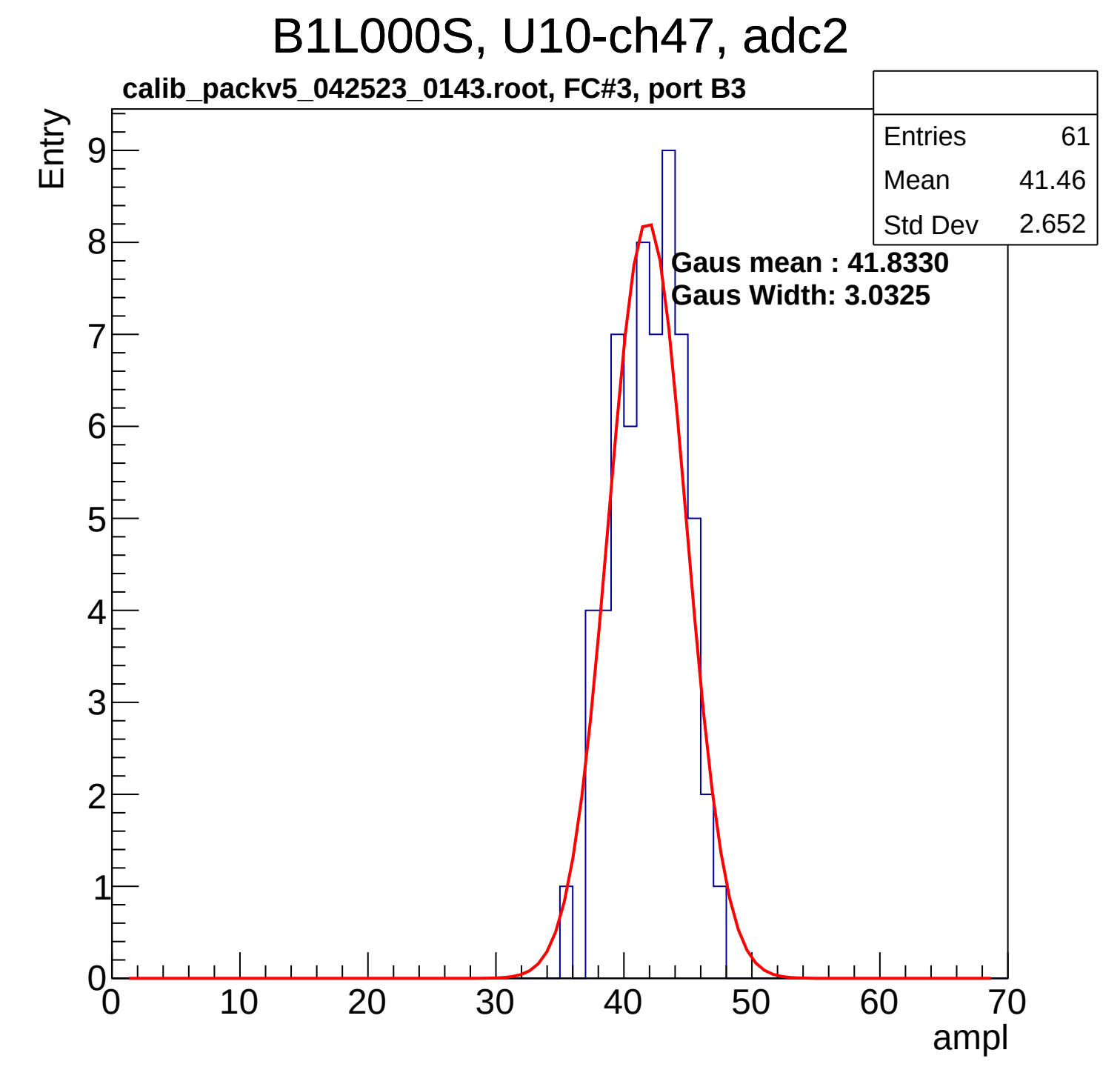
B1L000S, U10-ch47, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.46
Std Dev 2.652
Gaus mean : 41.8330
Gaus Width: 3.0325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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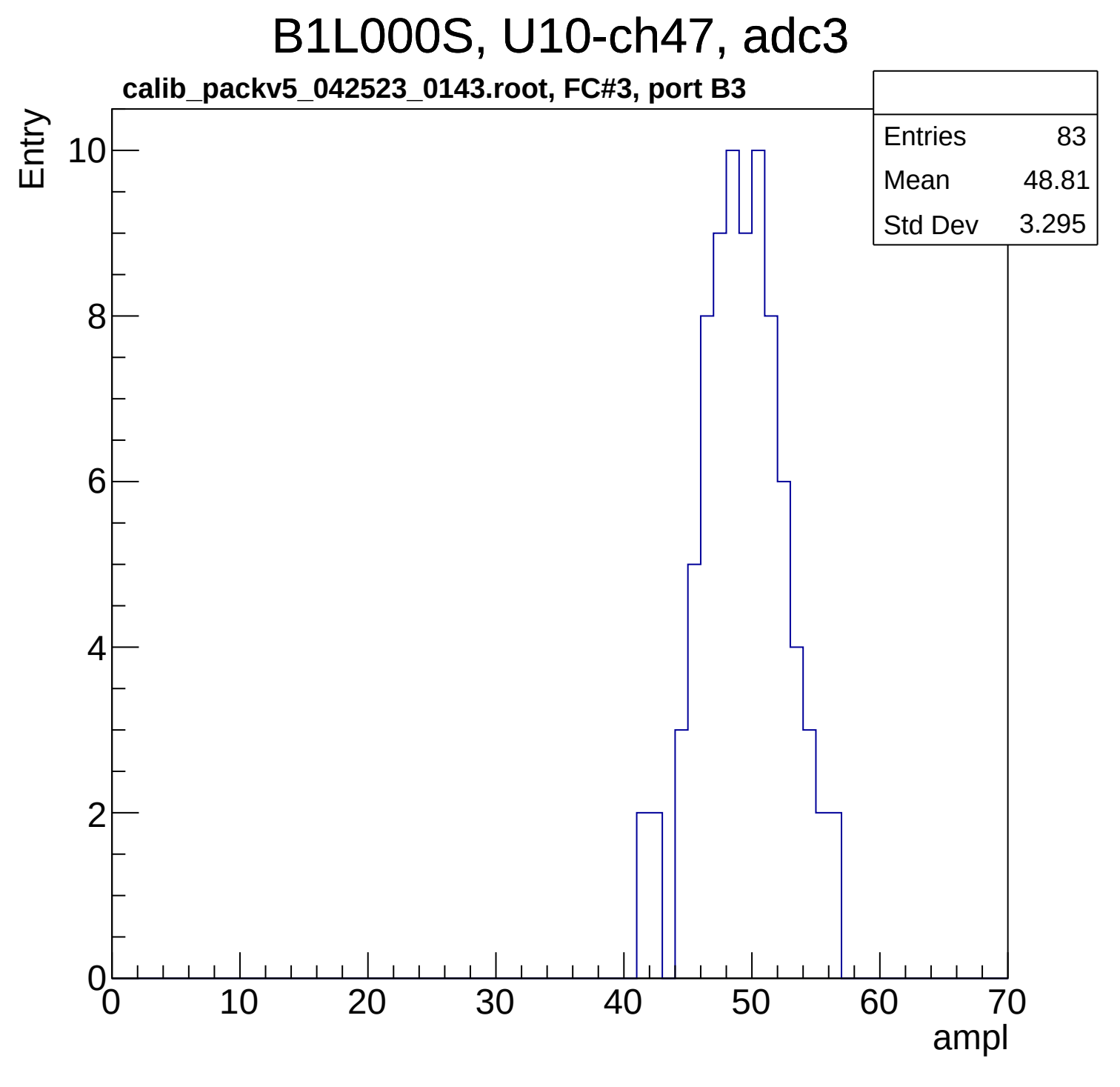
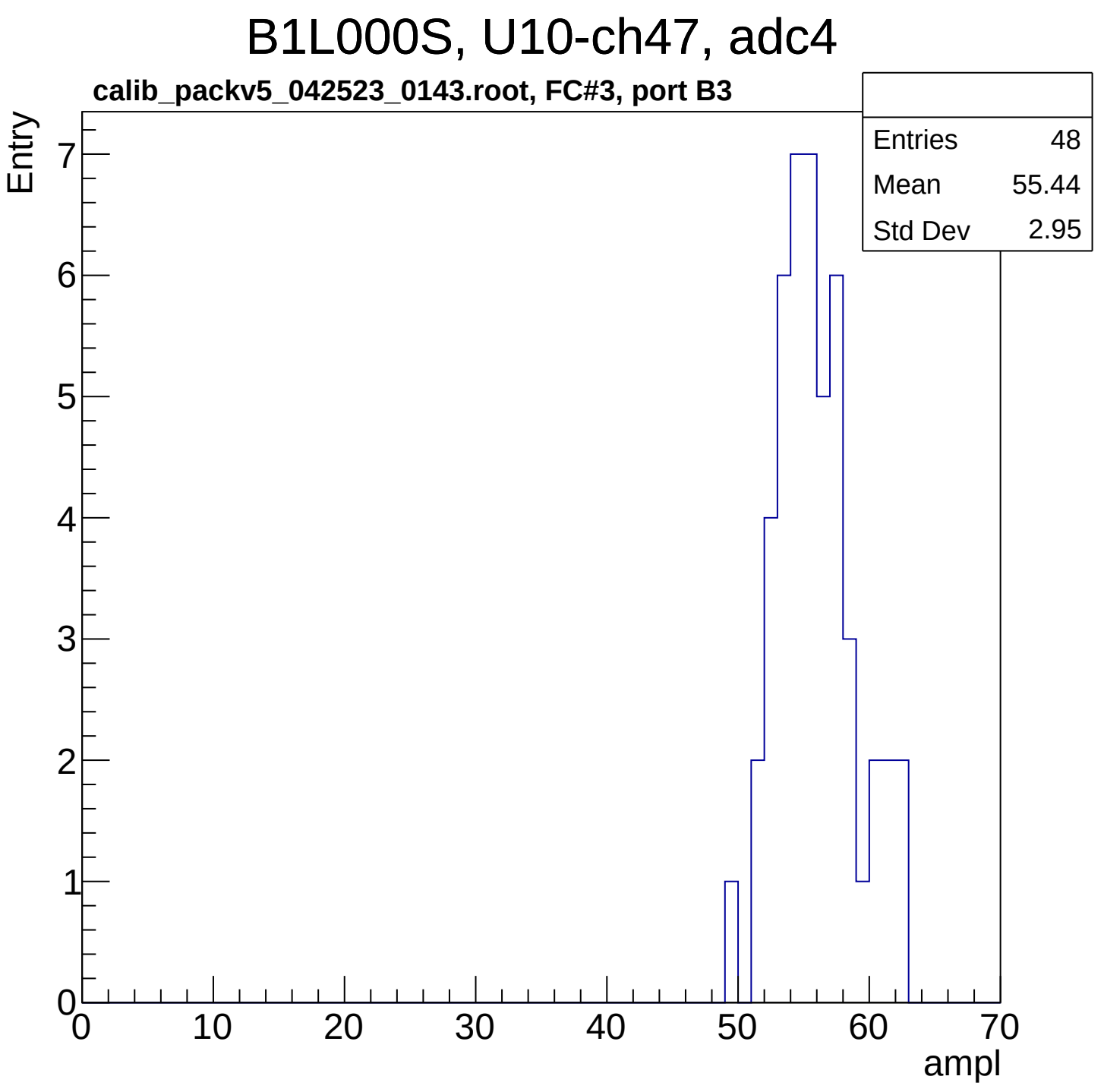
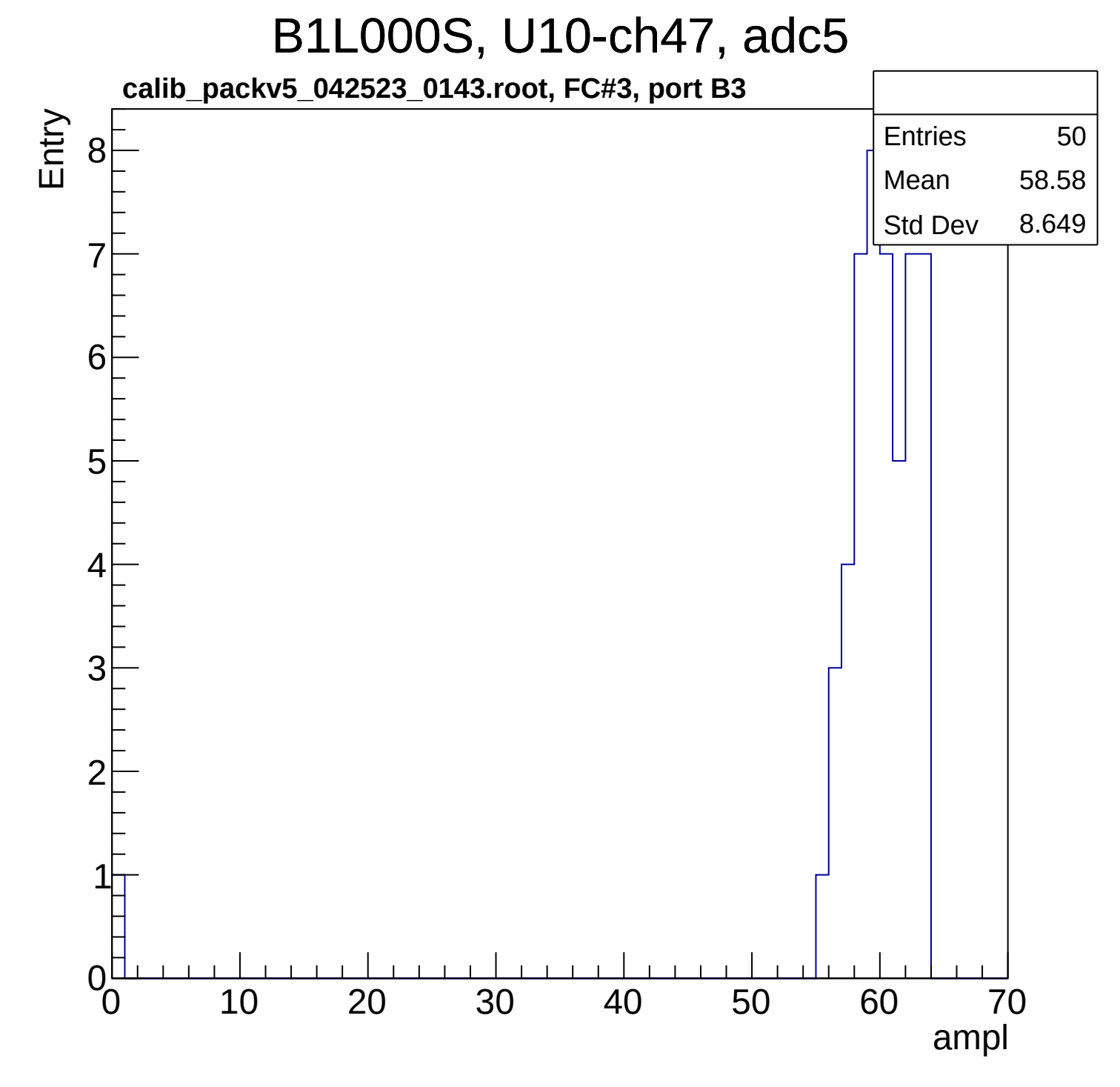

B1L000S, U10-ch47, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 48.81
Std Dev 3.295
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch47, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 55.44
Std Dev 2.95
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch47, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.58
Std Dev 8.649
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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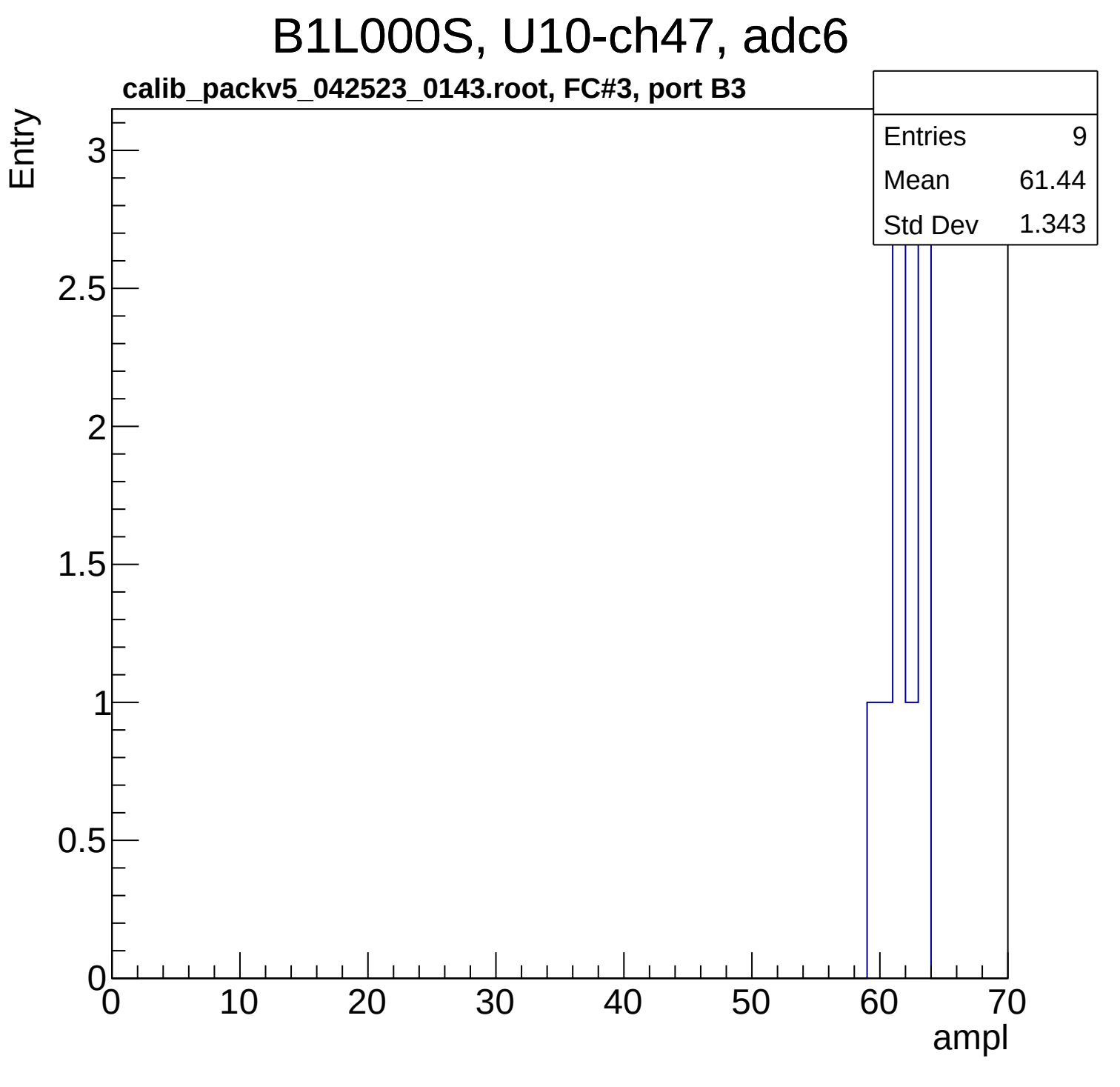
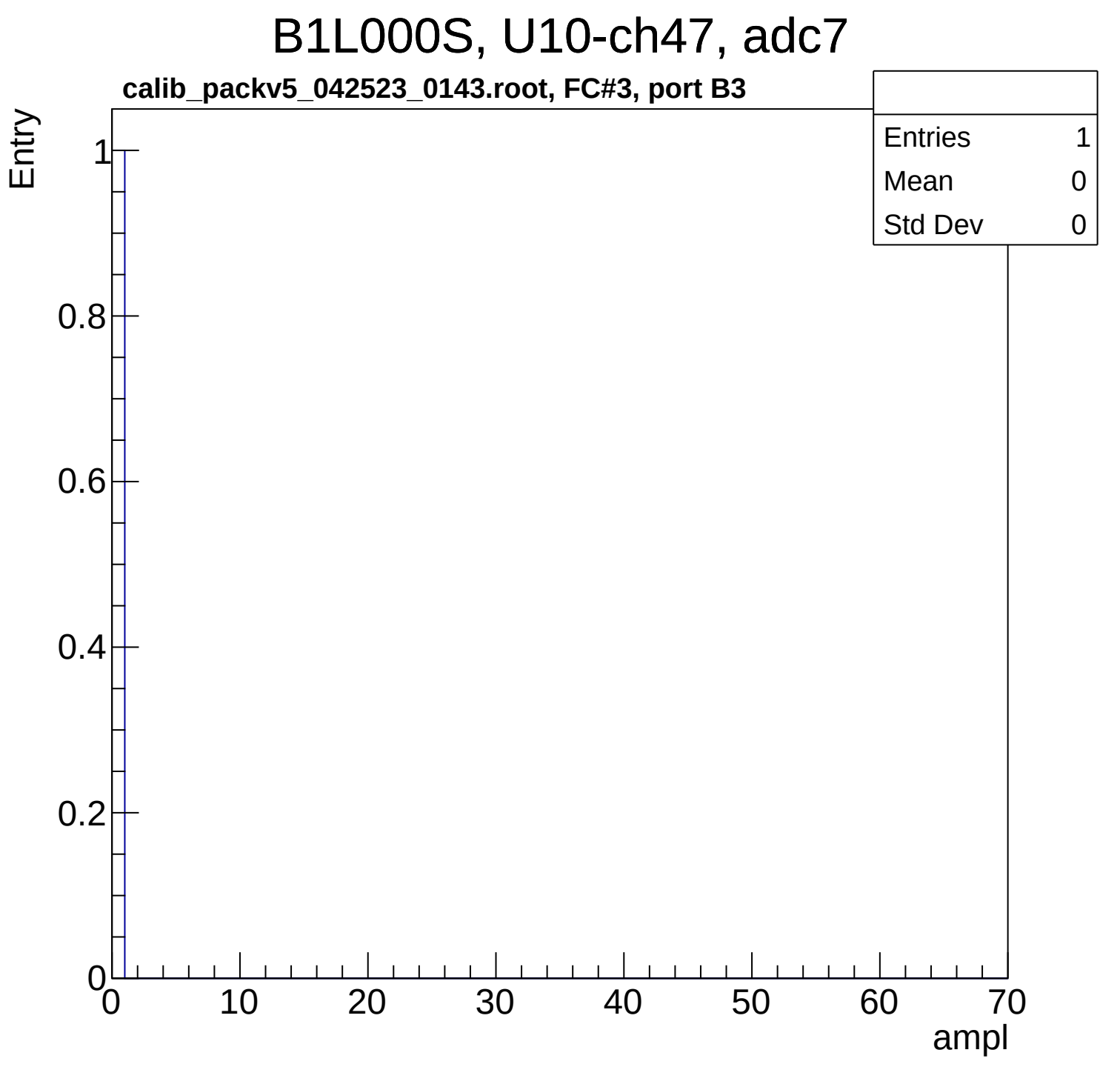
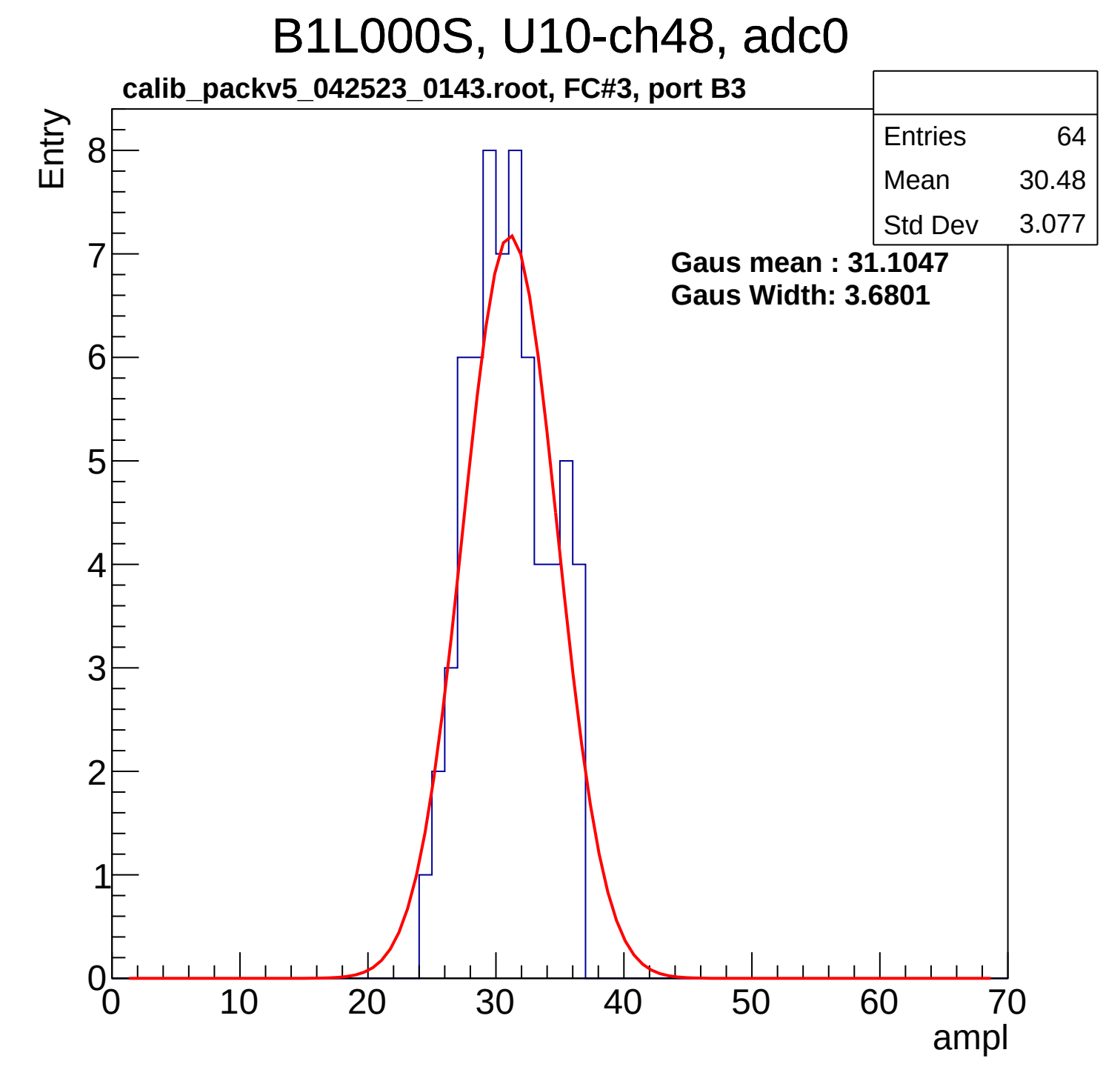
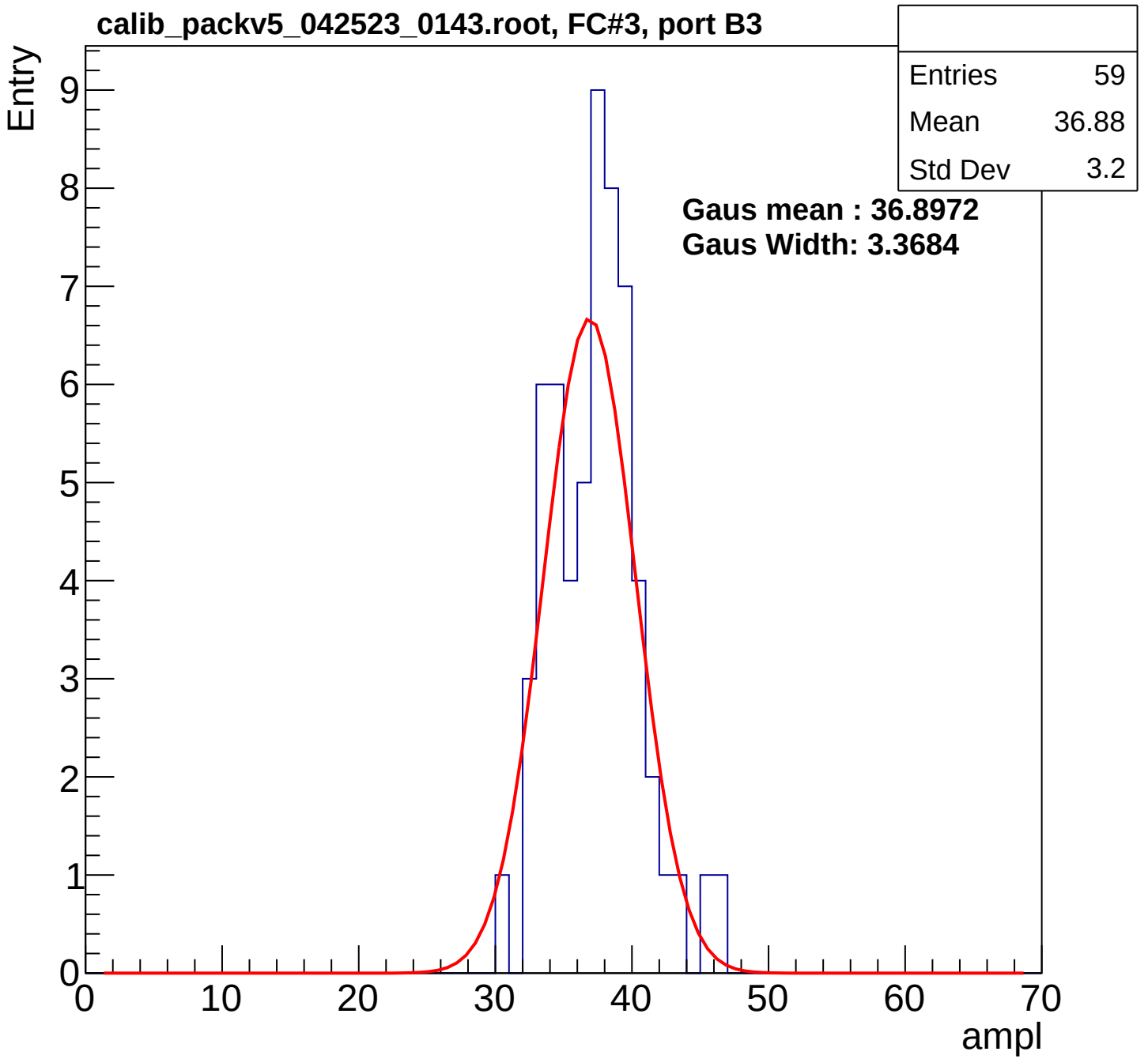
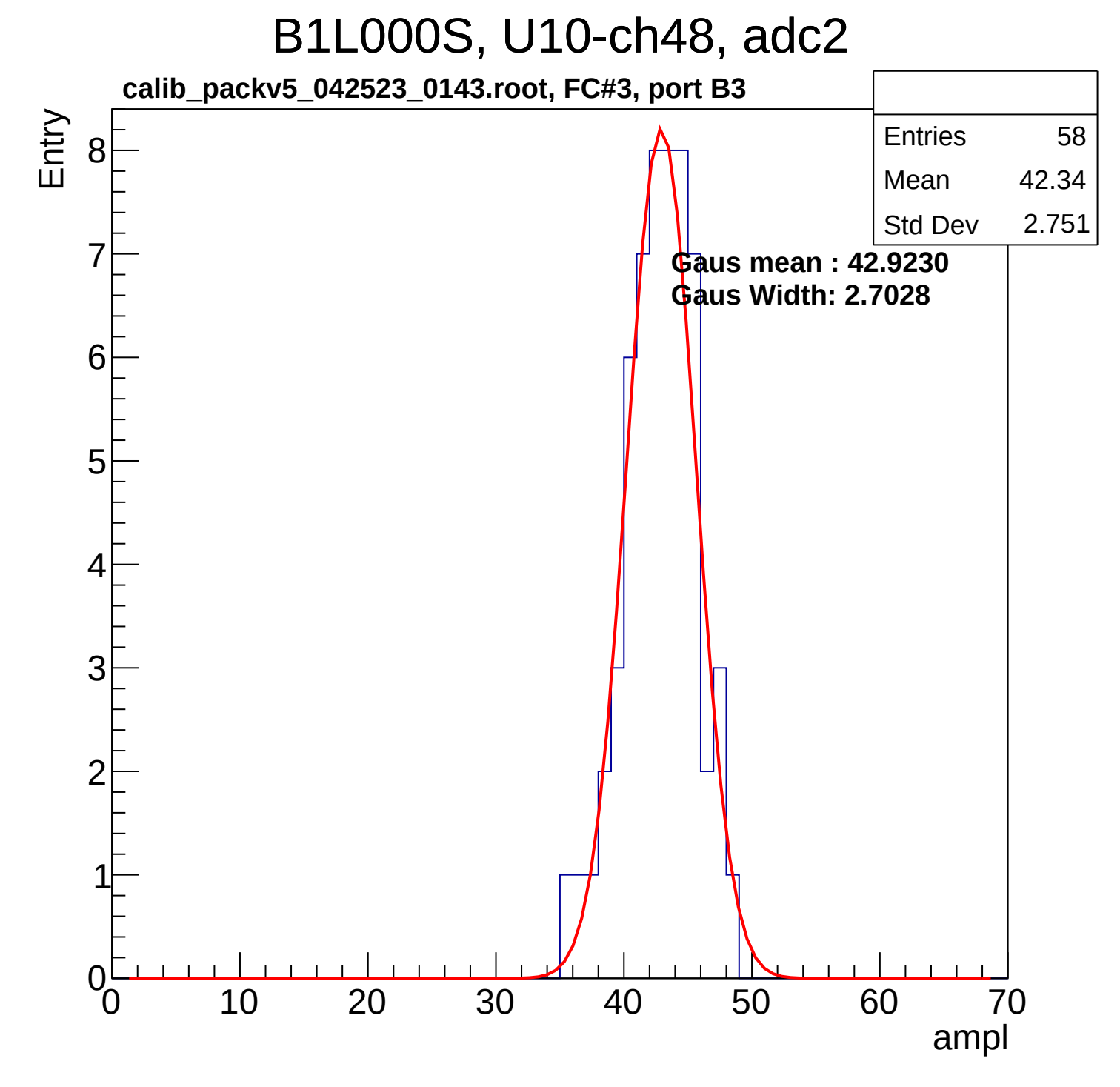
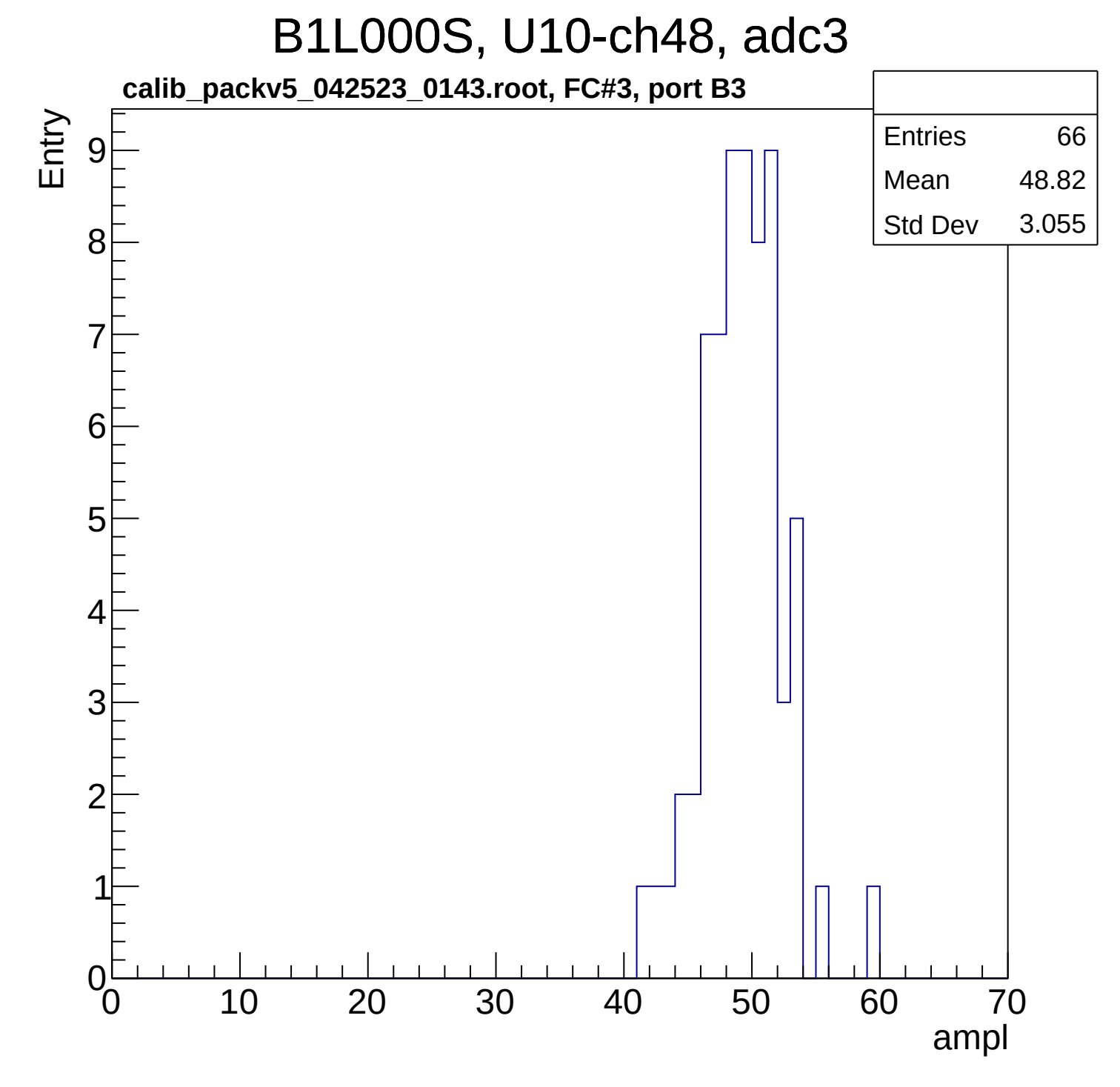

B1L000S, U10-ch47, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 61.44
Std Dev 1.343
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch47, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch48, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 30.48
Std Dev 3.077
Gaus mean : 31.1047
Gaus Width: 3.6801
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch48, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 36.88
Std Dev 3.2
Gaus mean : 36.8972
Gaus Width: 3.3684
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch48, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 42.34
Std Dev 2.751
Gaus mean : 42.9230
Gaus Width: 2.7028
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch48, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 48.82
Std Dev 3.055
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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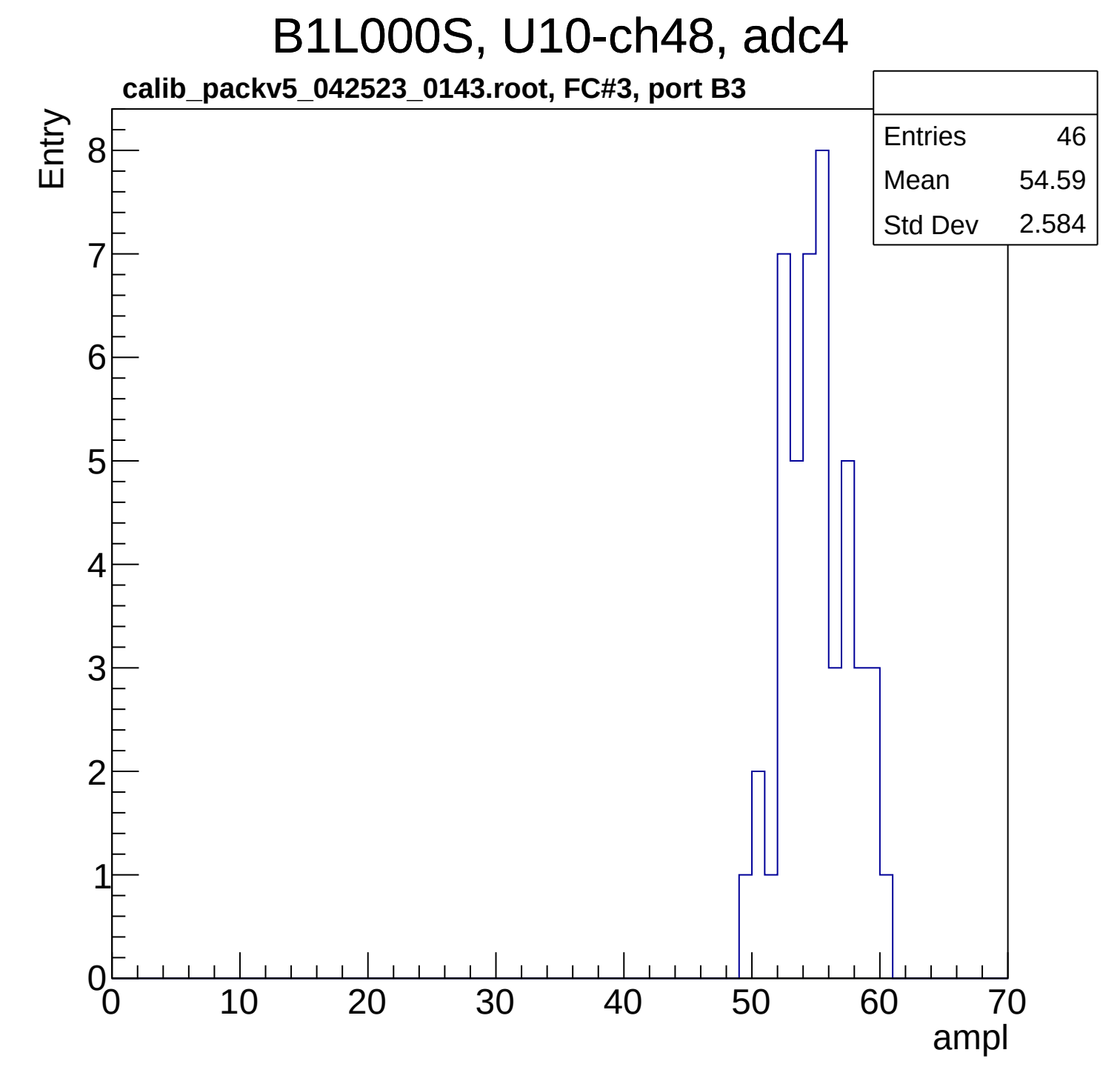
B1L000S, U10-ch48, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 54.59
Std Dev 2.584
Entry
ampl
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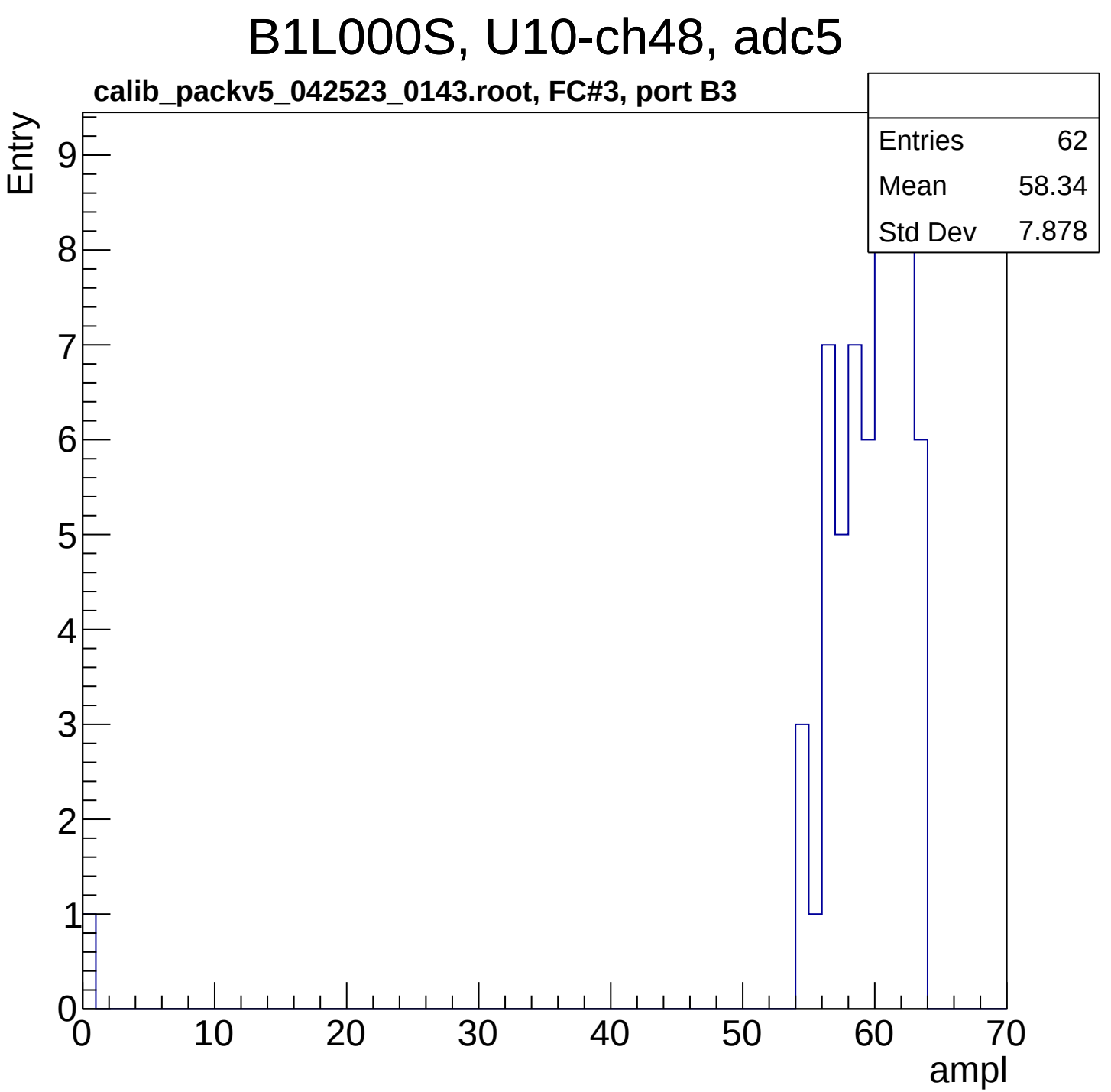
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch48, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 58.34
Std Dev 7.878
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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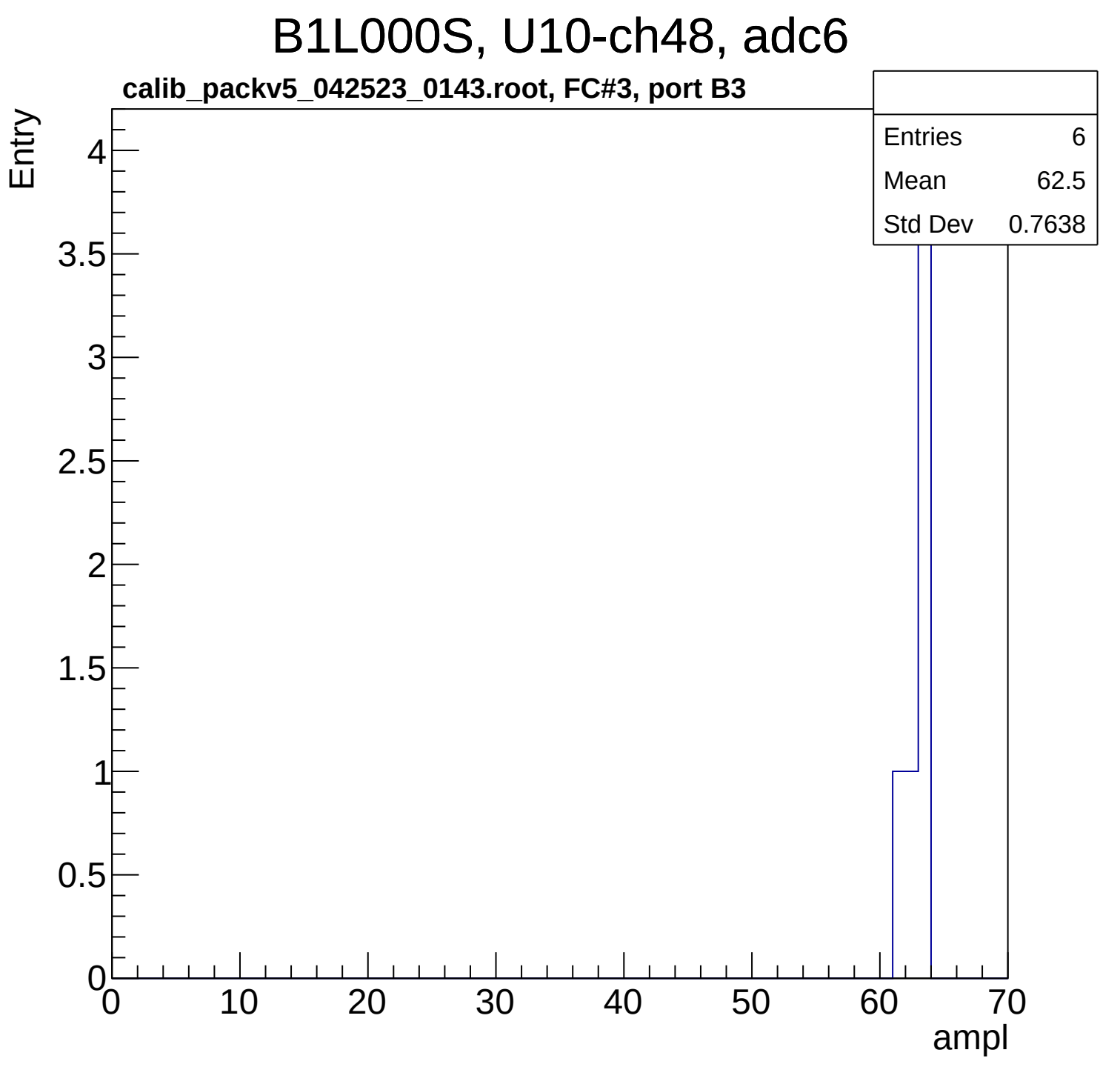
B1L000S, U10-ch48, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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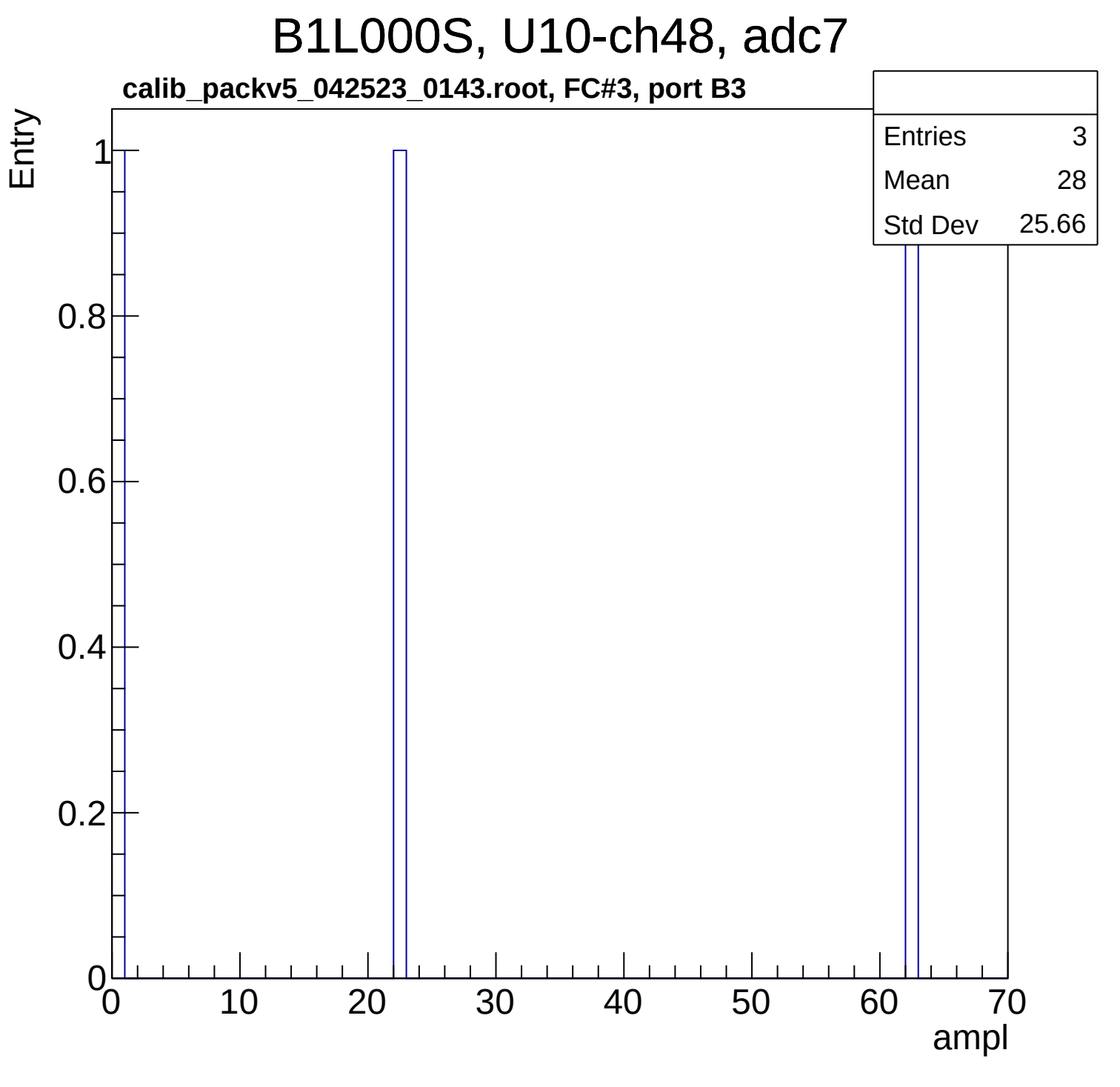
40
50
60
70


B1L000S, U10-ch48, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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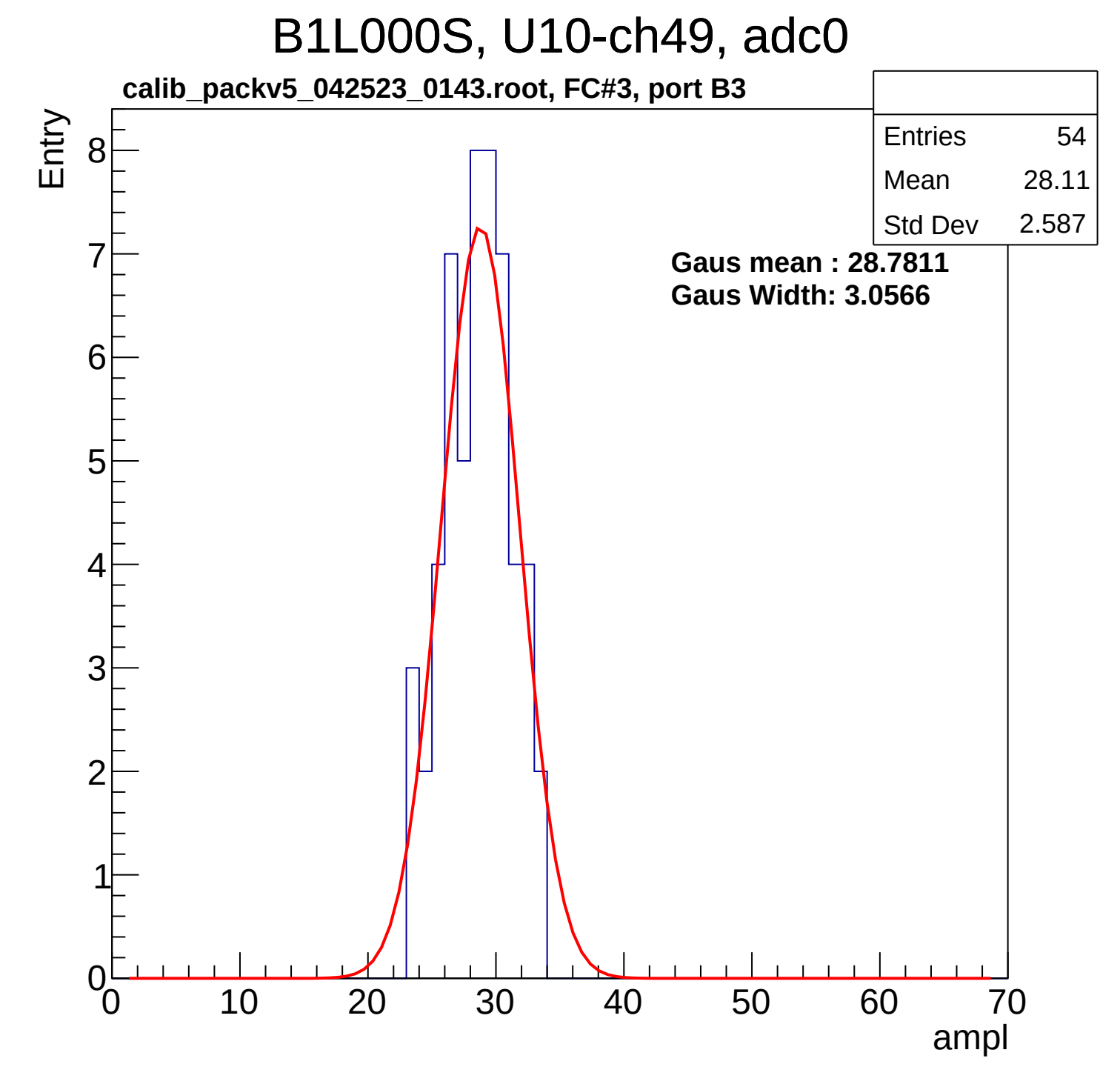
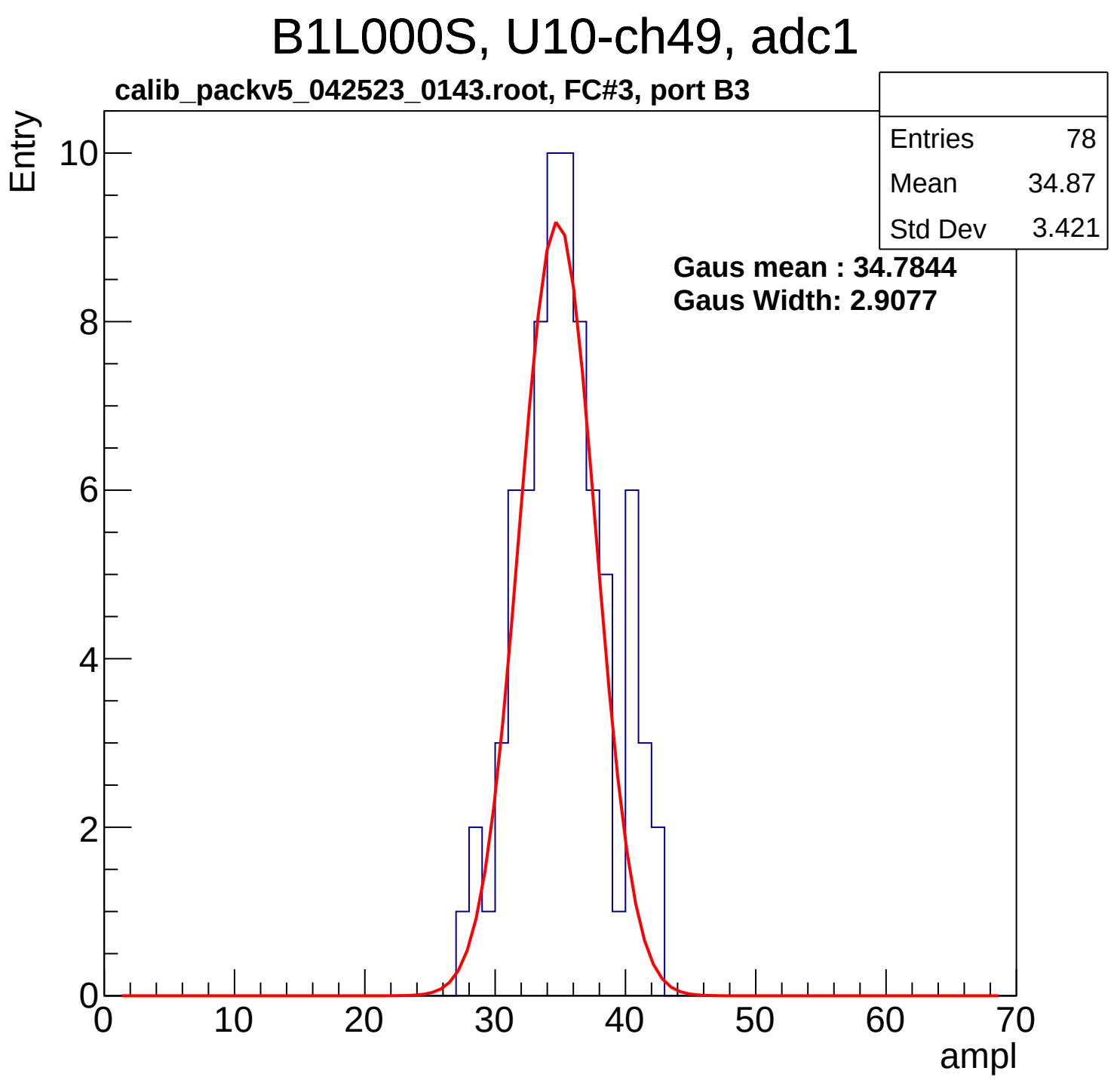

B1L000S, U10-ch49, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.11
Std Dev 2.587
Gaus mean : 28.7811
Gaus Width: 3.0566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch49, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 34.87
Std Dev 3.421
Gaus mean : 34.7844
Gaus Width: 2.9077
Entry
ampl
0
2
4
6
8
10
0
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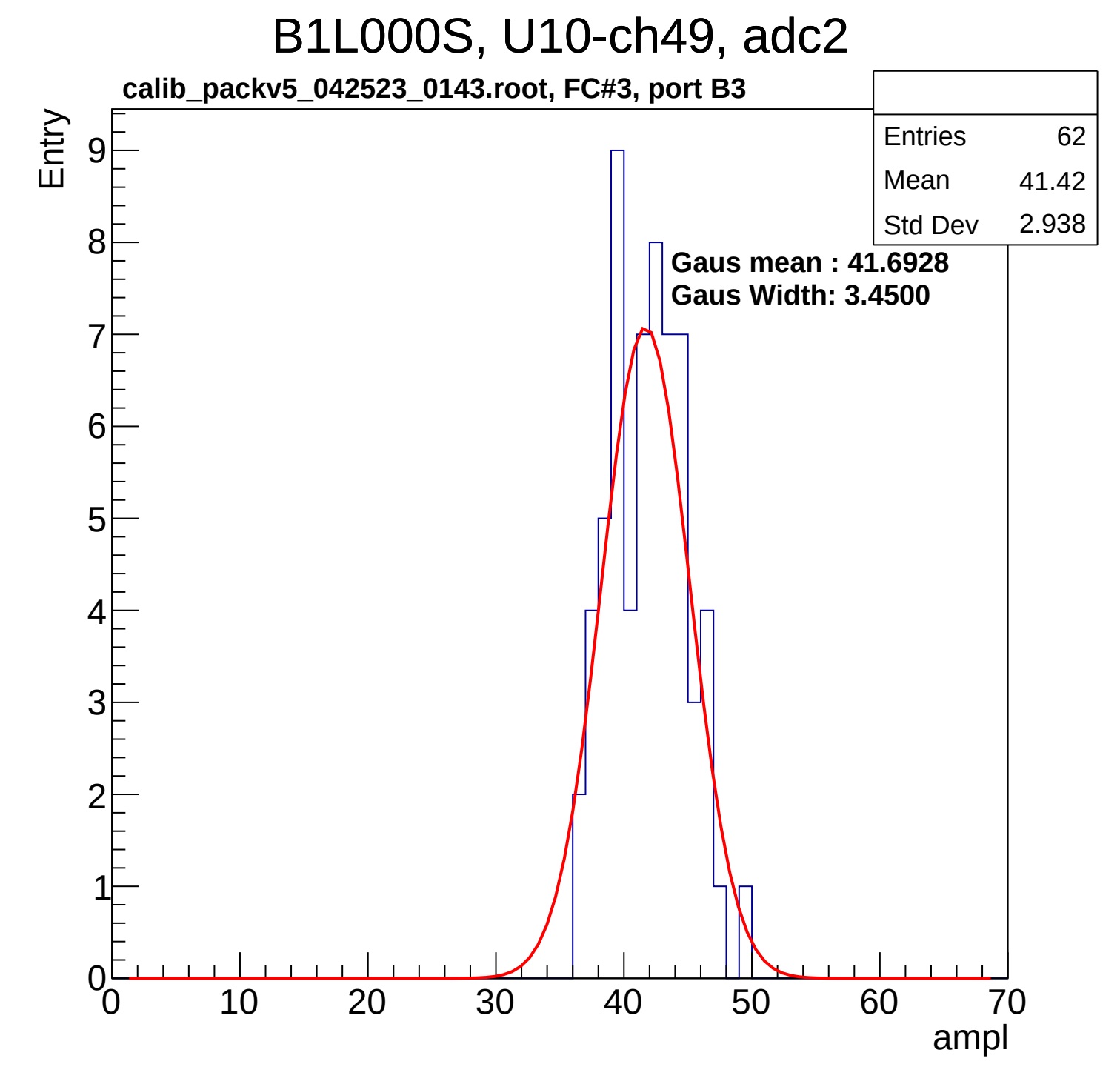
20
30
40
50
60
70


B1L000S, U10-ch49, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.42
Std Dev 2.938
Gaus mean : 41.6928
Gaus Width: 3.4500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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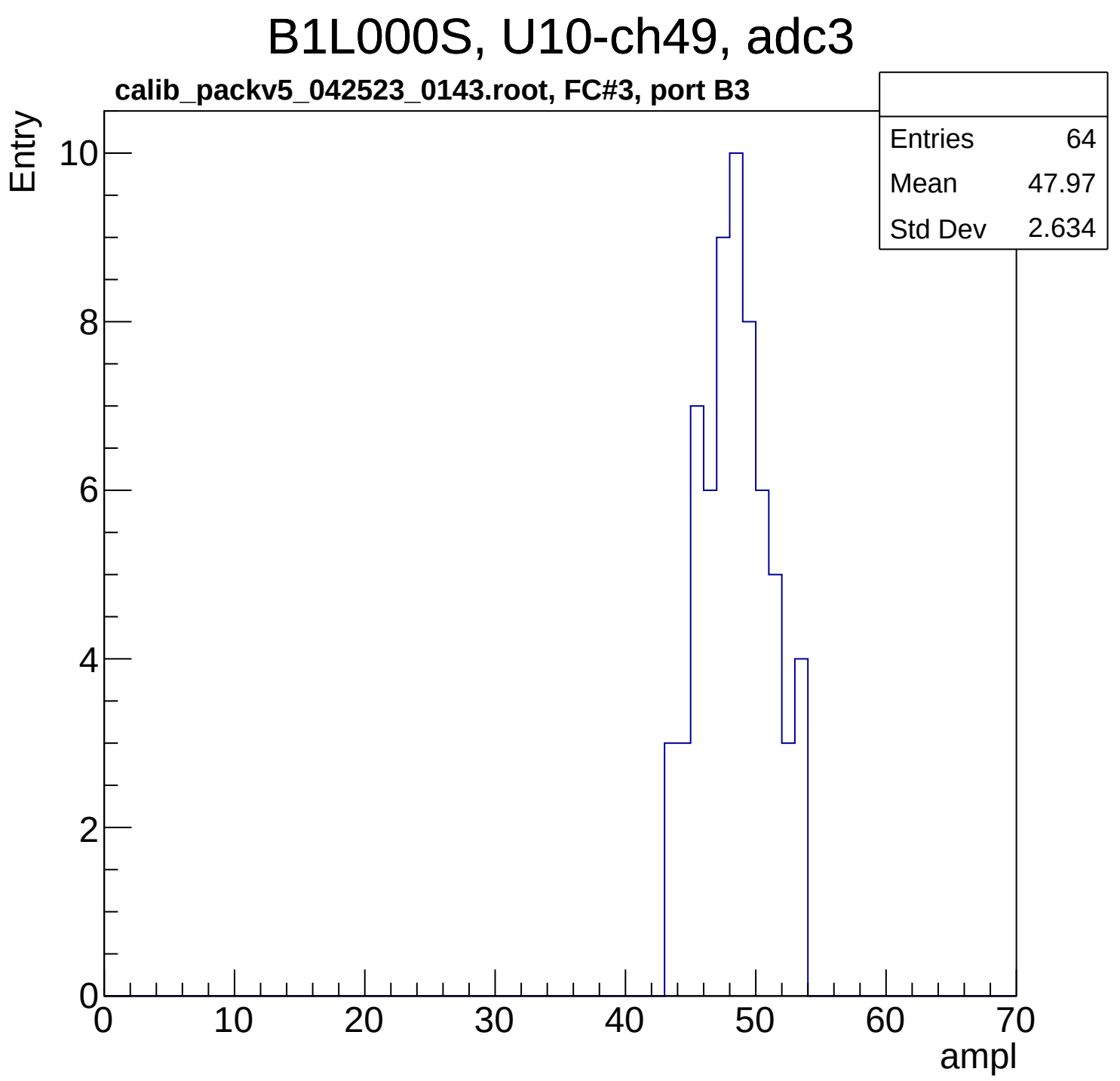
B1L000S, U10-ch49, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.97
Std Dev 2.634
Entry
0
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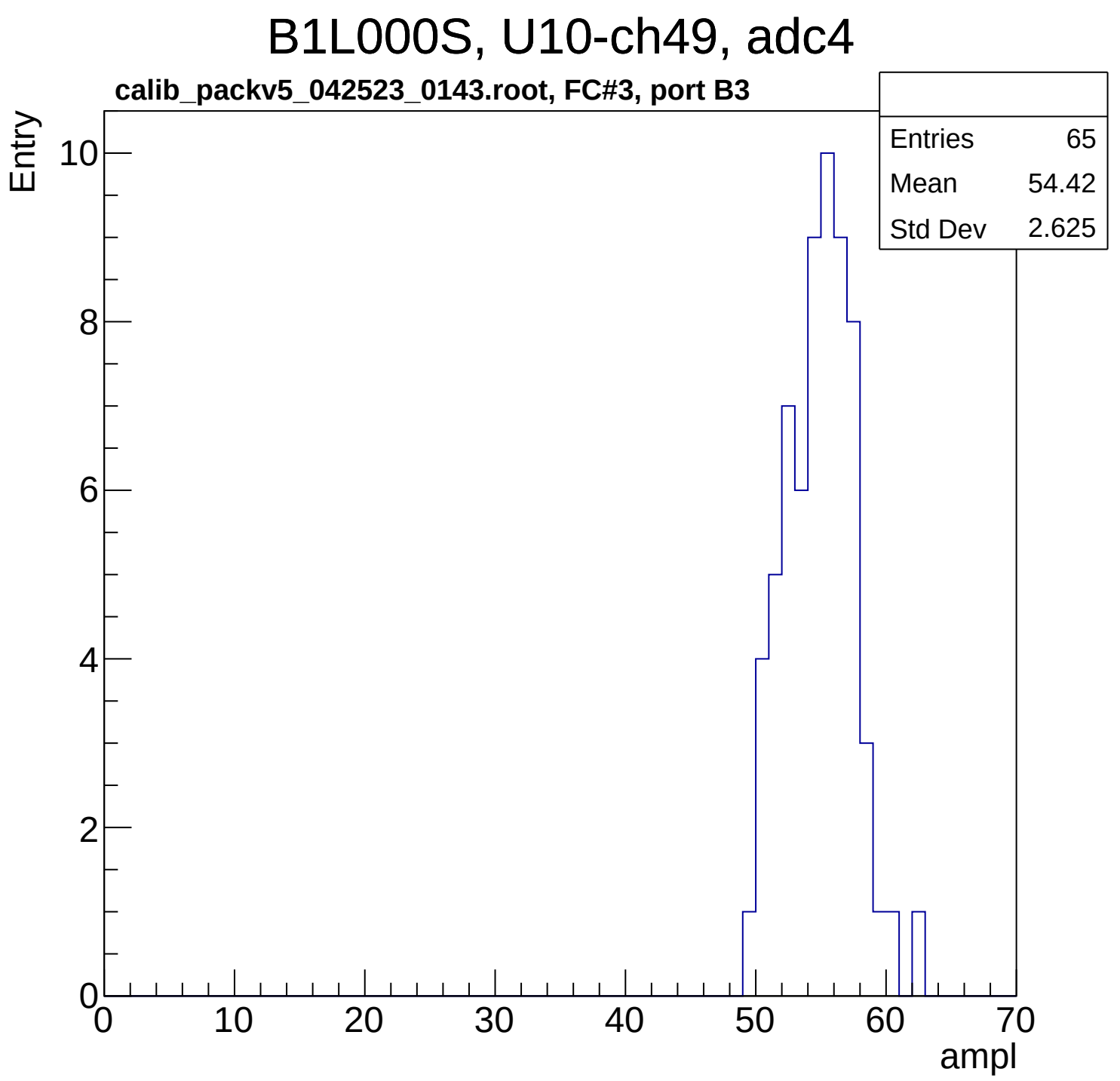
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch49, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 54.42
Std Dev 2.625
Entry
ampl
0
2
4
6
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10
0
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40
50
60
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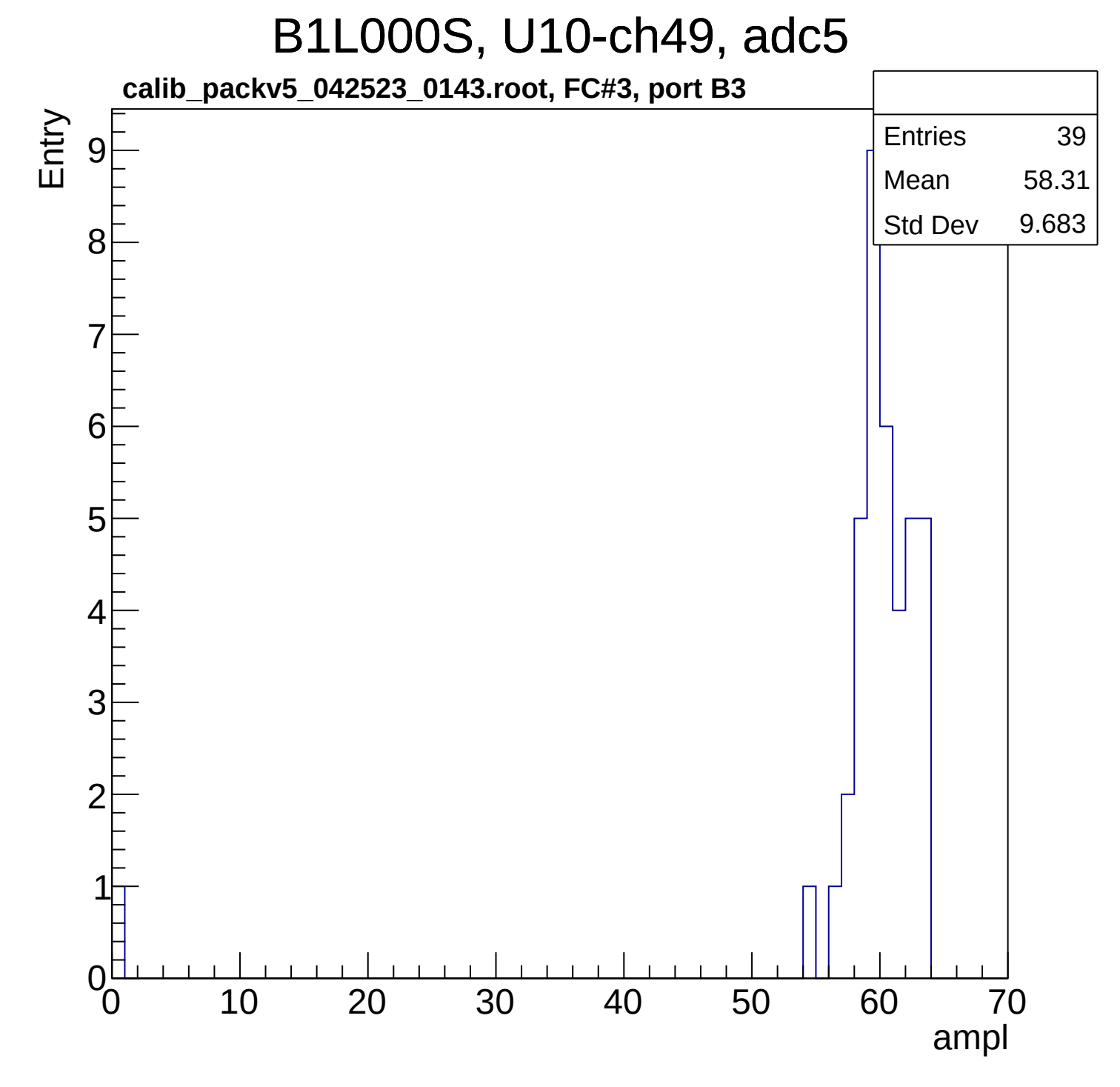
B1L000S, U10-ch49, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 58.31
Std Dev 9.683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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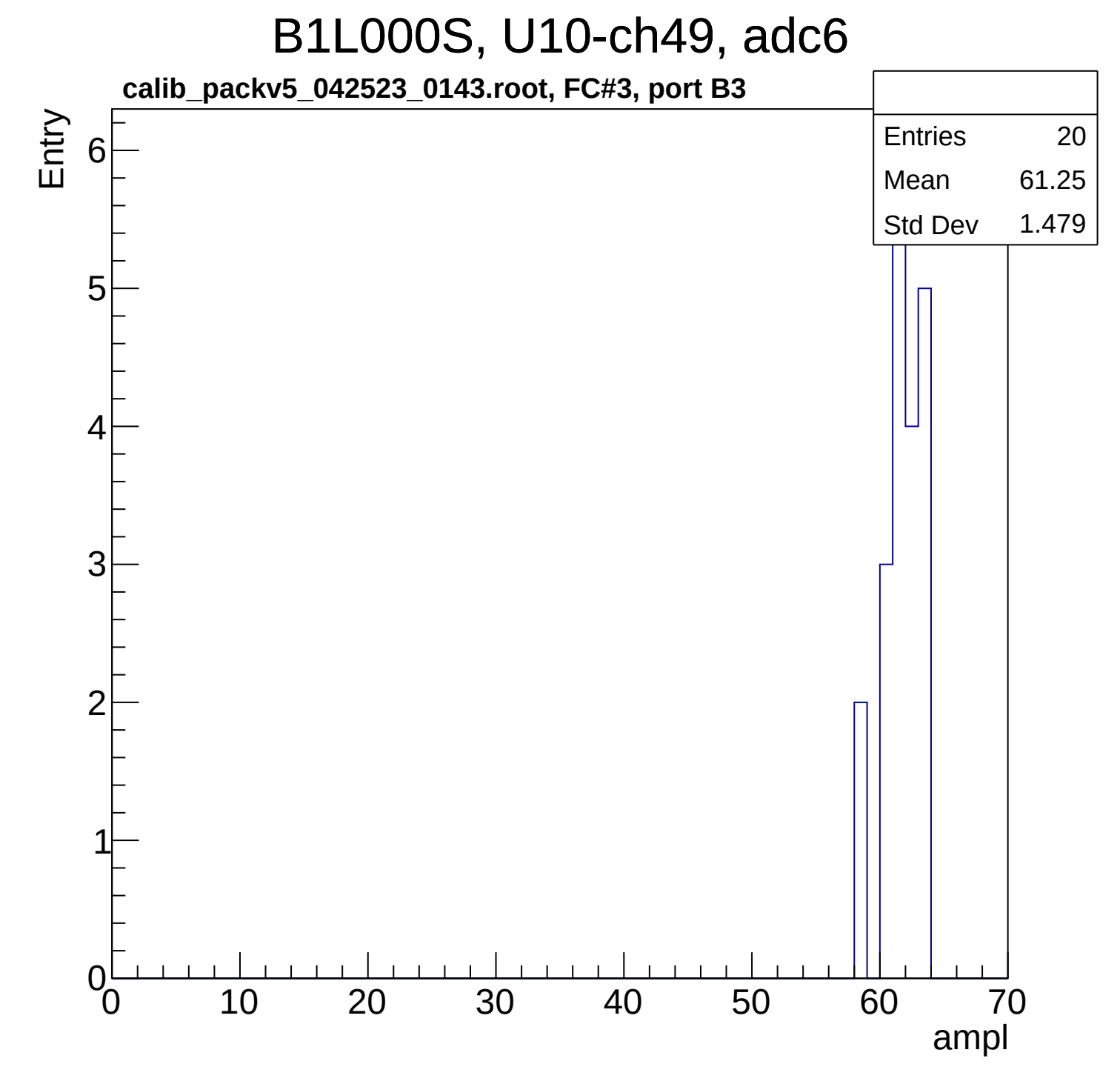
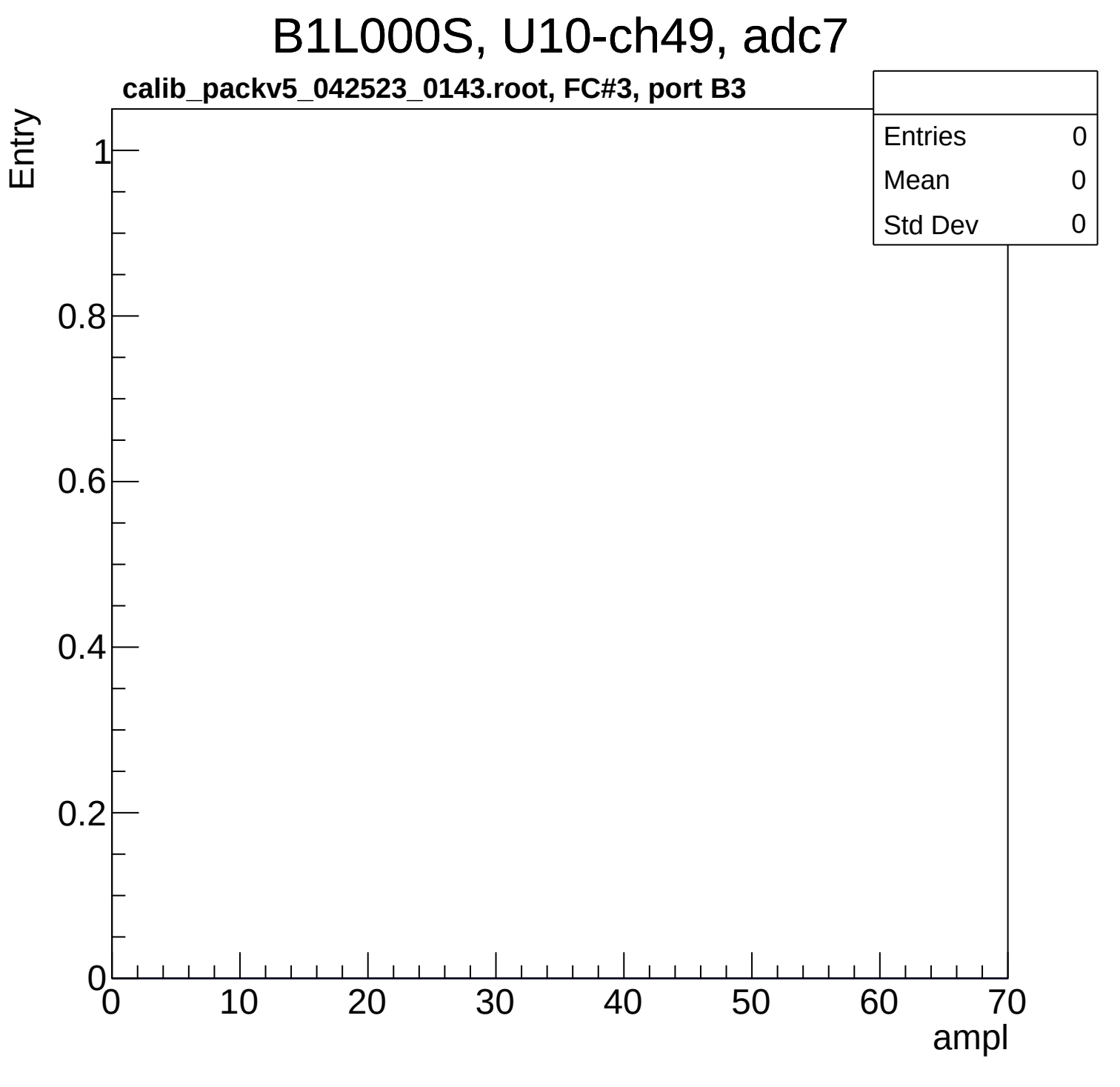

B1L000S, U10-ch49, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.25
Std Dev 1.479
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch49, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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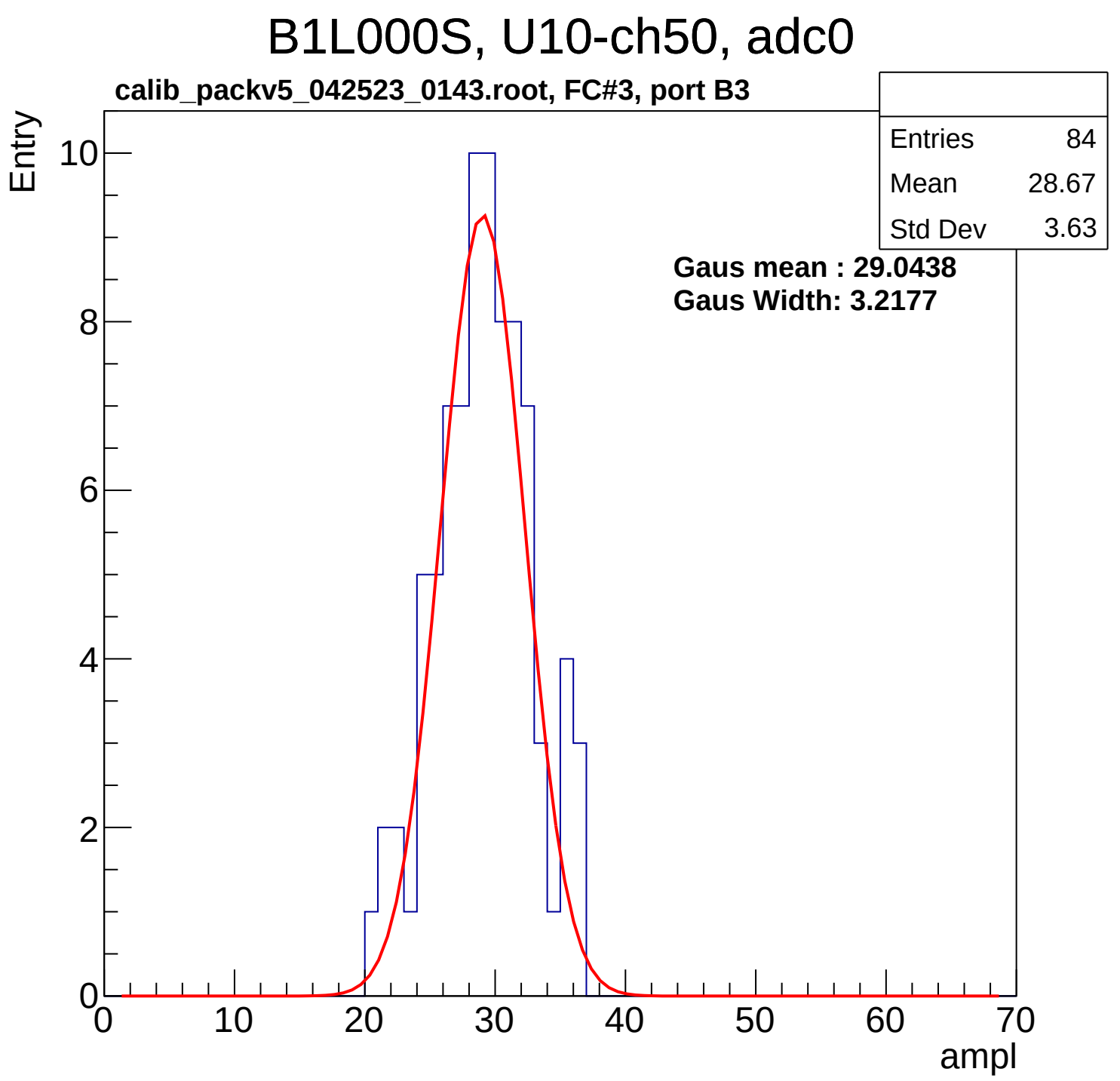
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch50, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 28.67
Std Dev 3.63
Gaus mean : 29.0438
Gaus Width: 3.2177
Entry
ampl
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20
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40
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60
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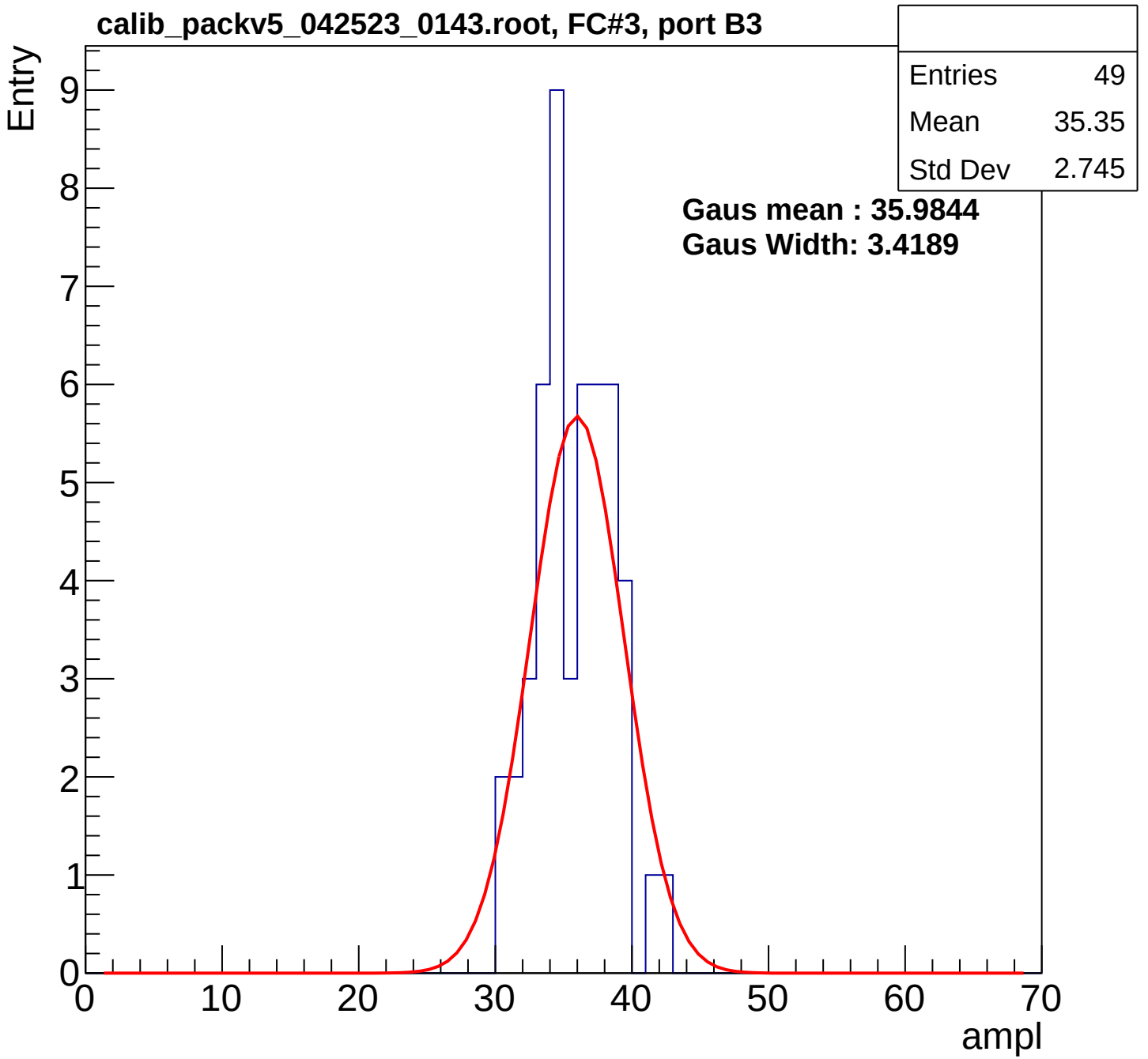
B1L000S, U10-ch50, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 35.35
Std Dev 2.745
Gaus mean : 35.9844
Gaus Width: 3.4189
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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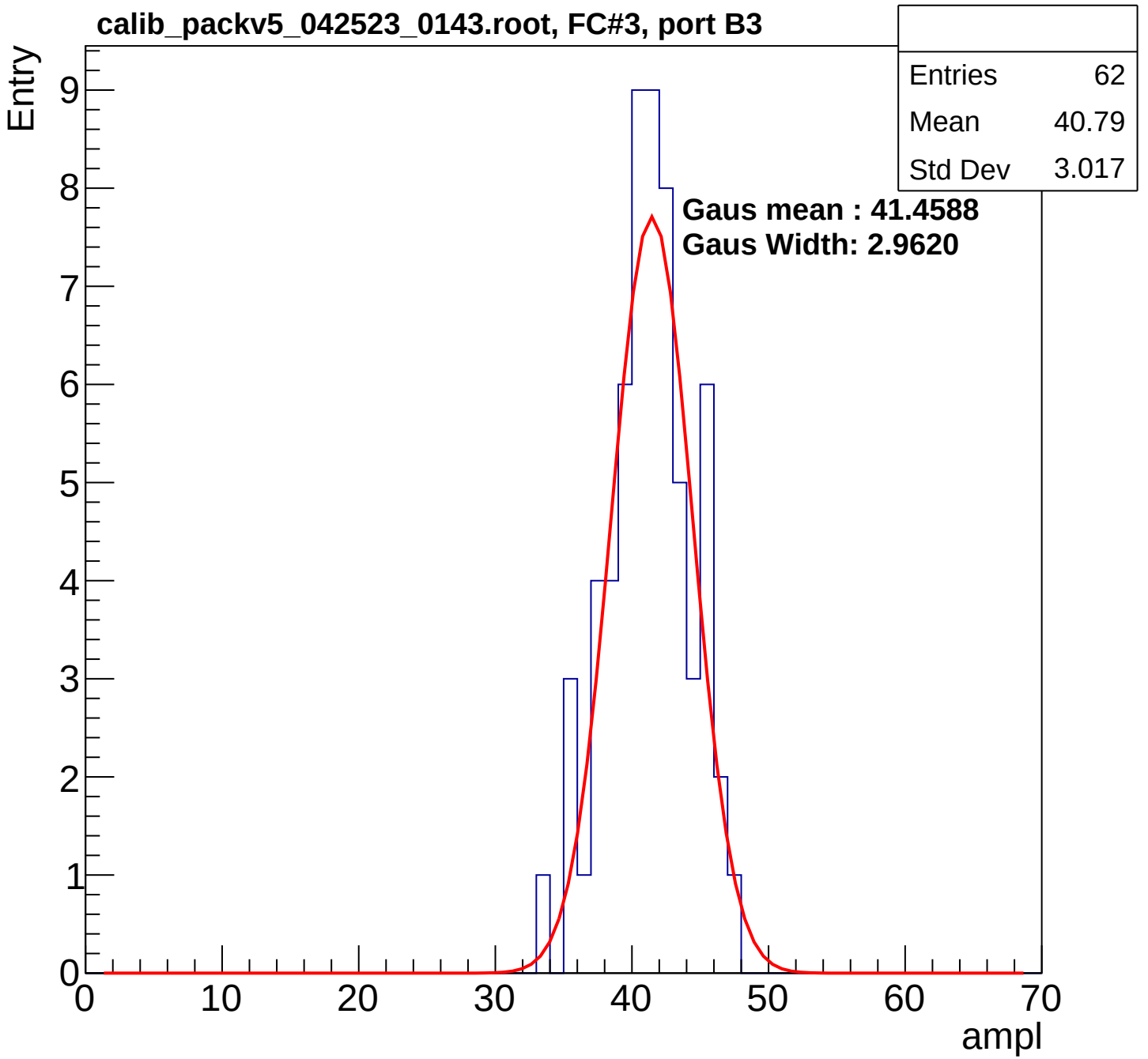
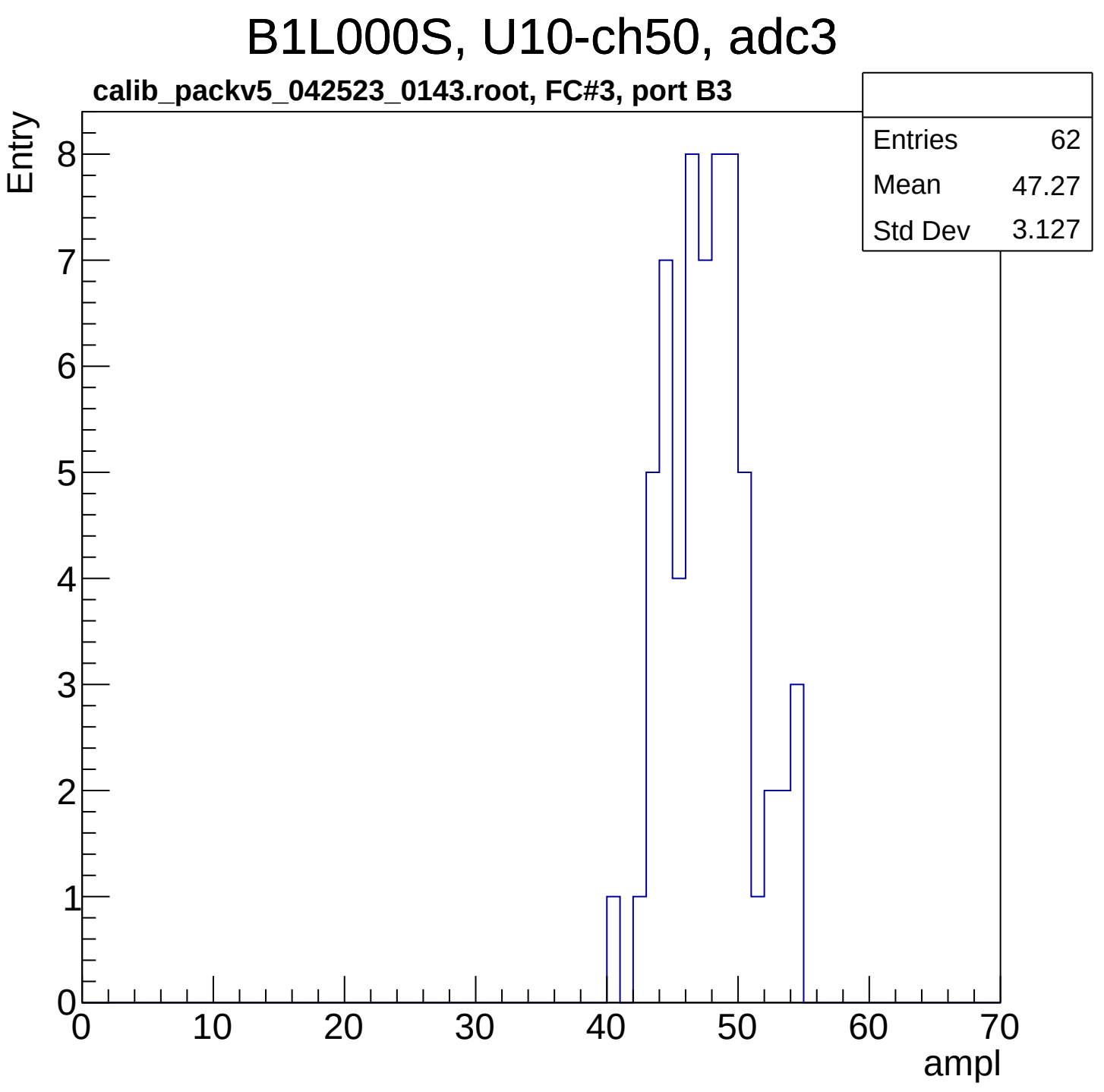

B1L000S, U10-ch50, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 40.79
Std Dev 3.017
Gaus mean : 41.4588
Gaus Width: 2.9620
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch50, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 47.27
Std Dev 3.127
Entry
ampl
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2
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4
5
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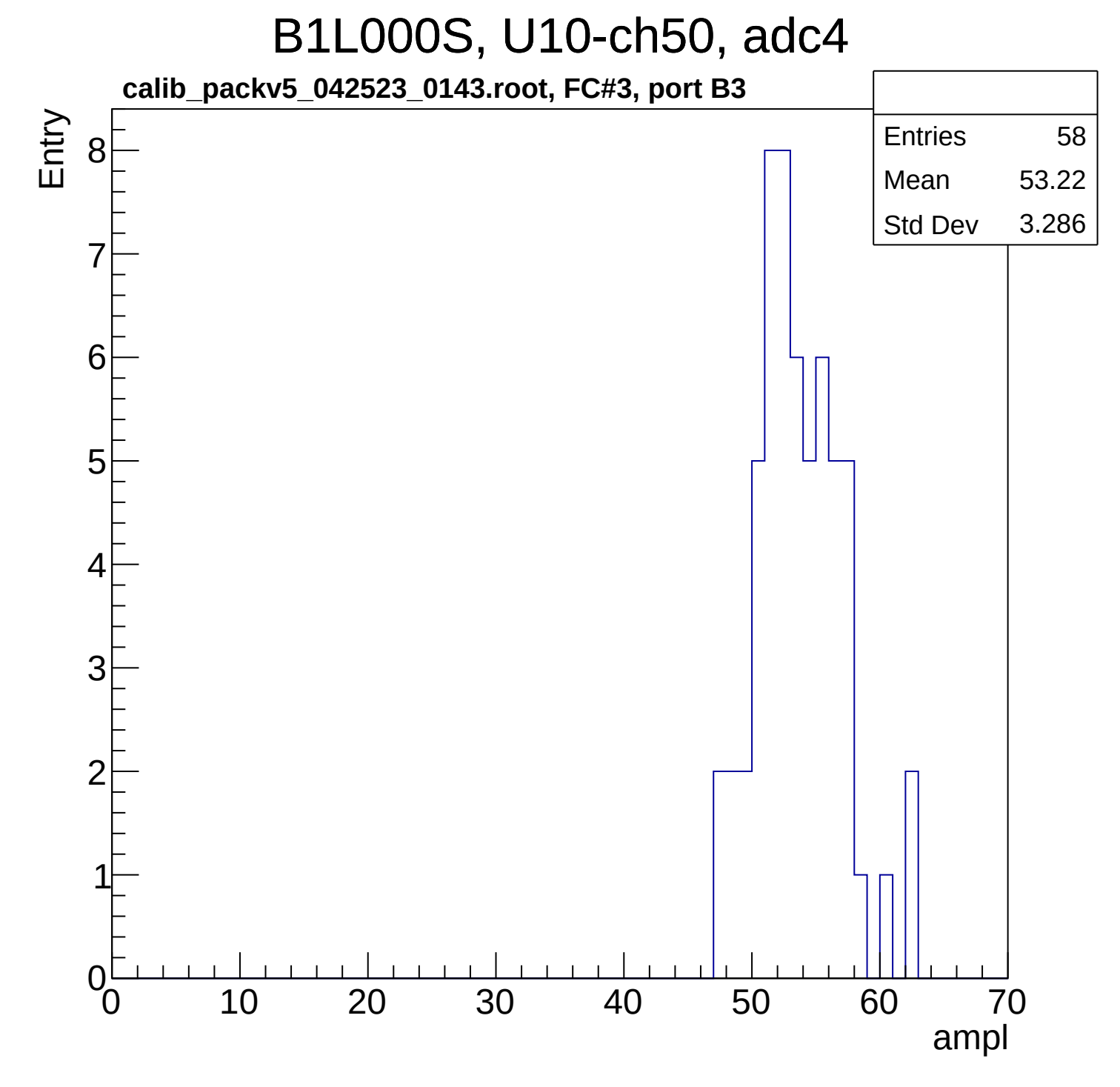
B1L000S, U10-ch50, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 53.22
Std Dev 3.286
Entry
ampl
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2
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4
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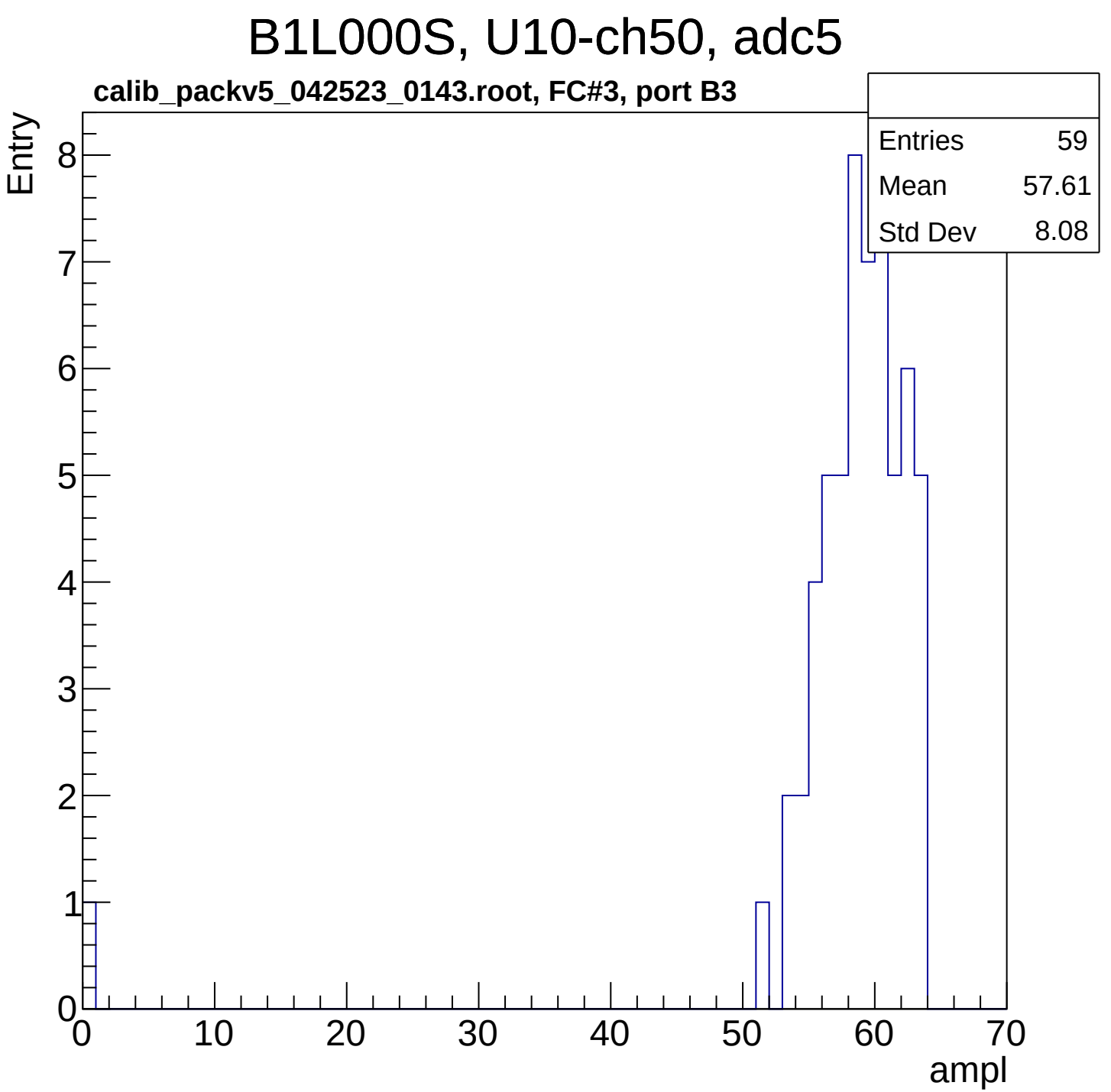
B1L000S, U10-ch50, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.61
Std Dev 8.08
Entry
ampl
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2
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40
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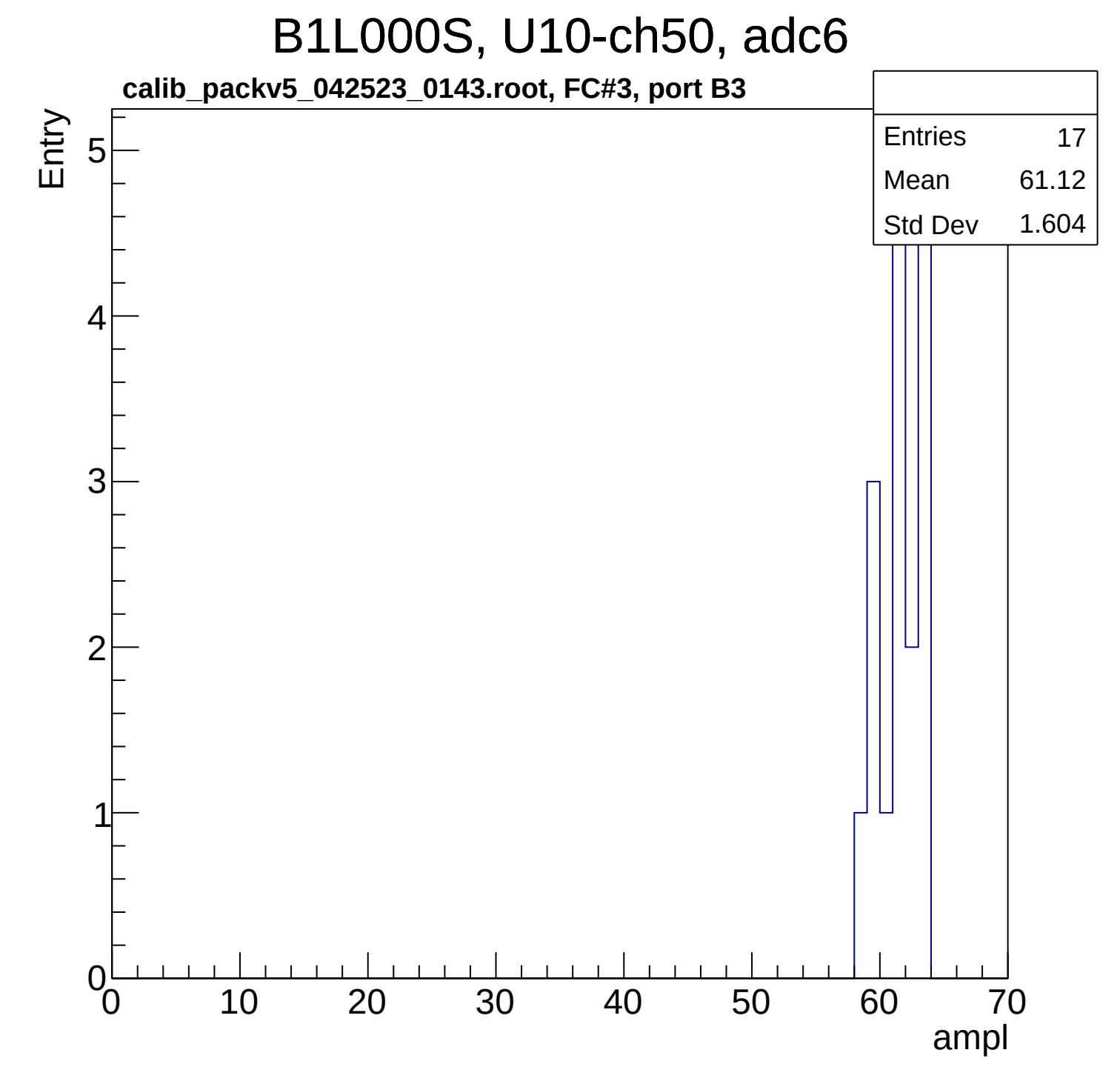
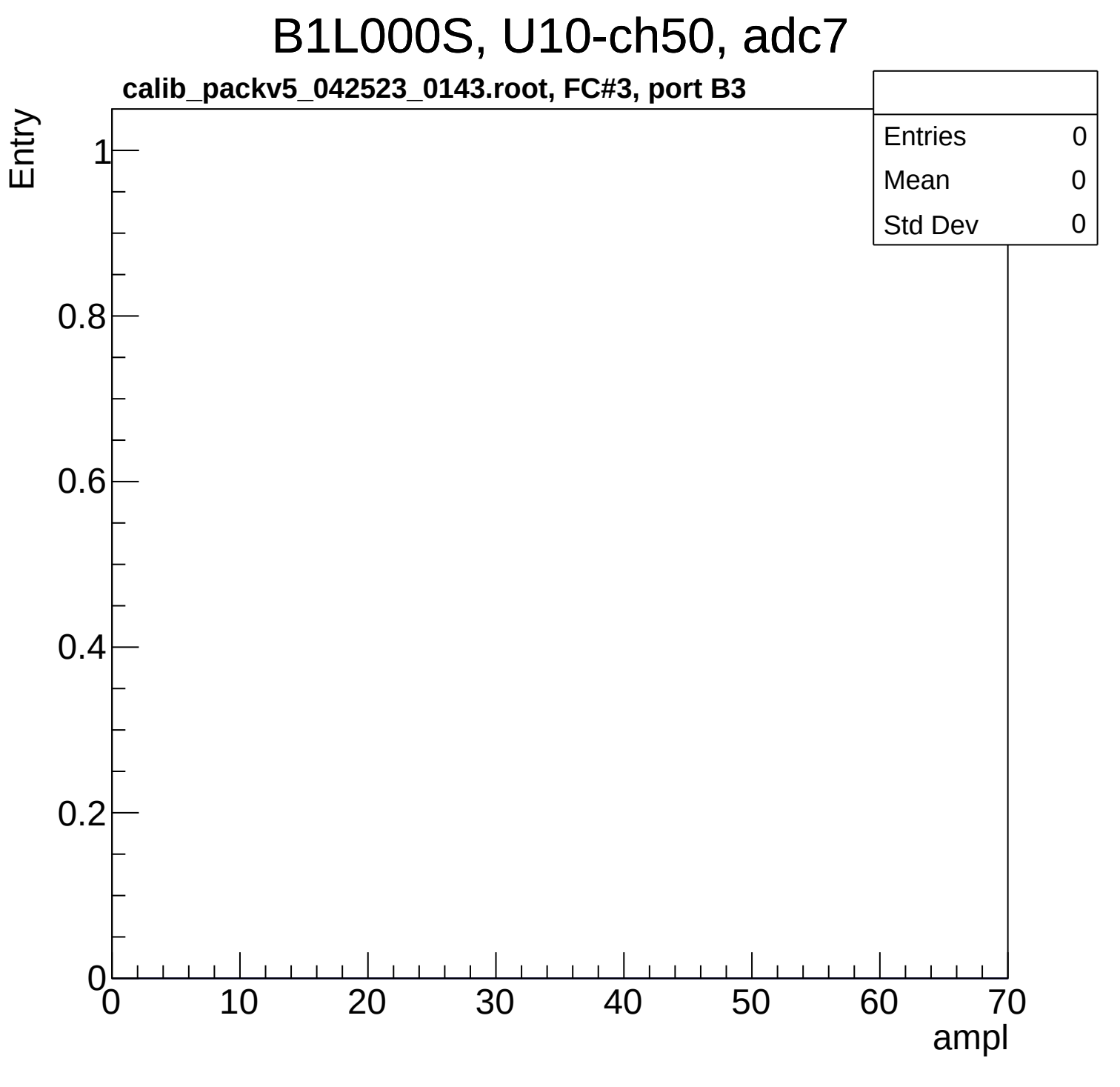
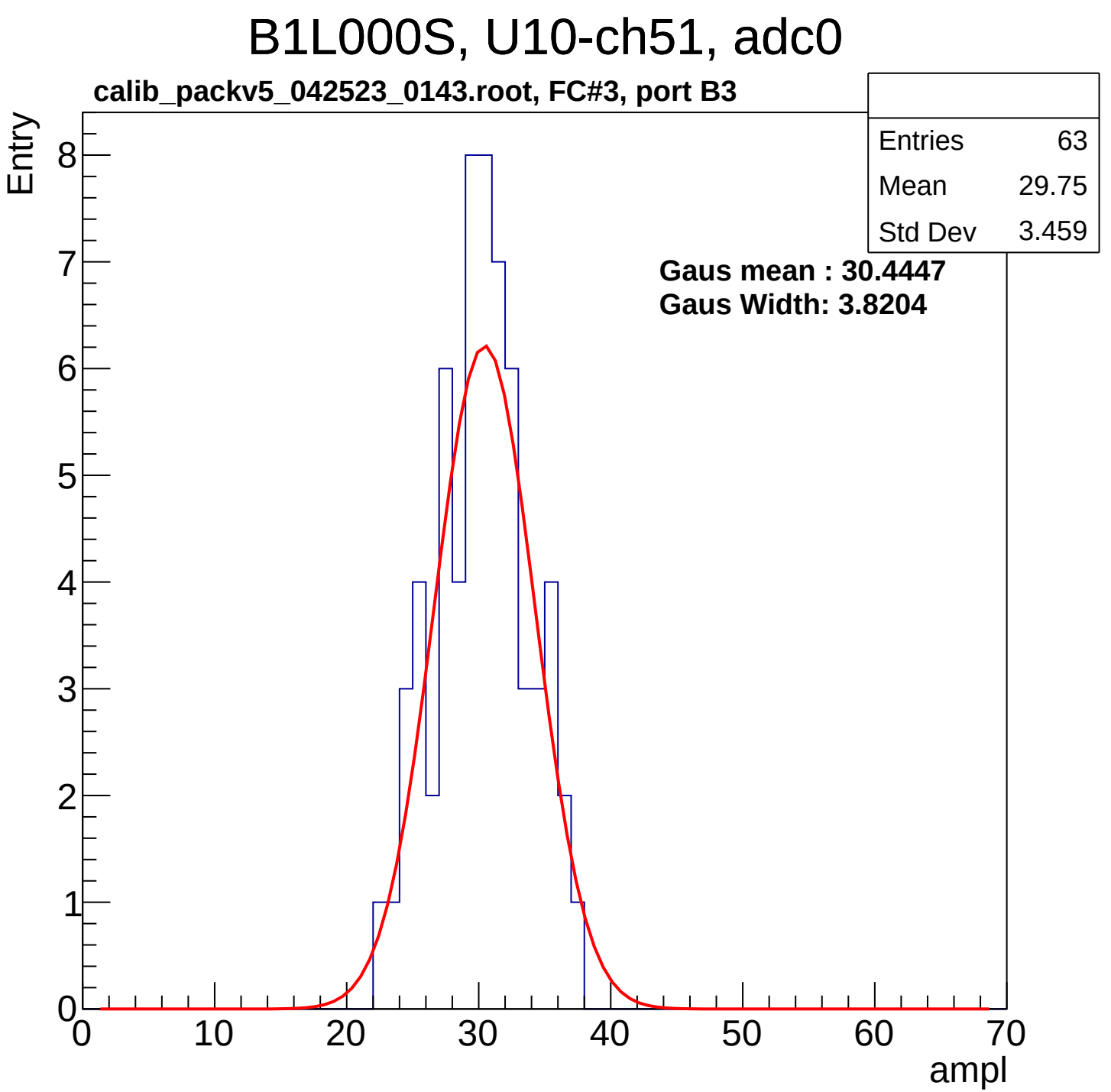

B1L000S, U10-ch50, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.12
Std Dev 1.604
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch50, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch51, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 29.75
Std Dev 3.459
Gaus mean : 30.4447
Gaus Width: 3.8204
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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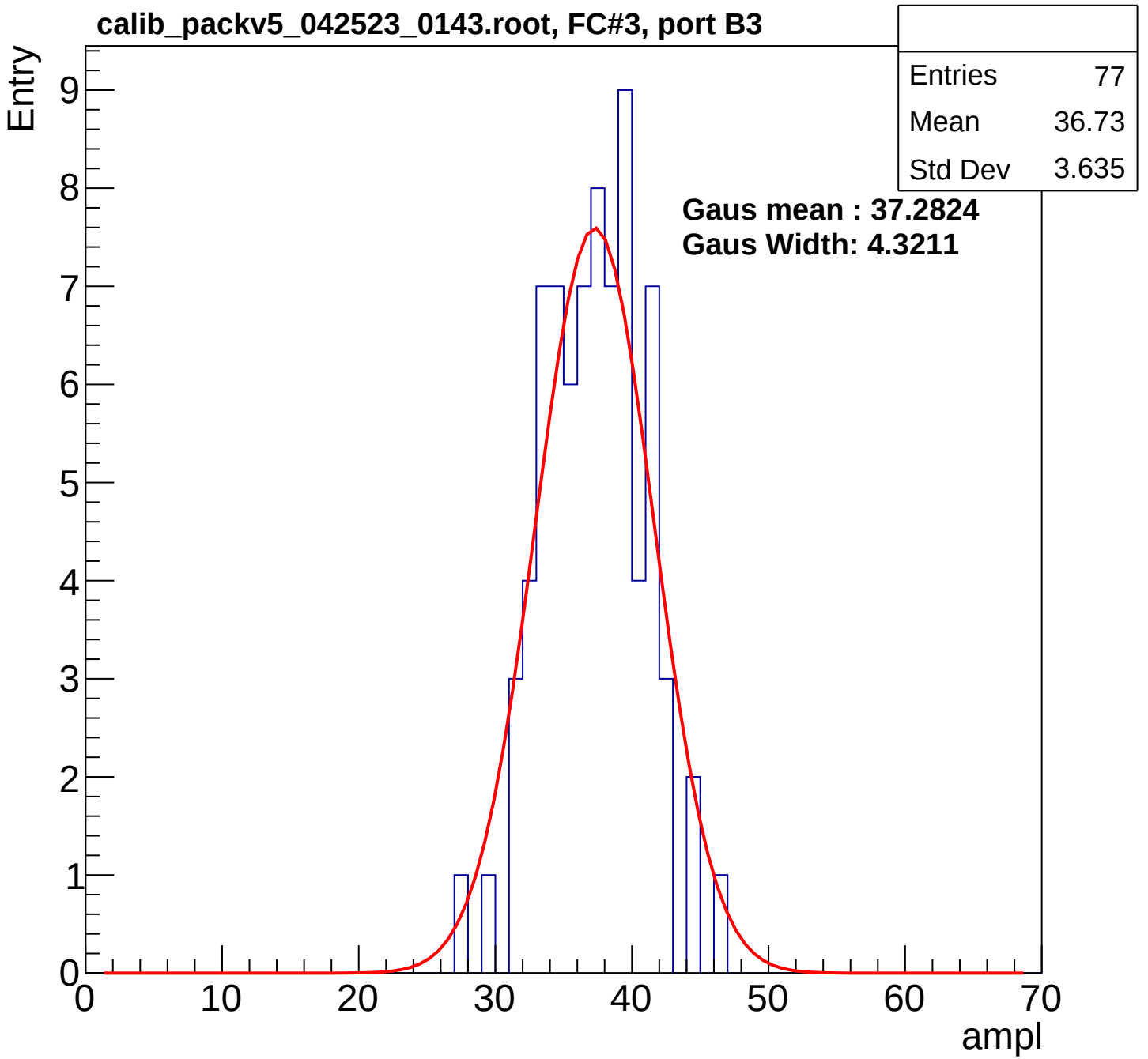
B1L000S, U10-ch51, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 36.73
Std Dev 3.635
Gaus mean : 37.2824
Gaus Width: 4.3211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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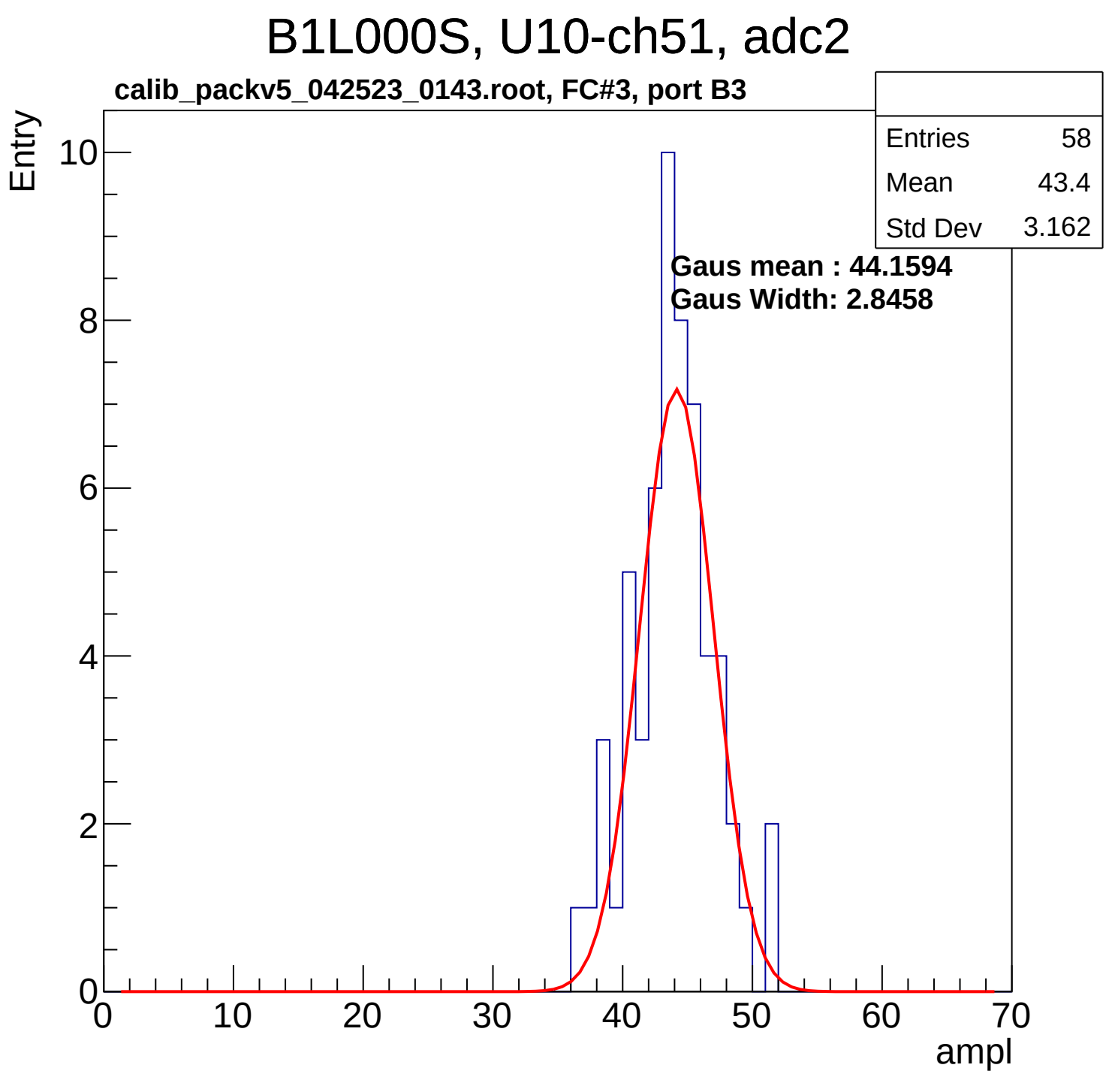
B1L000S, U10-ch51, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 43.4
Std Dev 3.162
Gaus mean : 44.1594
Gaus Width: 2.8458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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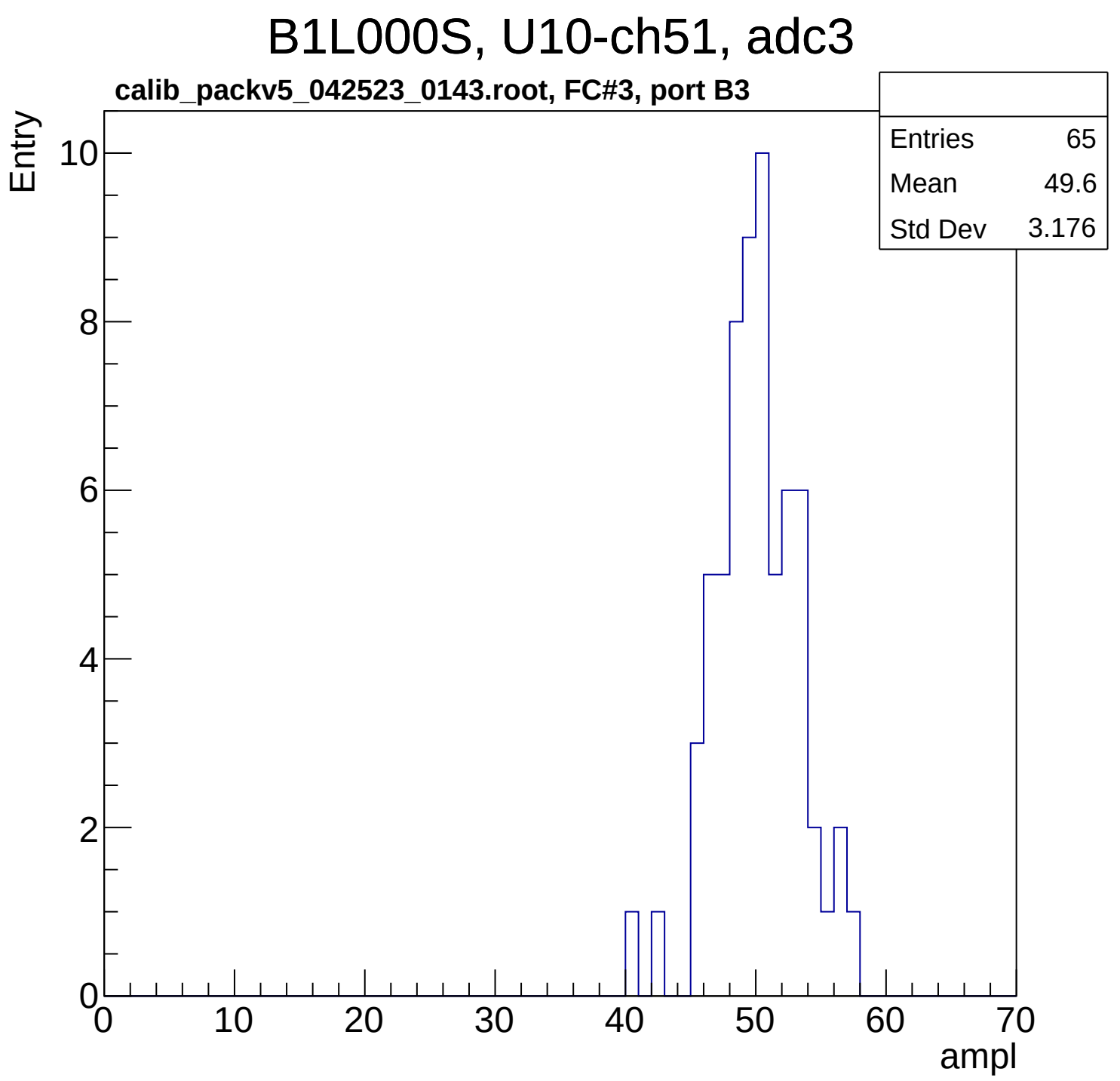
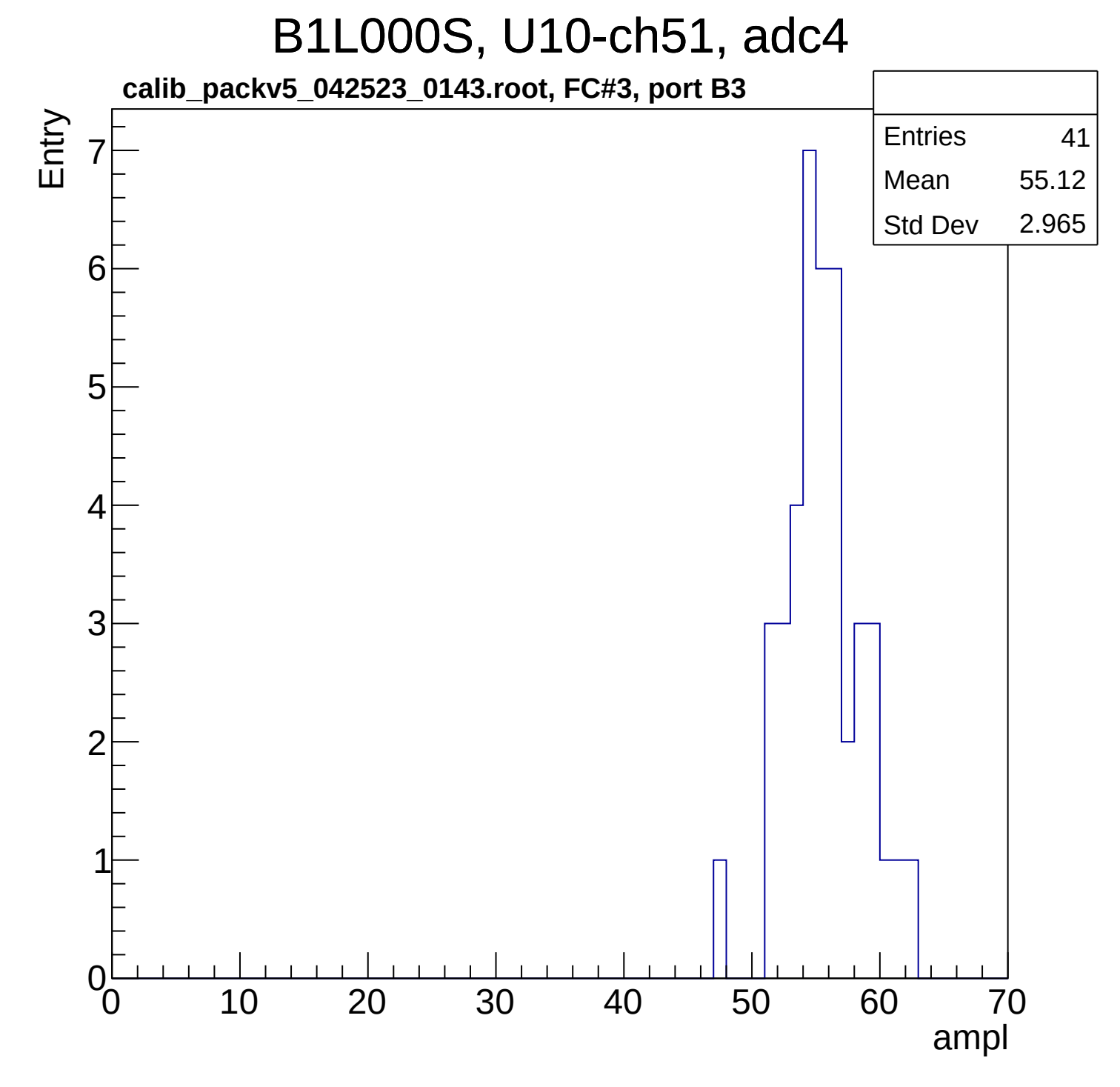
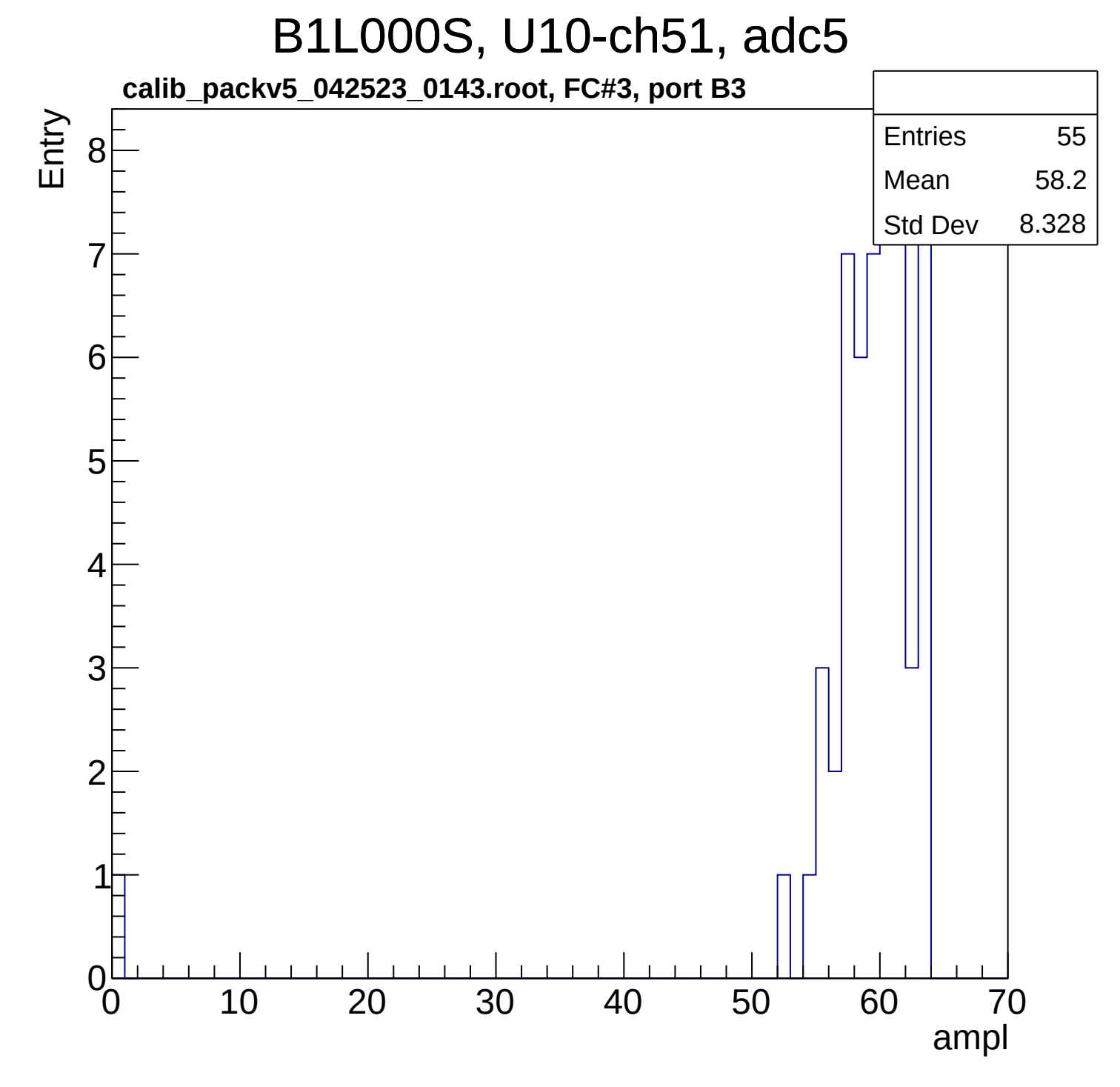

B1L000S, U10-ch51, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 49.6
Std Dev 3.176
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch51, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 55.12
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch51, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.2
Std Dev 8.328
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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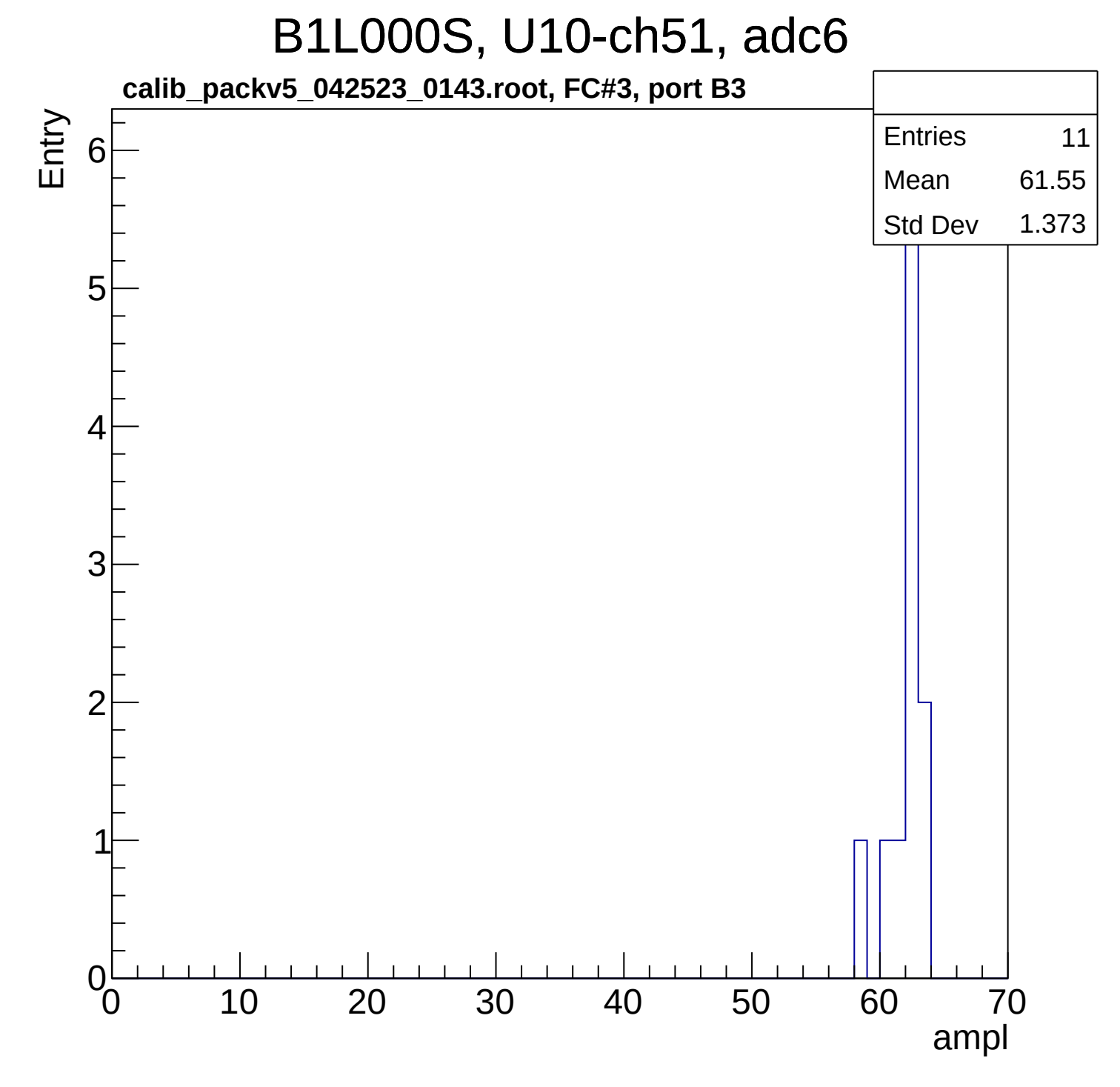
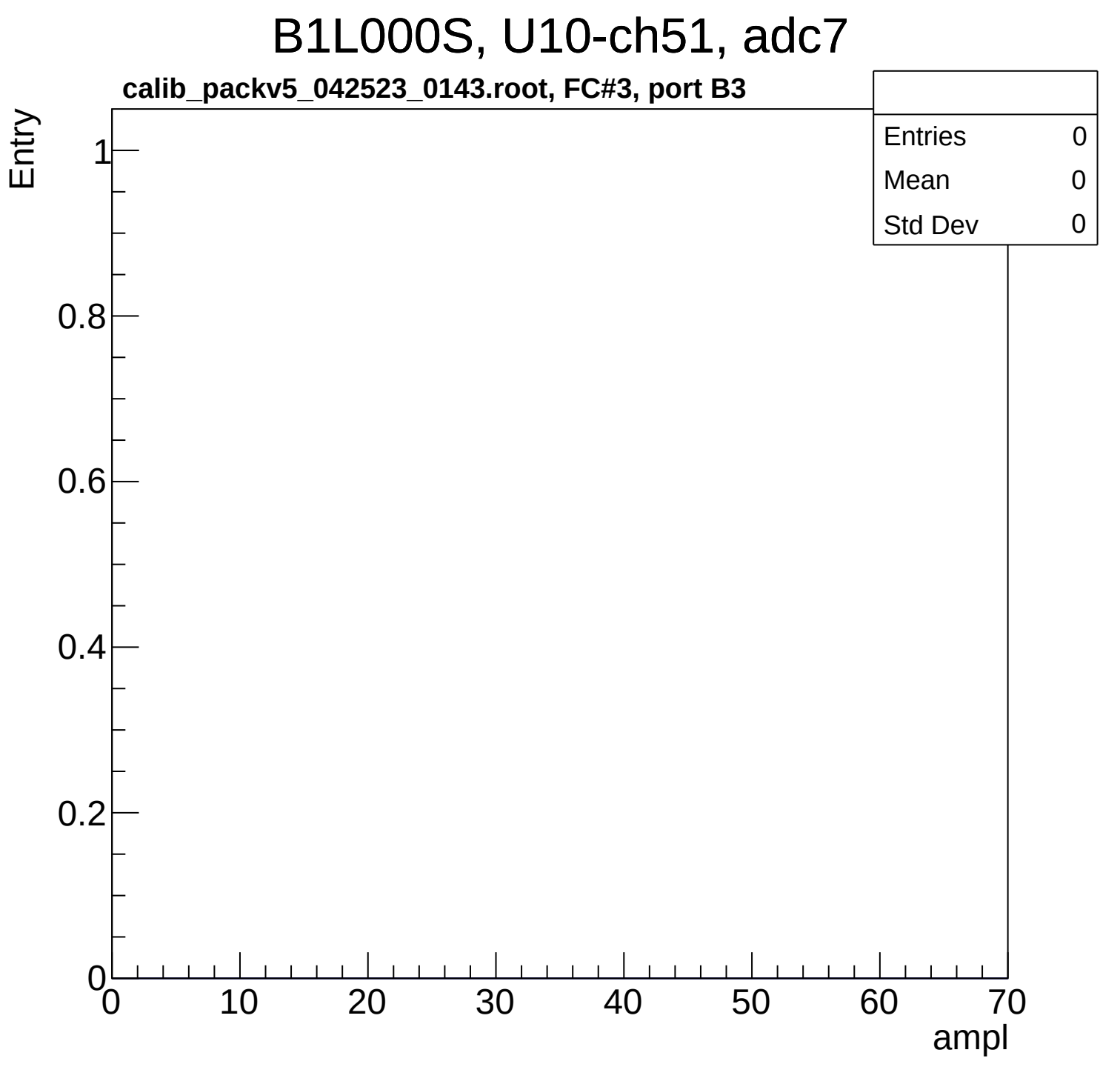
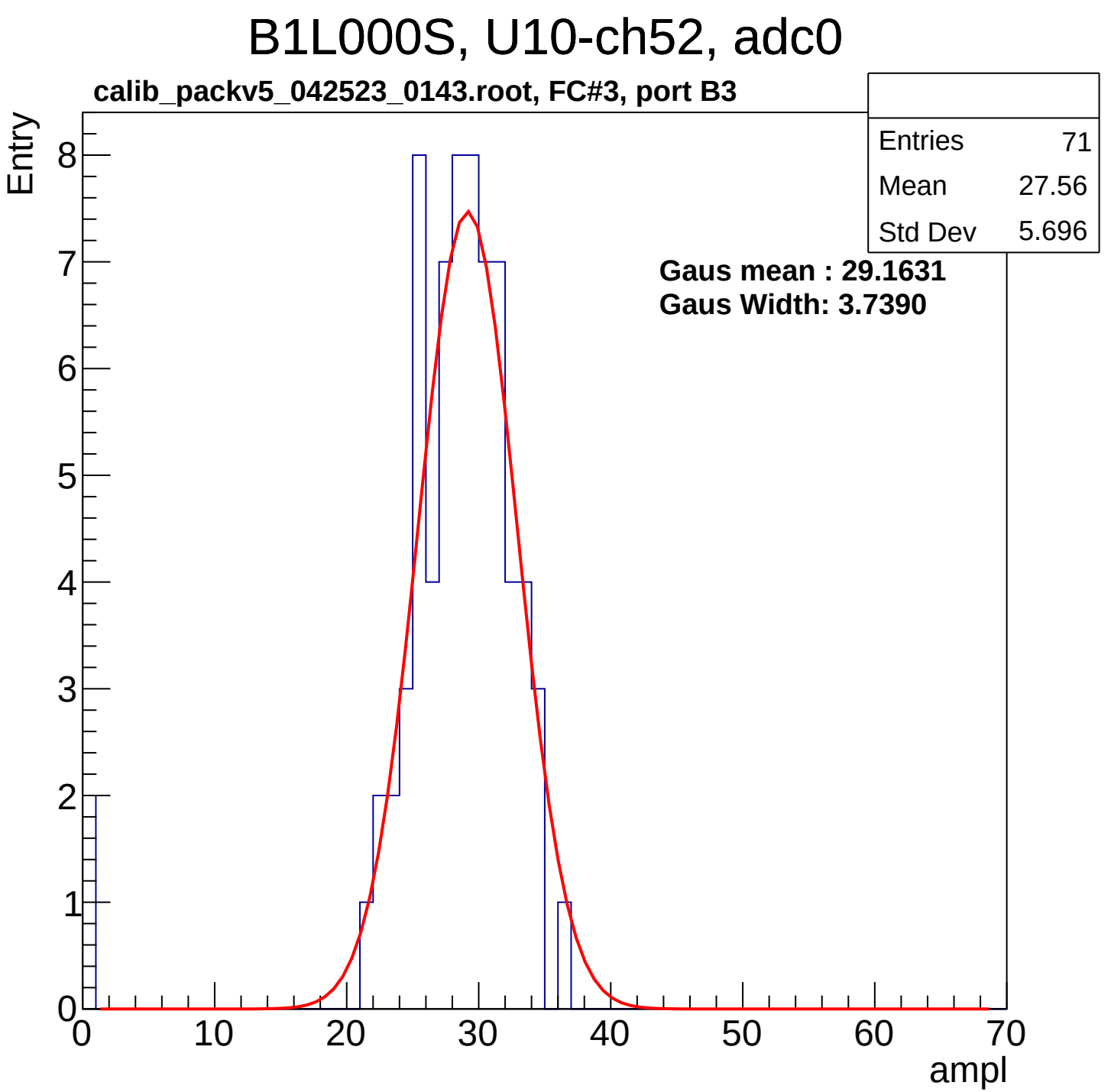
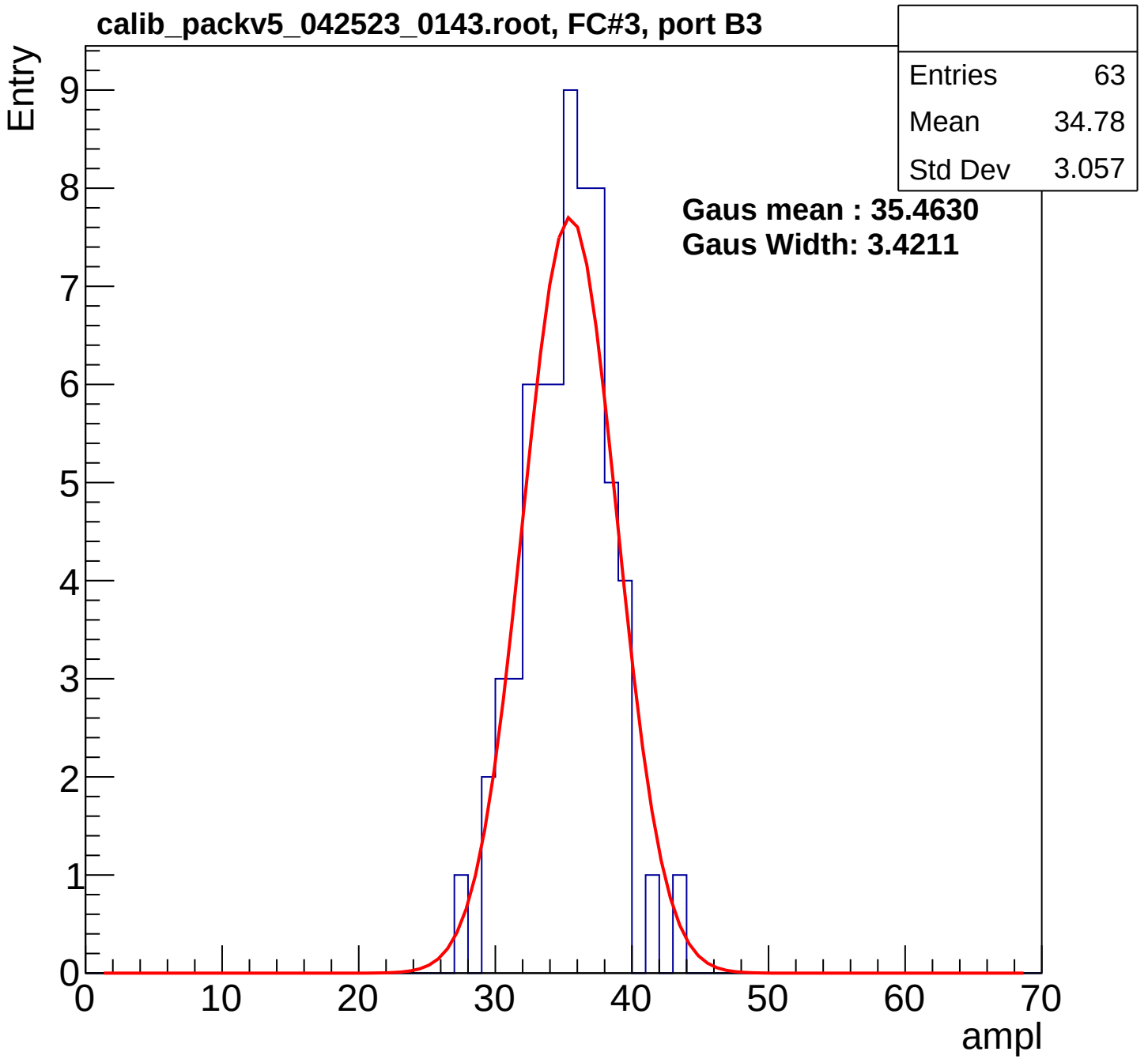
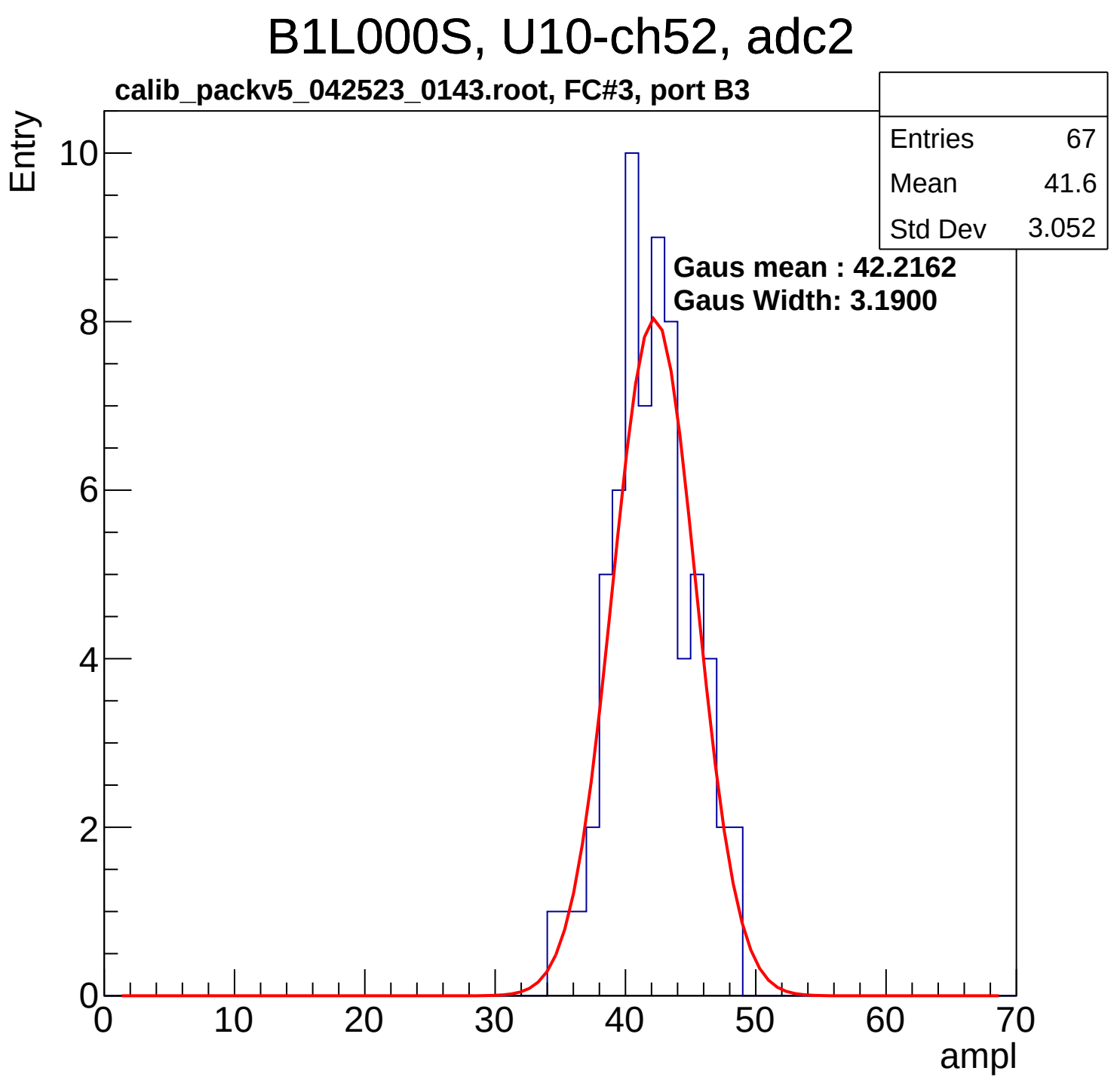
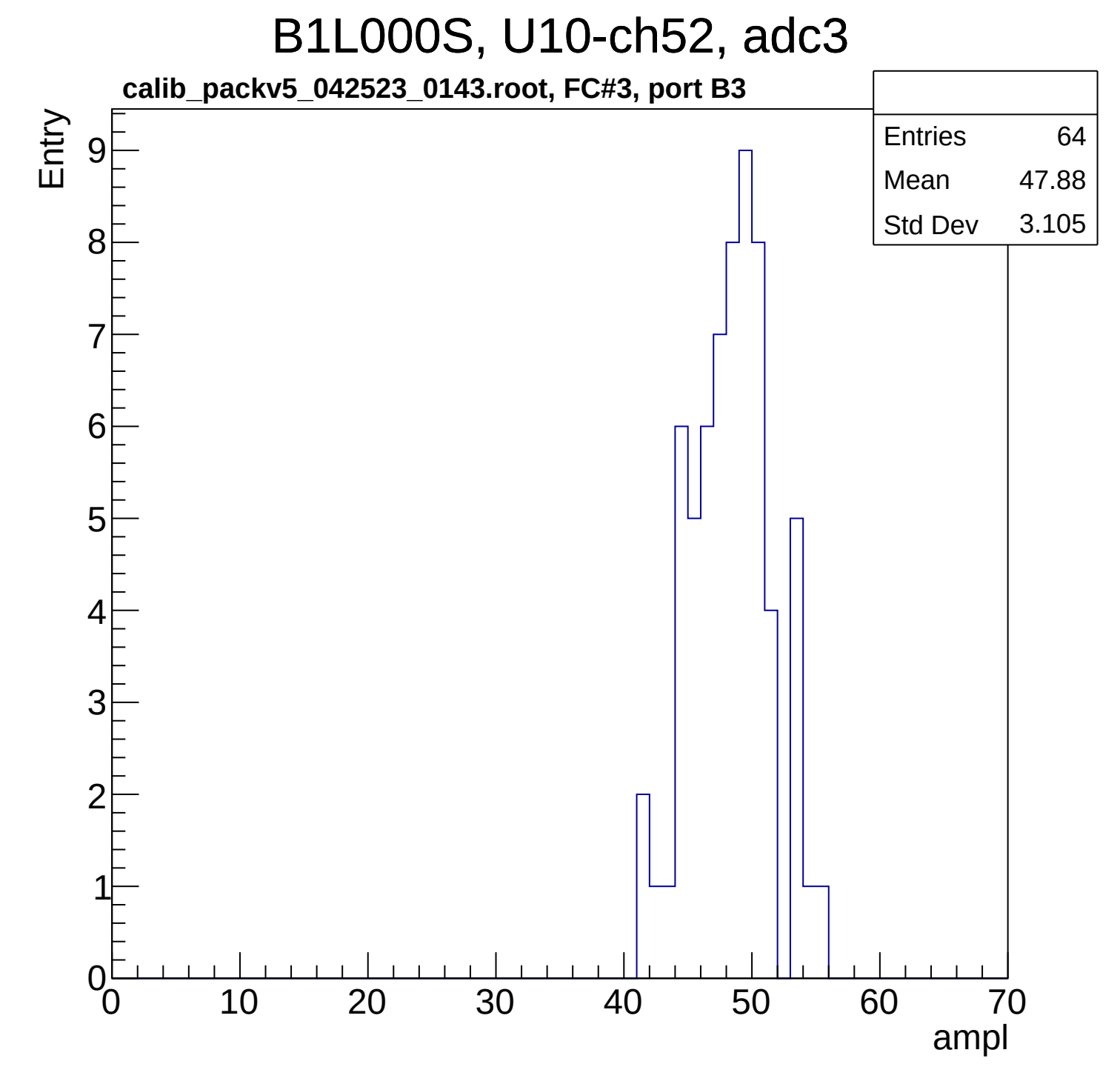

B1L000S, U10-ch51, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.55
Std Dev 1.373
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

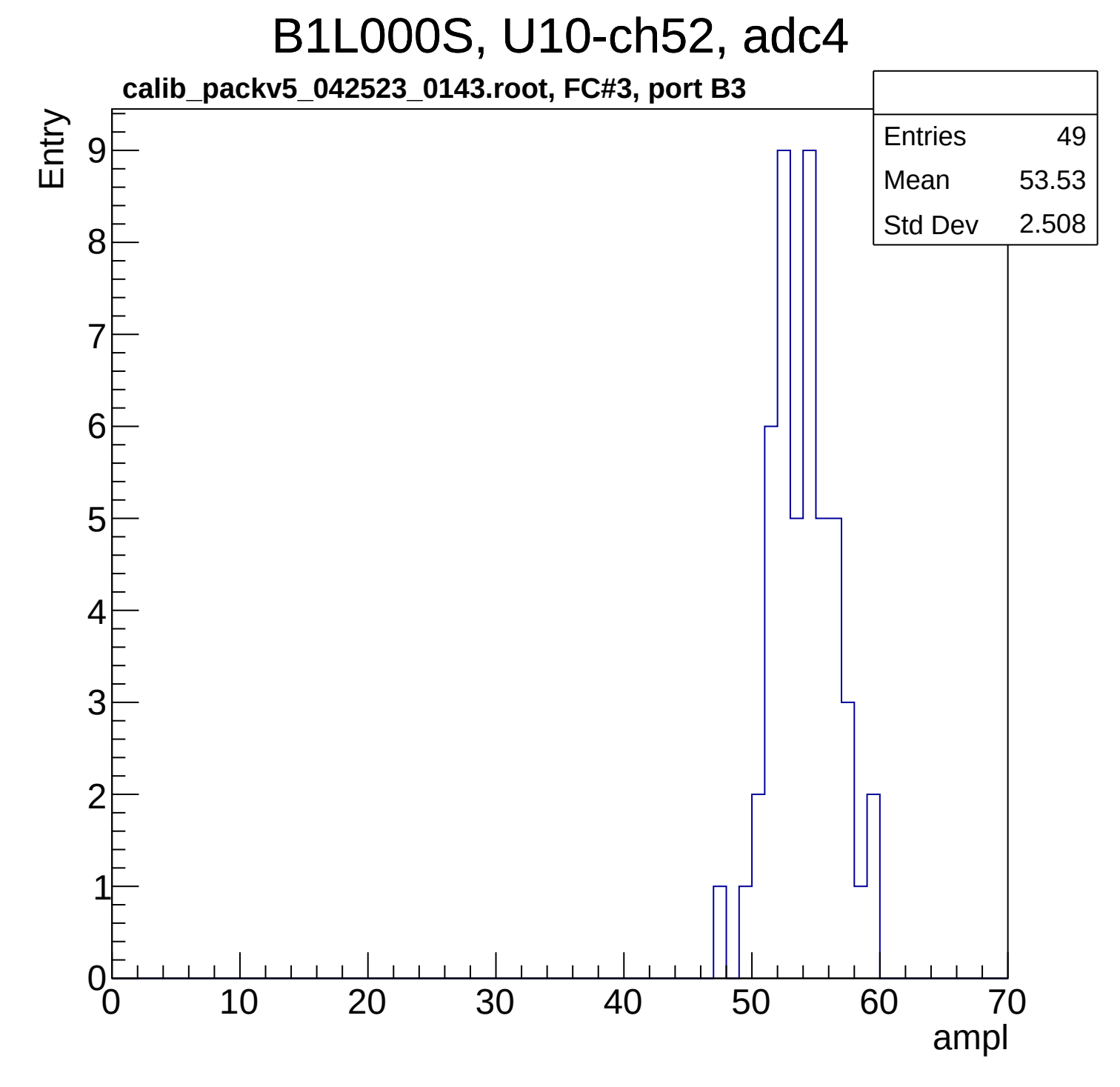

B1L000S, U10-ch51, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch52, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.56
Std Dev 5.696
Gaus mean : 29.1631
Gaus Width: 3.7390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch52, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.78
Std Dev 3.057
Gaus mean : 35.4630
Gaus Width: 3.4211
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch52, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.6
Std Dev 3.052
Gaus mean : 42.2162
Gaus Width: 3.1900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch52, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.88
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch52, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.53
Std Dev 2.508
Entry
ampl
0
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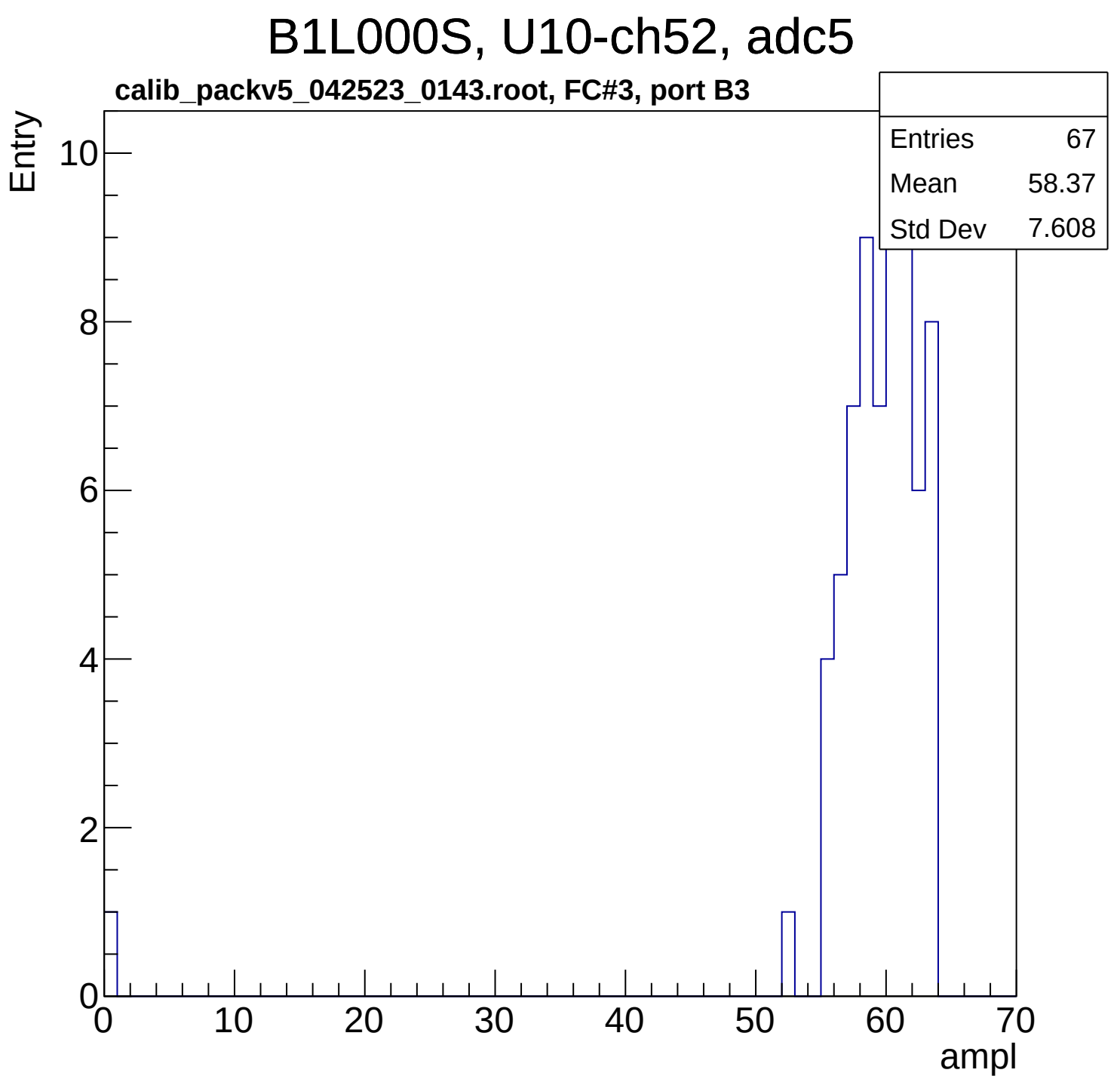
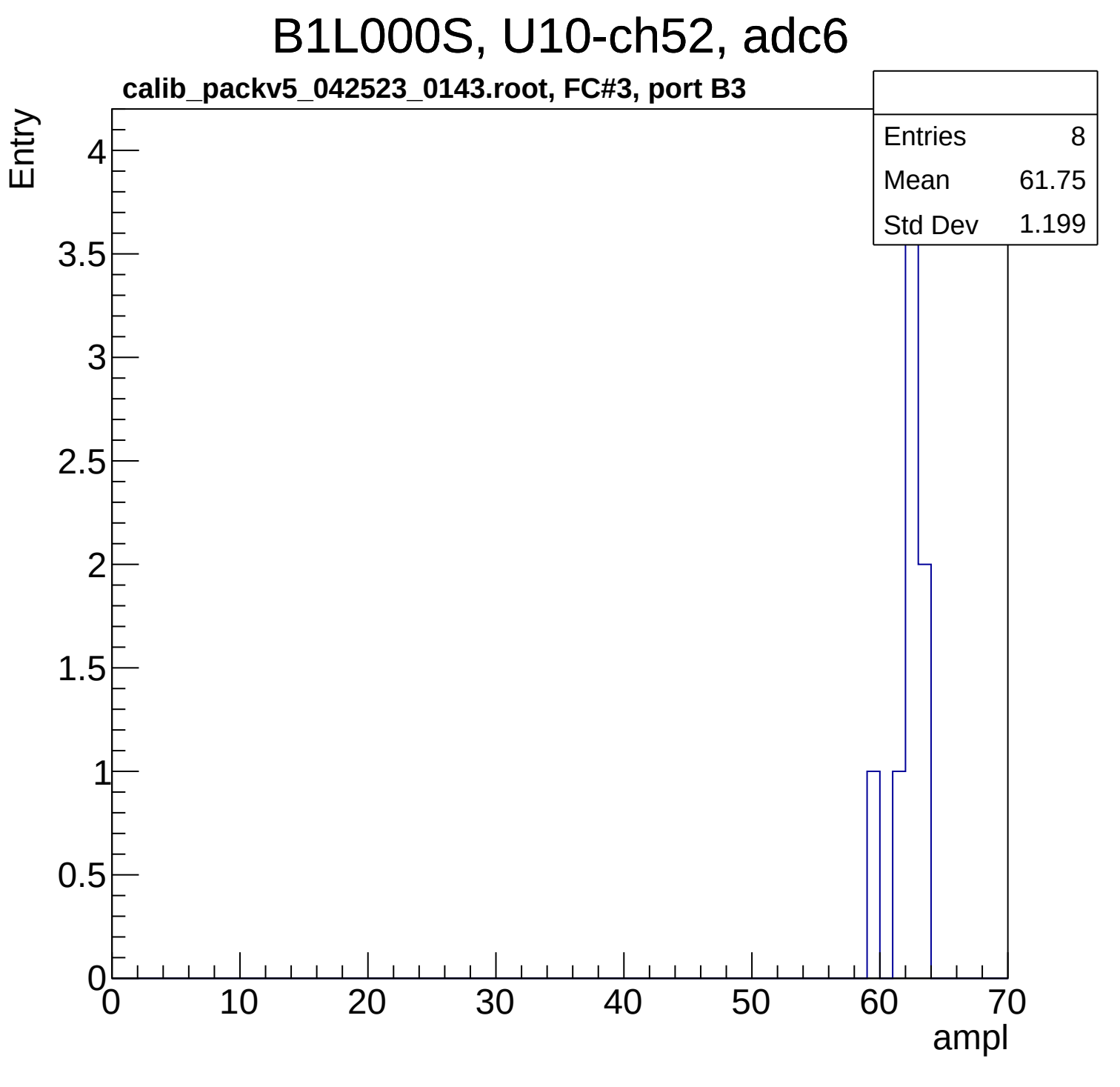
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch52, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 58.37
Std Dev 7.608
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch52, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.75
Std Dev 1.199
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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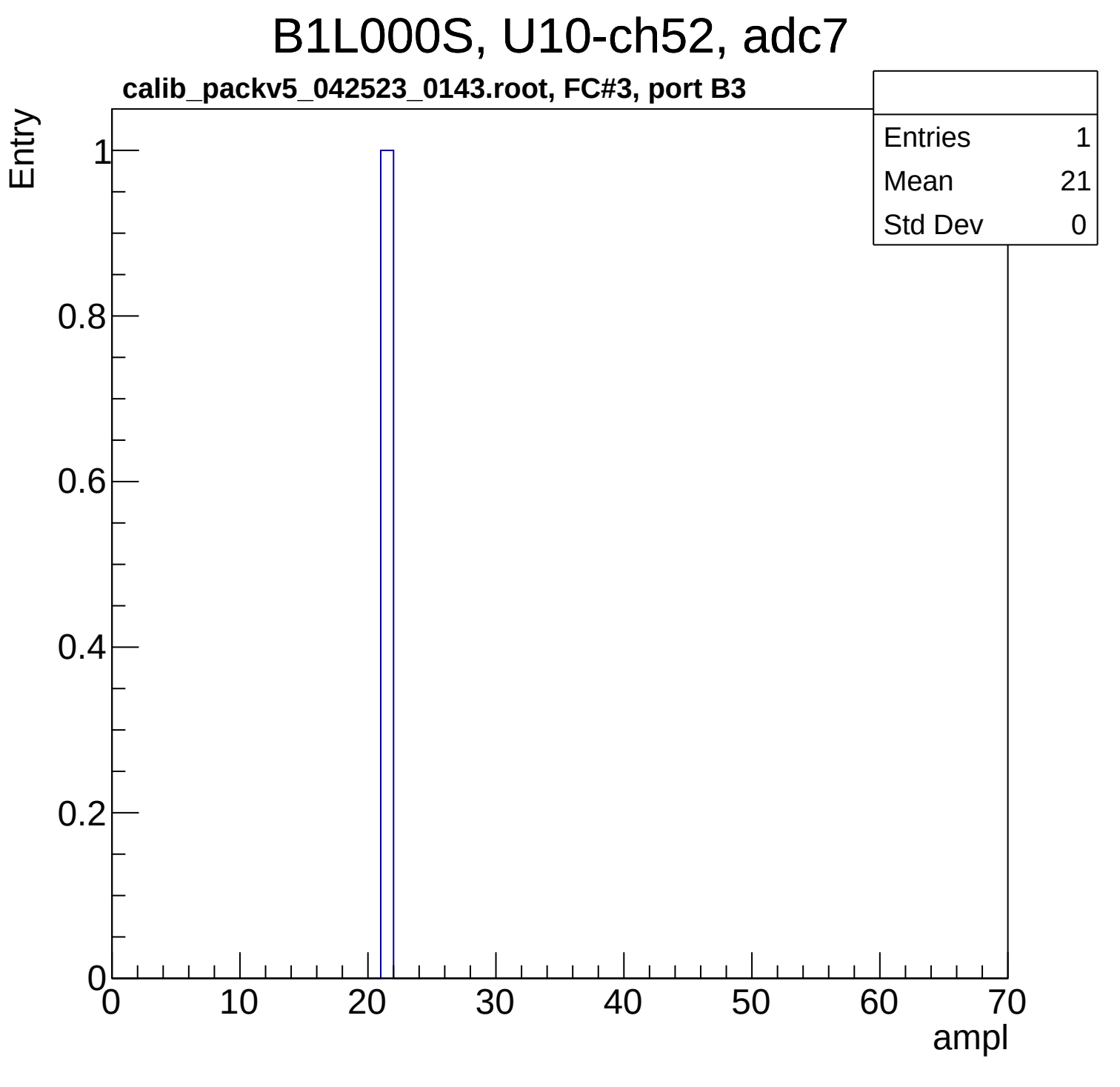
B1L000S, U10-ch52, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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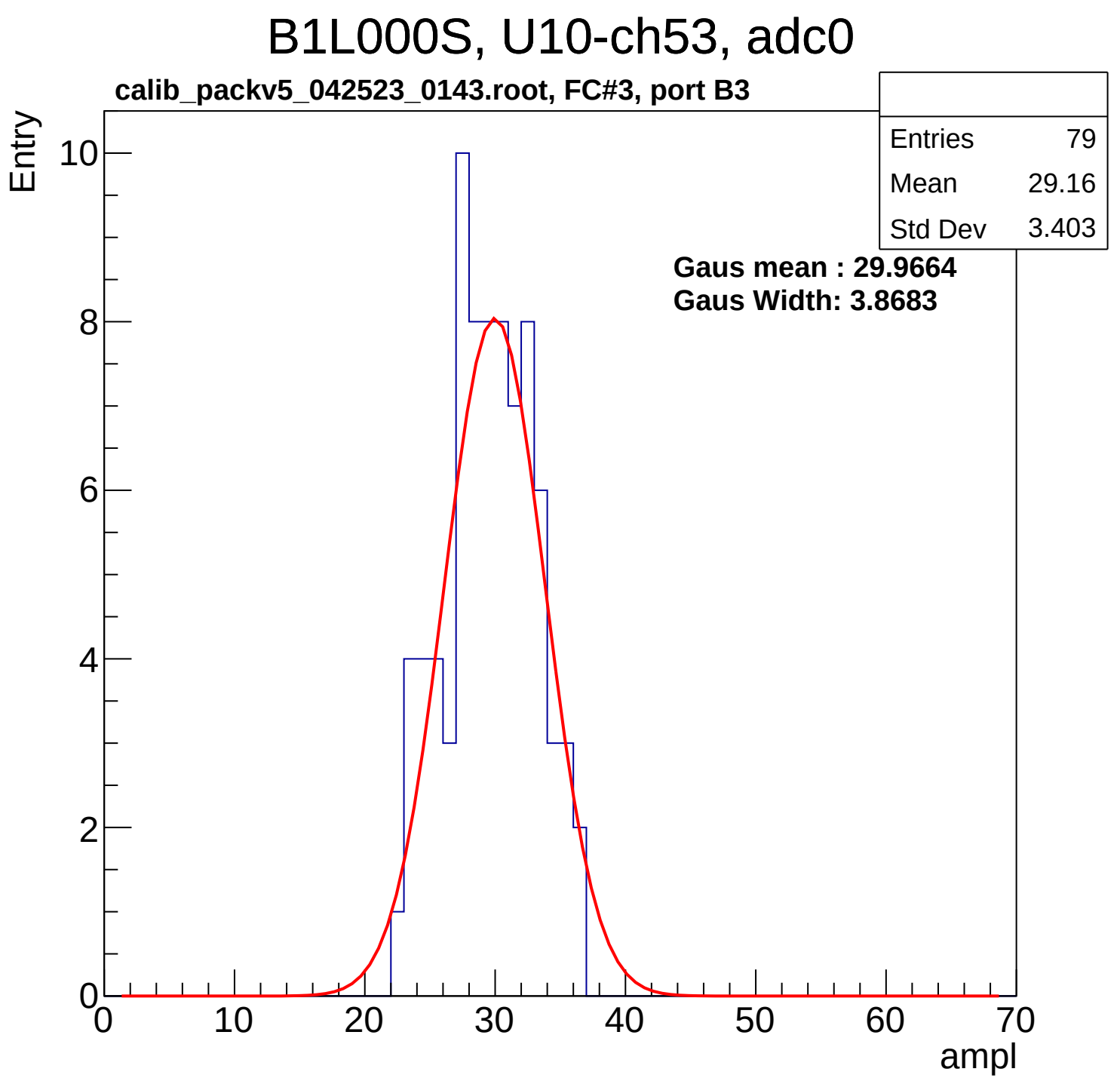
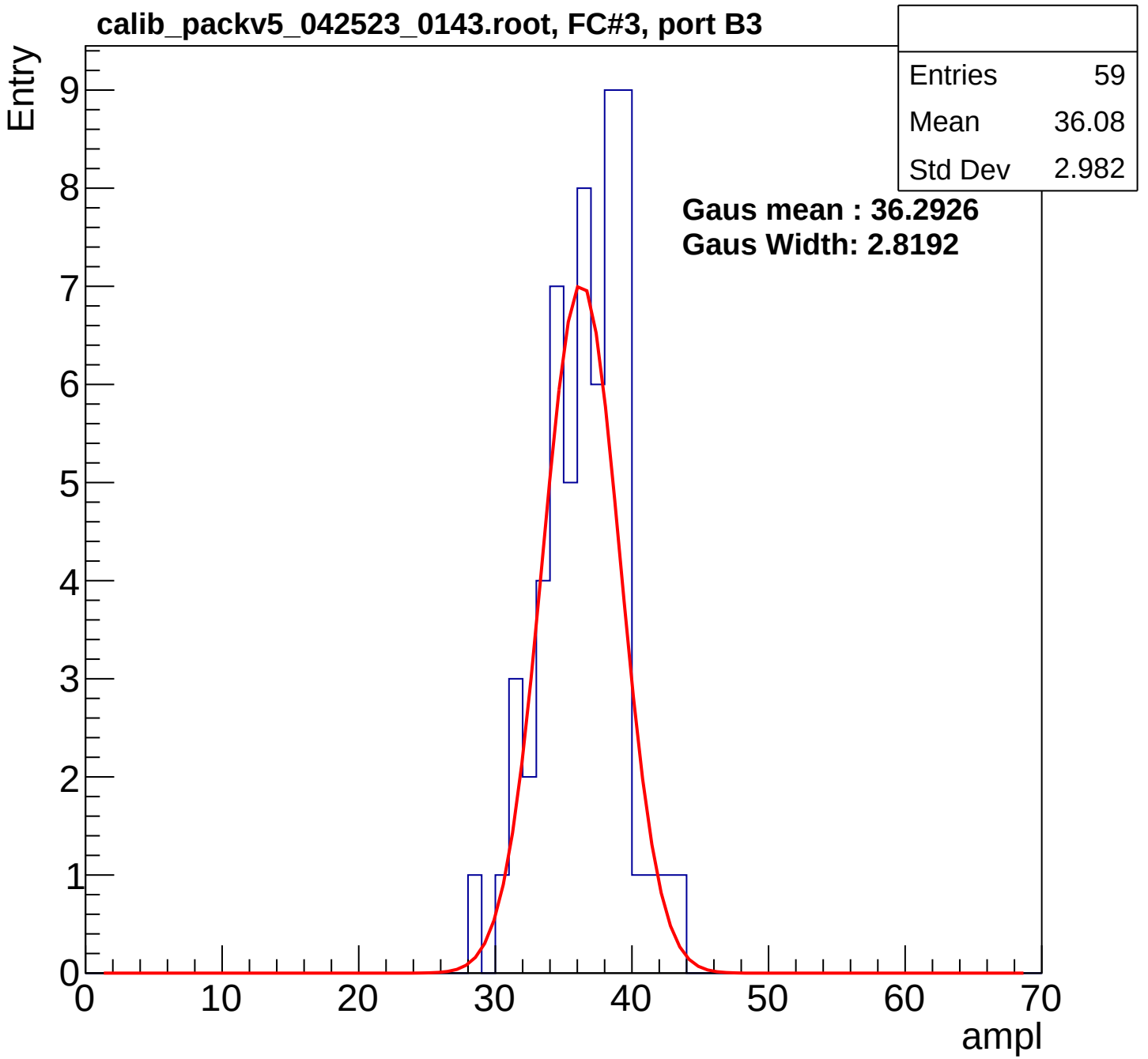
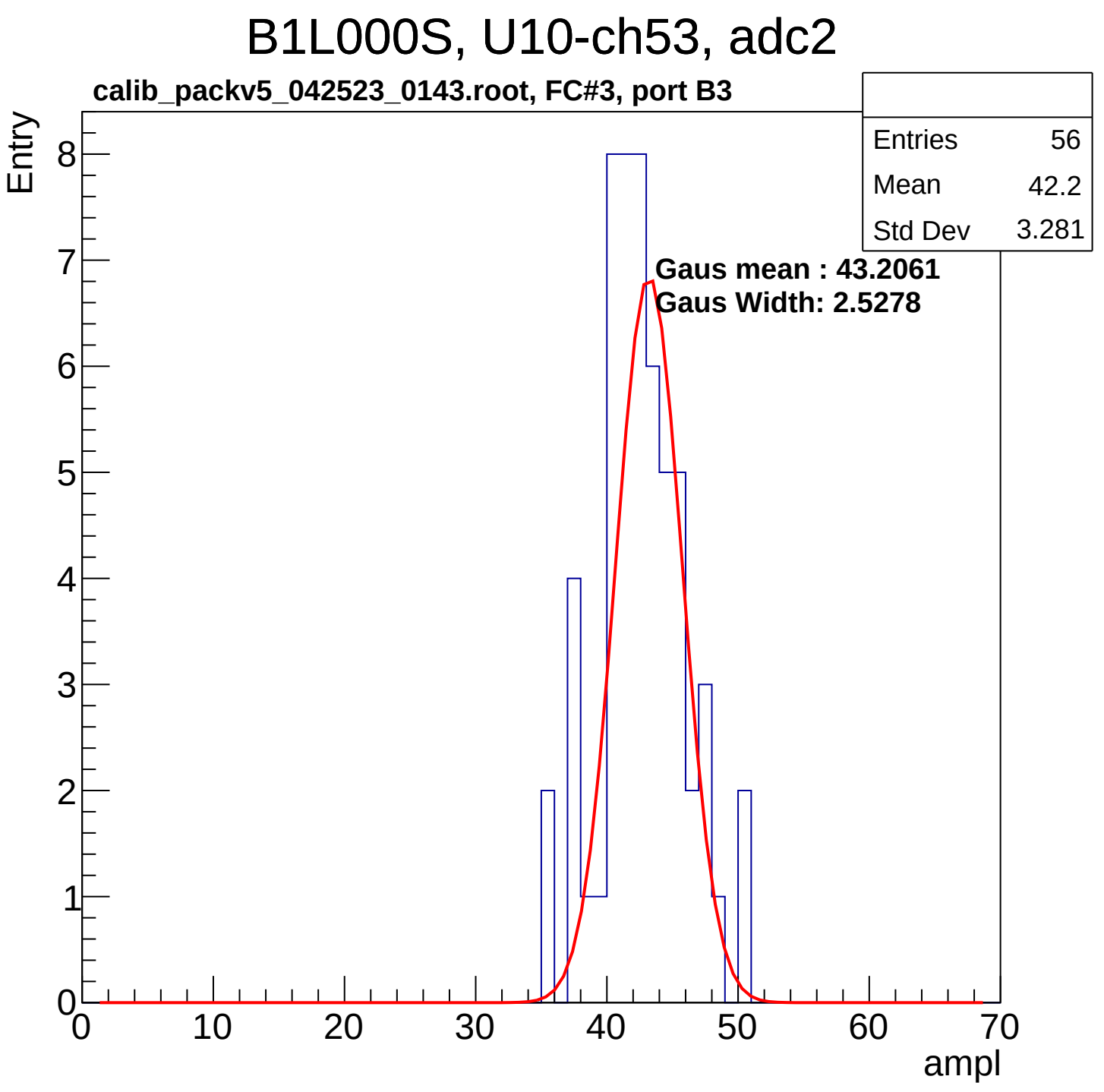
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch53, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 29.16
Std Dev 3.403
Gaus mean : 29.9664
Gaus Width: 3.8683
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch53, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 36.08
Std Dev 2.982
Gaus mean : 36.2926
Gaus Width: 2.8192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch53, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.2
Std Dev 3.281
Gaus mean : 43.2061
Gaus Width: 2.5278
Entry
ampl
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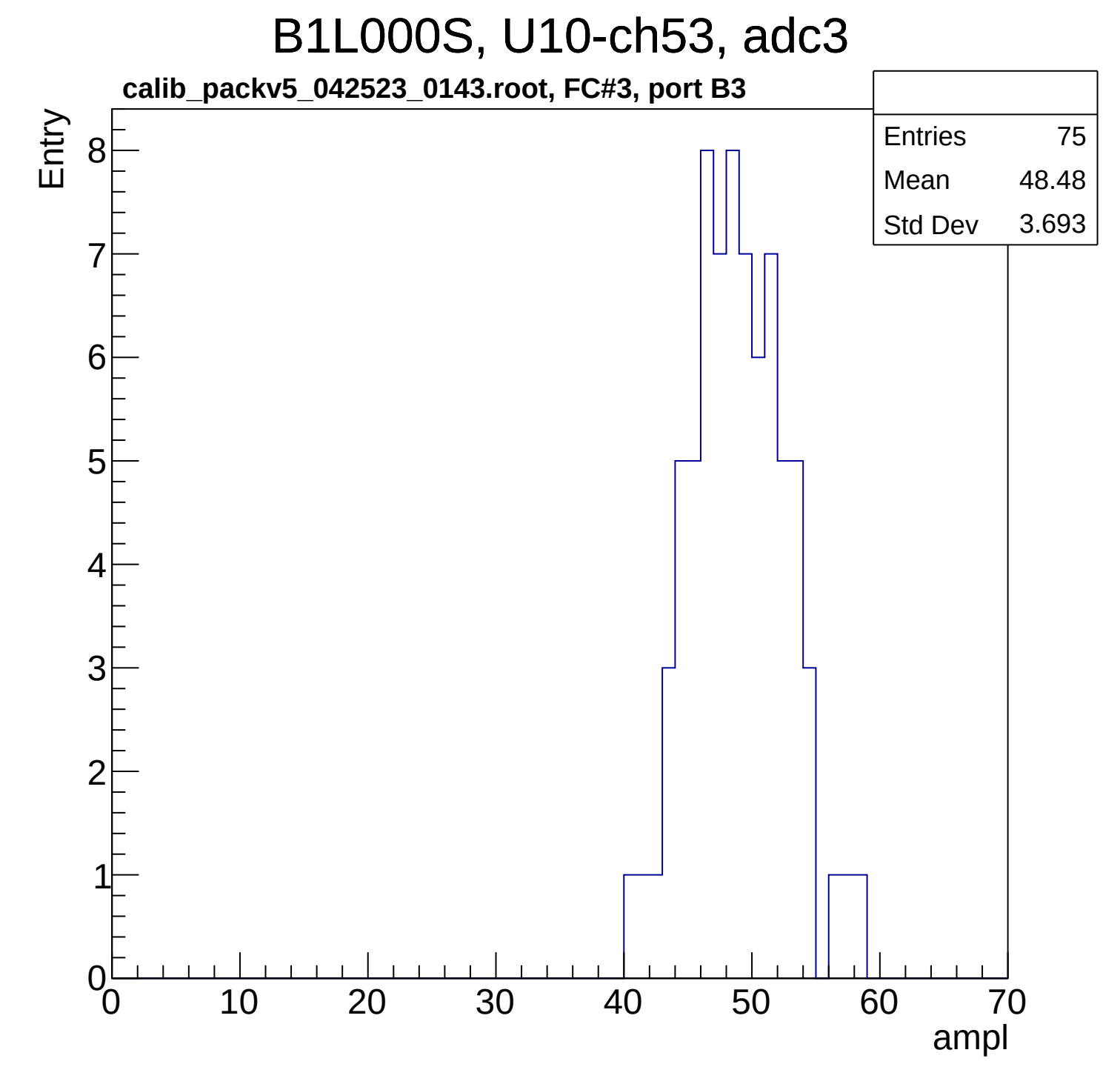
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch53, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 48.48
Std Dev 3.693
Entry
ampl
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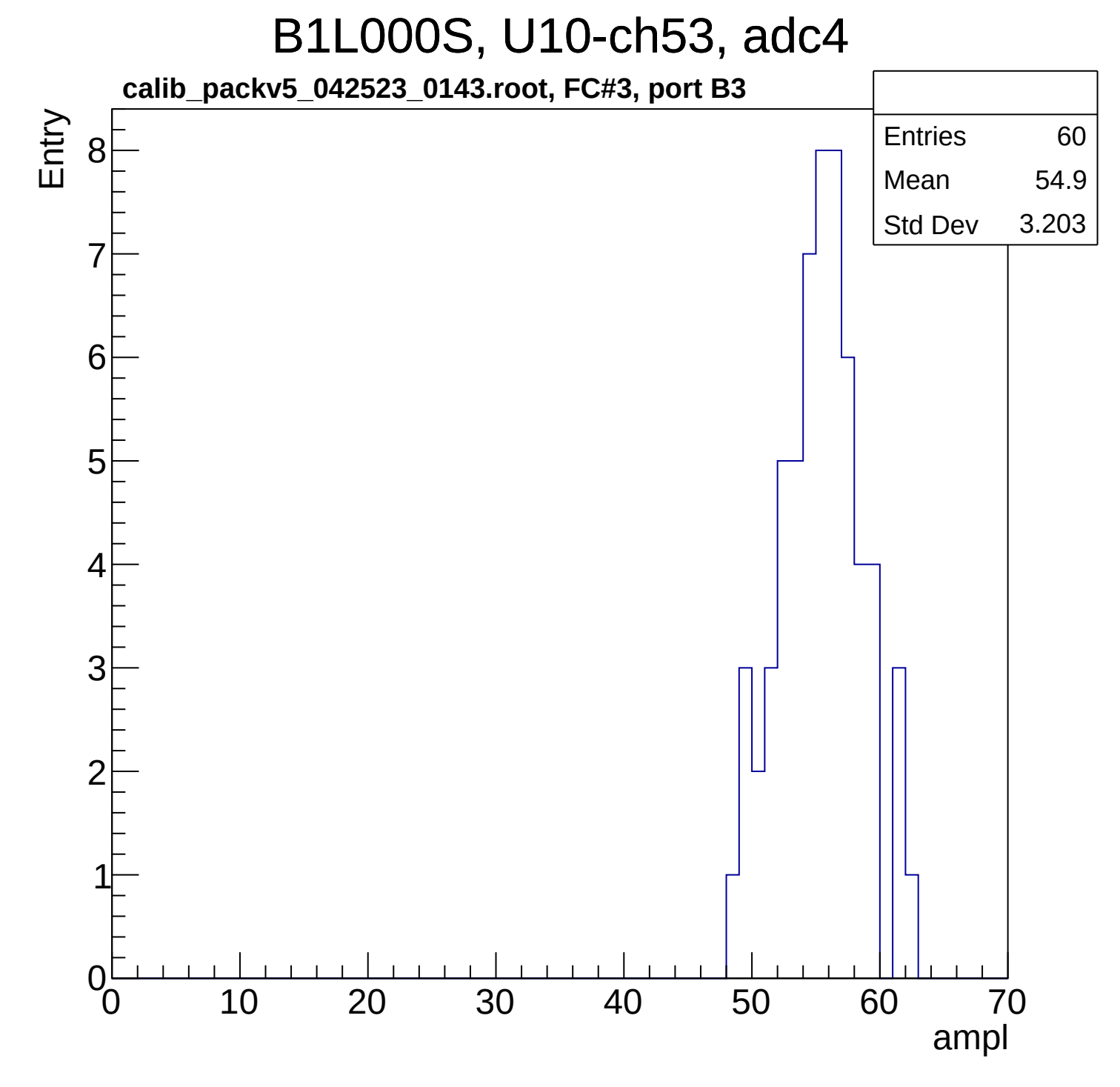
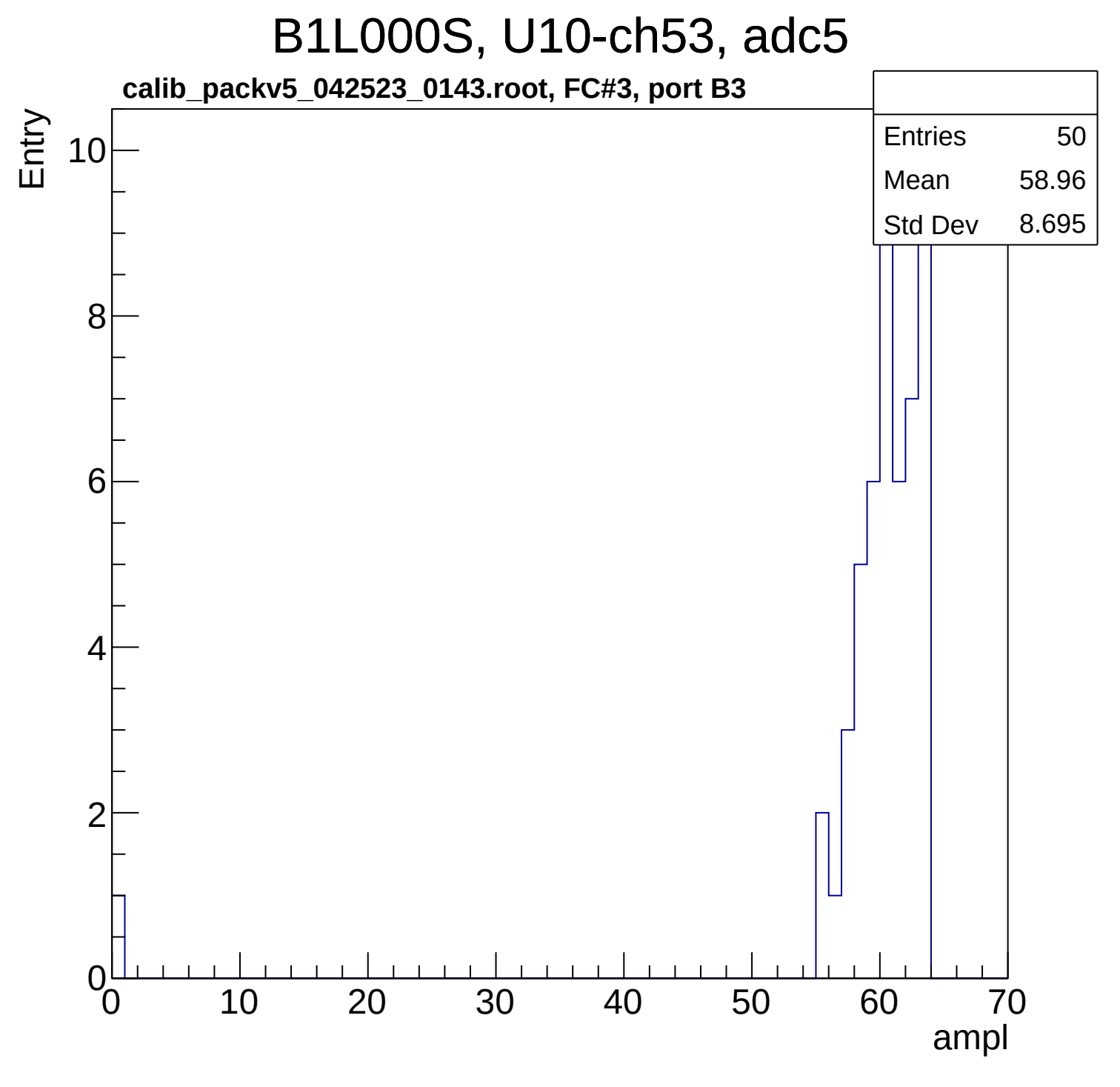
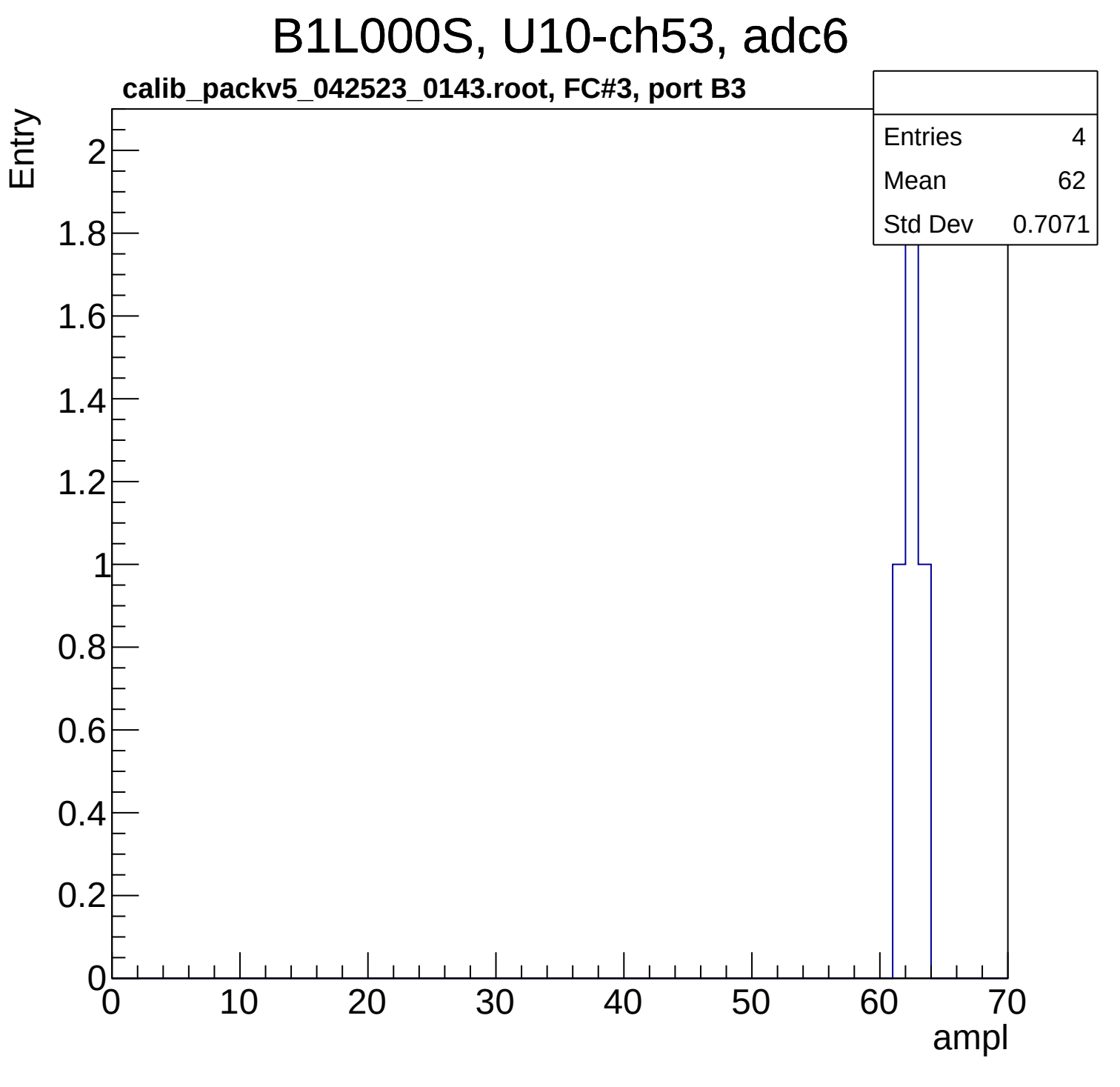
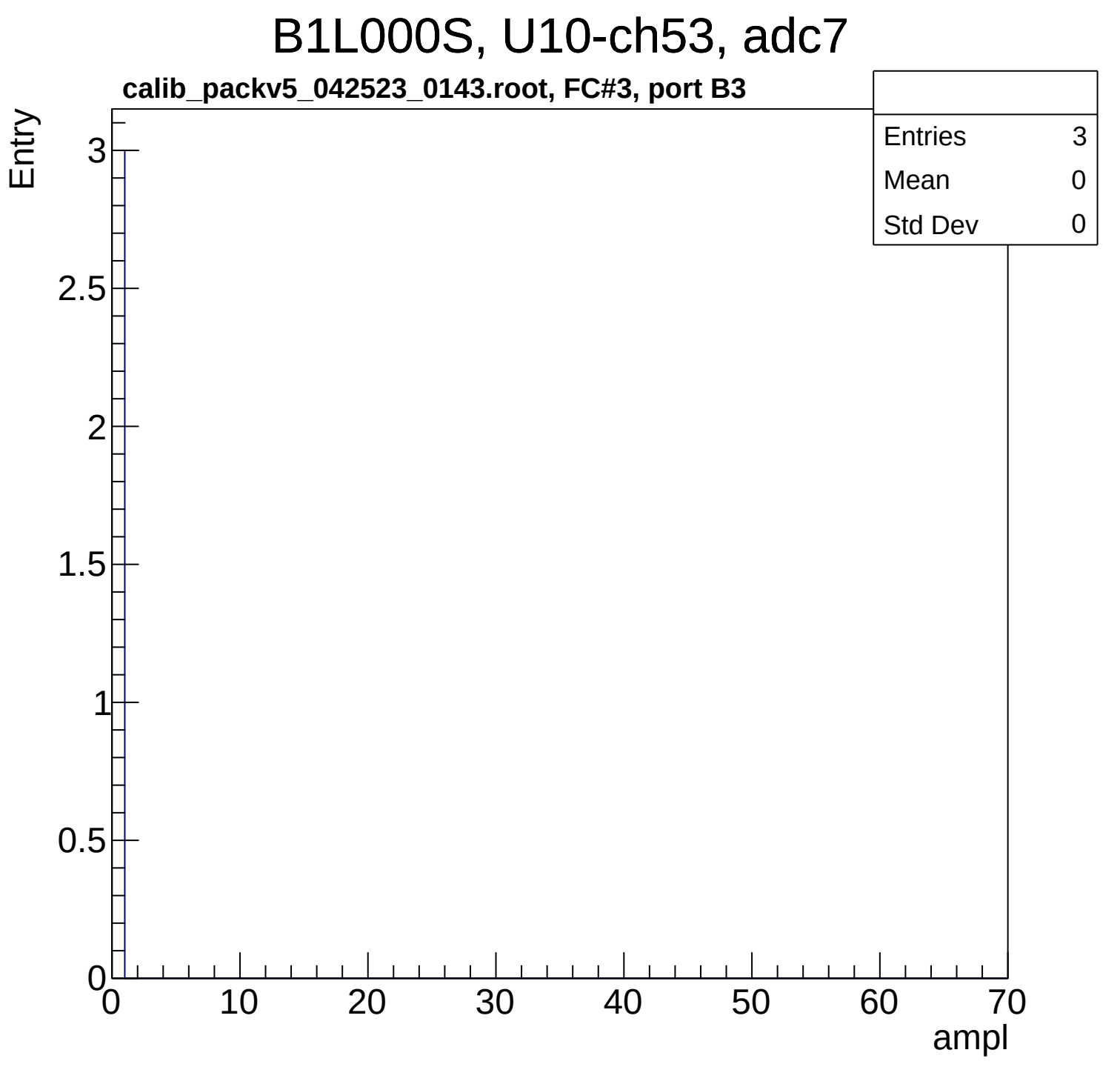
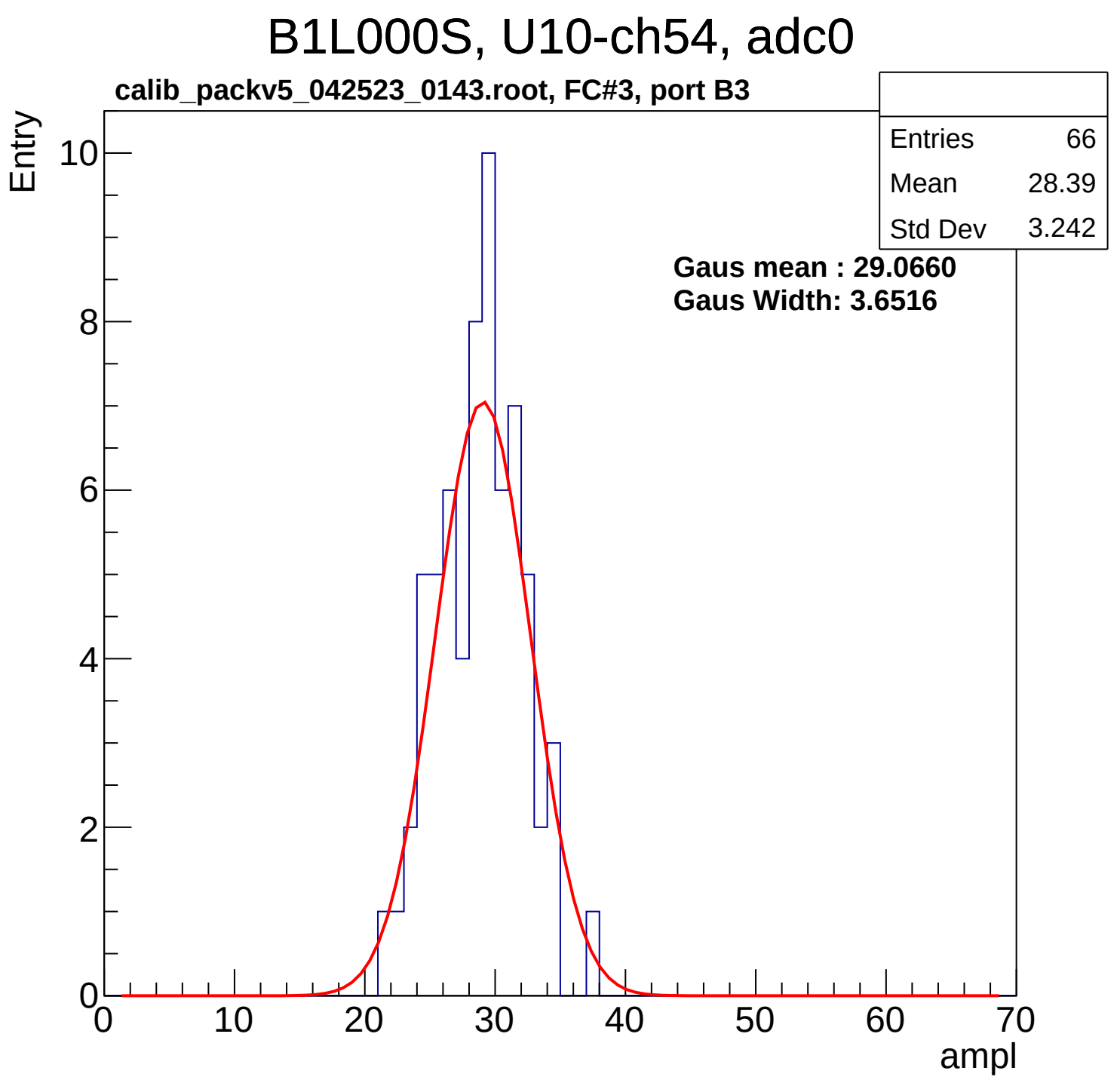
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

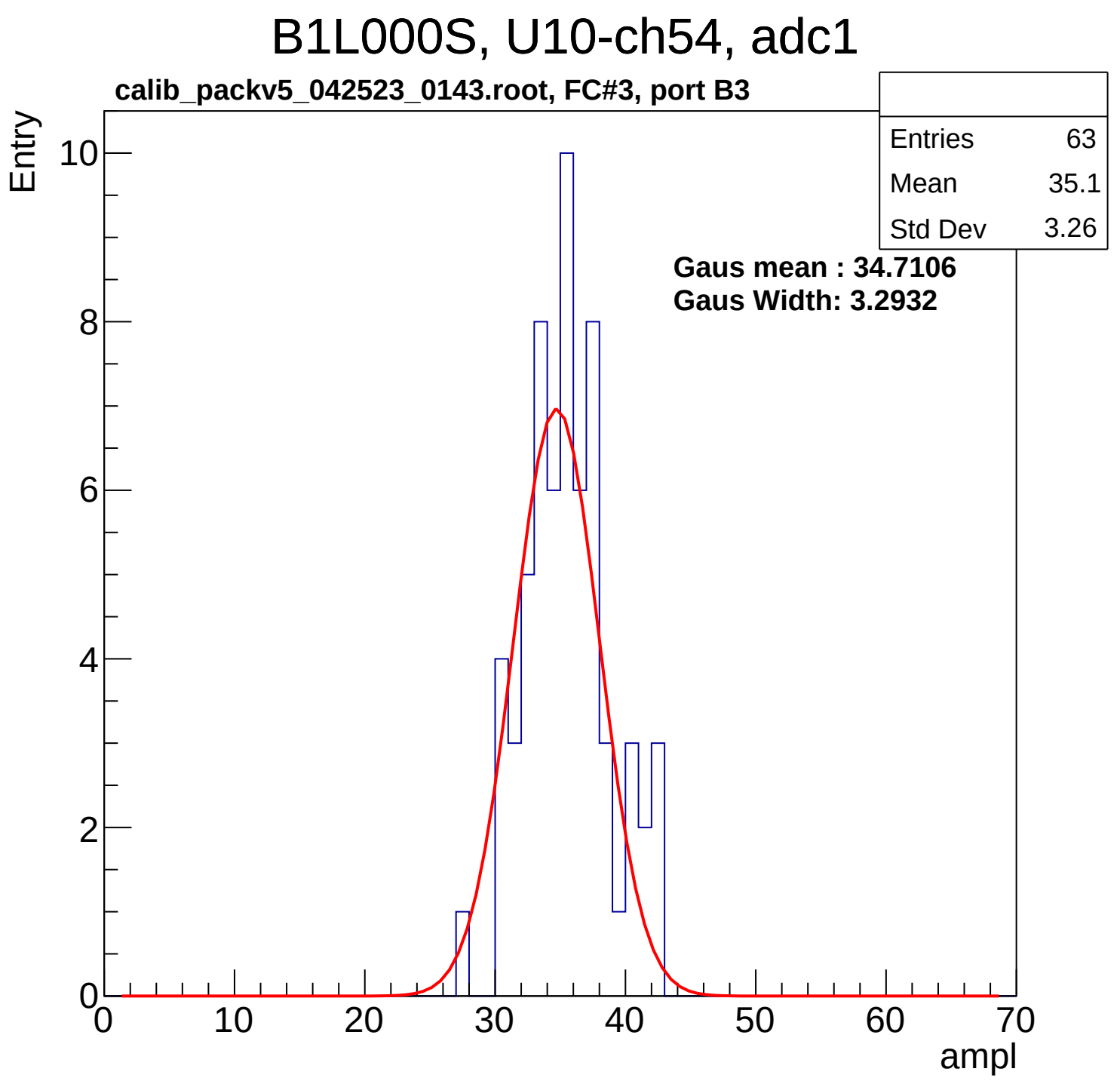

B1L000S, U10-ch53, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 54.9
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

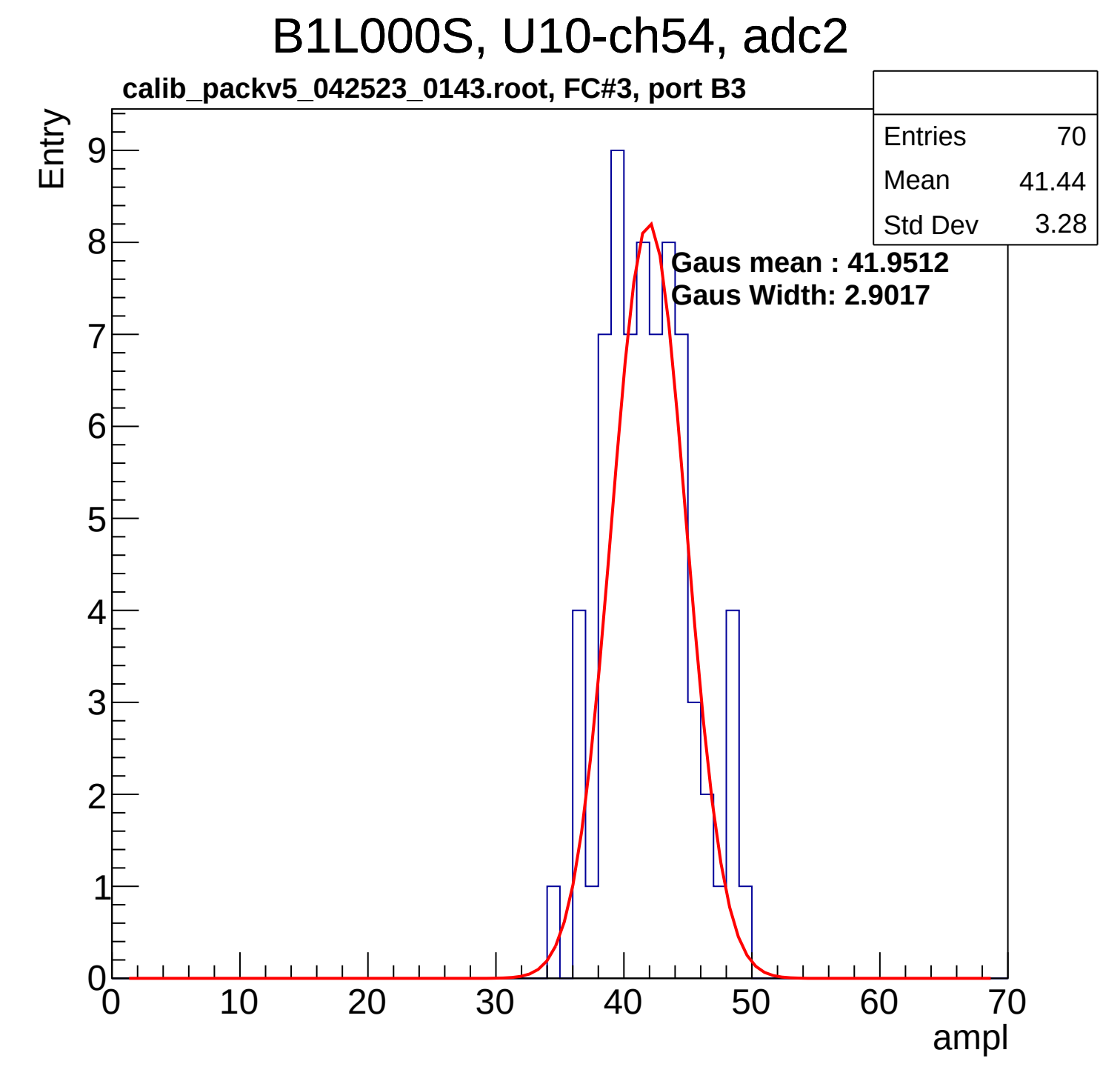

B1L000S, U10-ch53, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.96
Std Dev 8.695
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch53, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch53, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch54, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.39
Std Dev 3.242
Gaus mean : 29.0660
Gaus Width: 3.6516
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch54, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.1
Std Dev 3.26
Gaus mean : 34.7106
Gaus Width: 3.2932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch54, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.44
Std Dev 3.28
Gaus mean : 41.9512
Gaus Width: 2.9017
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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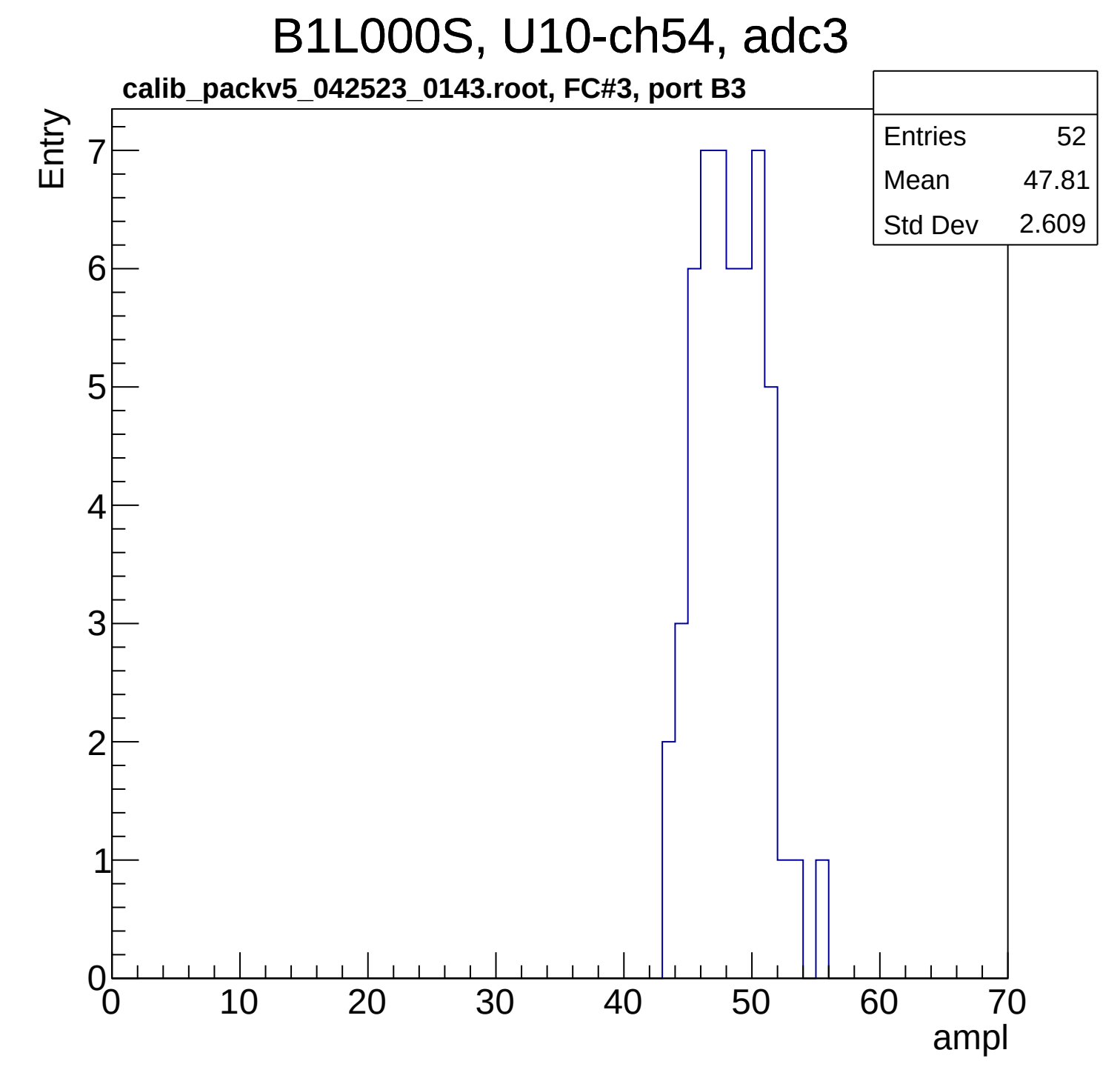
B1L000S, U10-ch54, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 47.81
Std Dev 2.609
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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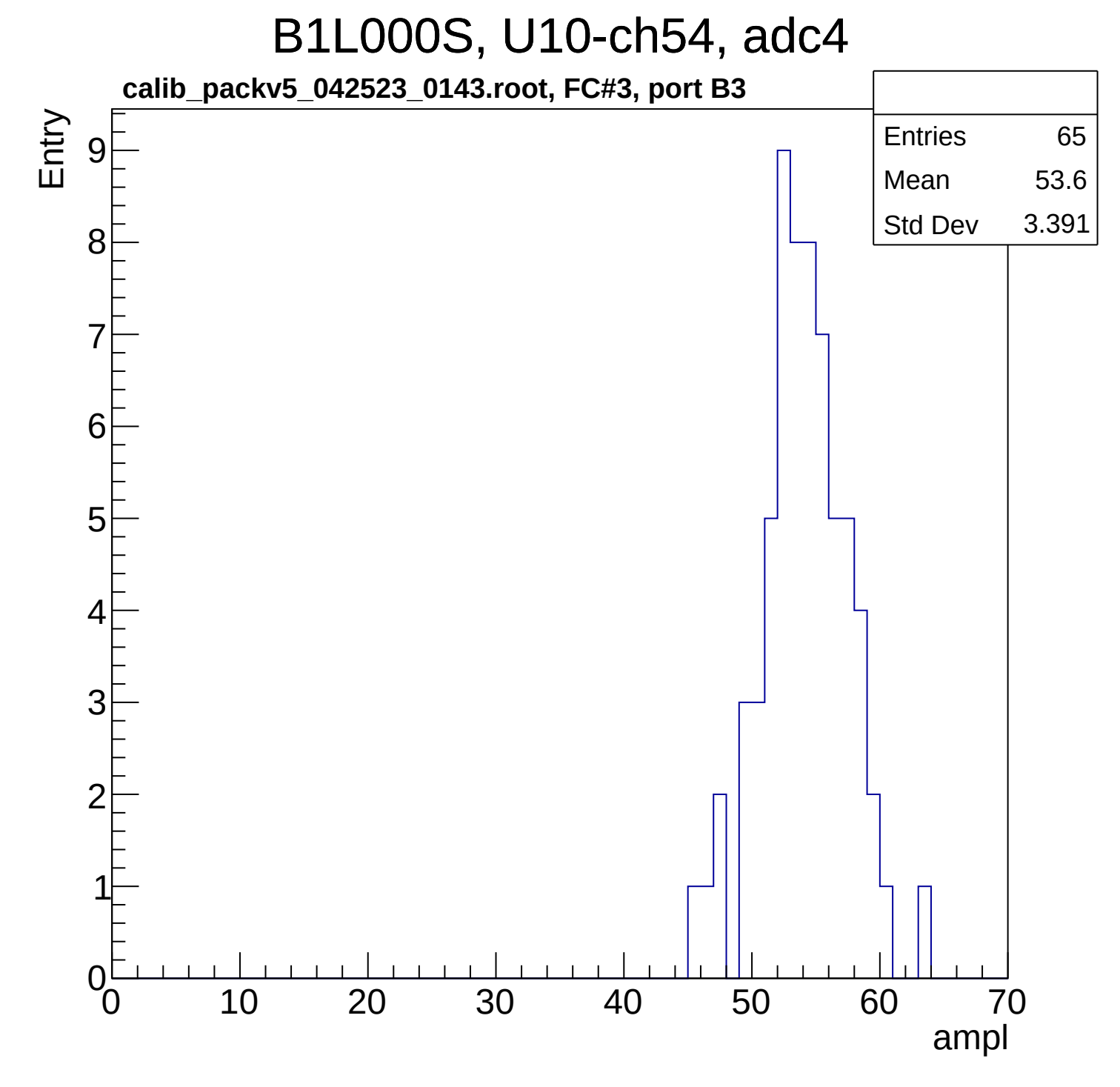
B1L000S, U10-ch54, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 53.6
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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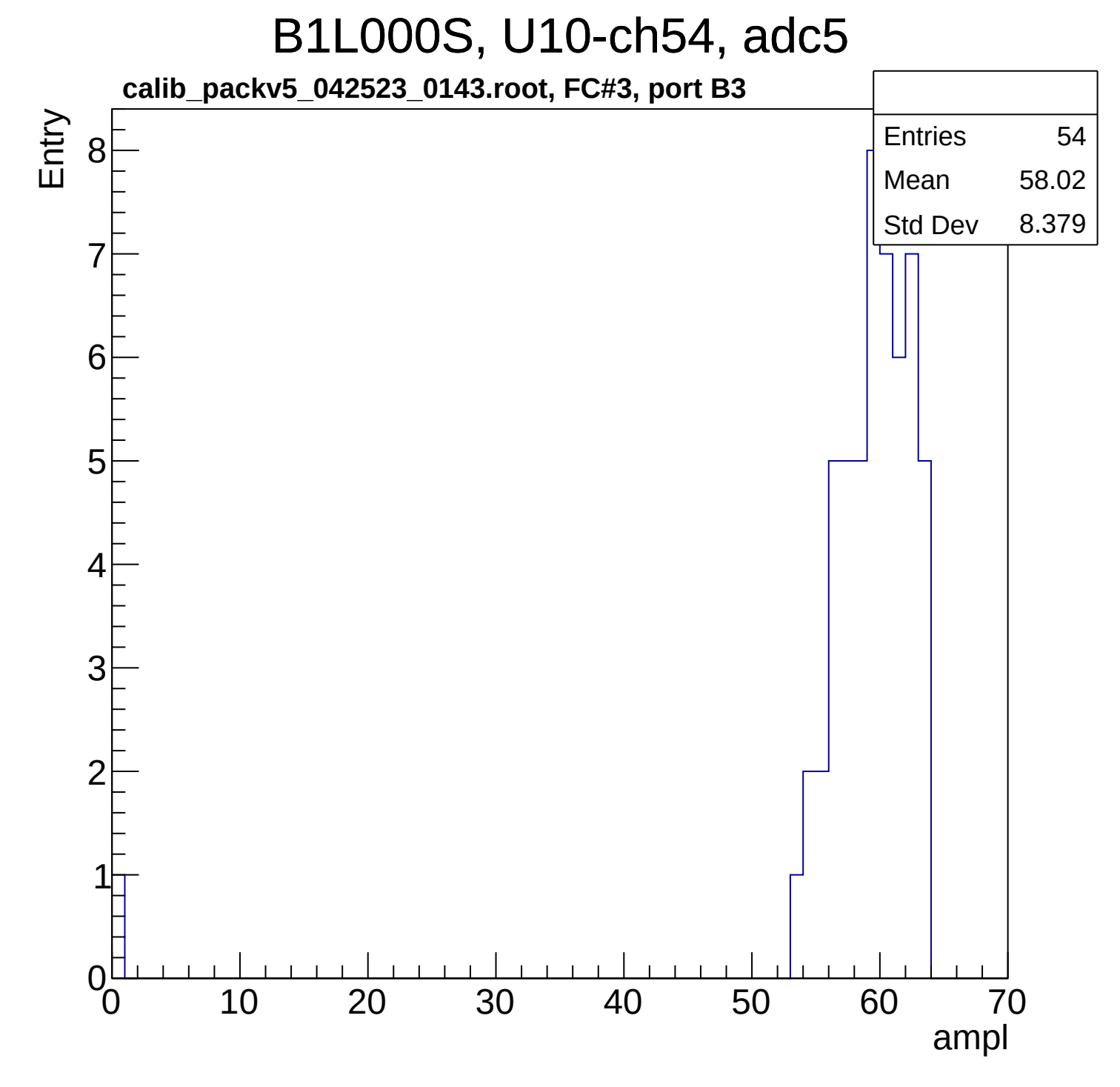
B1L000S, U10-ch54, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.02
Std Dev 8.379
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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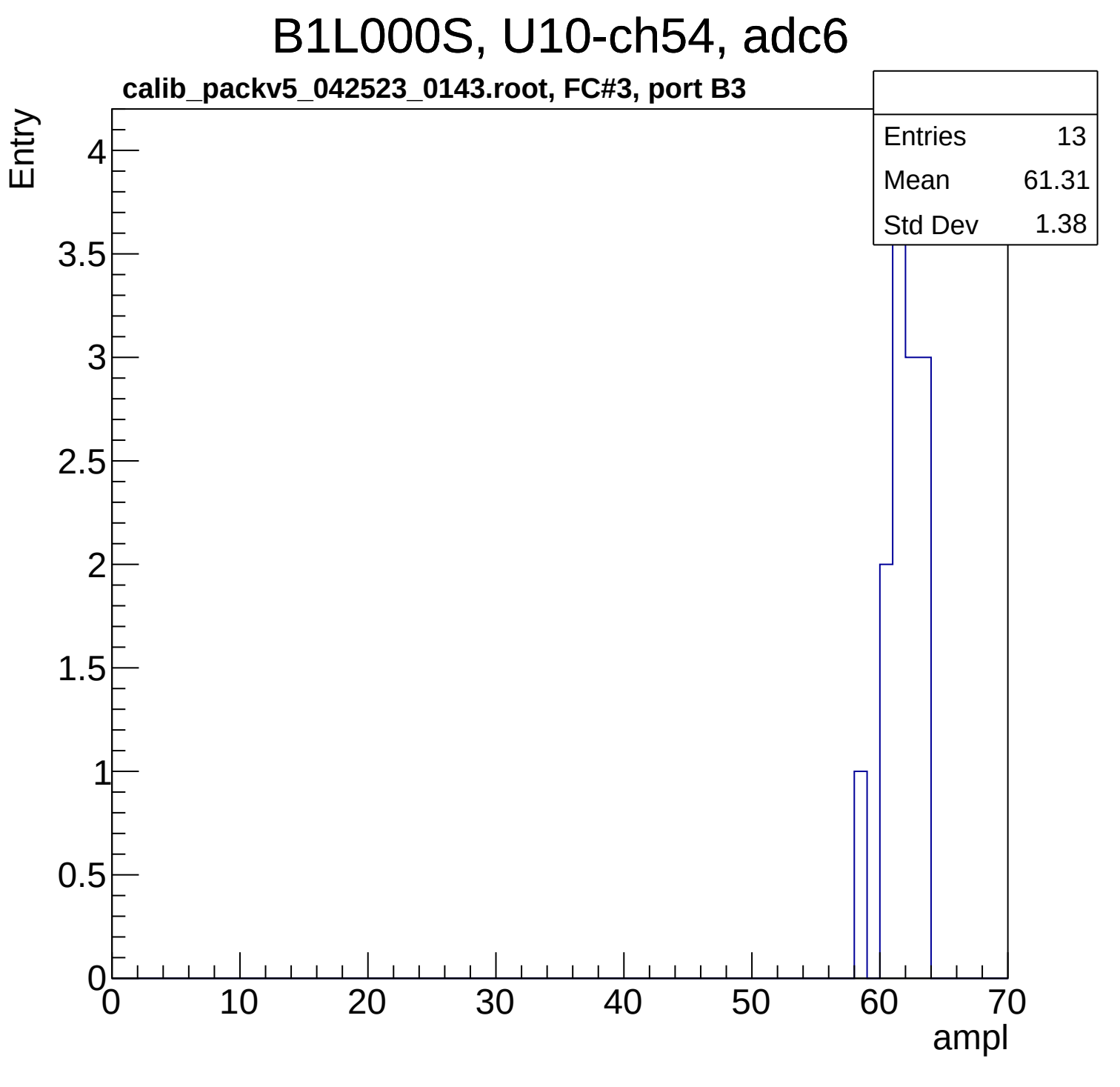
B1L000S, U10-ch54, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.31
Std Dev 1.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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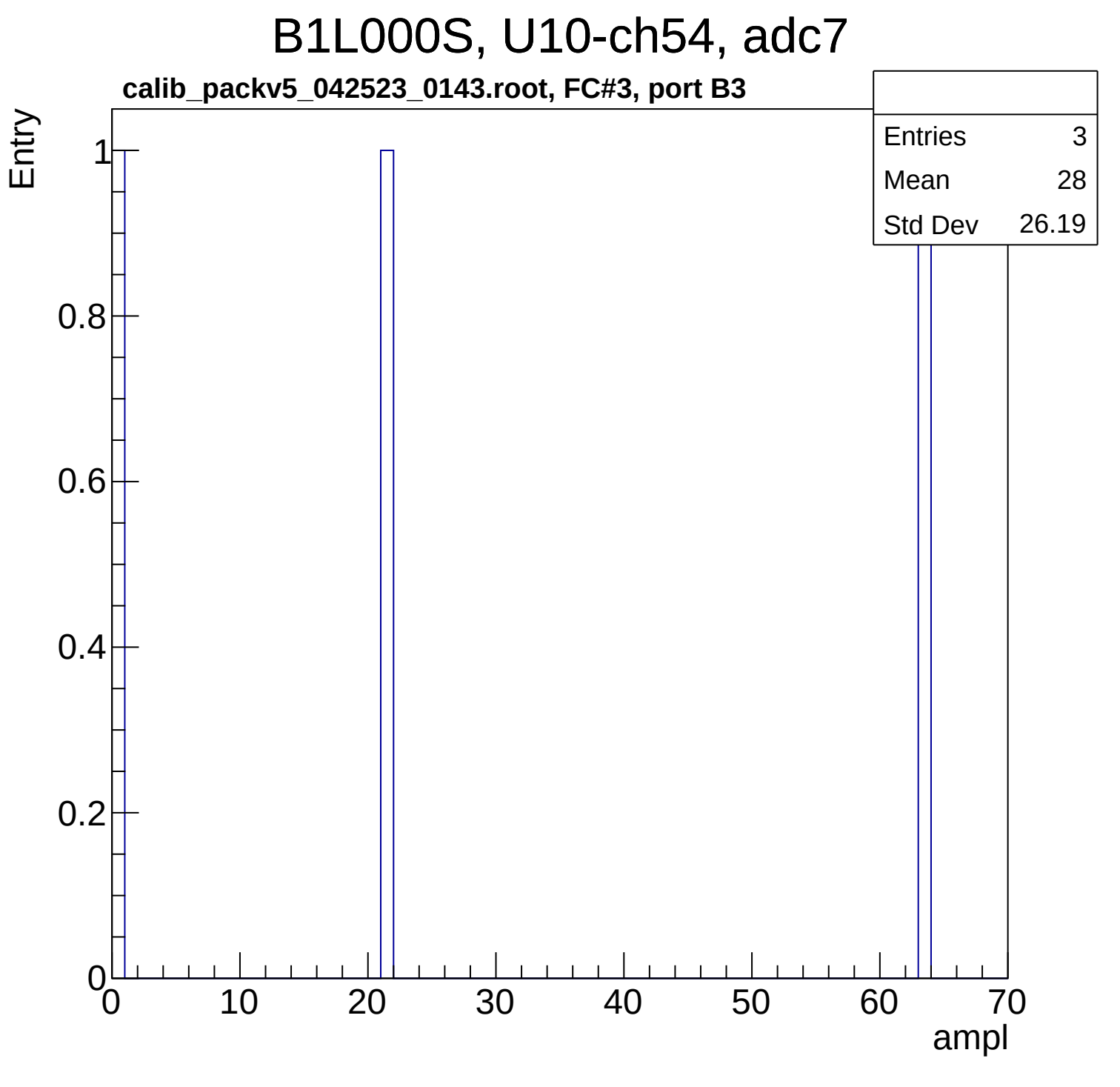
B1L000S, U10-ch54, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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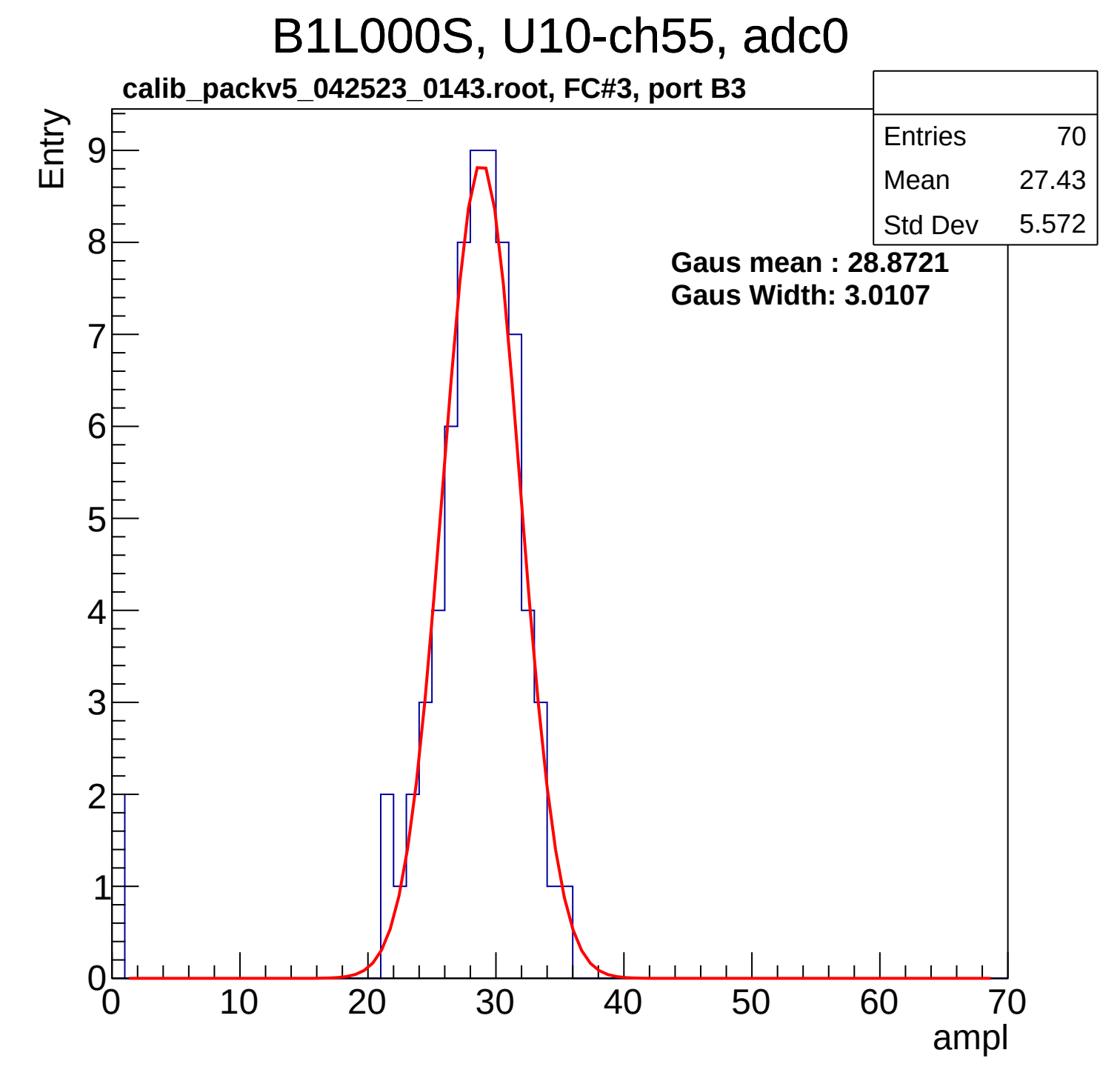
B1L000S, U10-ch55, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 27.43
Std Dev 5.572
Gaus mean : 28.8721
Gaus Width: 3.0107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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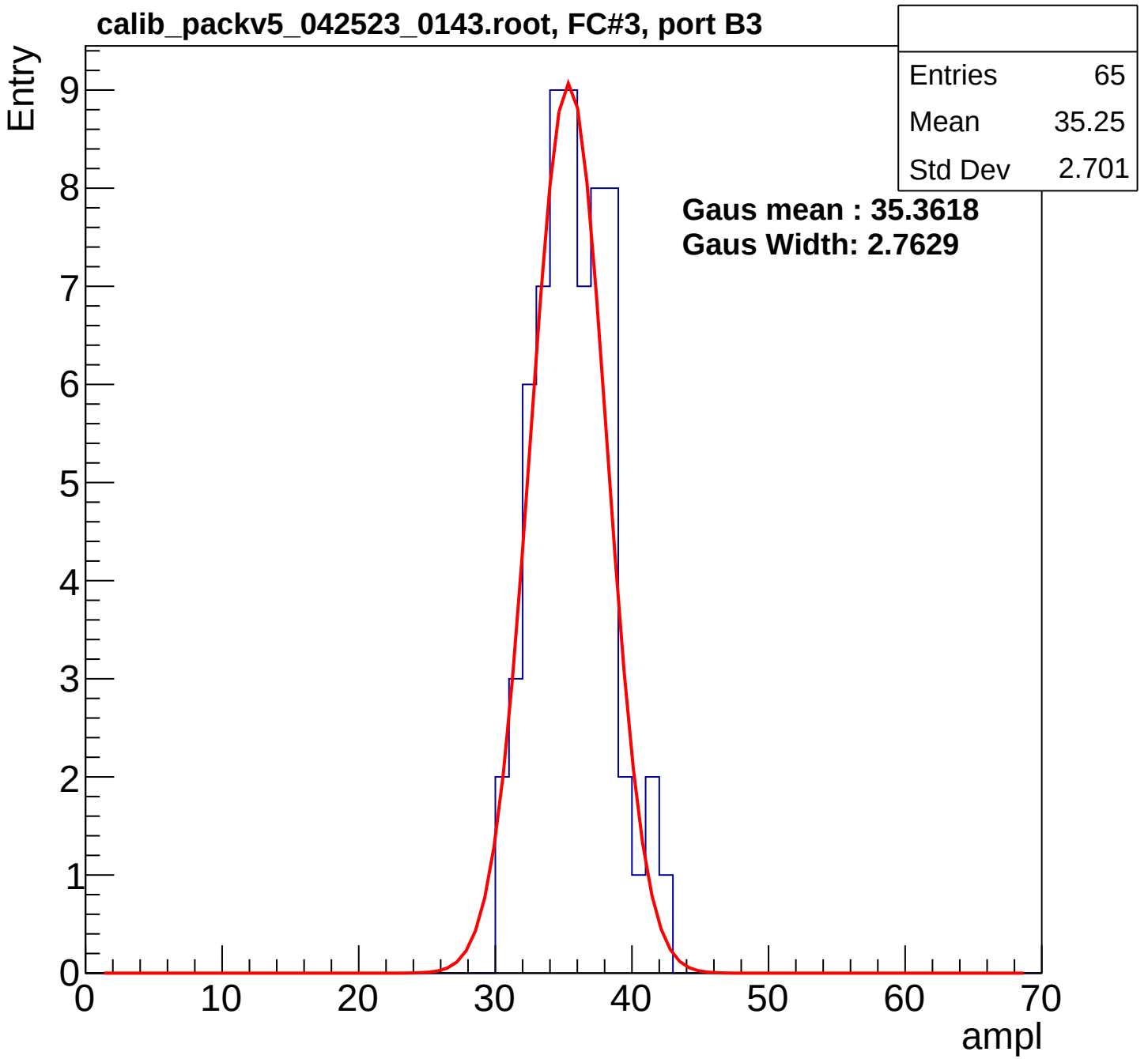
B1L000S, U10-ch55, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.25
Std Dev 2.701
Gaus mean : 35.3618
Gaus Width: 2.7629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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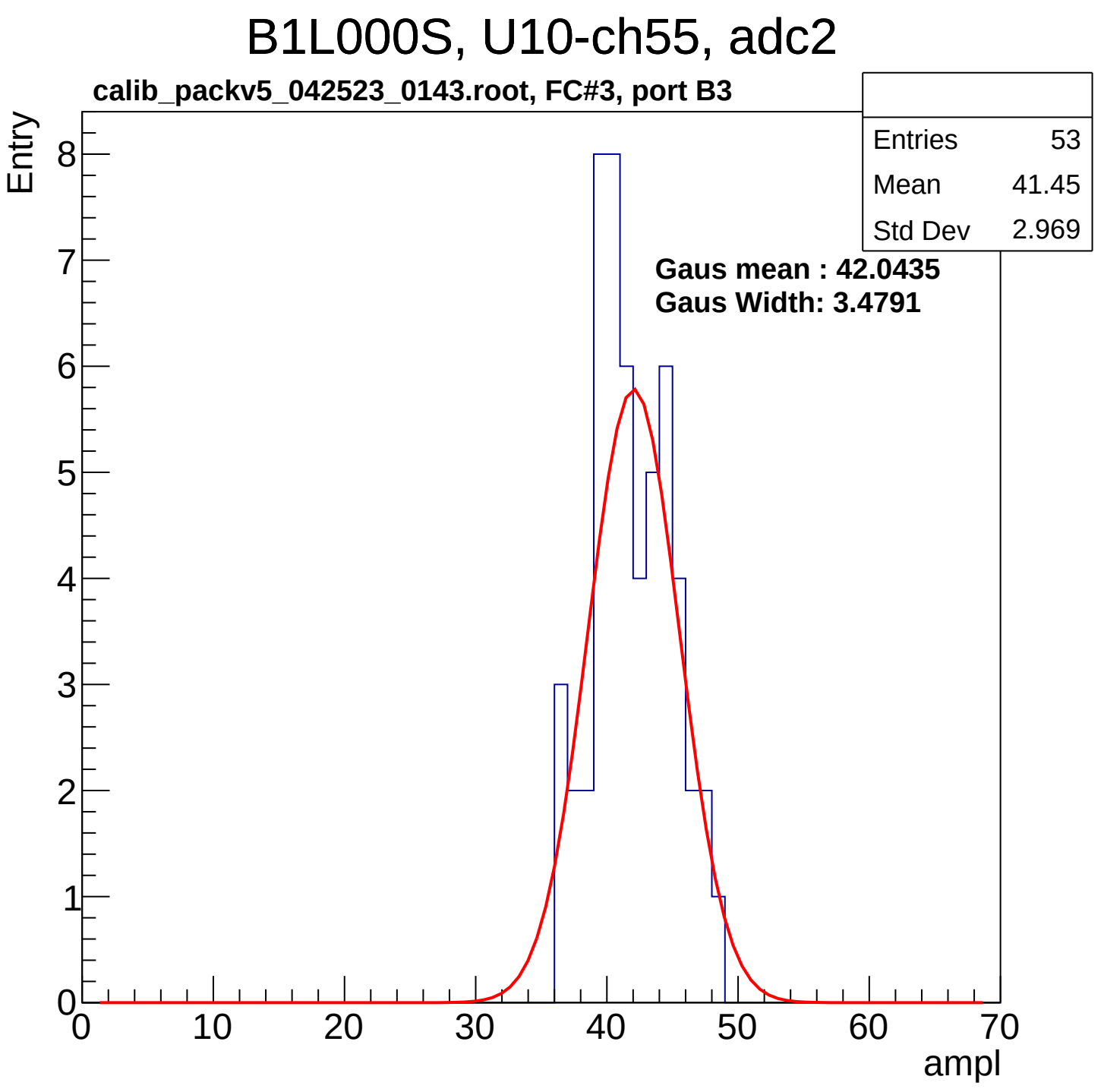
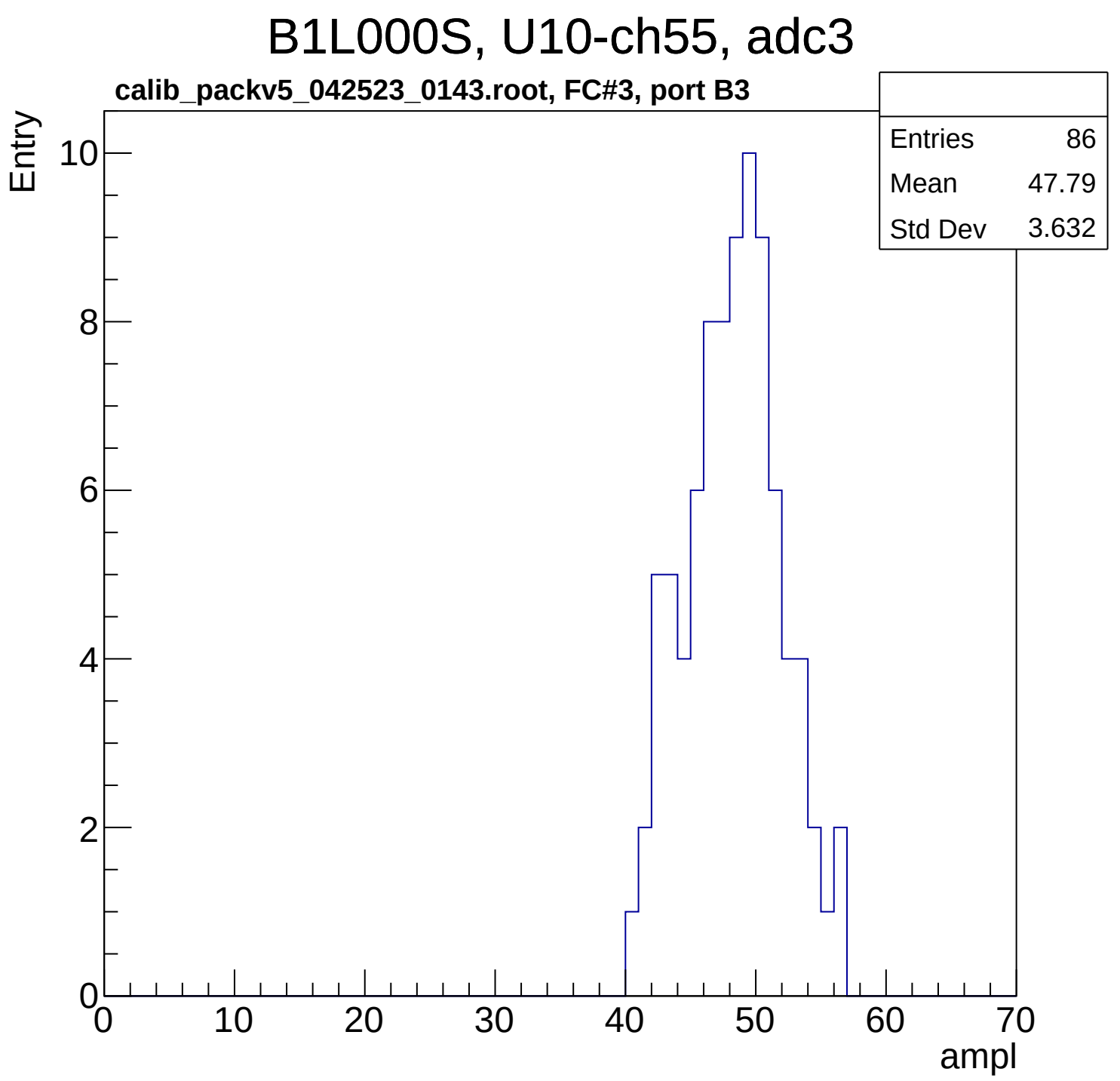
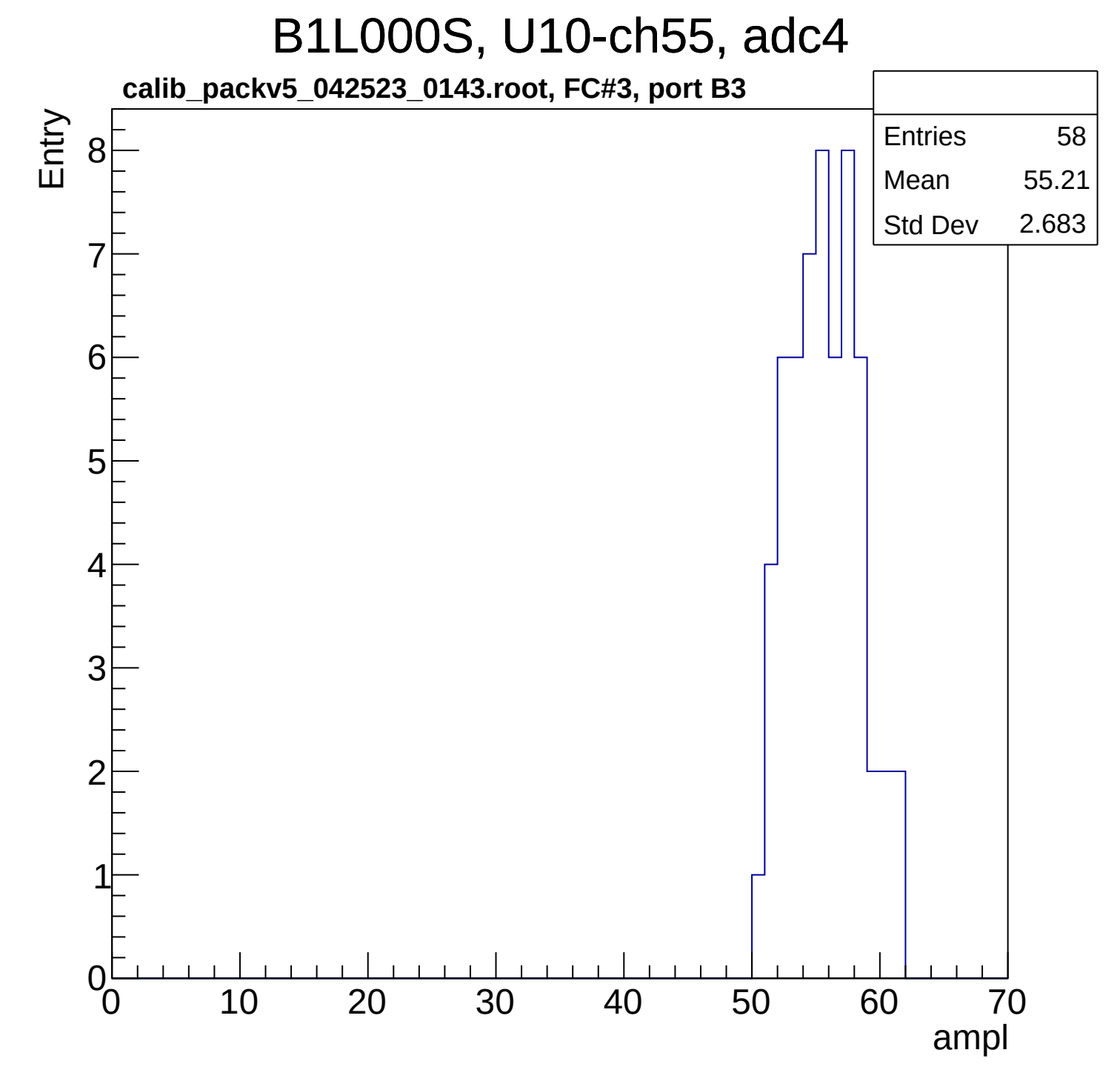

B1L000S, U10-ch55, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 41.45
Std Dev 2.969
Gaus mean : 42.0435
Gaus Width: 3.4791
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch55, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 47.79
Std Dev 3.632
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch55, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 55.21
Std Dev 2.683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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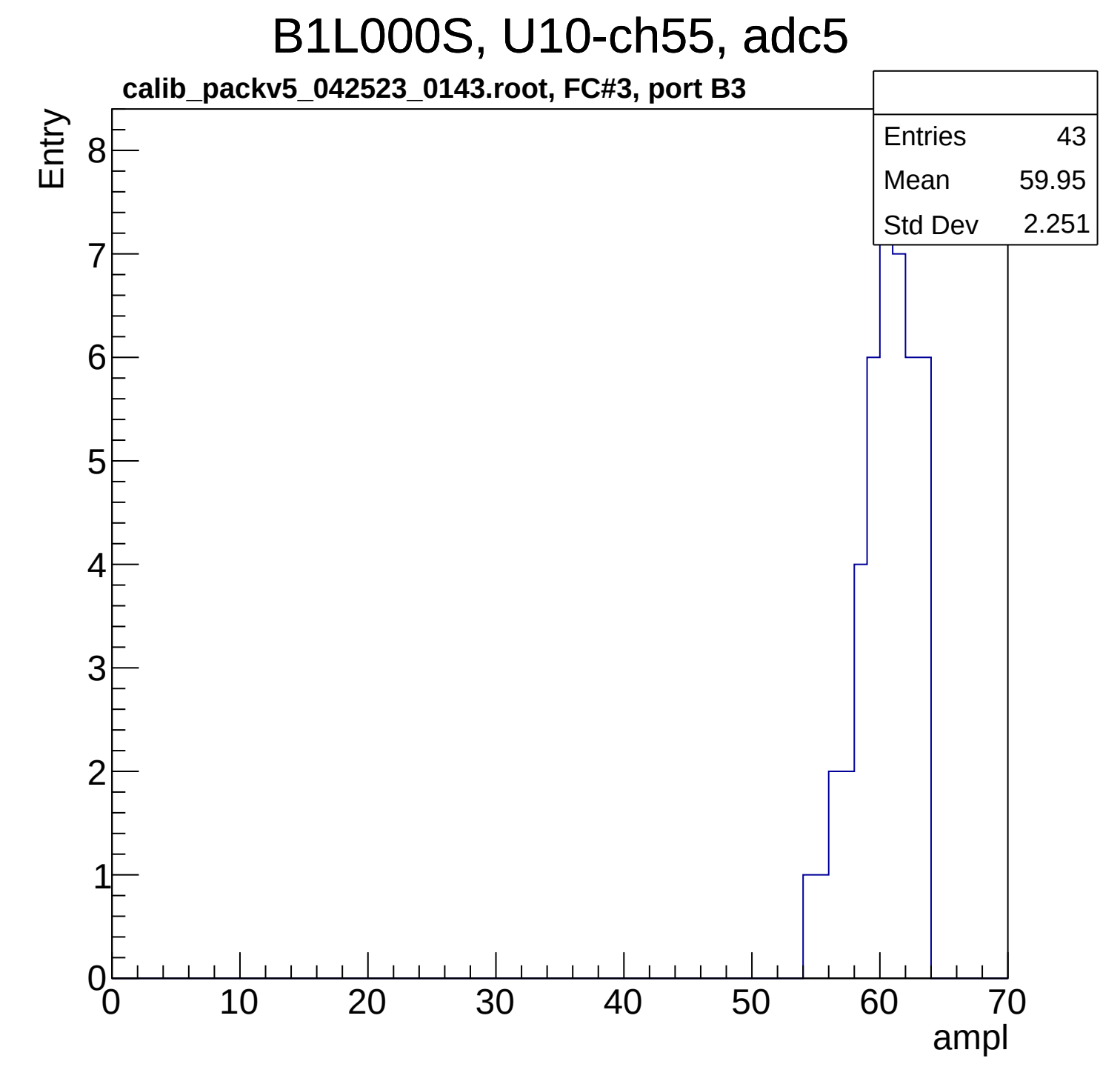
B1L000S, U10-ch55, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 59.95
Std Dev 2.251
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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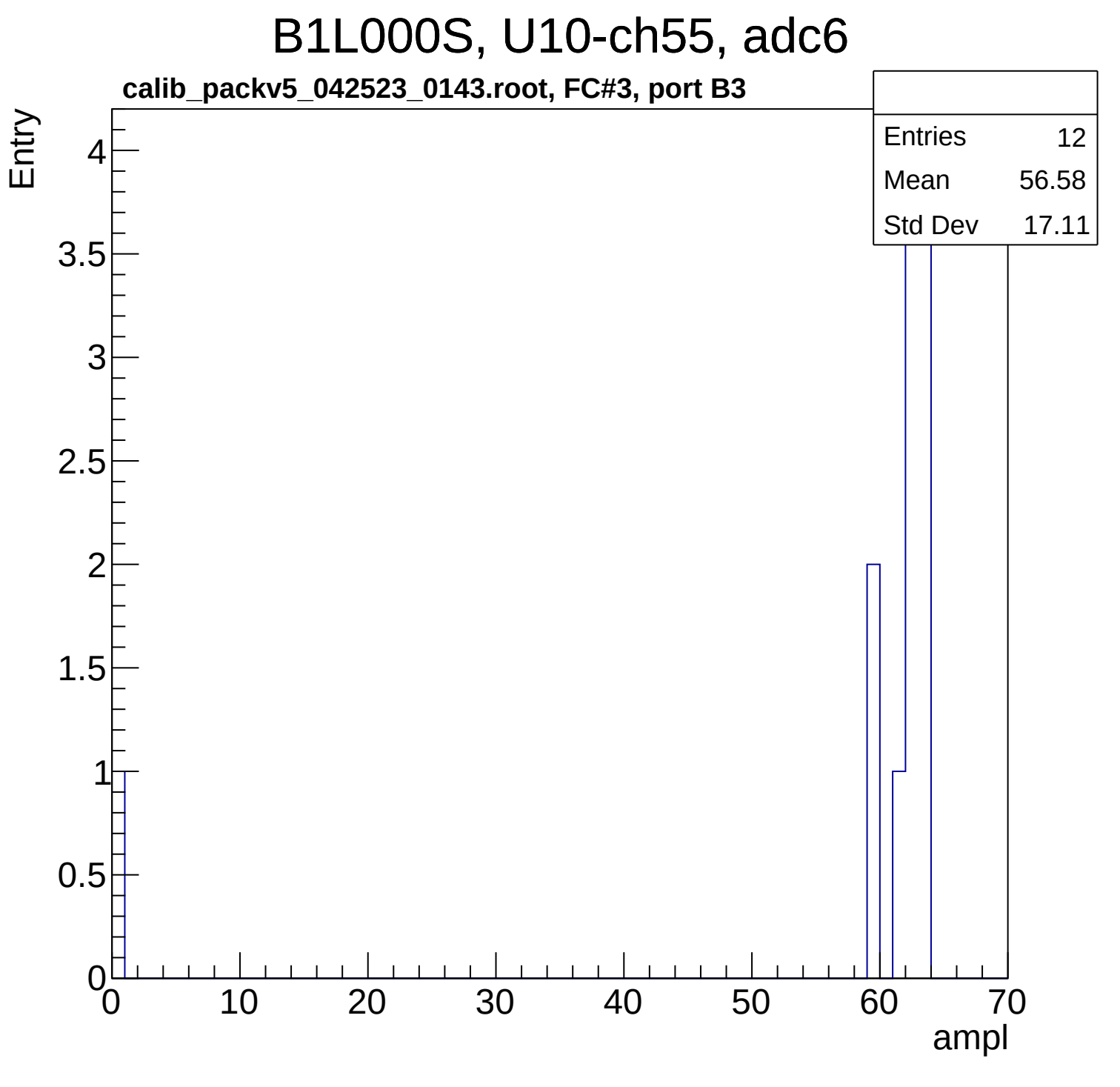
B1L000S, U10-ch55, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.58
Std Dev 17.11
Entry
ampl
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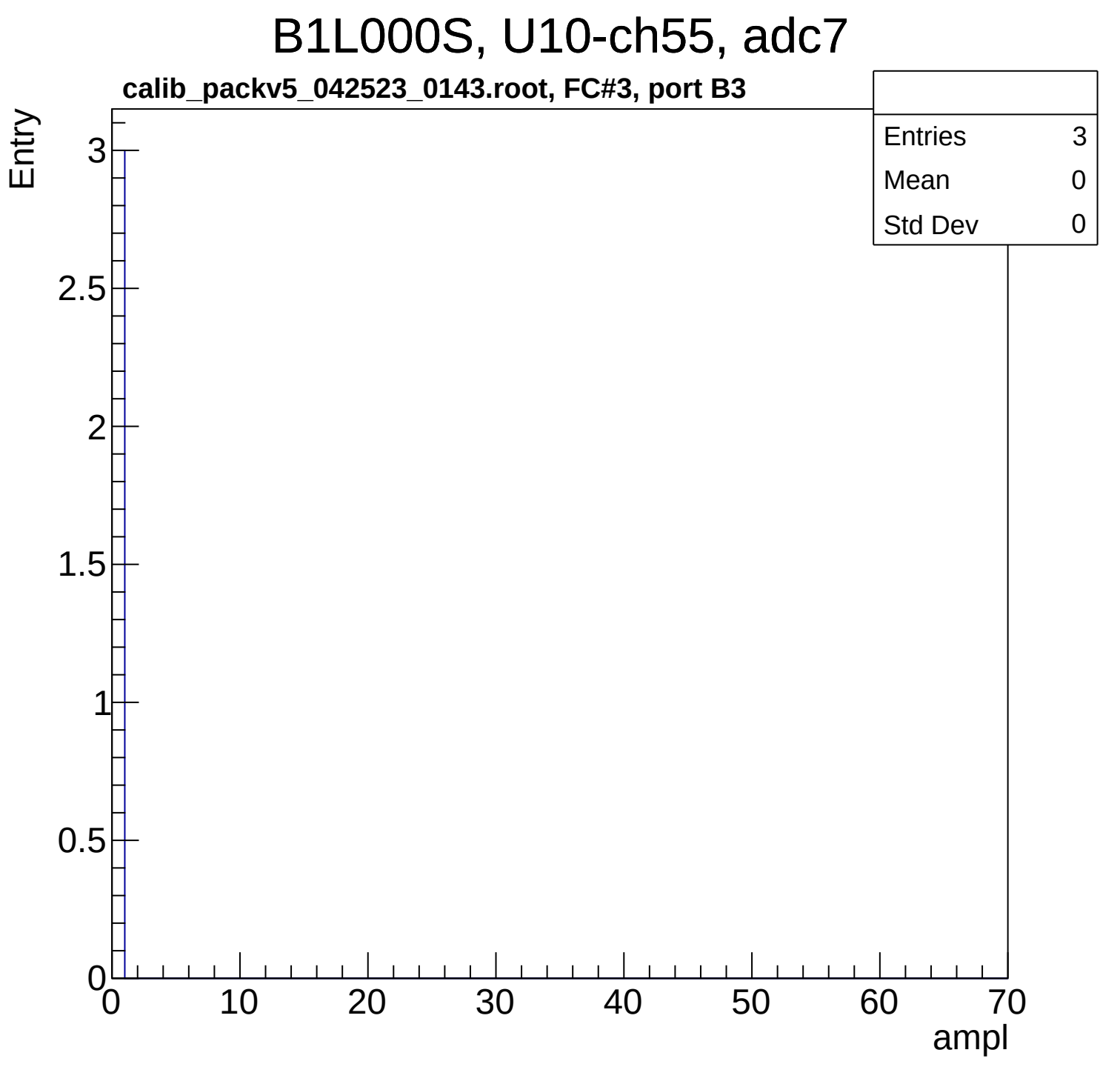
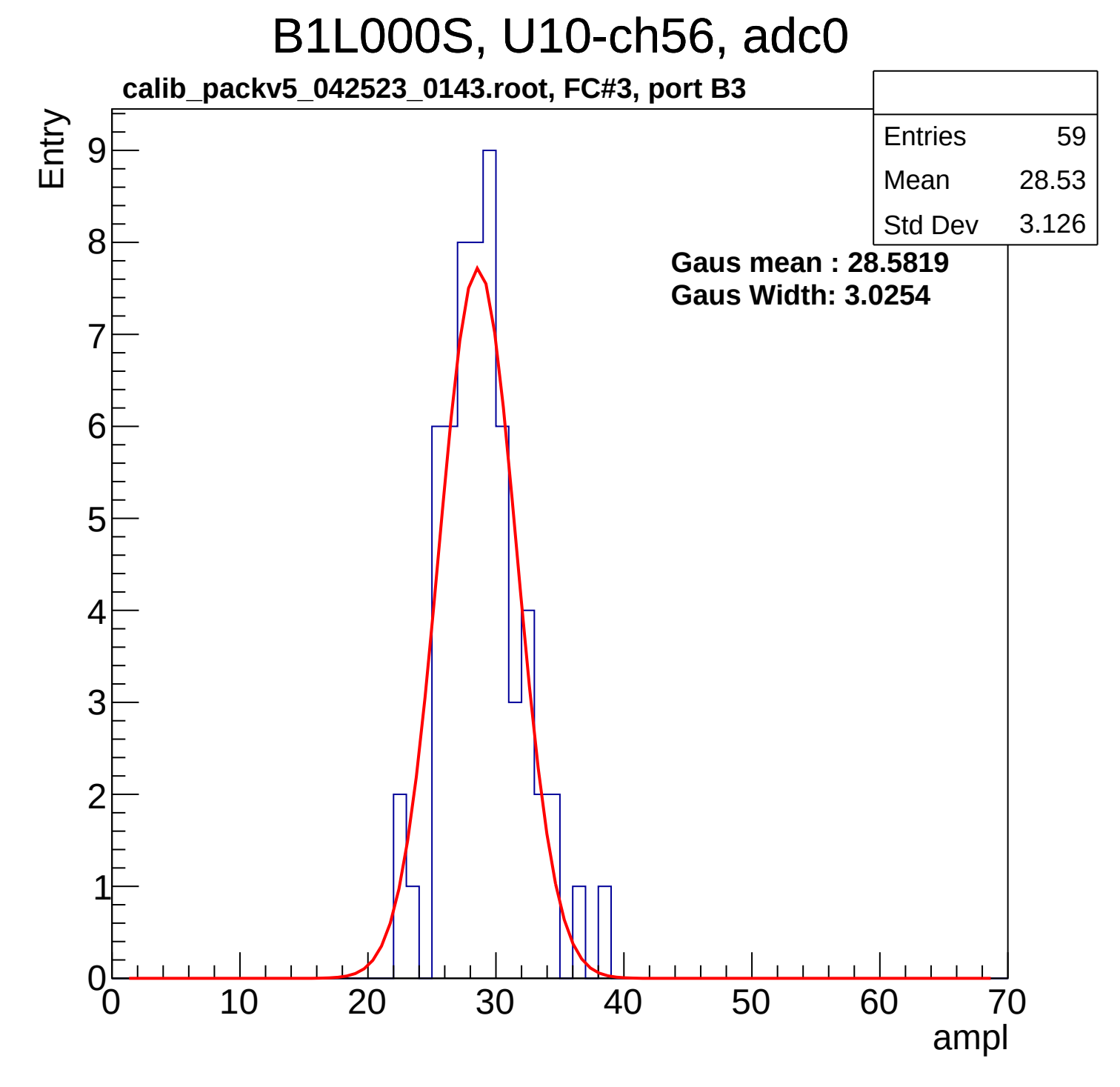
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch55, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch56, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 28.53
Std Dev 3.126
Gaus mean : 28.5819
Gaus Width: 3.0254
Entry
ampl
0
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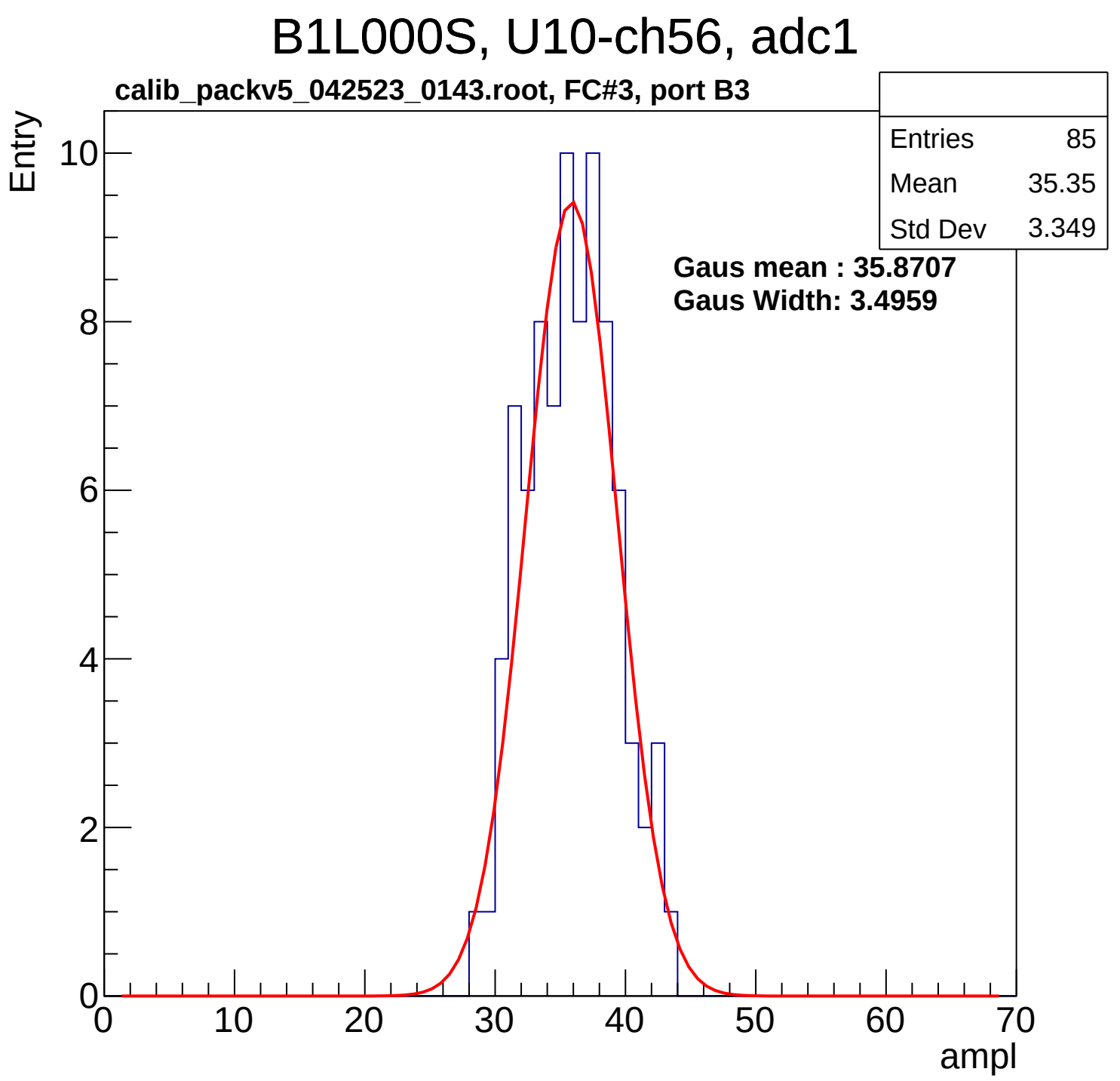
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch56, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 85
Mean 35.35
Std Dev 3.349
Gaus mean : 35.8707
Gaus Width: 3.4959
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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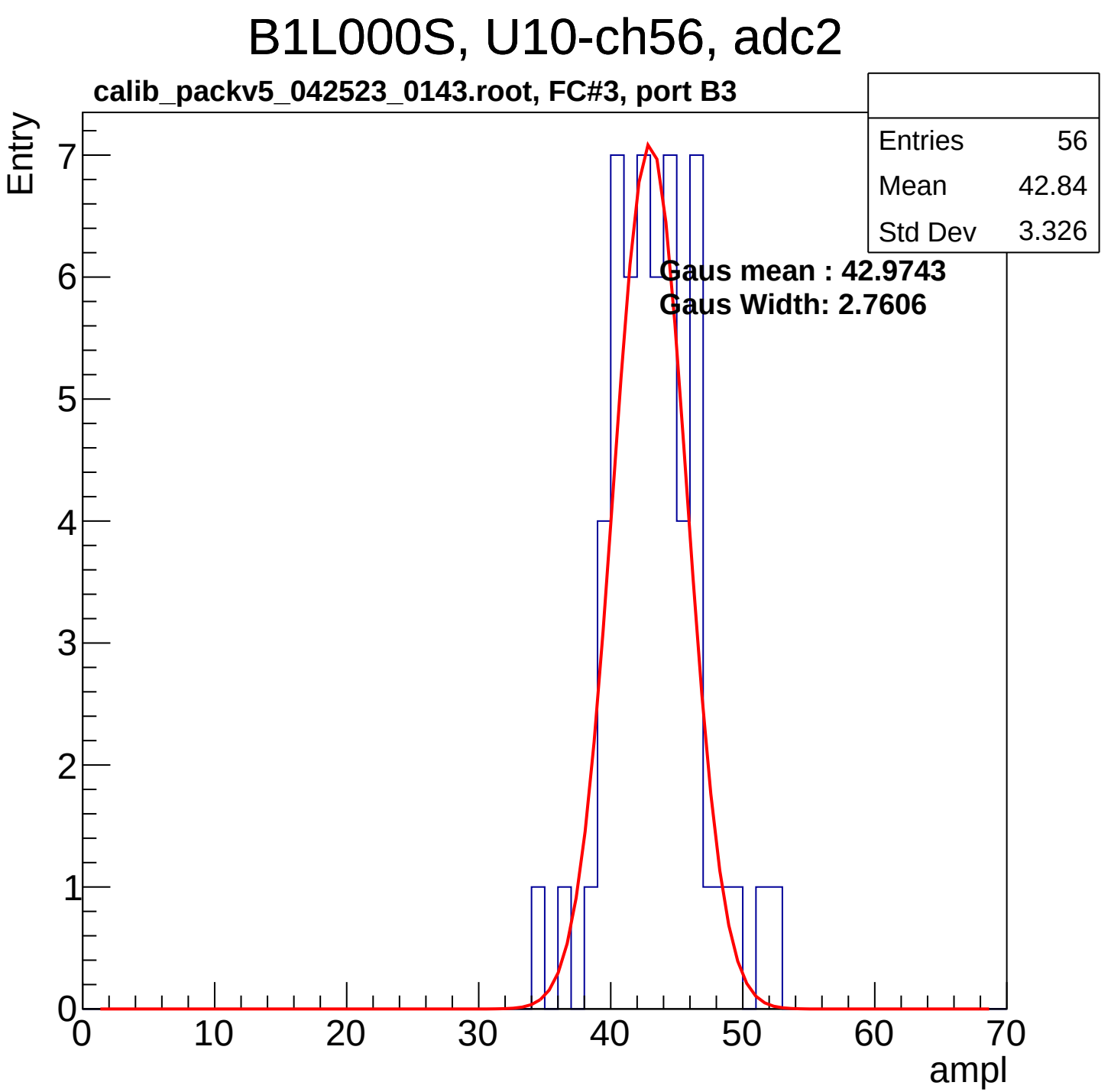
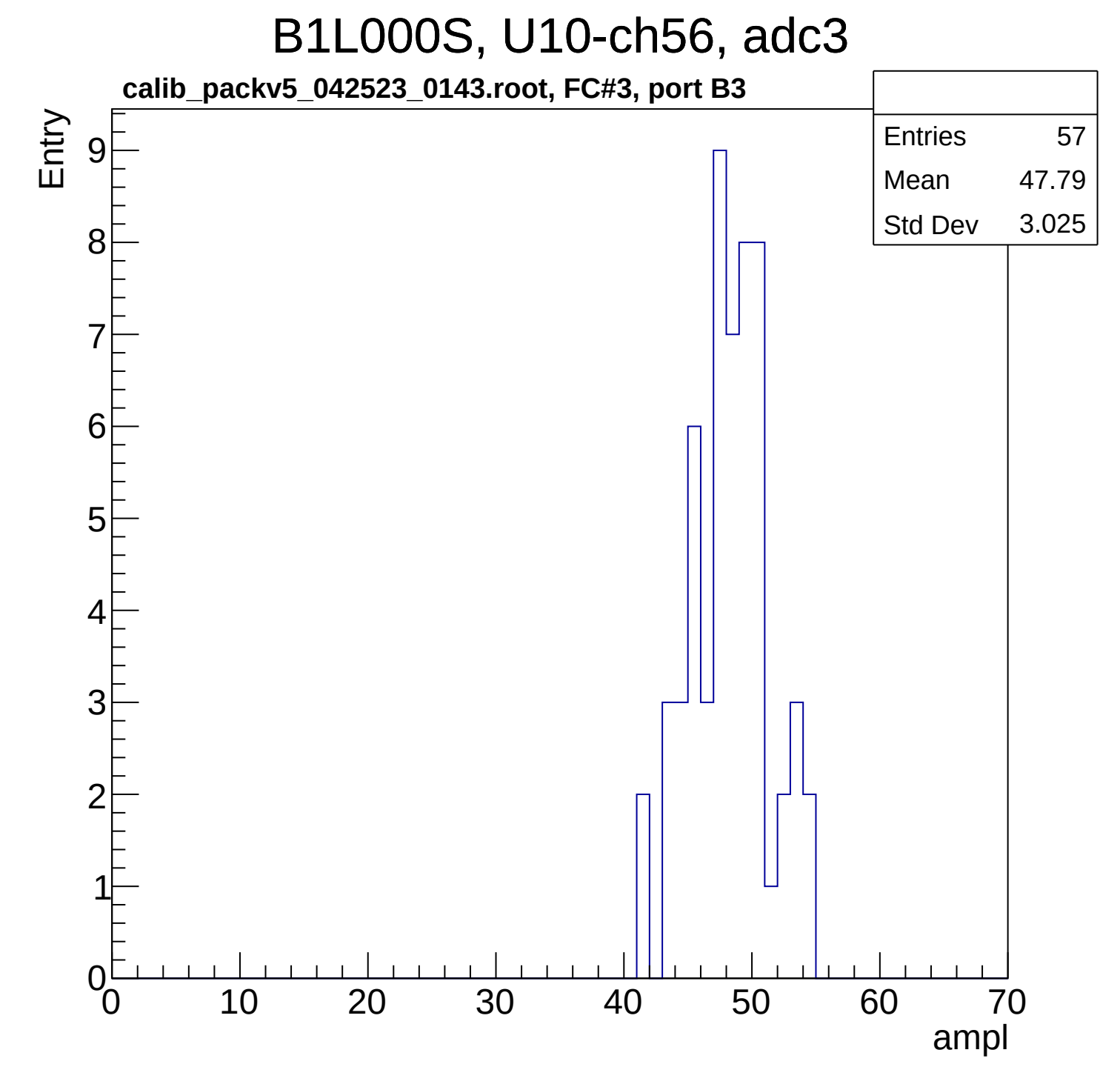

B1L000S, U10-ch56, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.84
Std Dev 3.326
Gaus mean : 42.9743
Gaus Width: 2.7606
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch56, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.79
Std Dev 3.025
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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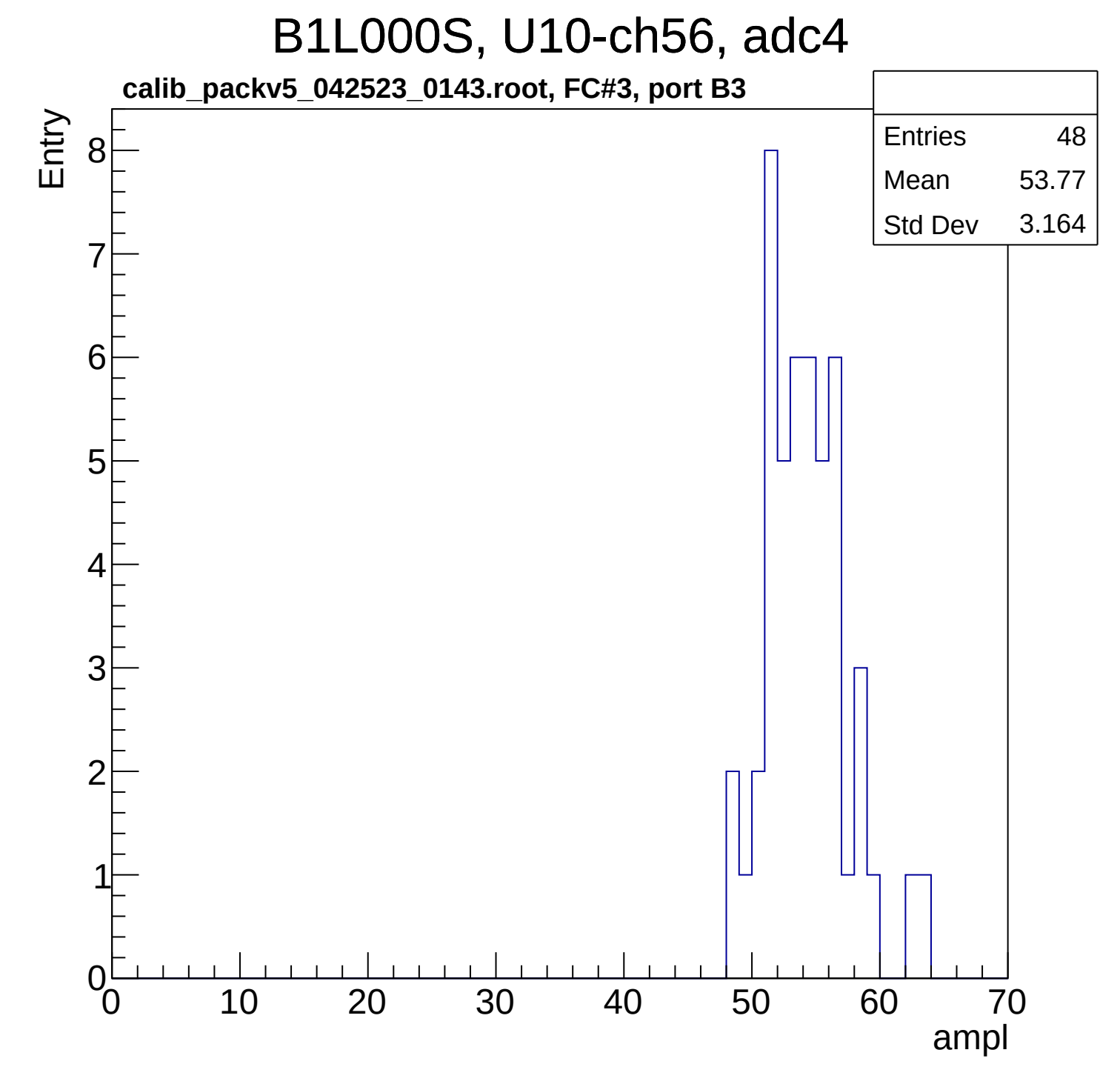
B1L000S, U10-ch56, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.77
Std Dev 3.164
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
50
60
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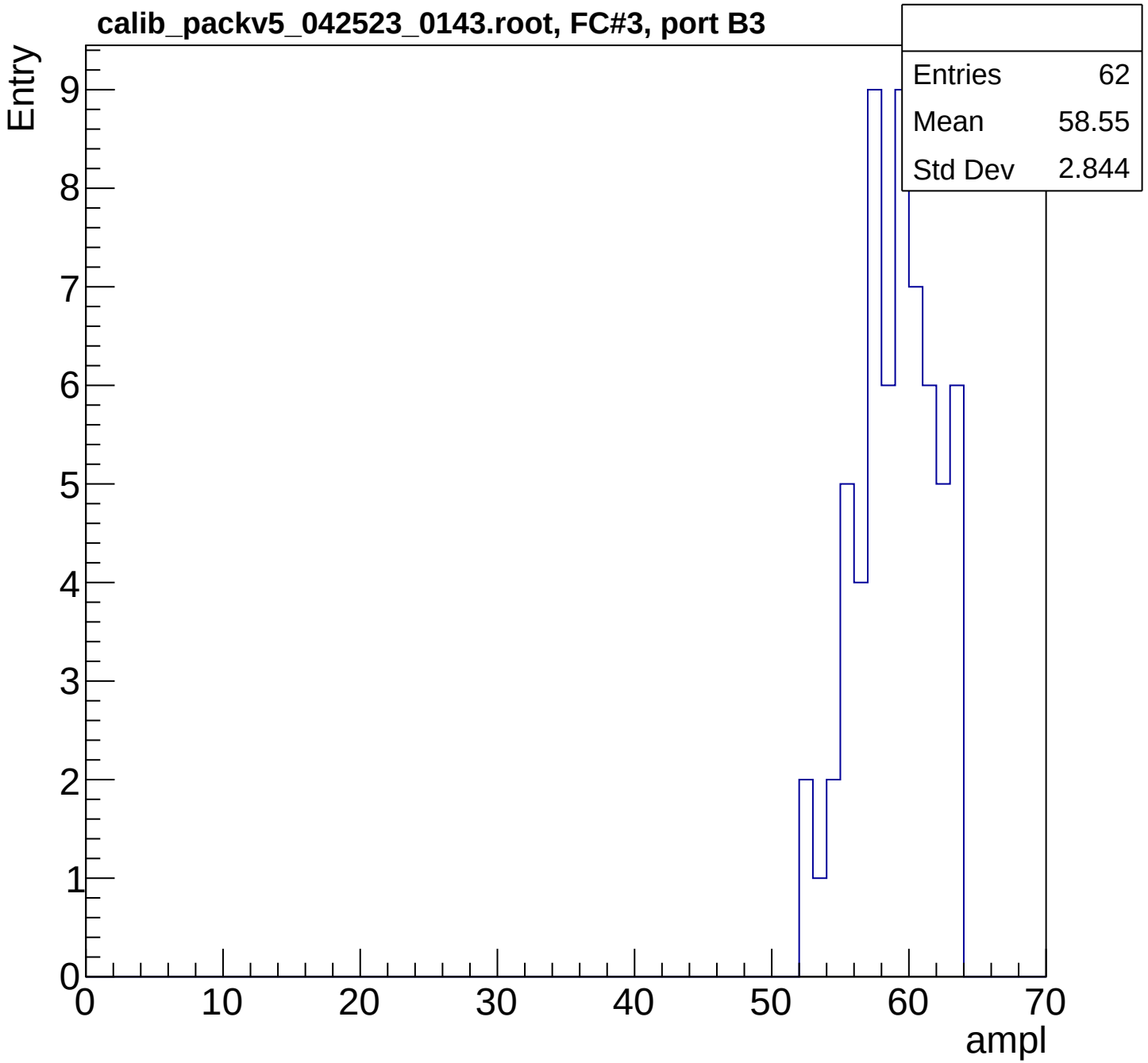
B1L000S, U10-ch56, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 58.55
Std Dev 2.844
Entry
ampl
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5
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60
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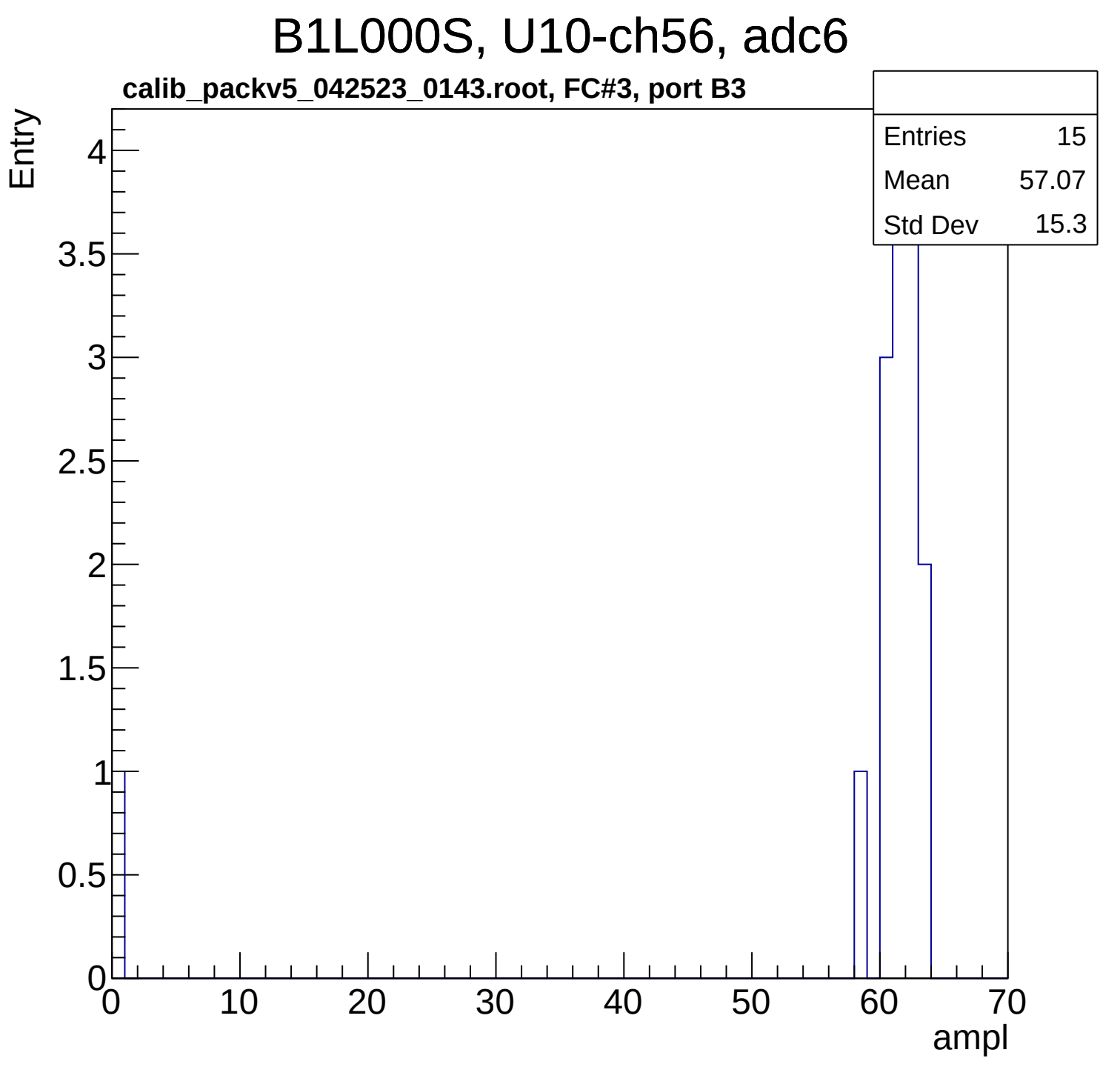
B1L000S, U10-ch56, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.07
Std Dev 15.3
Entry
ampl
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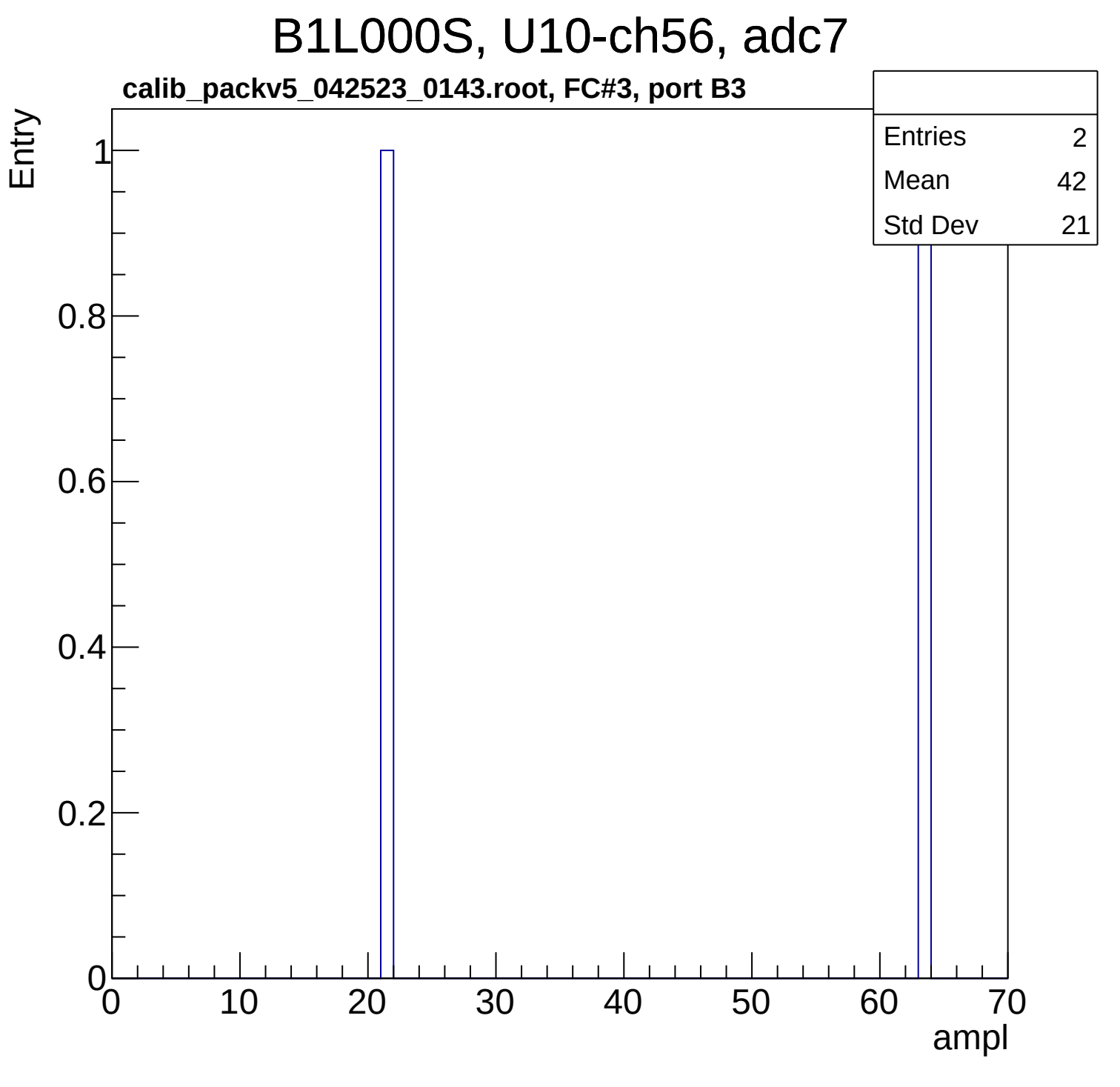
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch56, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 42
Std Dev 21
Entry
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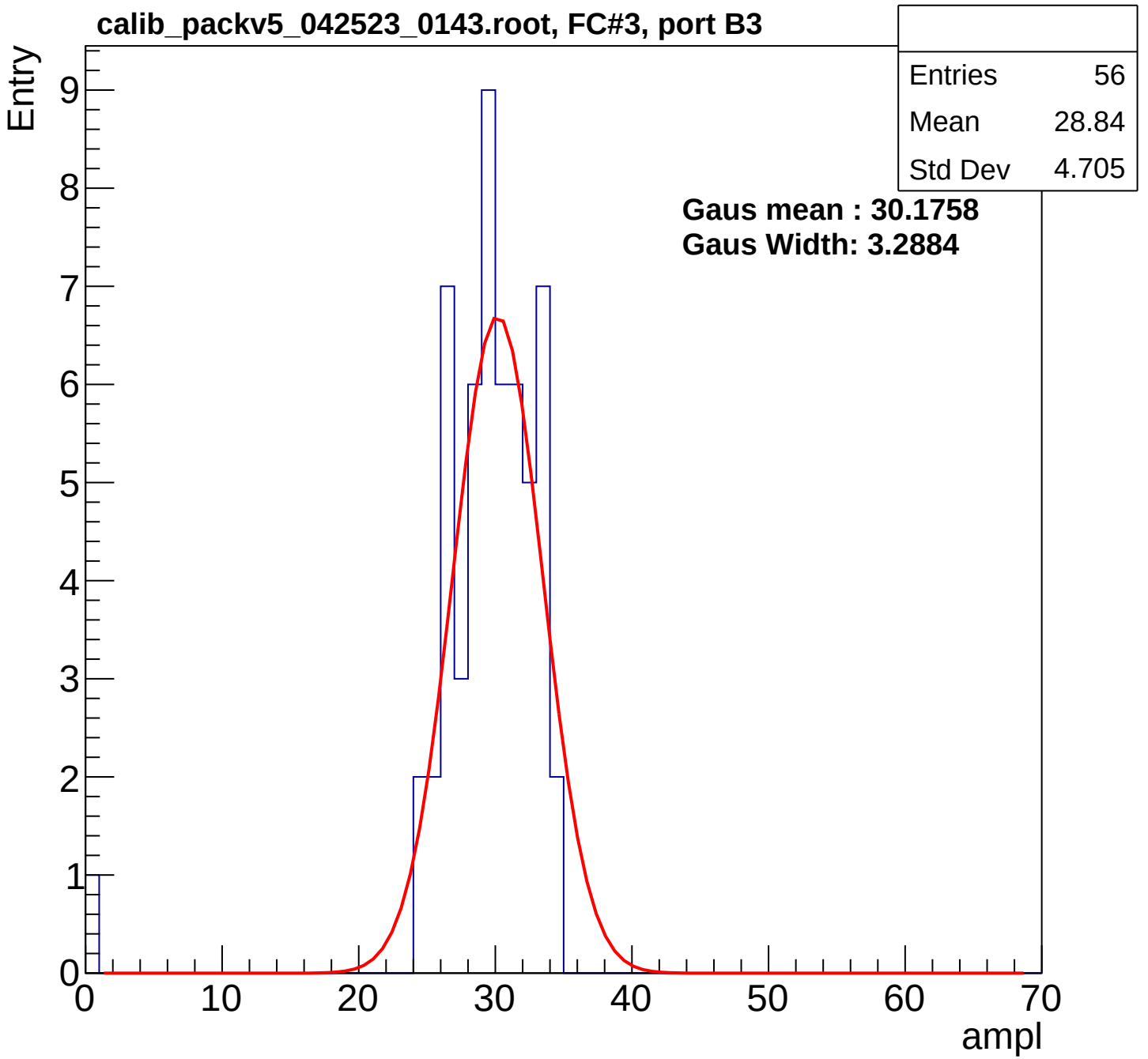
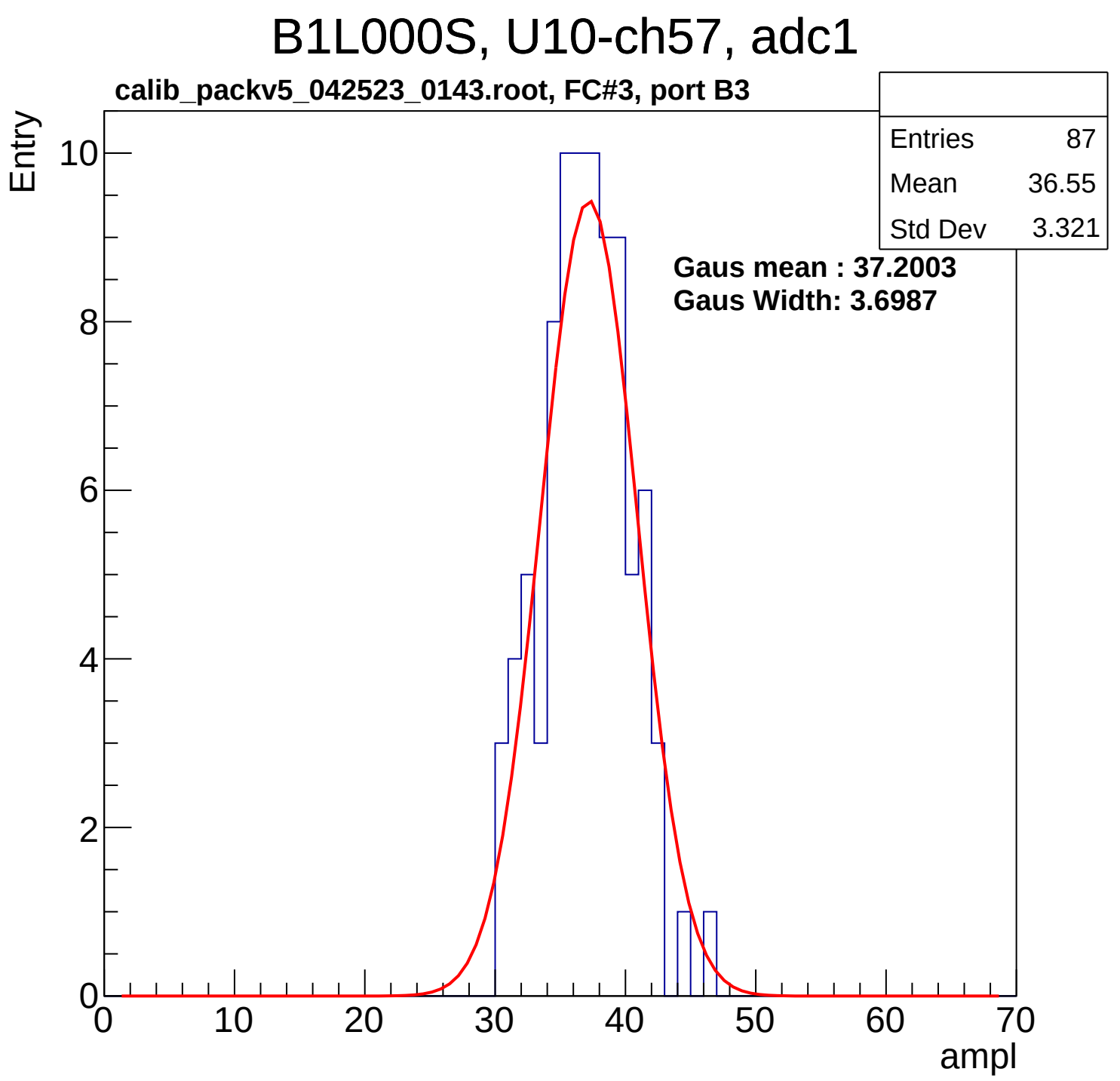
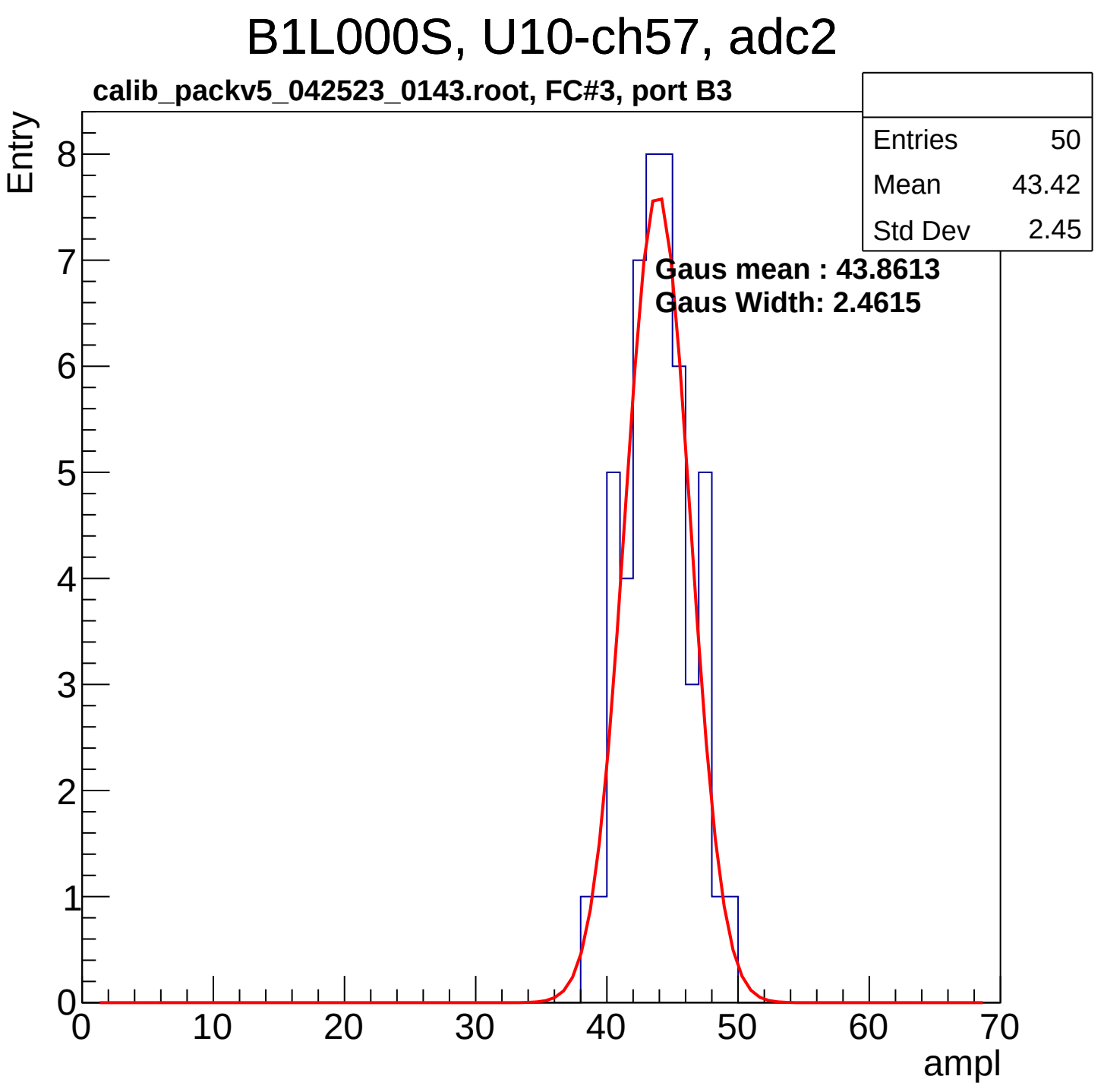
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch57, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 28.84
Std Dev 4.705
Gaus mean : 30.1758
Gaus Width: 3.2884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch57, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 87
Mean 36.55
Std Dev 3.321
Gaus mean : 37.2003
Gaus Width: 3.6987
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch57, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 43.42
Std Dev 2.45
Gaus mean : 43.8613
Gaus Width: 2.4615
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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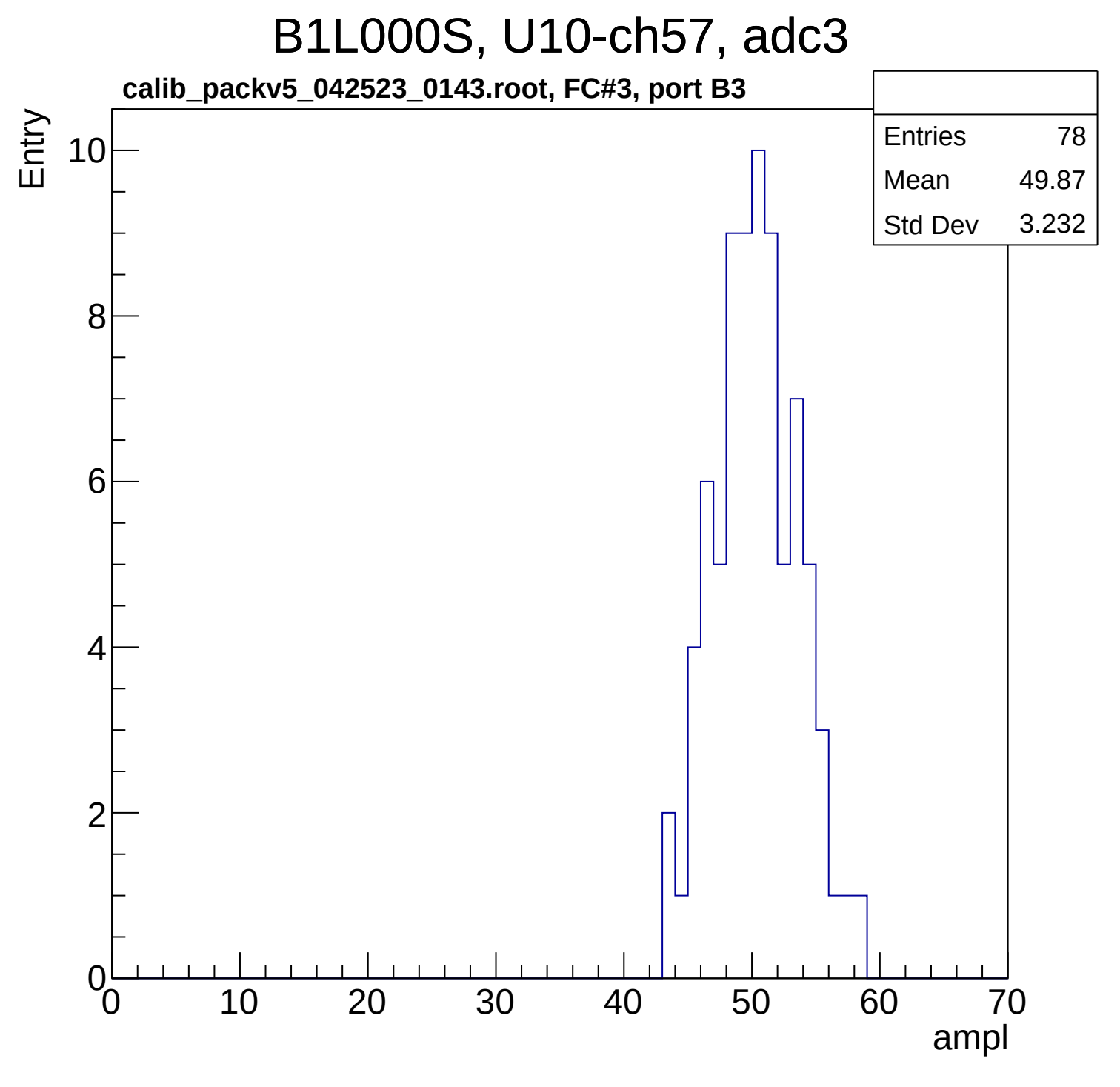
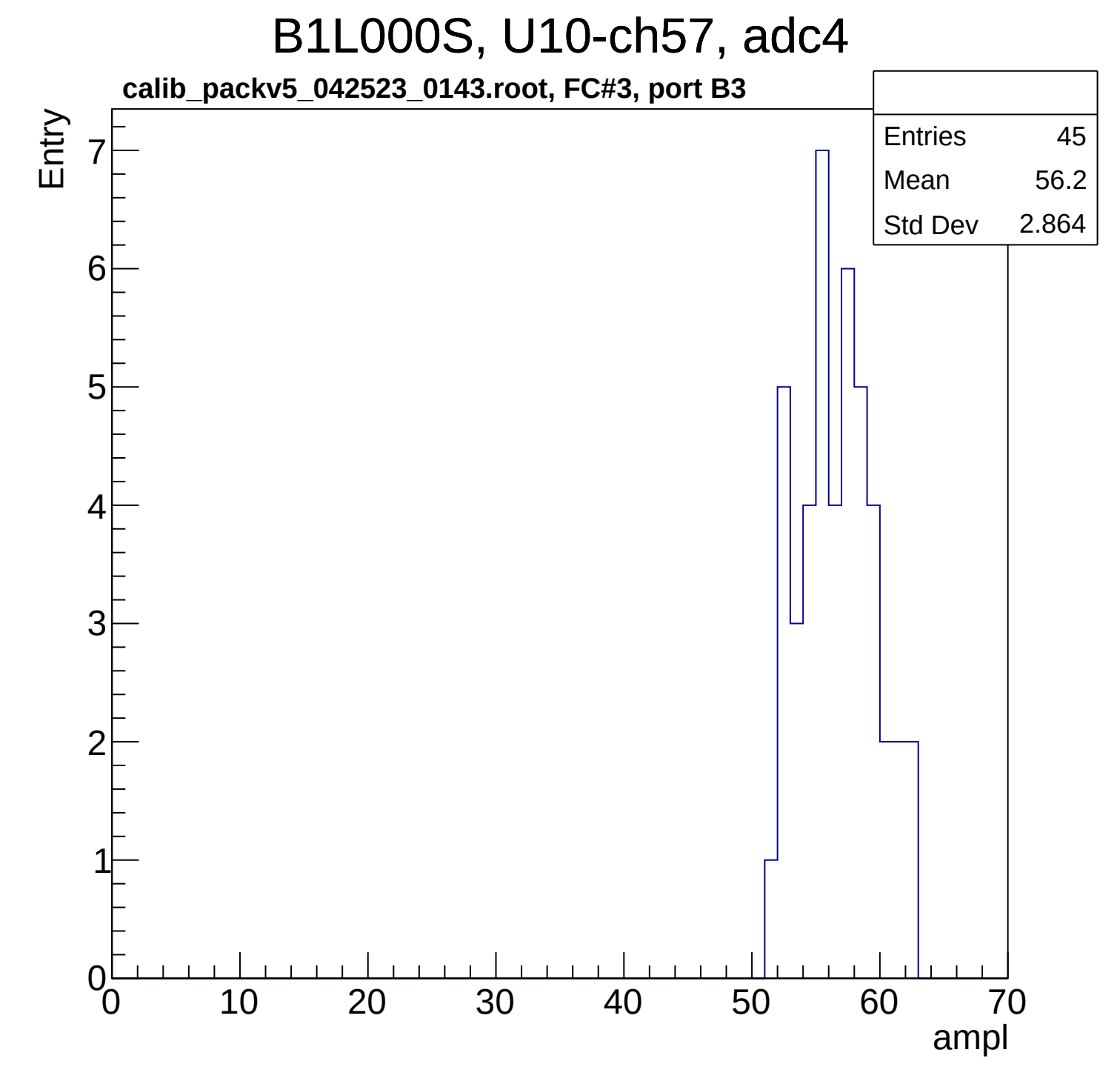
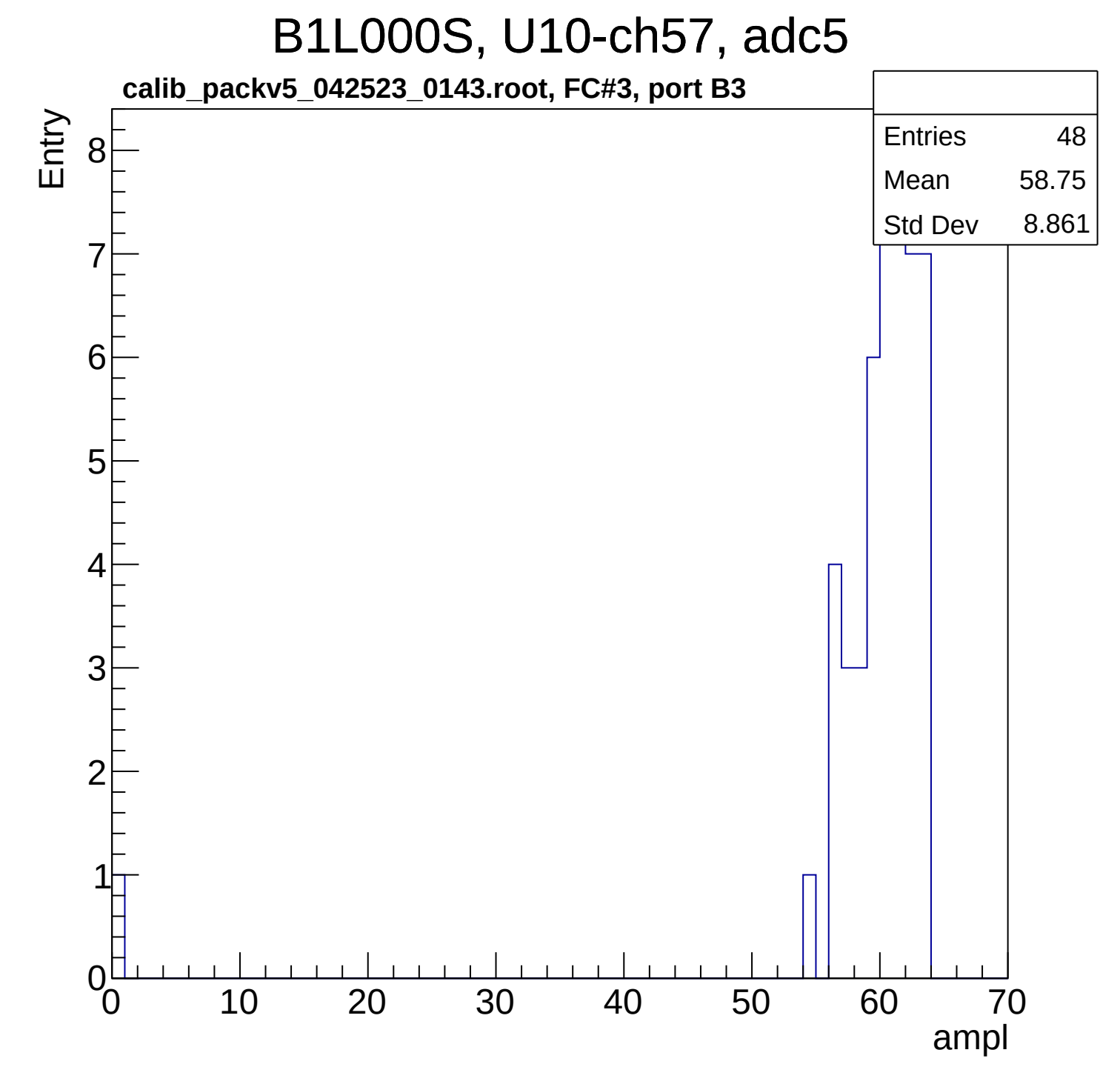

B1L000S, U10-ch57, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 49.87
Std Dev 3.232
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch57, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 56.2
Std Dev 2.864
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch57, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.75
Std Dev 8.861
Entry
ampl
0
1
2
3
4
5
6
7
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0
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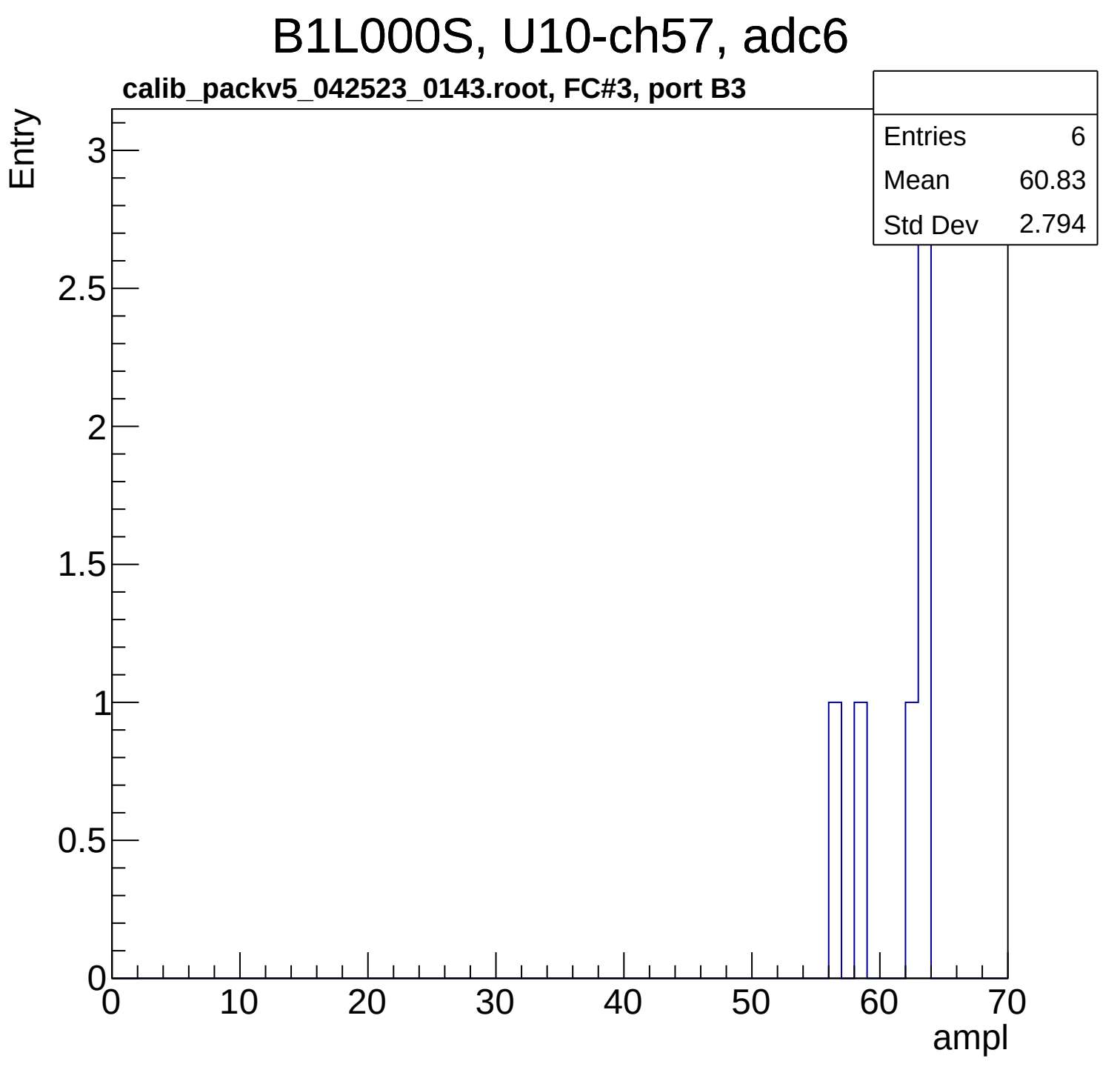
B1L000S, U10-ch57, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 60.83
Std Dev 2.794
Entry
ampl
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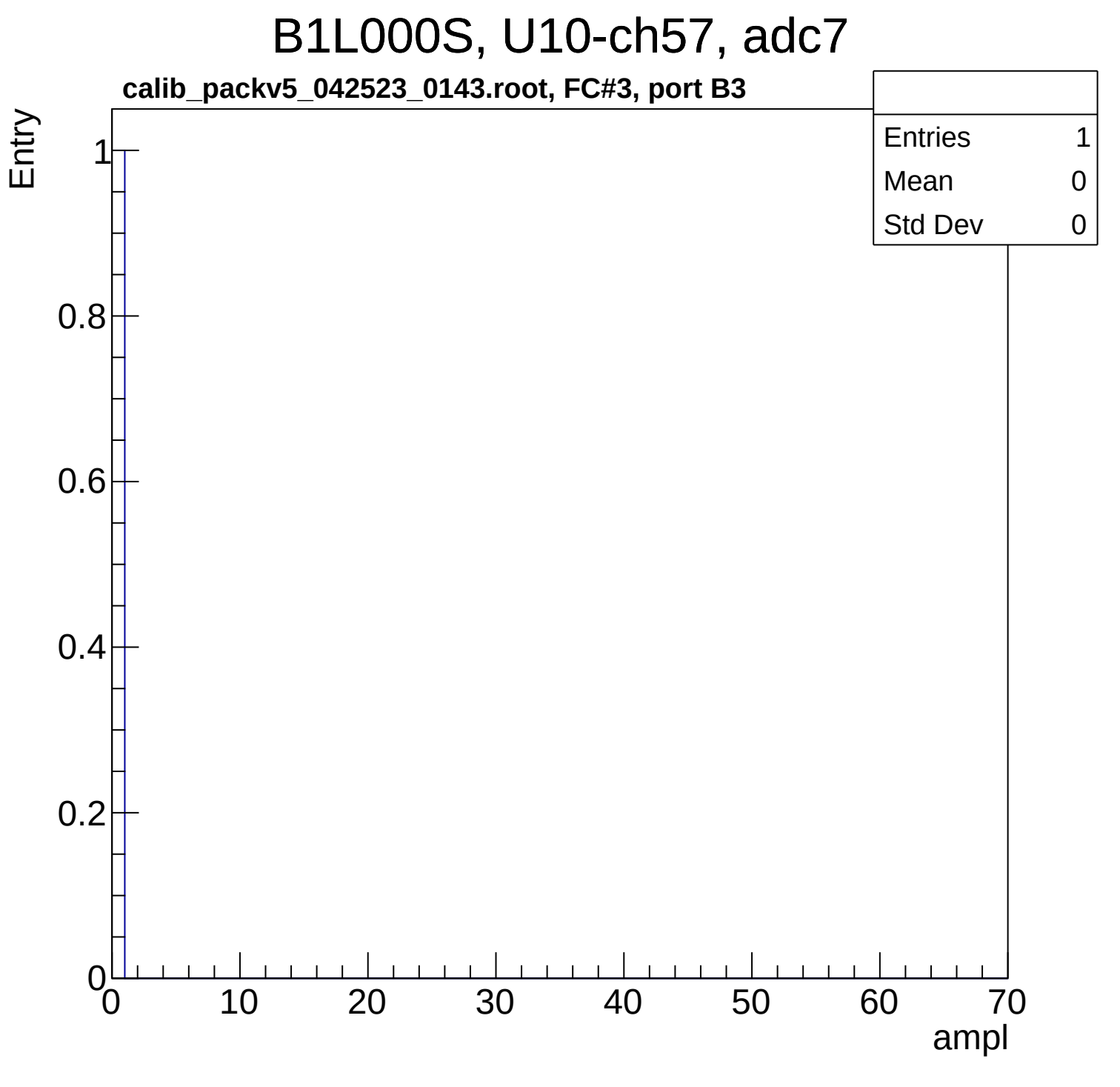
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch57, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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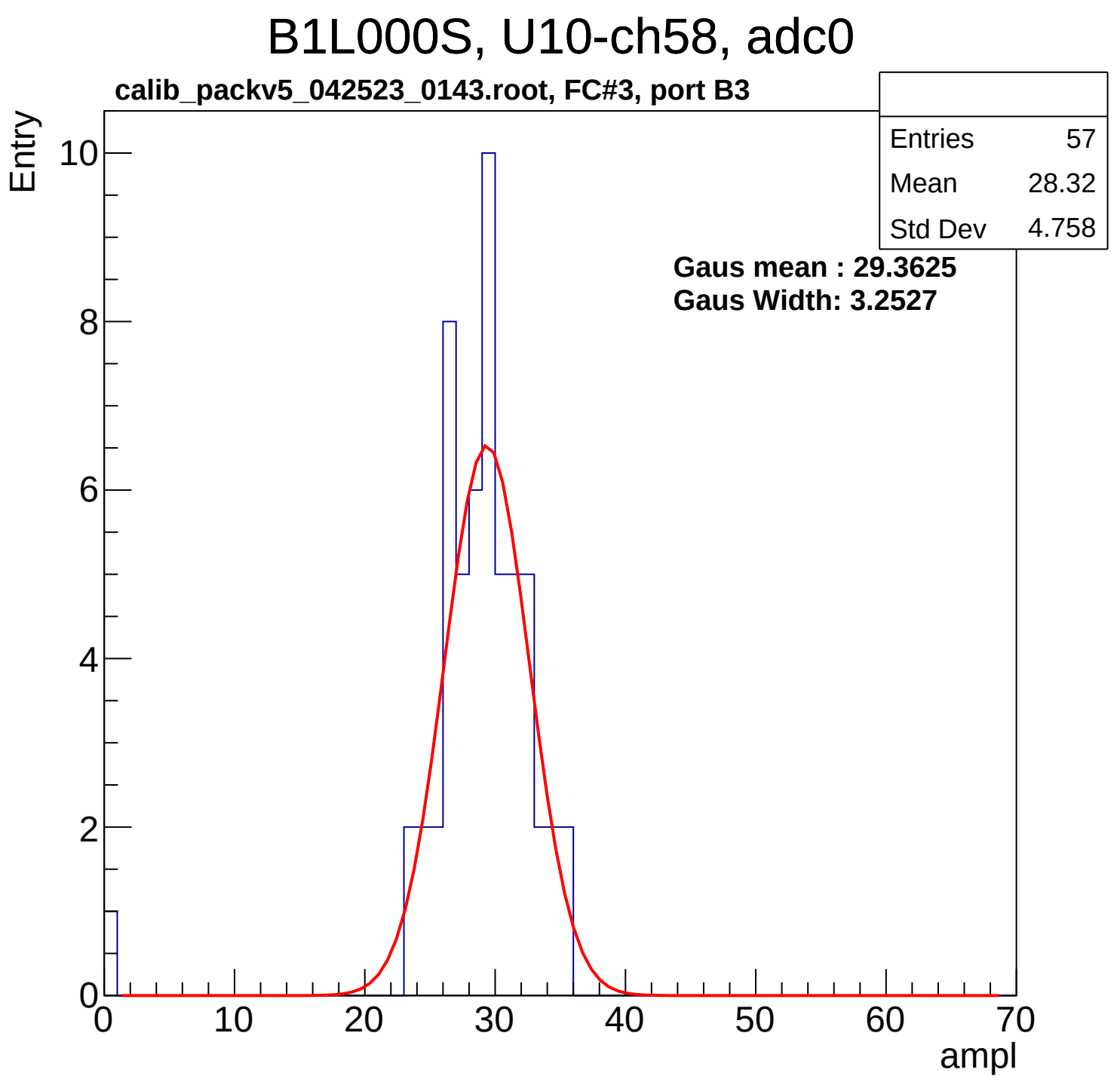
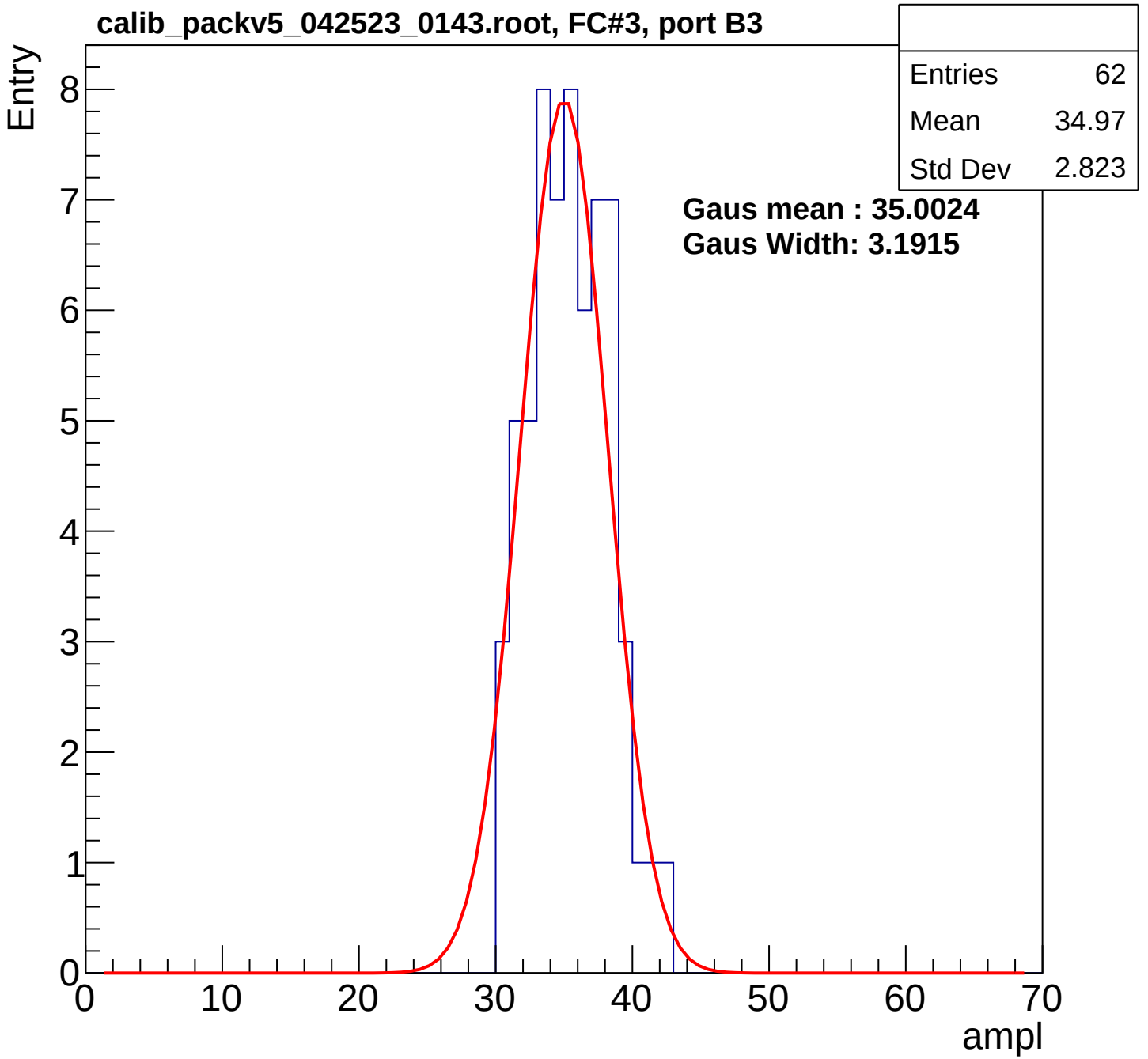
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch58, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.32
Std Dev 4.758
Gaus mean : 29.3625
Gaus Width: 3.2527
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch58, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 34.97
Std Dev 2.823
Gaus mean : 35.0024
Gaus Width: 3.1915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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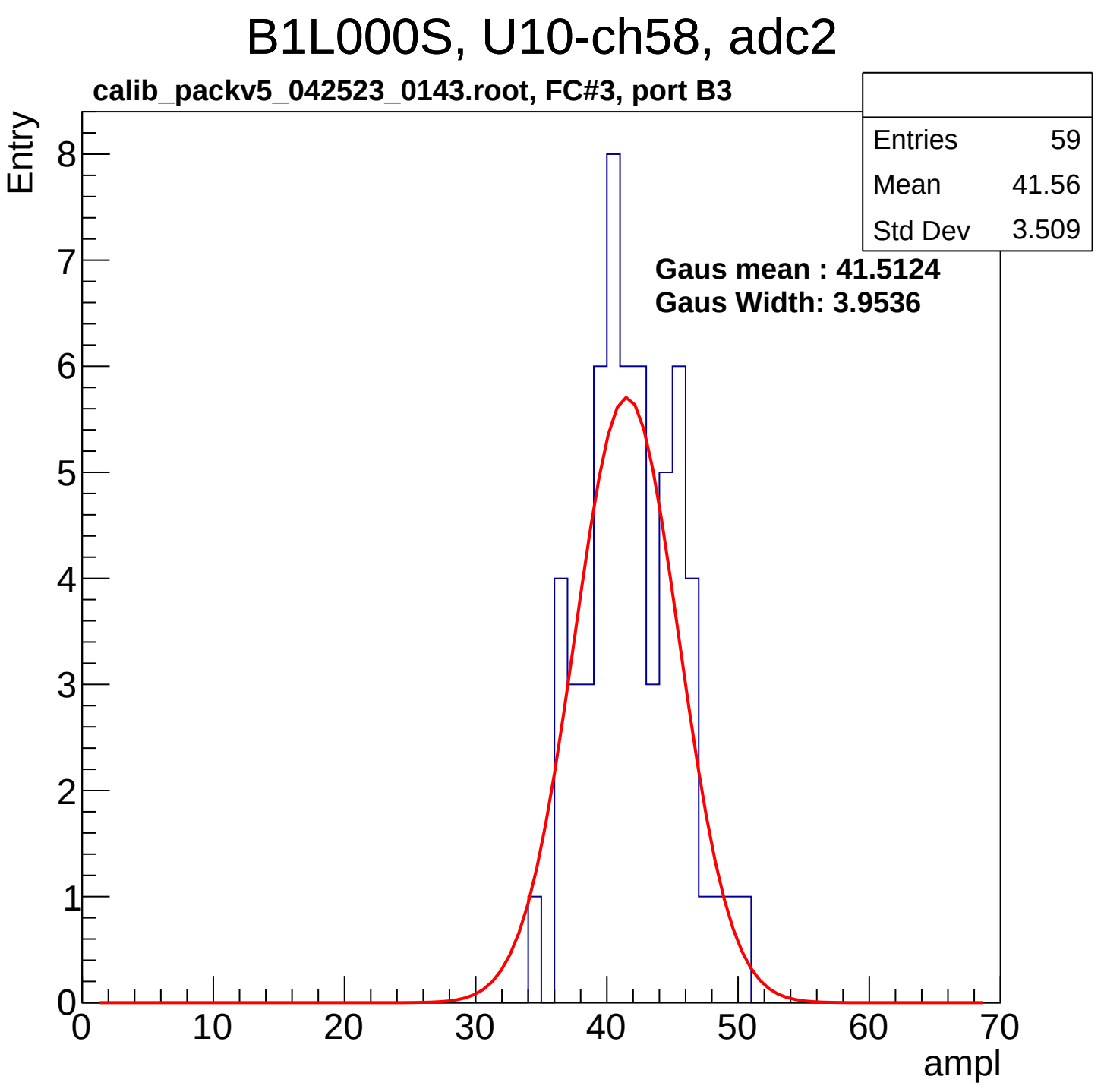
B1L000S, U10-ch58, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 41.56
Std Dev 3.509
Gaus mean : 41.5124
Gaus Width: 3.9536
Entry
ampl
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1
2
3
4
5
6
7
8
0
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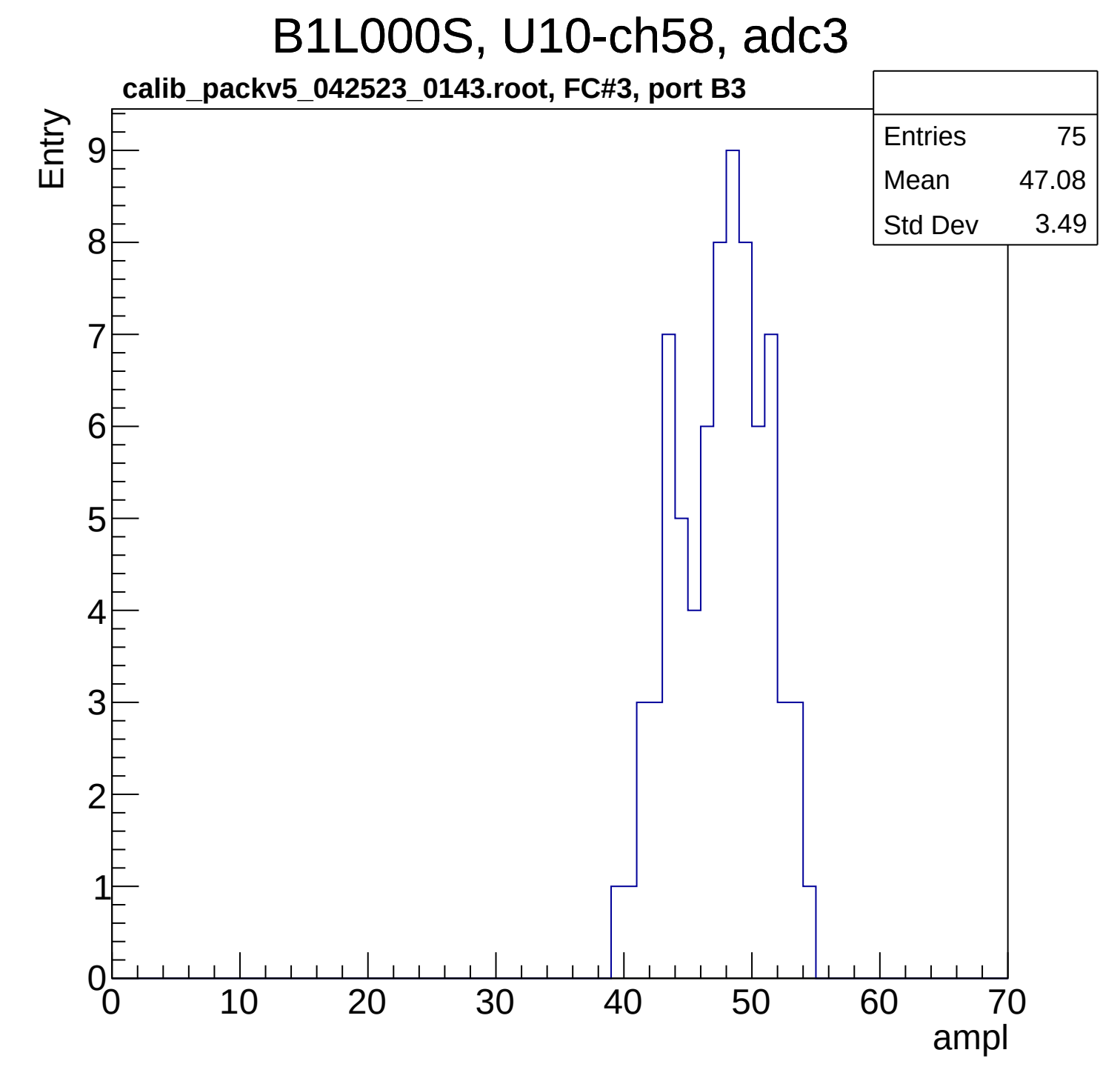
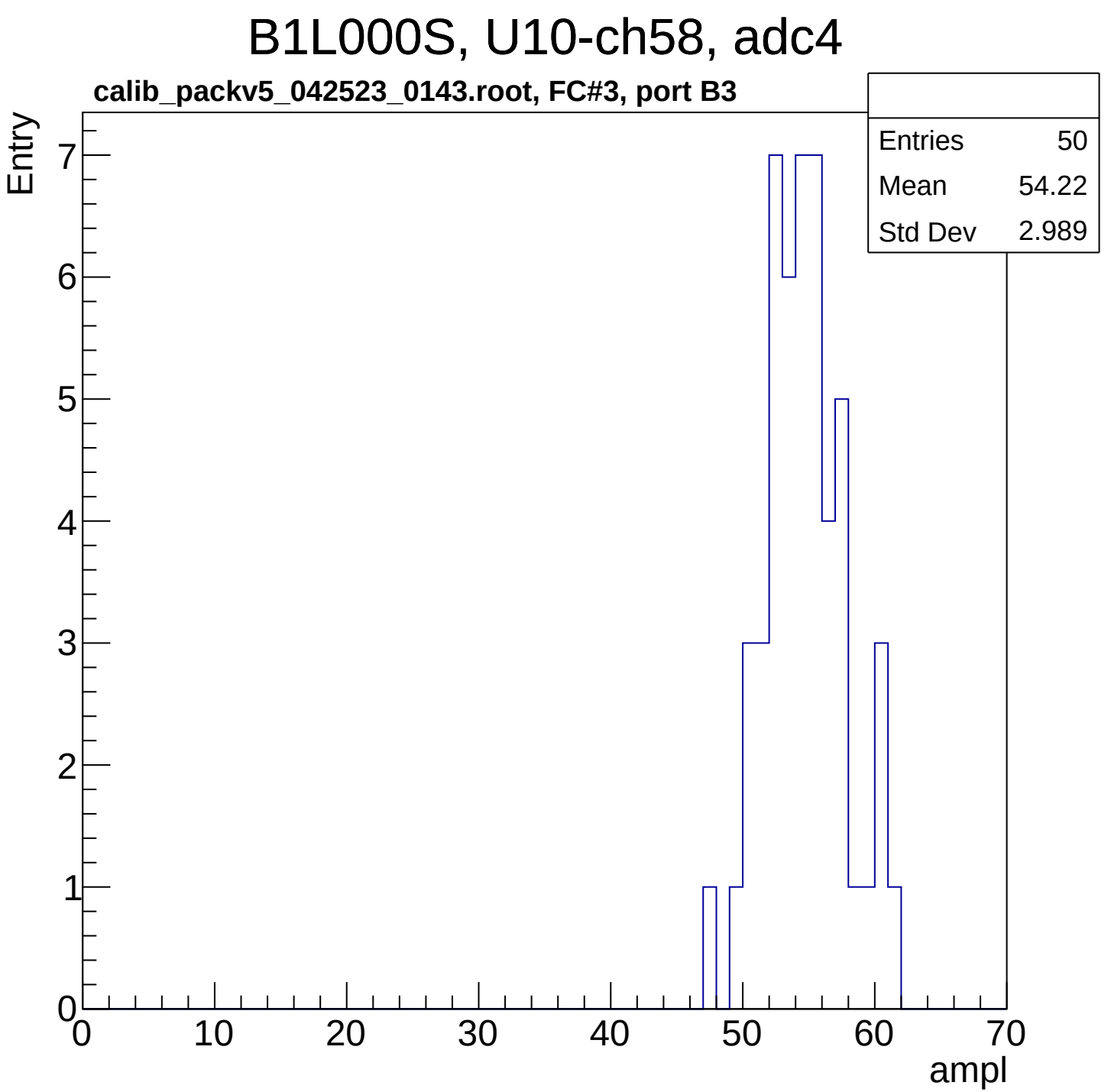
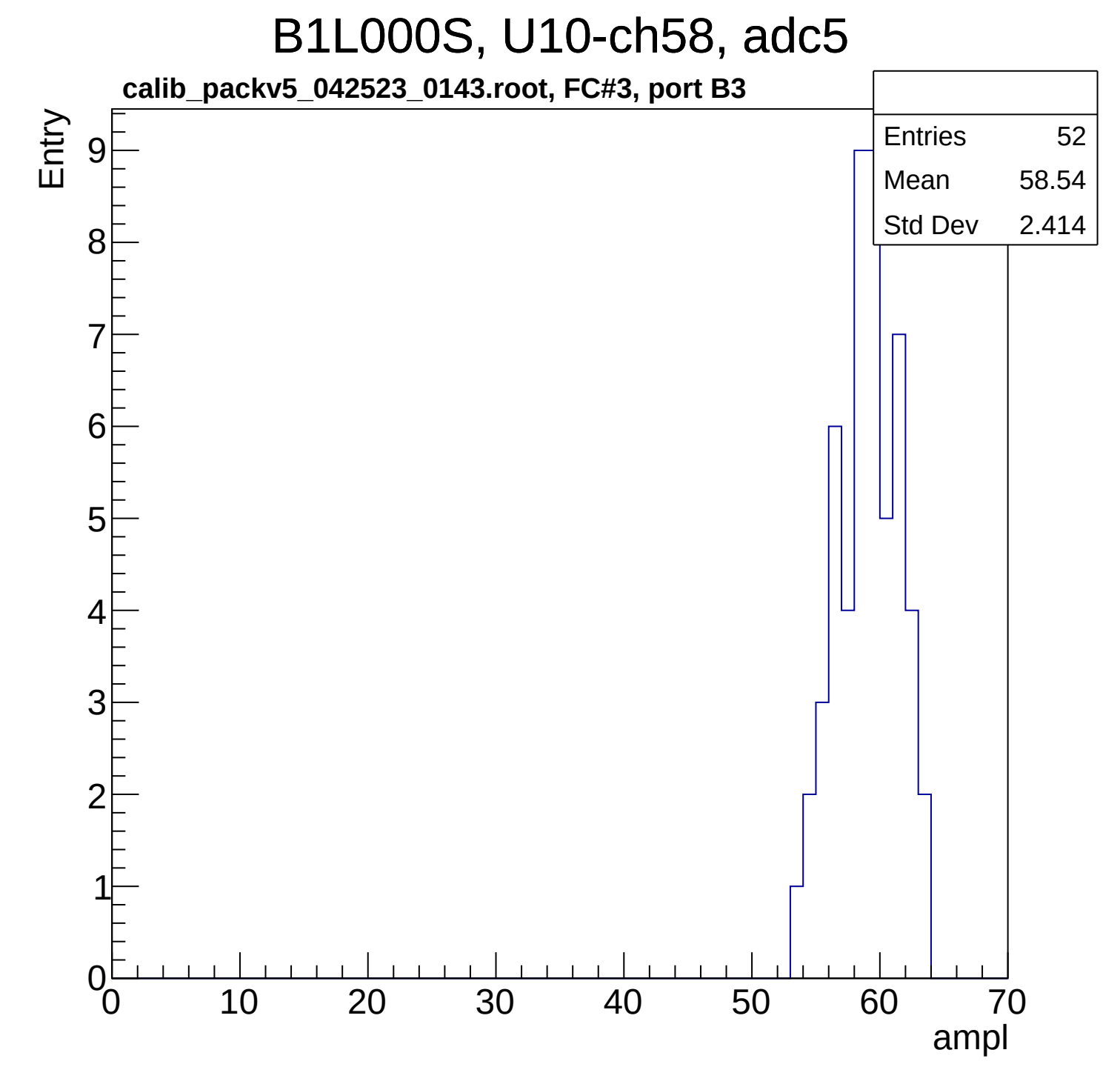

B1L000S, U10-ch58, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 47.08
Std Dev 3.49
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch58, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 54.22
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch58, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.54
Std Dev 2.414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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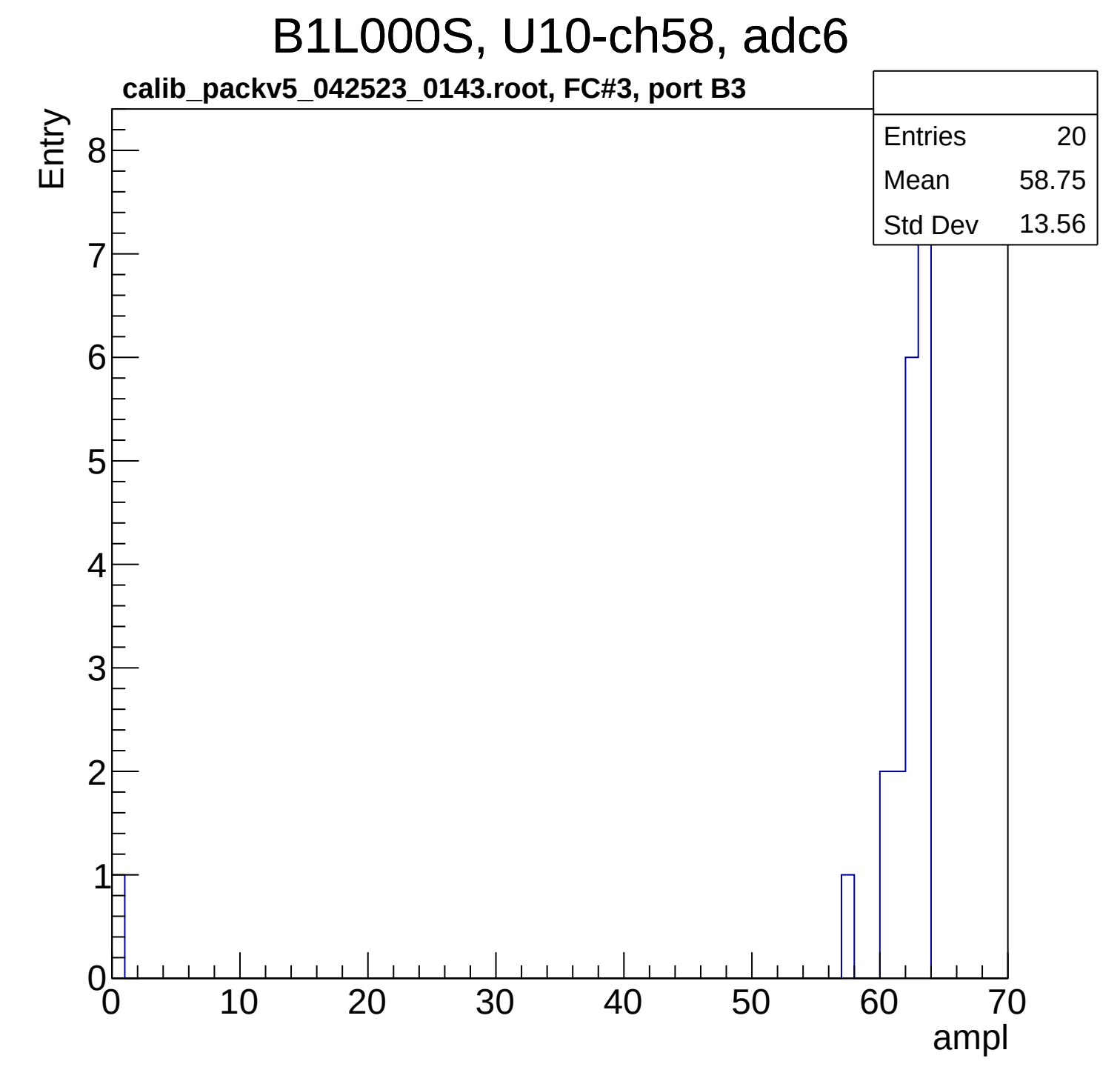
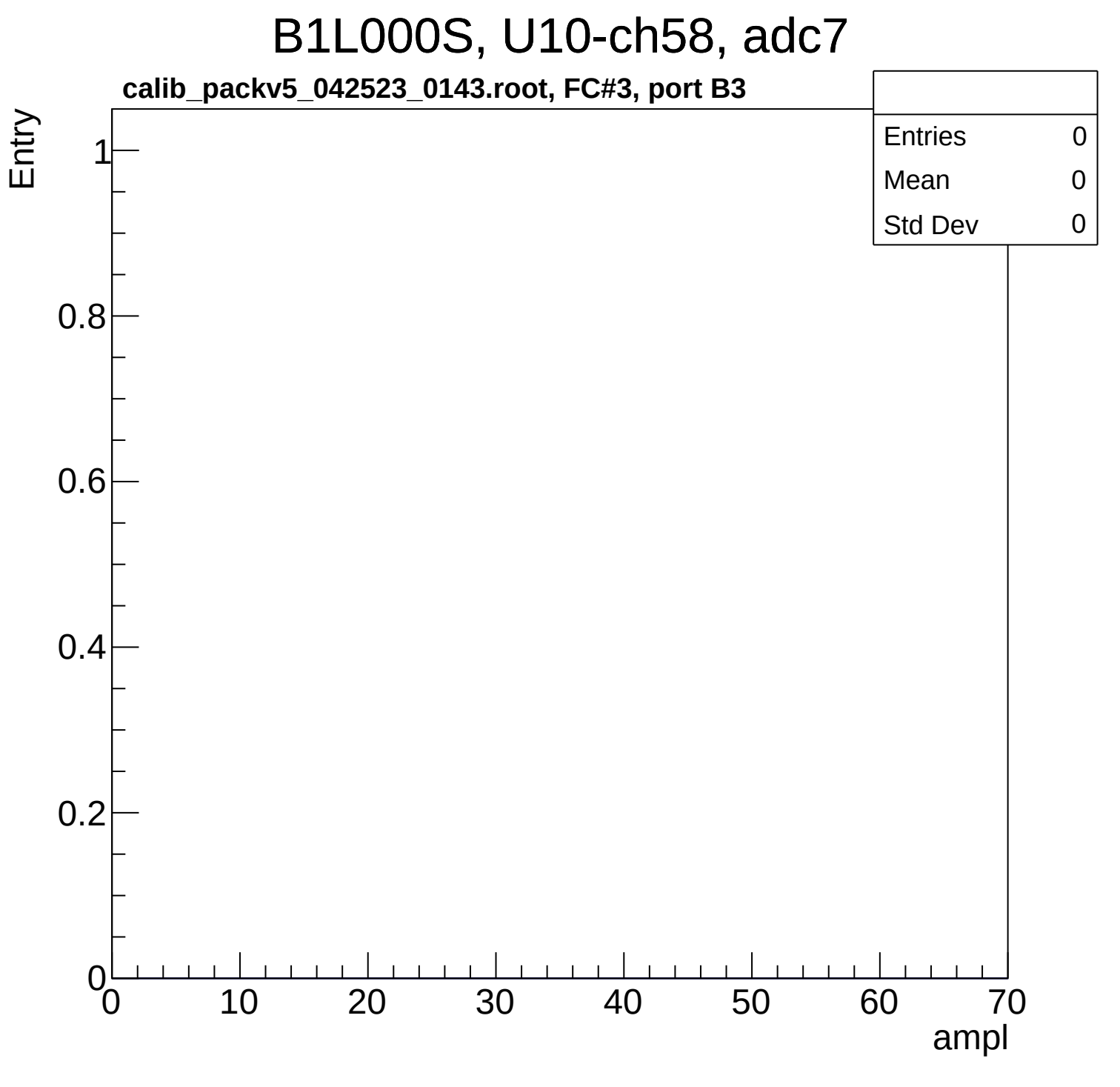
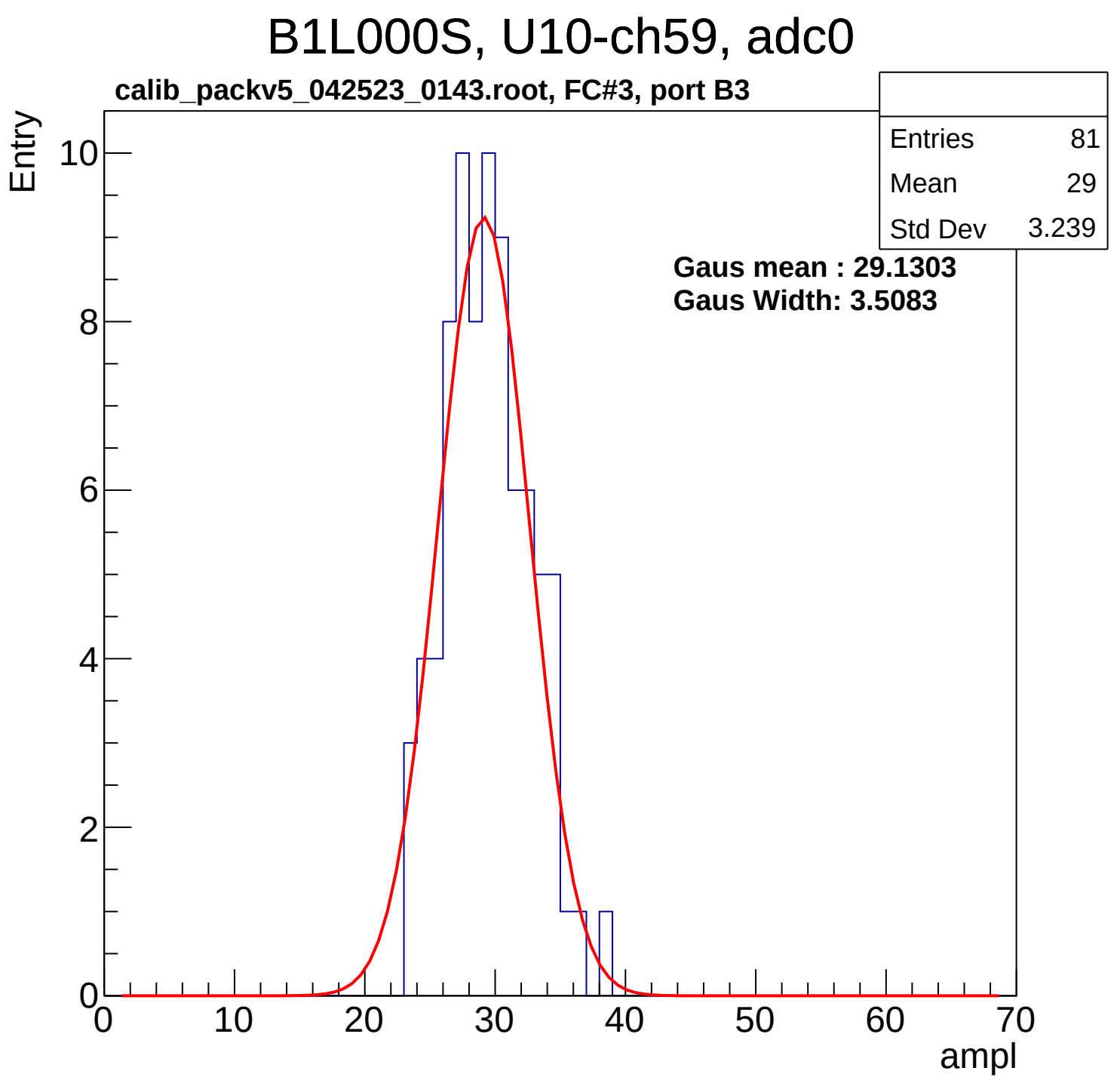
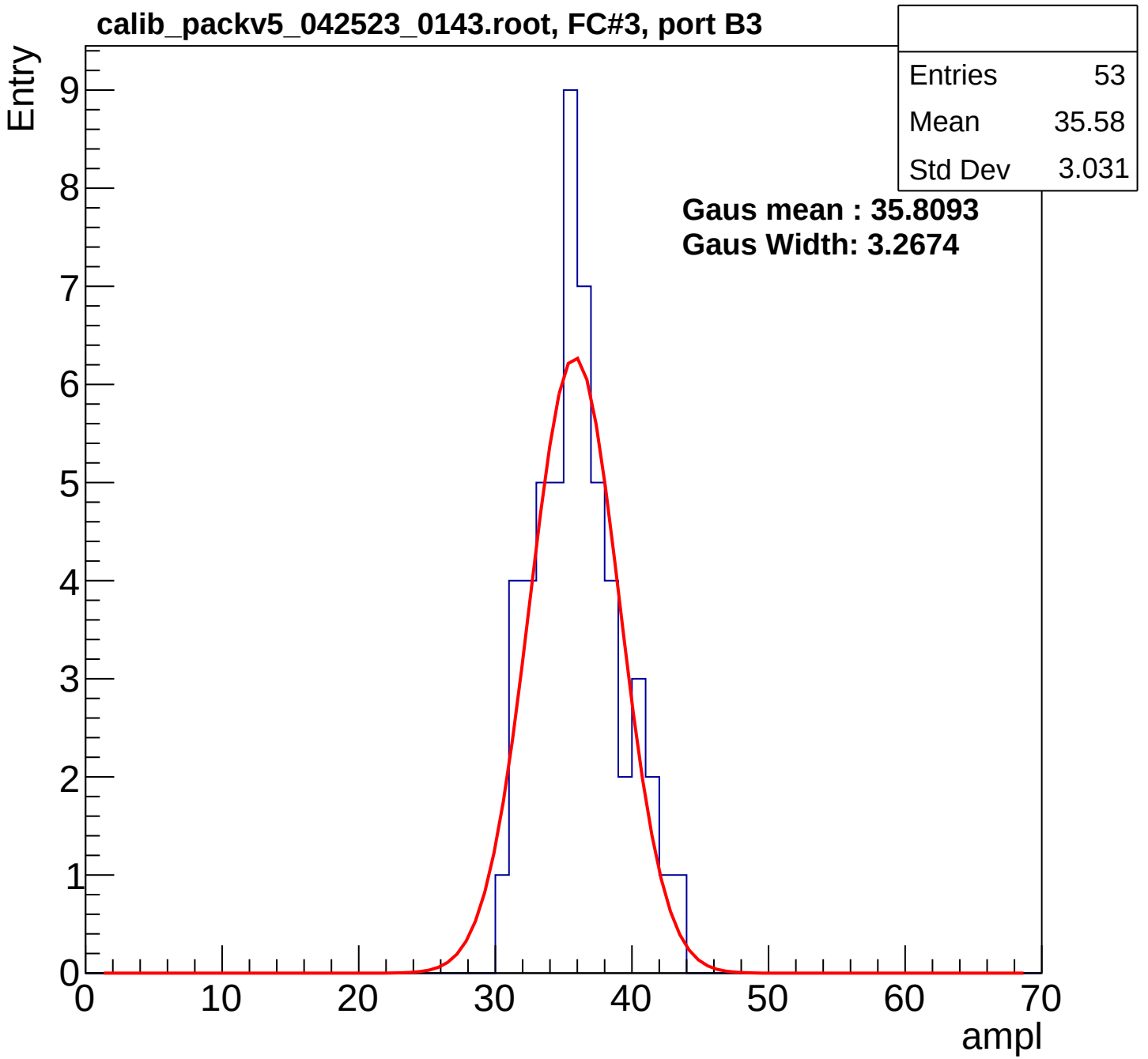
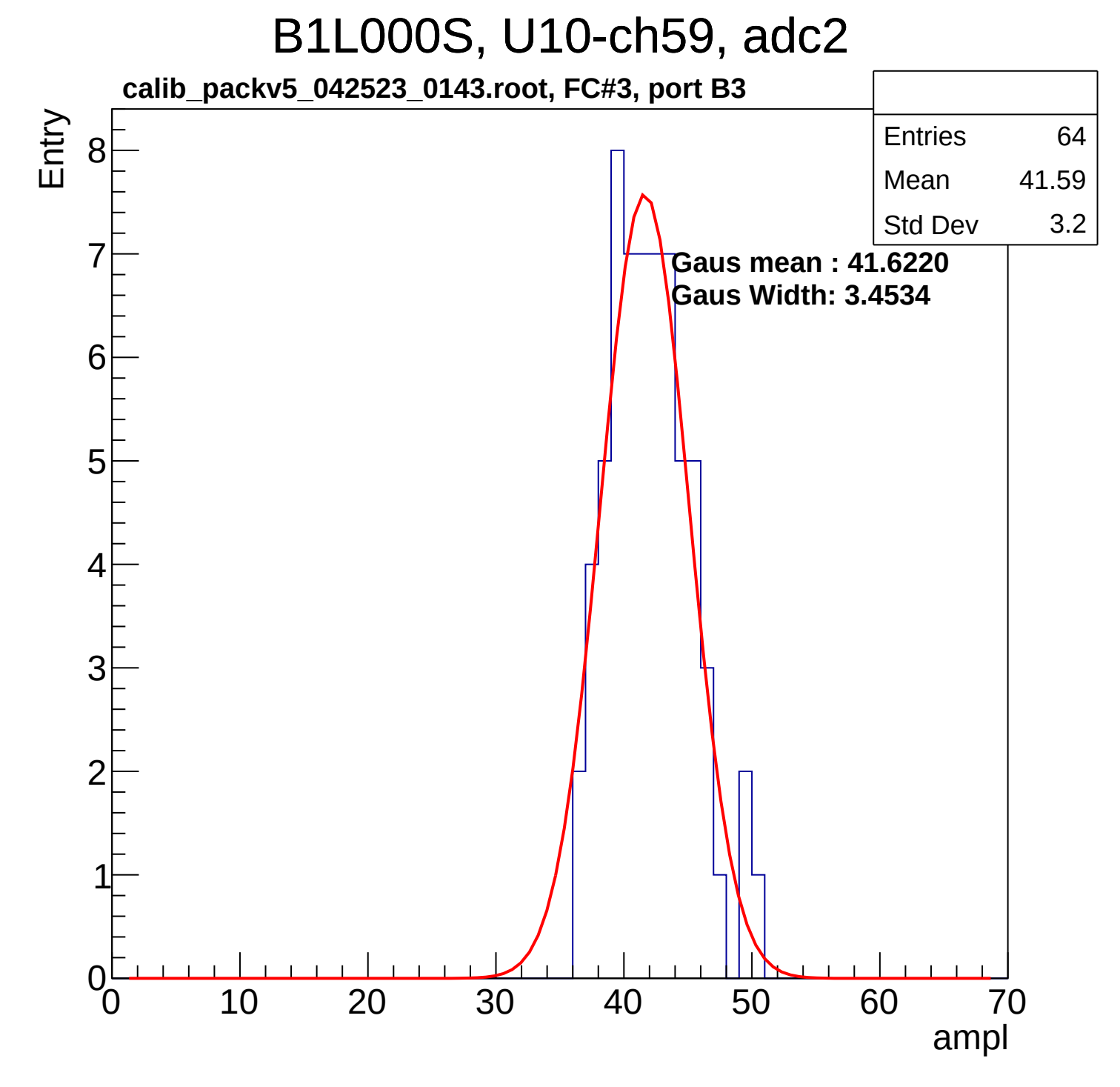
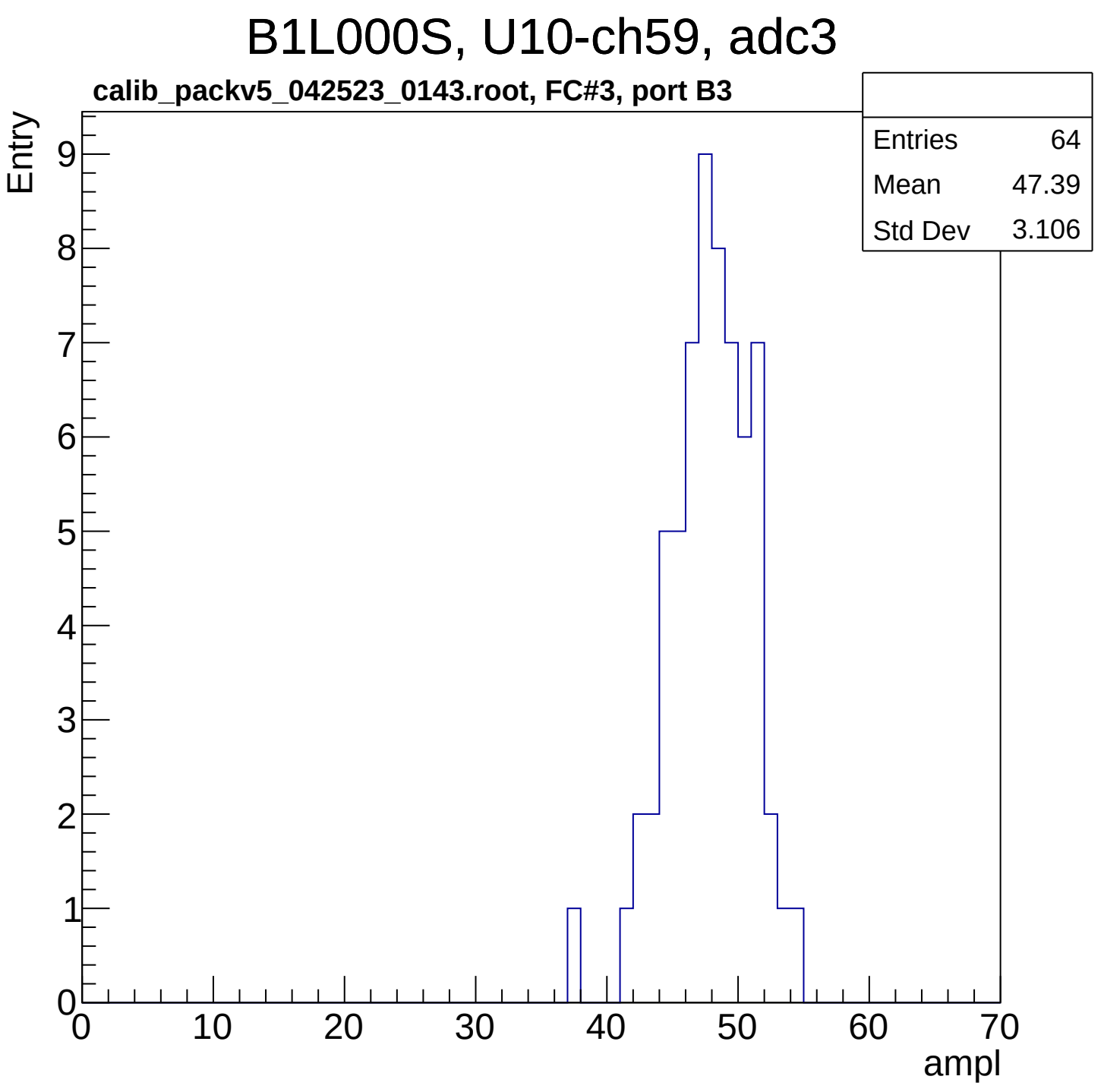

B1L000S, U10-ch58, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.75
Std Dev 13.56
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch58, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch59, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 29
Std Dev 3.239
Gaus mean : 29.1303
Gaus Width: 3.5083
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch59, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 35.58
Std Dev 3.031
Gaus mean : 35.8093
Gaus Width: 3.2674
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U10-ch59, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 41.59
Std Dev 3.2
Gaus mean : 41.6220
Gaus Width: 3.4534
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch59, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.39
Std Dev 3.106
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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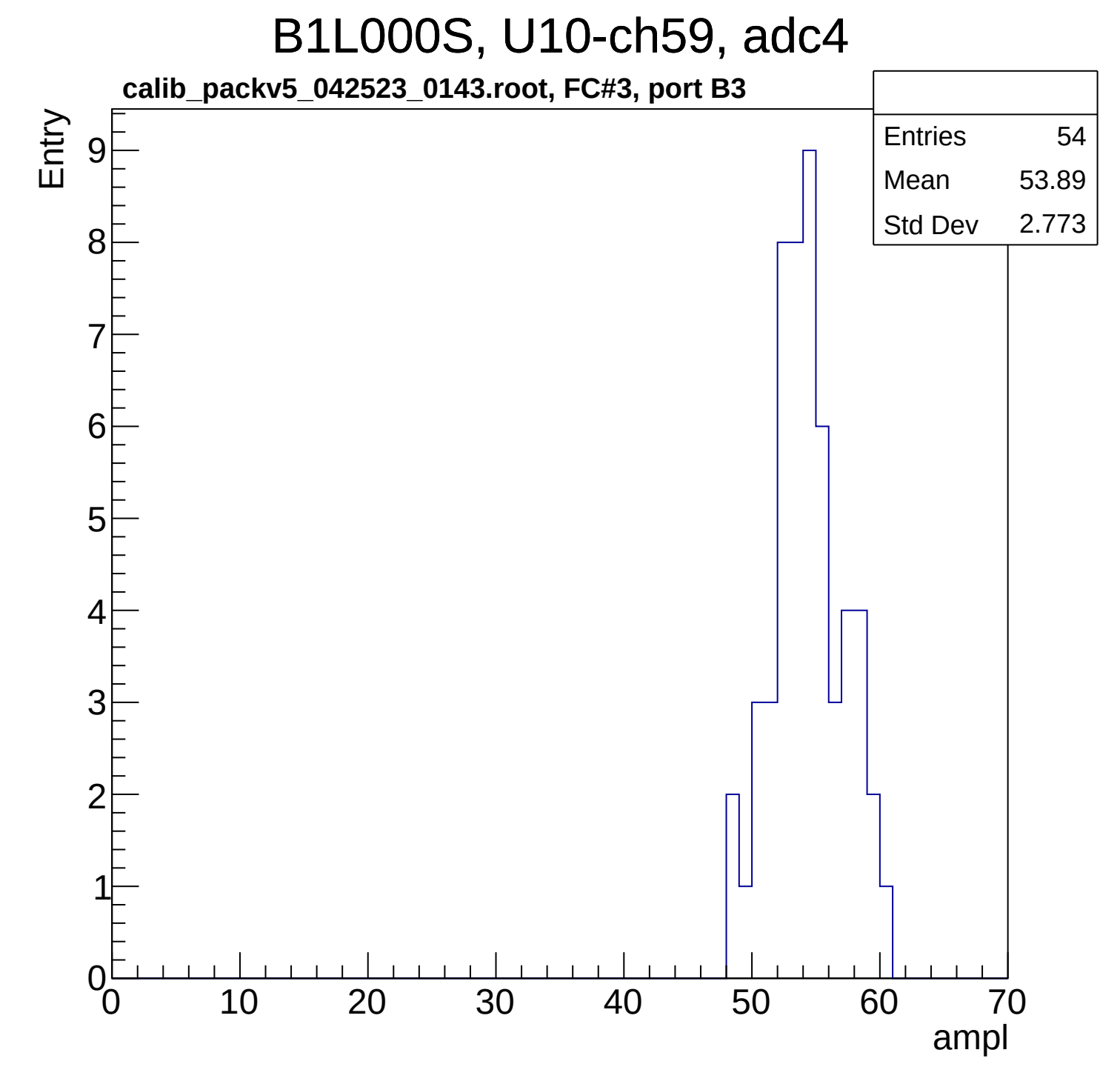
B1L000S, U10-ch59, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.89
Std Dev 2.773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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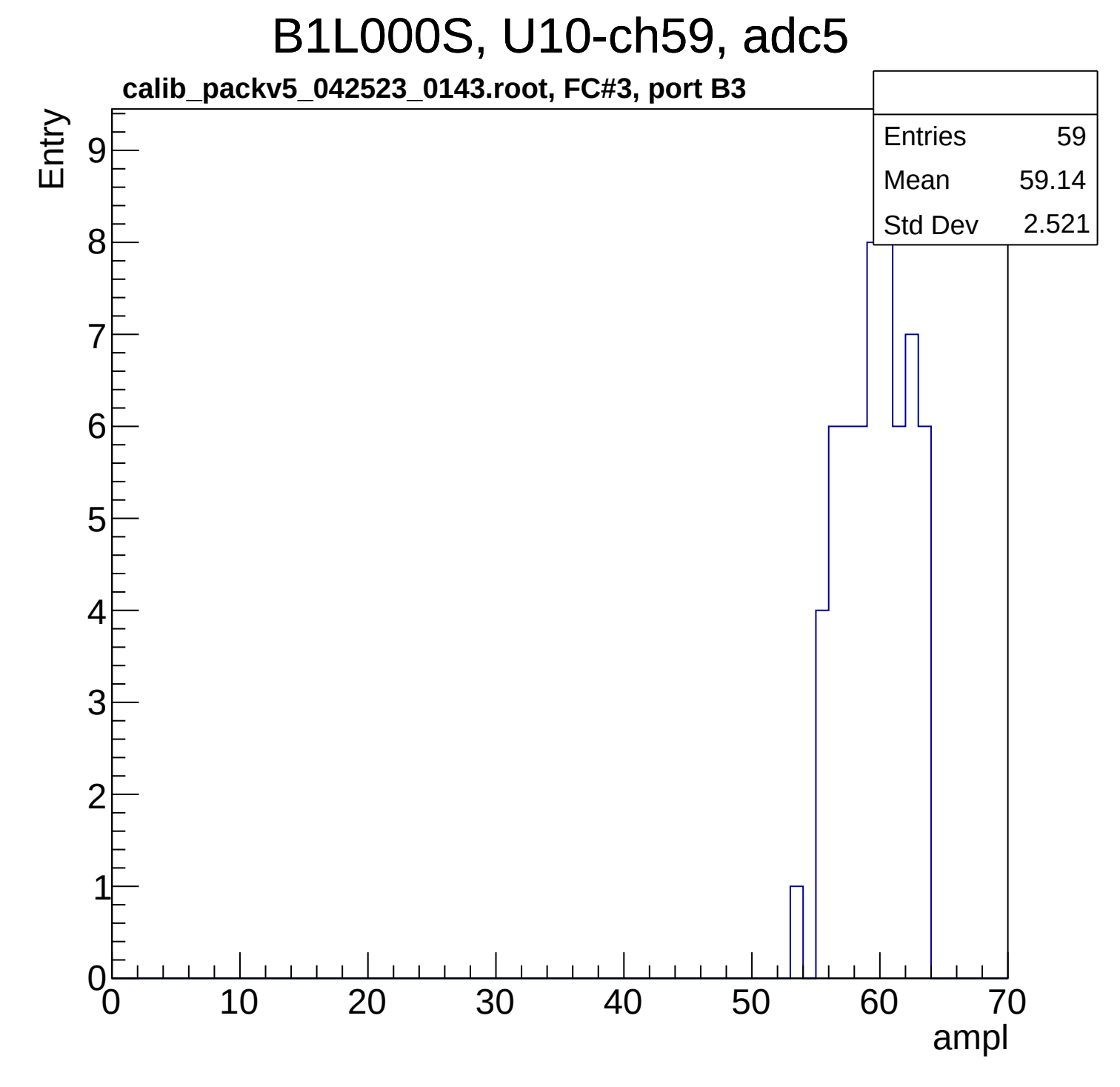
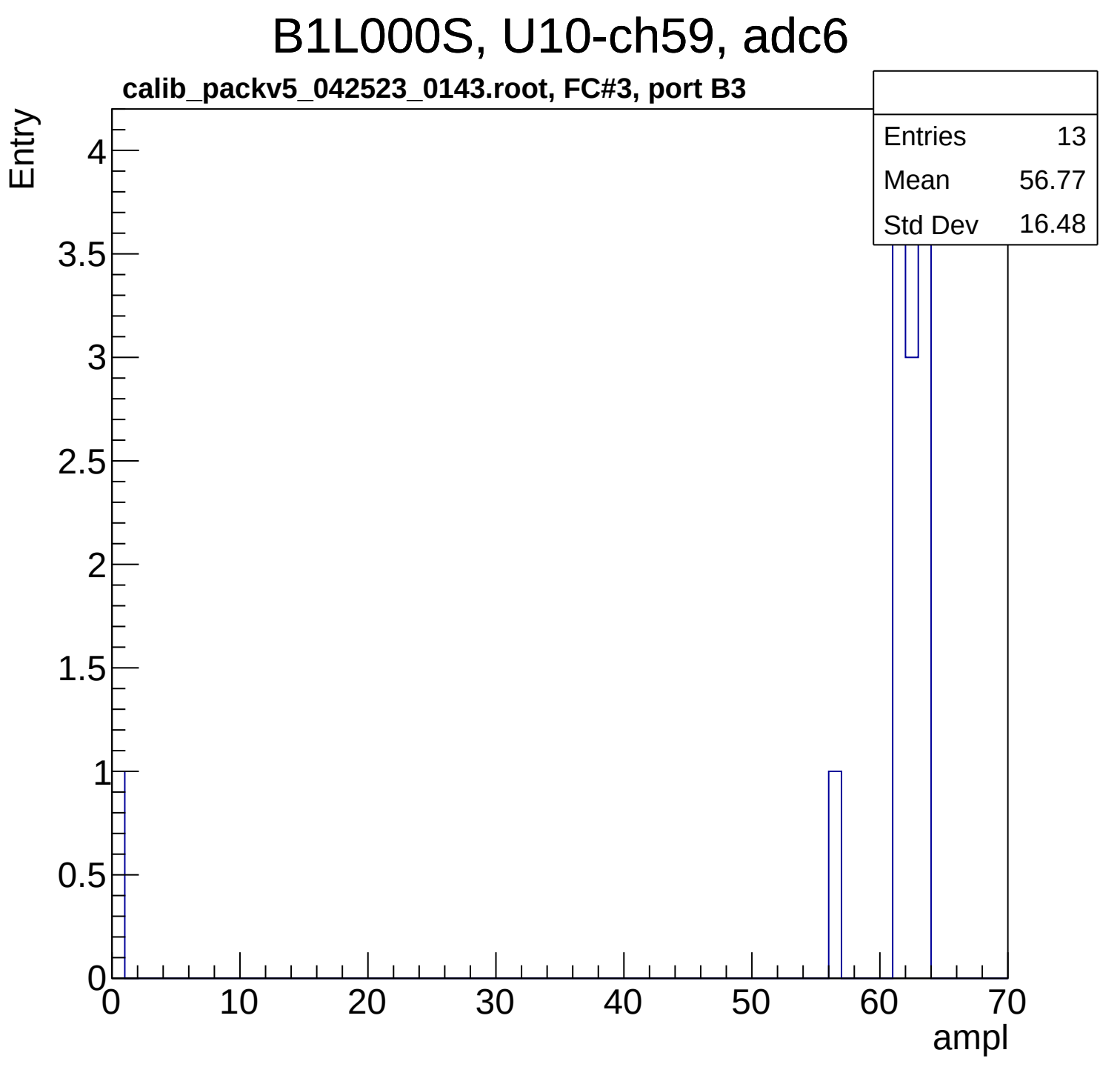

B1L000S, U10-ch59, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 59.14
Std Dev 2.521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch59, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.77
Std Dev 16.48
Entry
ampl
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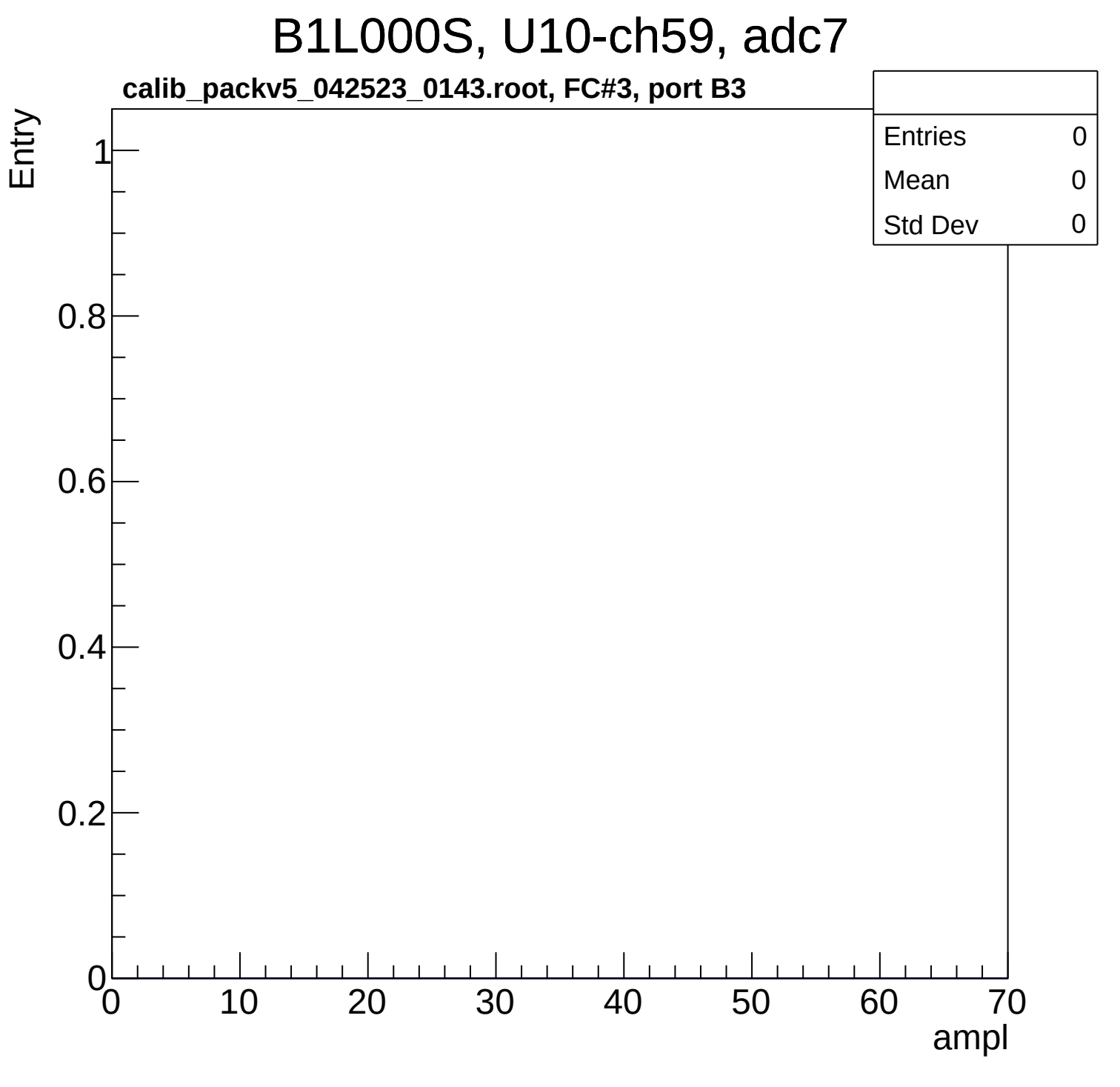
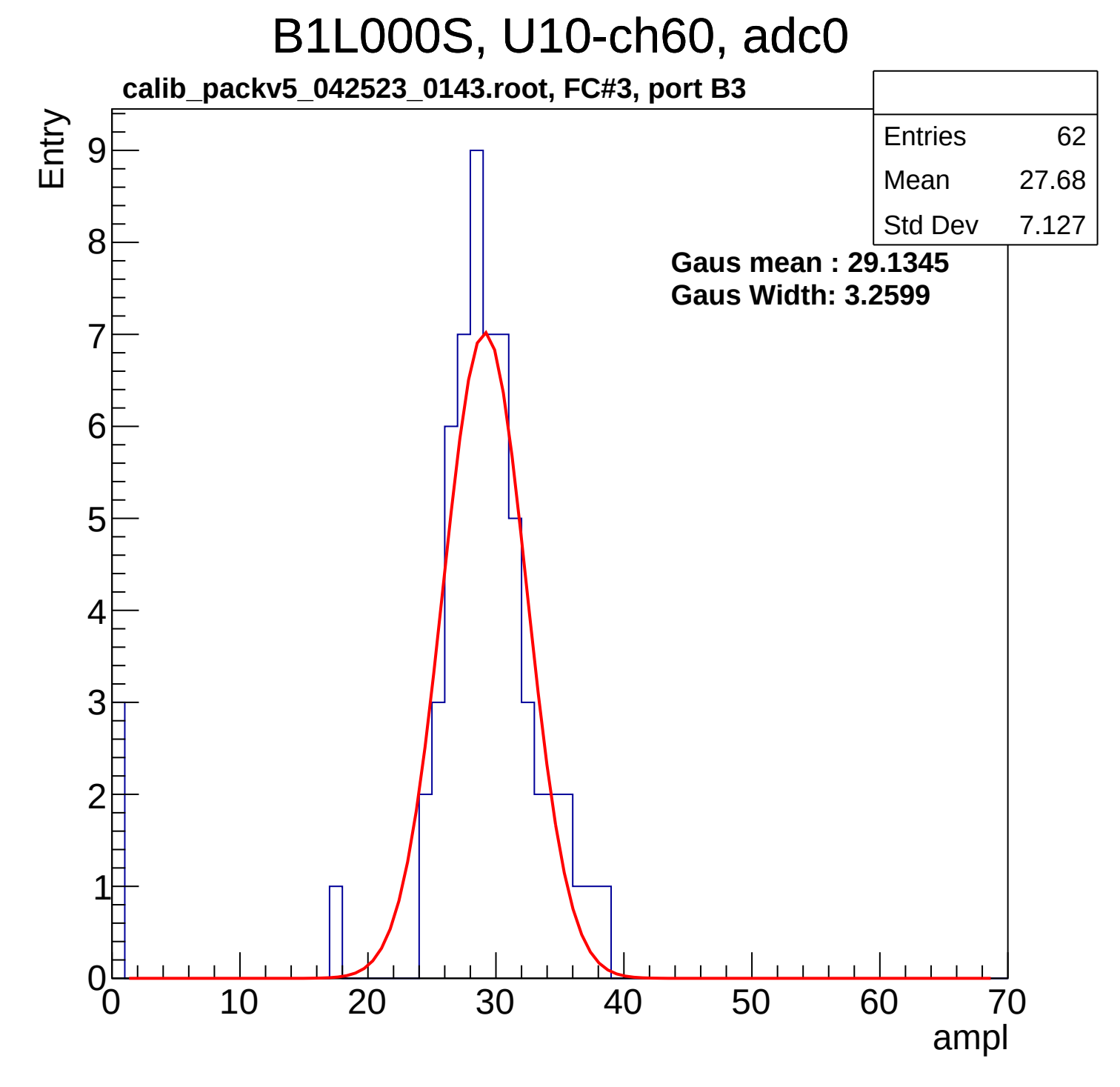
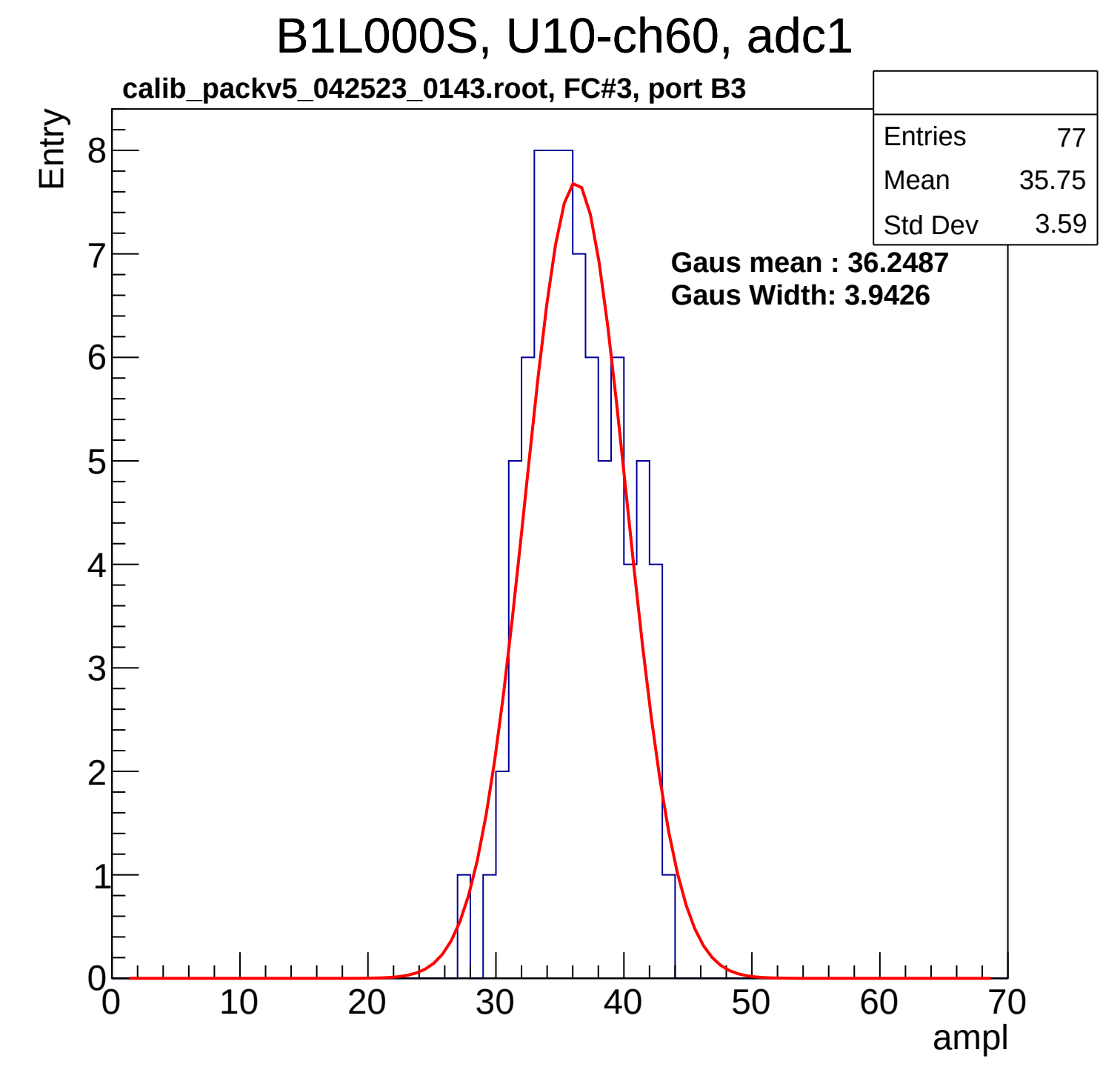
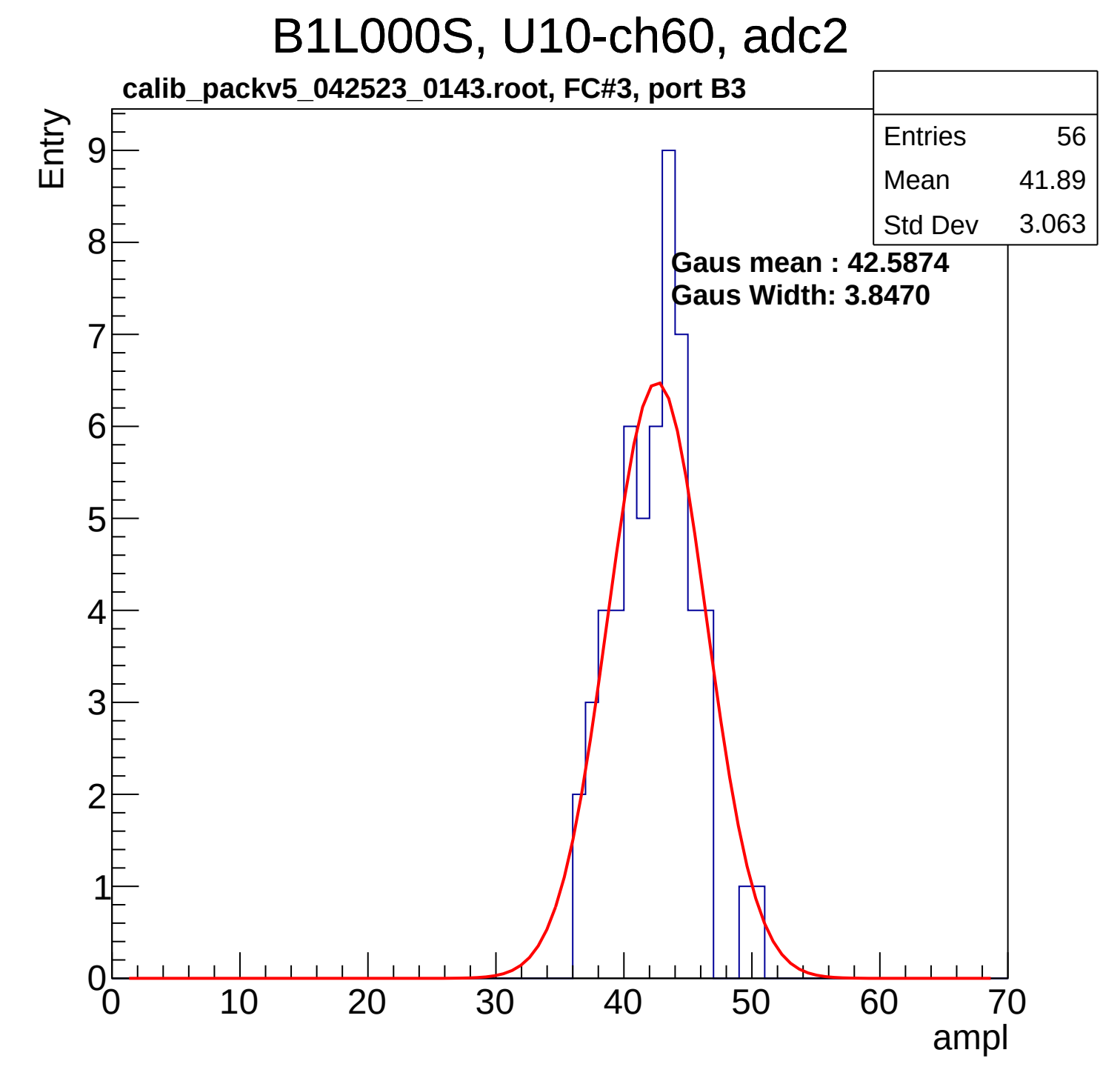
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch59, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch60, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.68
Std Dev 7.127
Gaus mean : 29.1345
Gaus Width: 3.2599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch60, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 35.75
Std Dev 3.59
Gaus mean : 36.2487
Gaus Width: 3.9426
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch60, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.89
Std Dev 3.063
Gaus mean : 42.5874
Gaus Width: 3.8470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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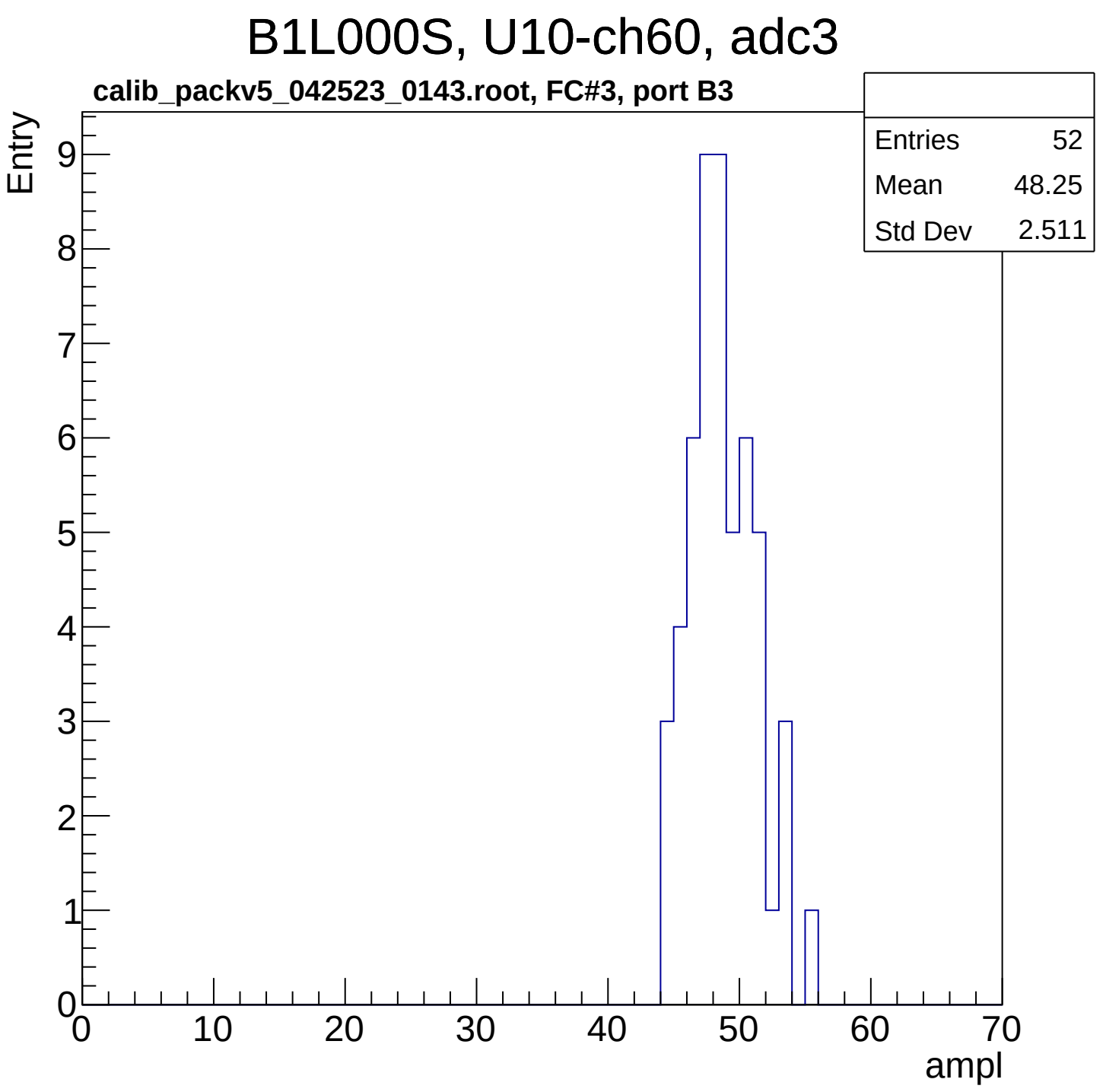
20
30
40
50
60
70


B1L000S, U10-ch60, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 48.25
Std Dev 2.511
Entry
ampl
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3
4
5
6
7
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0
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60
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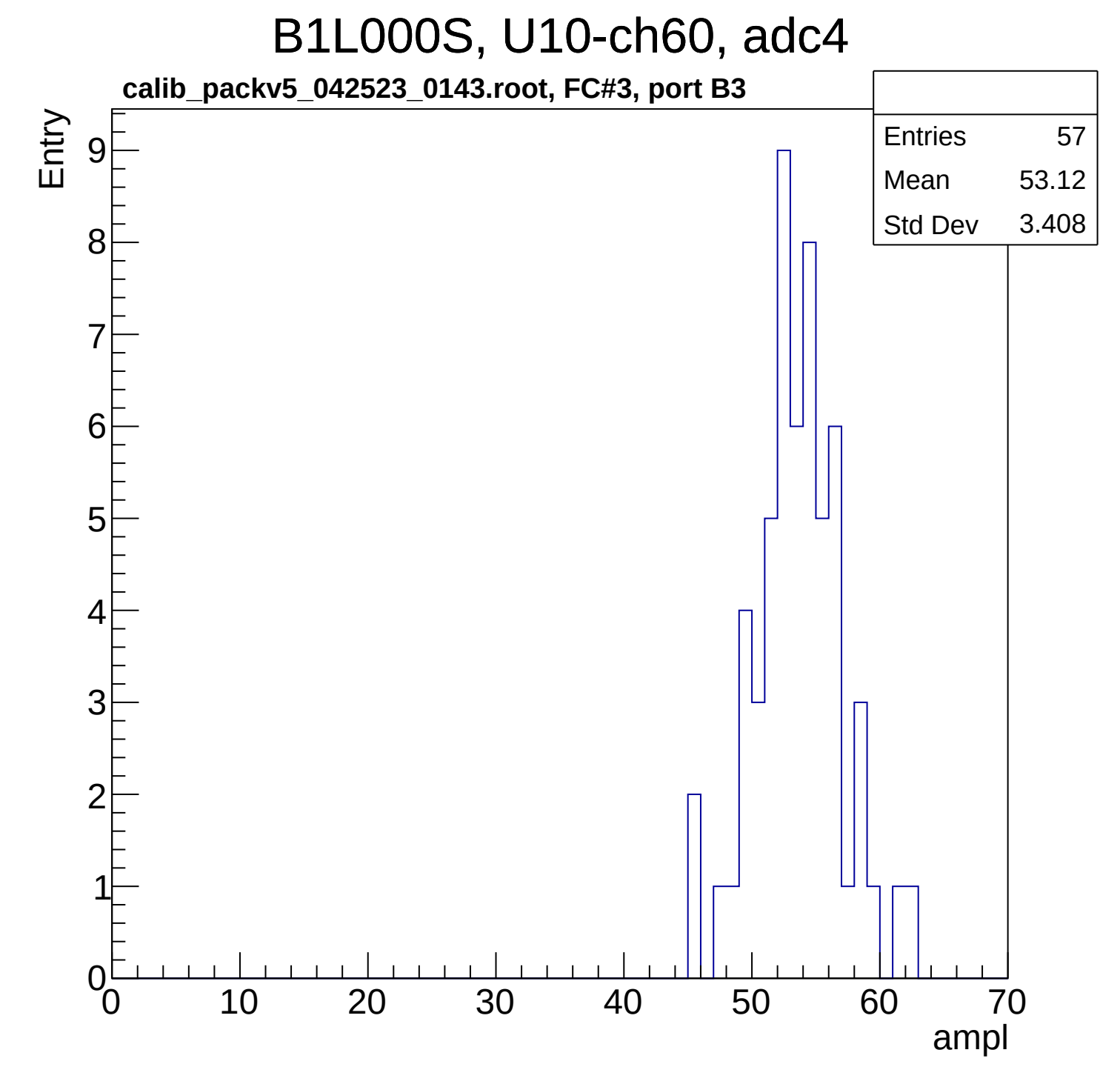
B1L000S, U10-ch60, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.12
Std Dev 3.408
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
50
60
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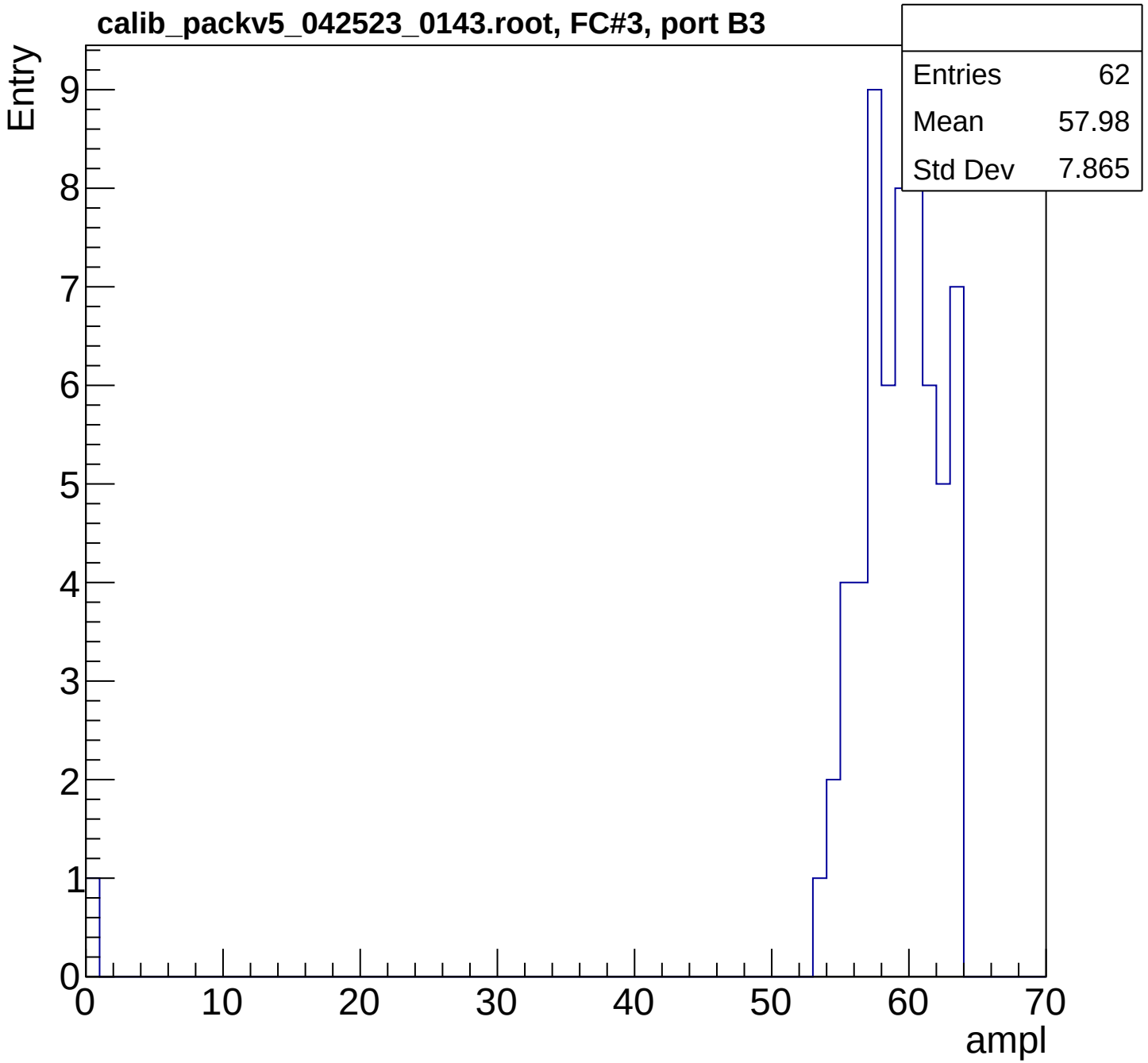
B1L000S, U10-ch60, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.98
Std Dev 7.865
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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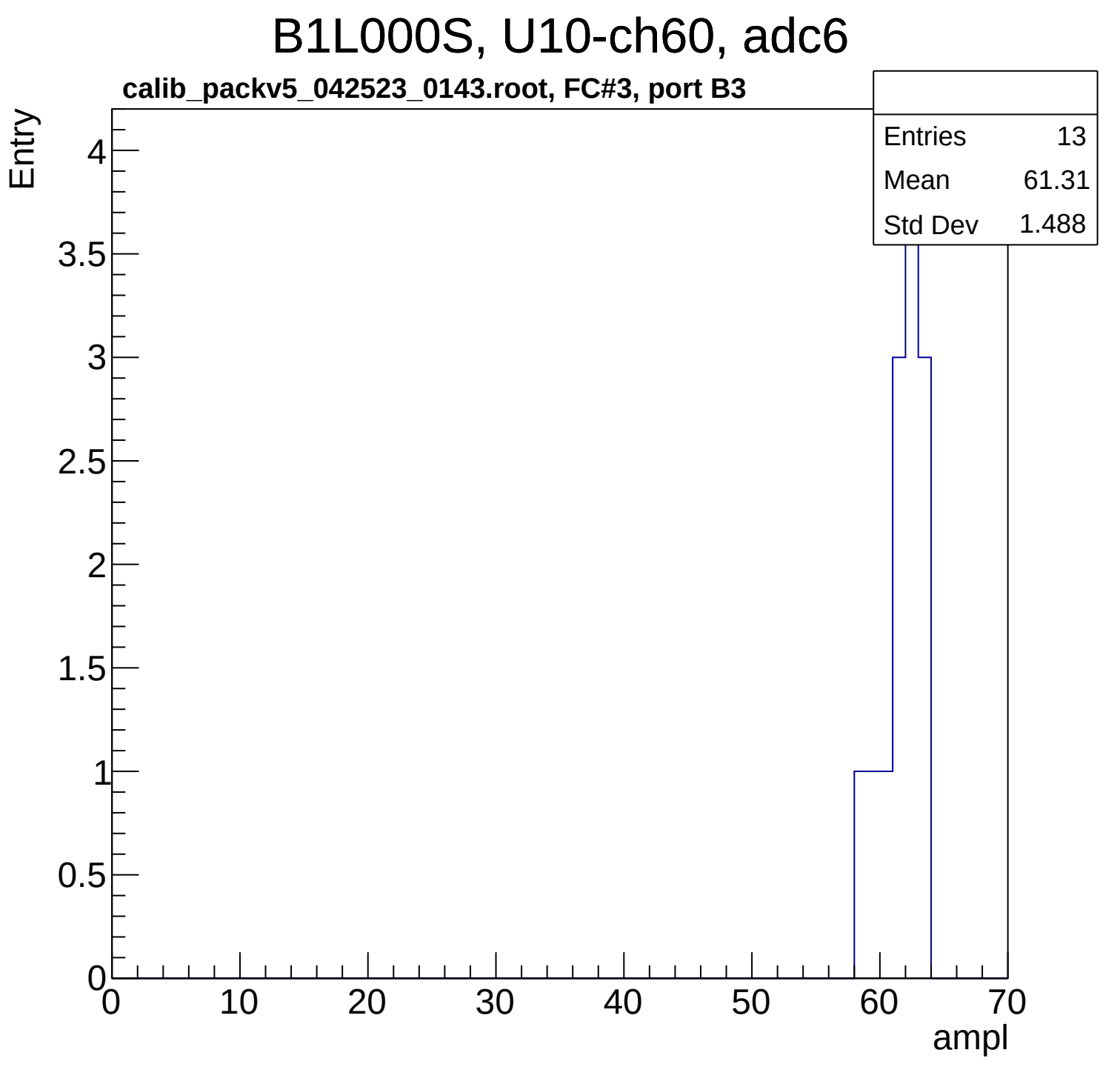
B1L000S, U10-ch60, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.31
Std Dev 1.488
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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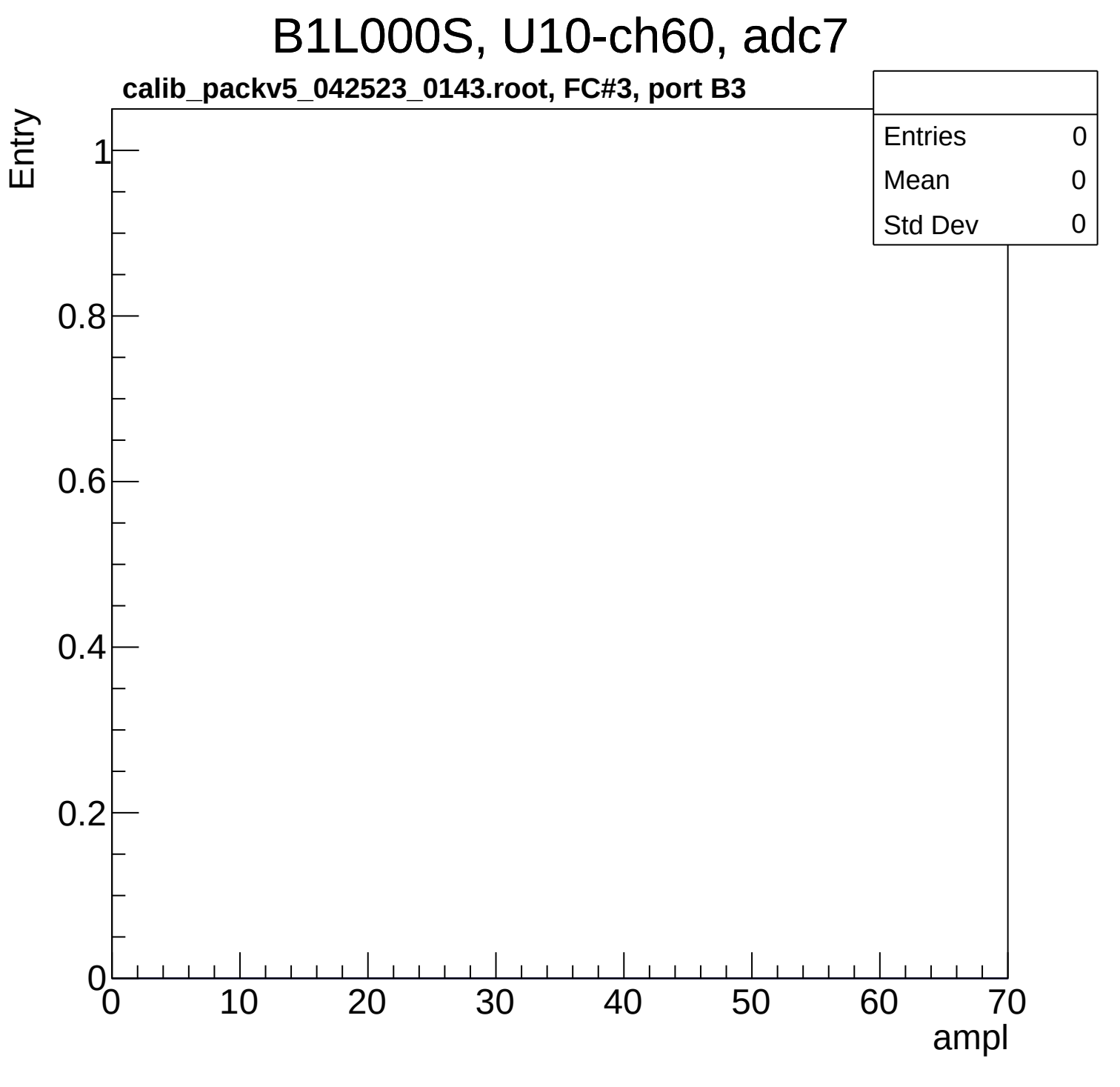
60
70


B1L000S, U10-ch60, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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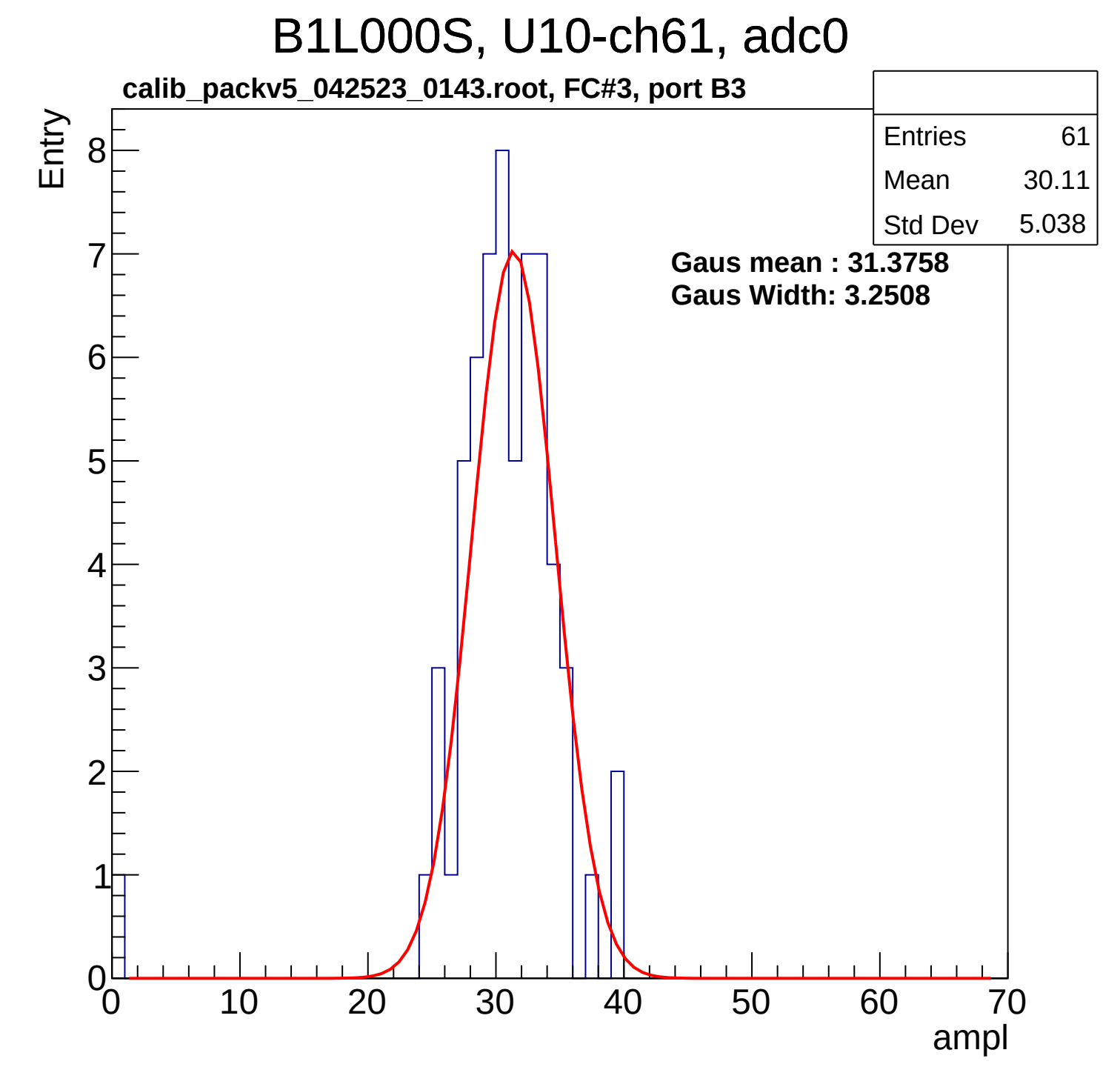
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch61, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 30.11
Std Dev 5.038
Gaus mean : 31.3758
Gaus Width: 3.2508
Entry
ampl
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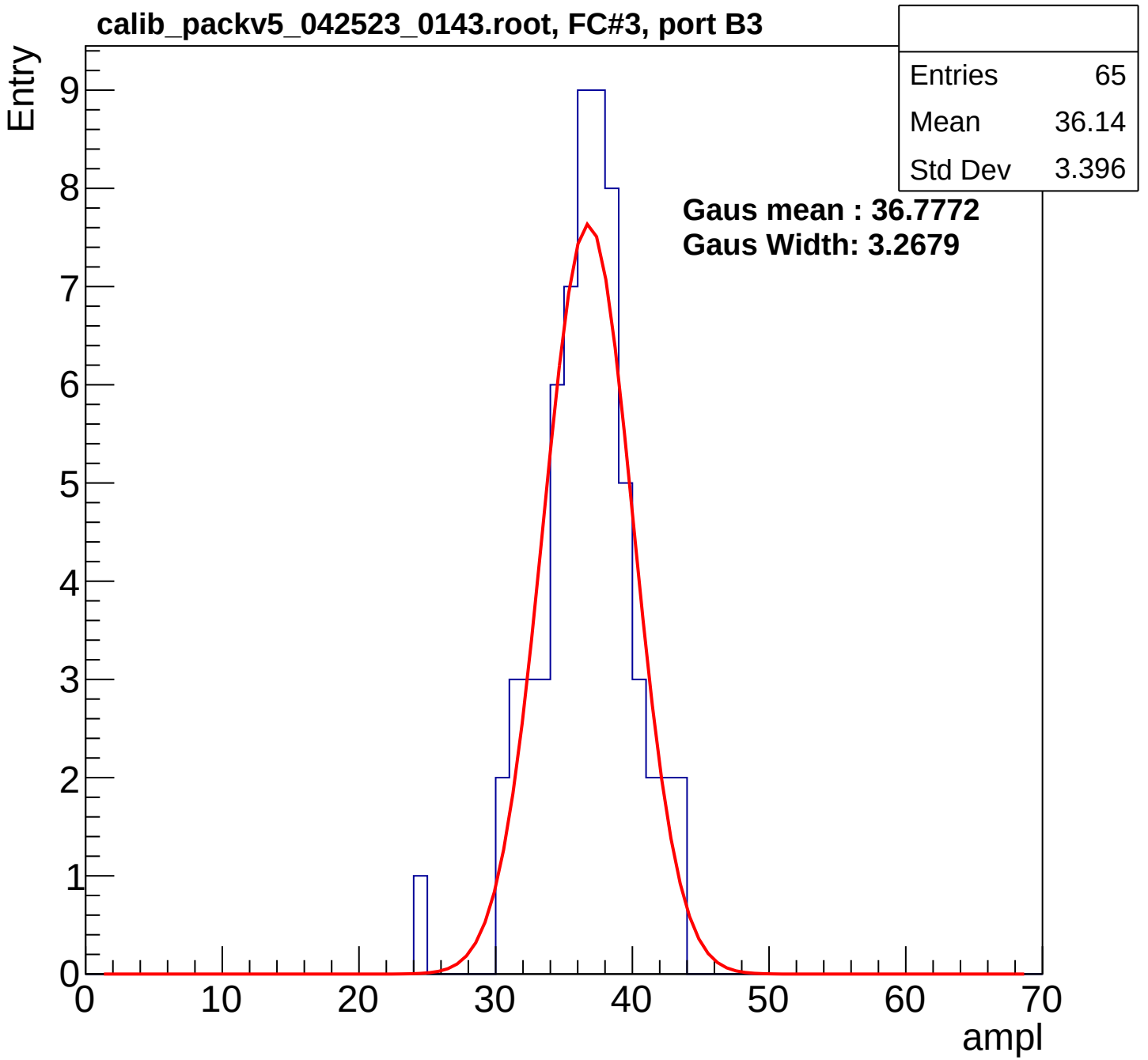
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch61, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 36.14
Std Dev 3.396
Gaus mean : 36.7772
Gaus Width: 3.2679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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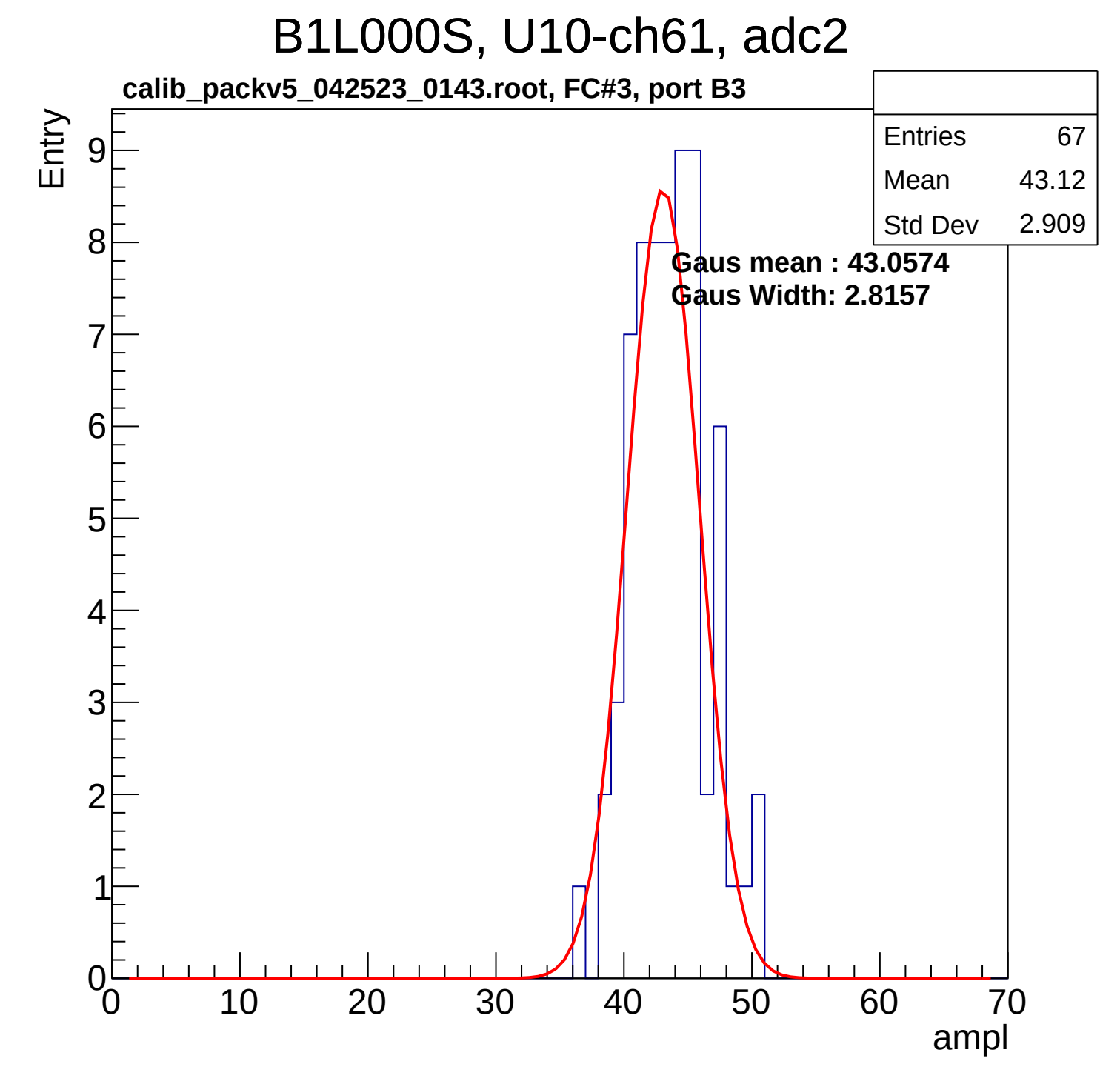
B1L000S, U10-ch61, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 43.12
Std Dev 2.909
Gaus mean : 43.0574
Gaus Width: 2.8157
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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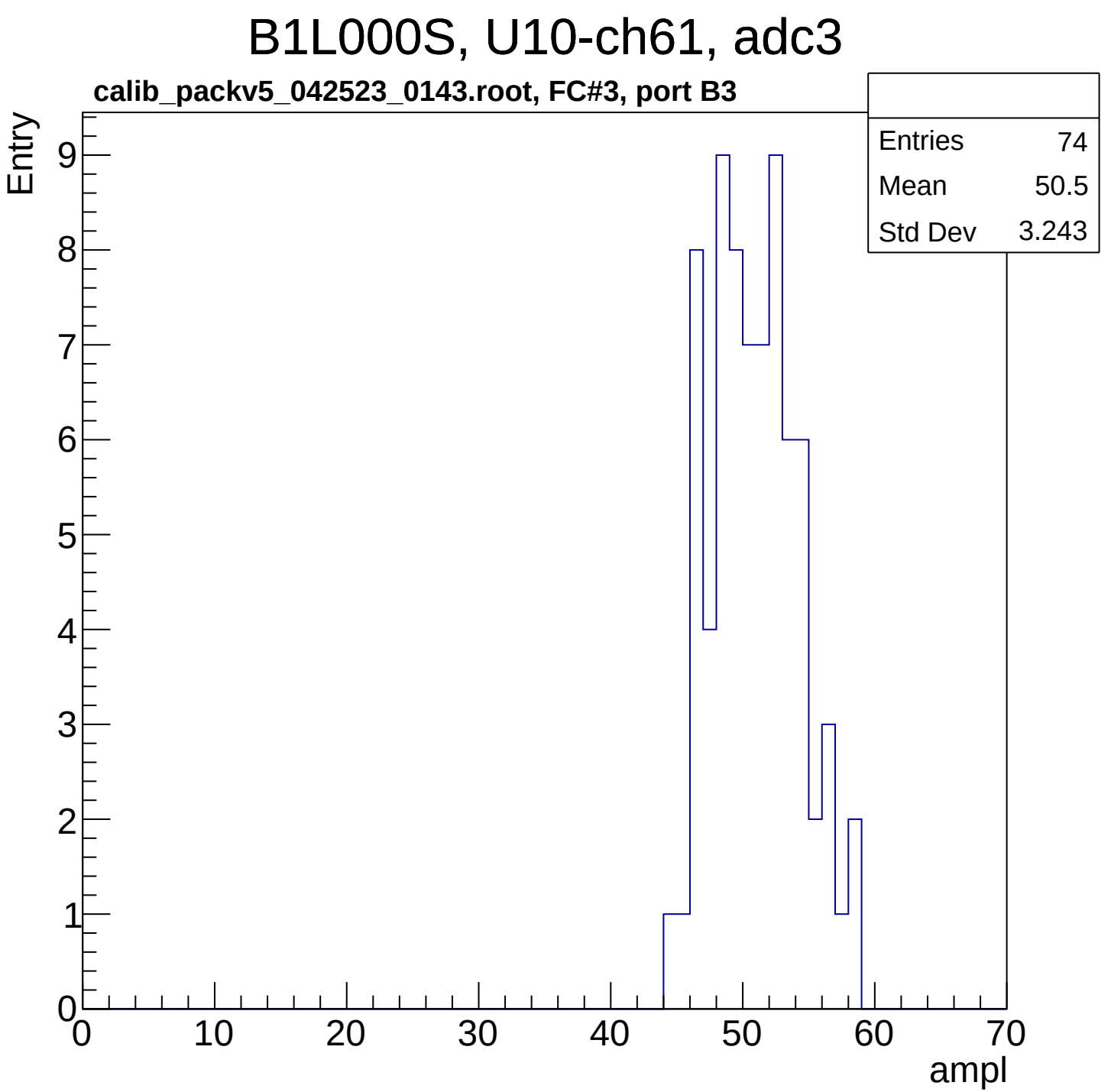
B1L000S, U10-ch61, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 50.5
Std Dev 3.243
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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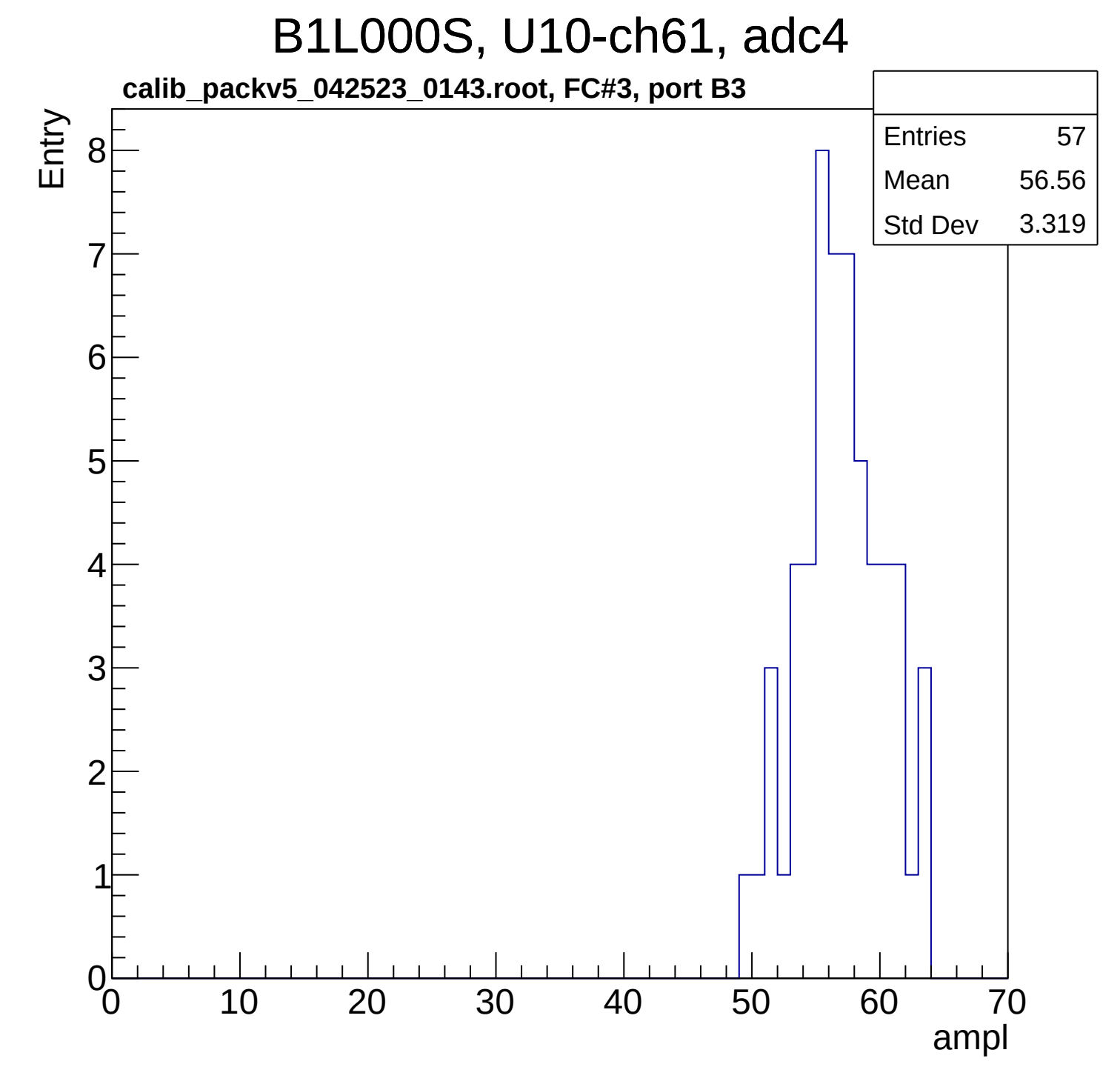
B1L000S, U10-ch61, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 56.56
Std Dev 3.319
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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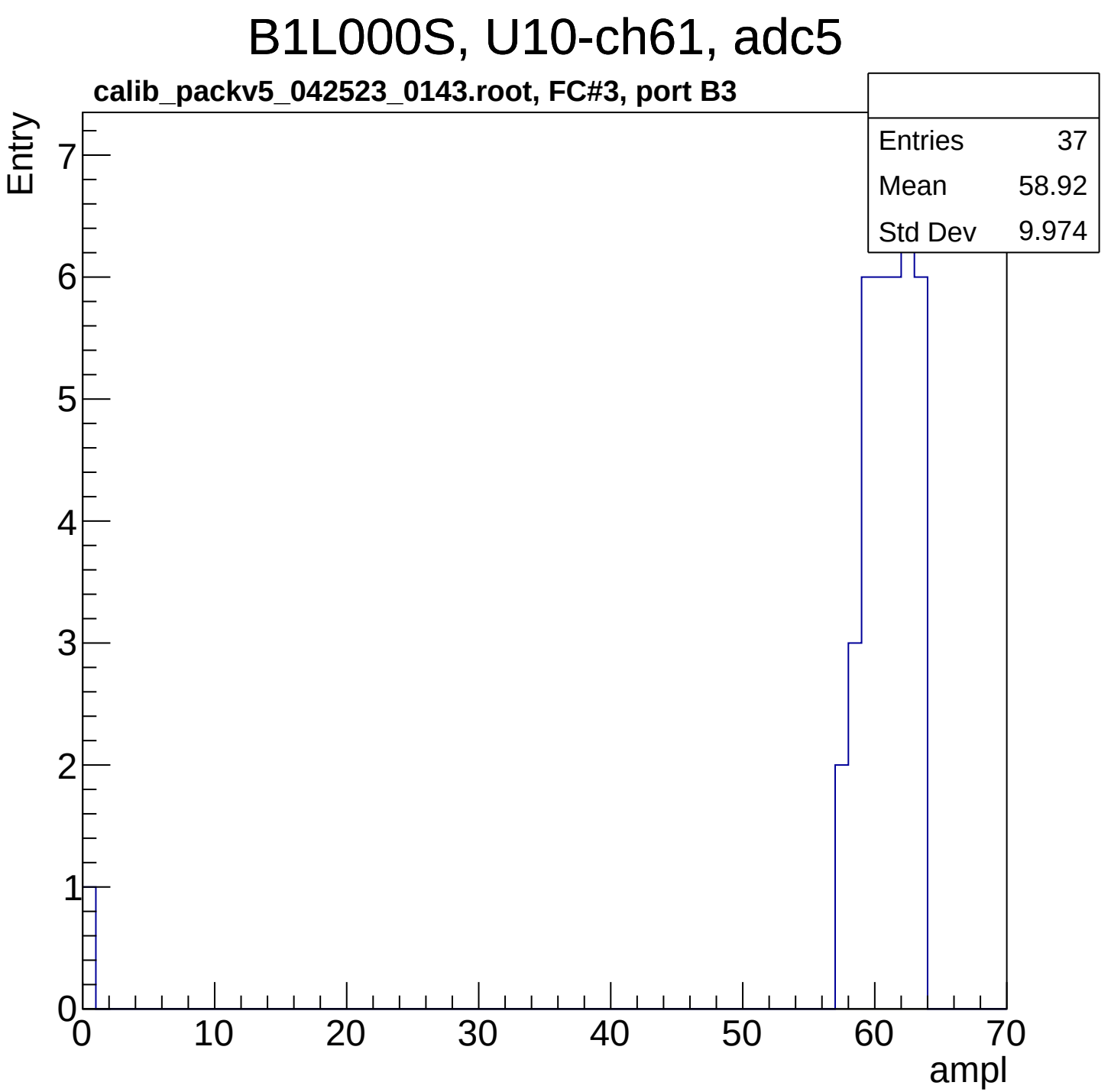
B1L000S, U10-ch61, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 58.92
Std Dev 9.974
Entry
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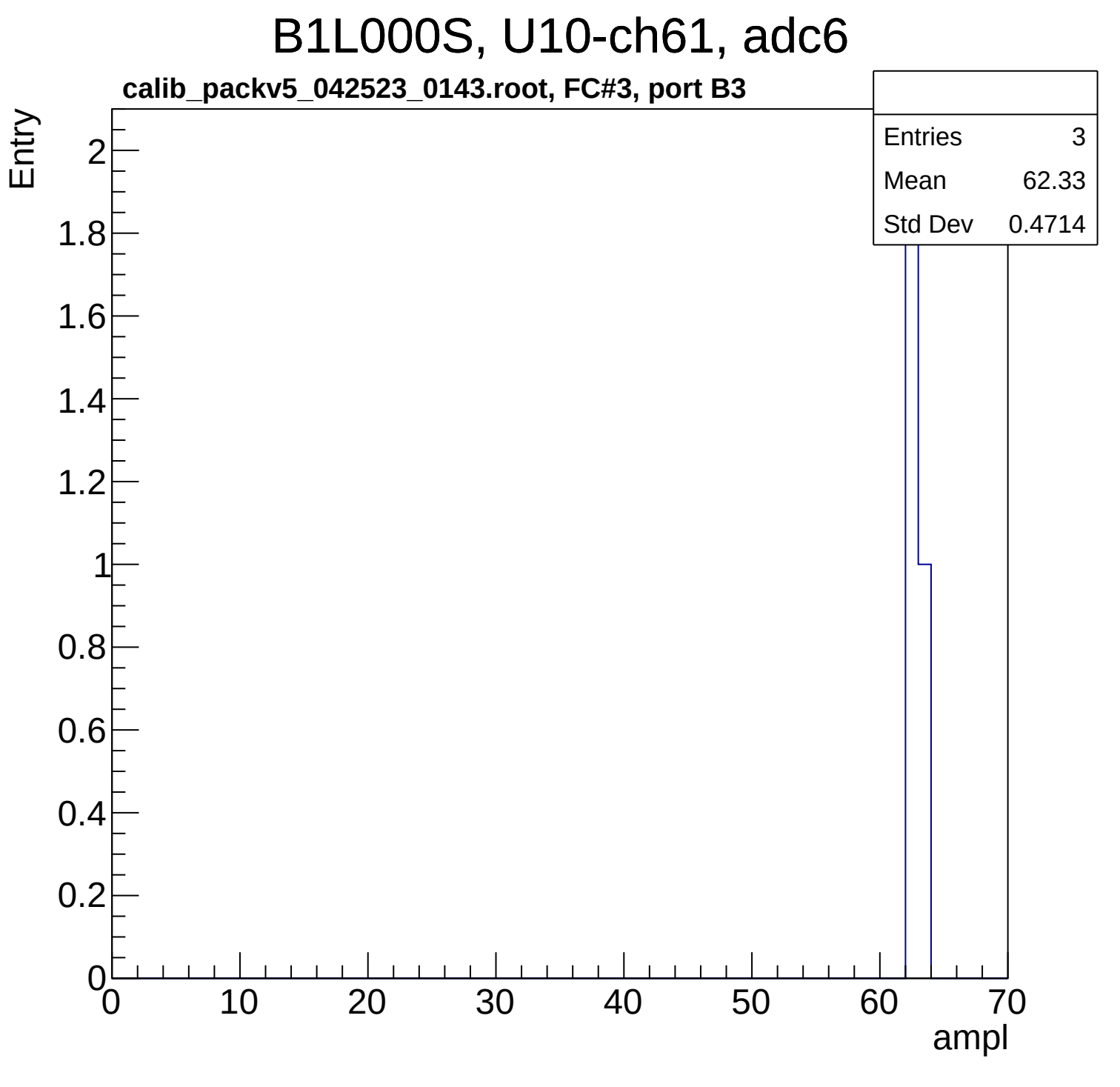
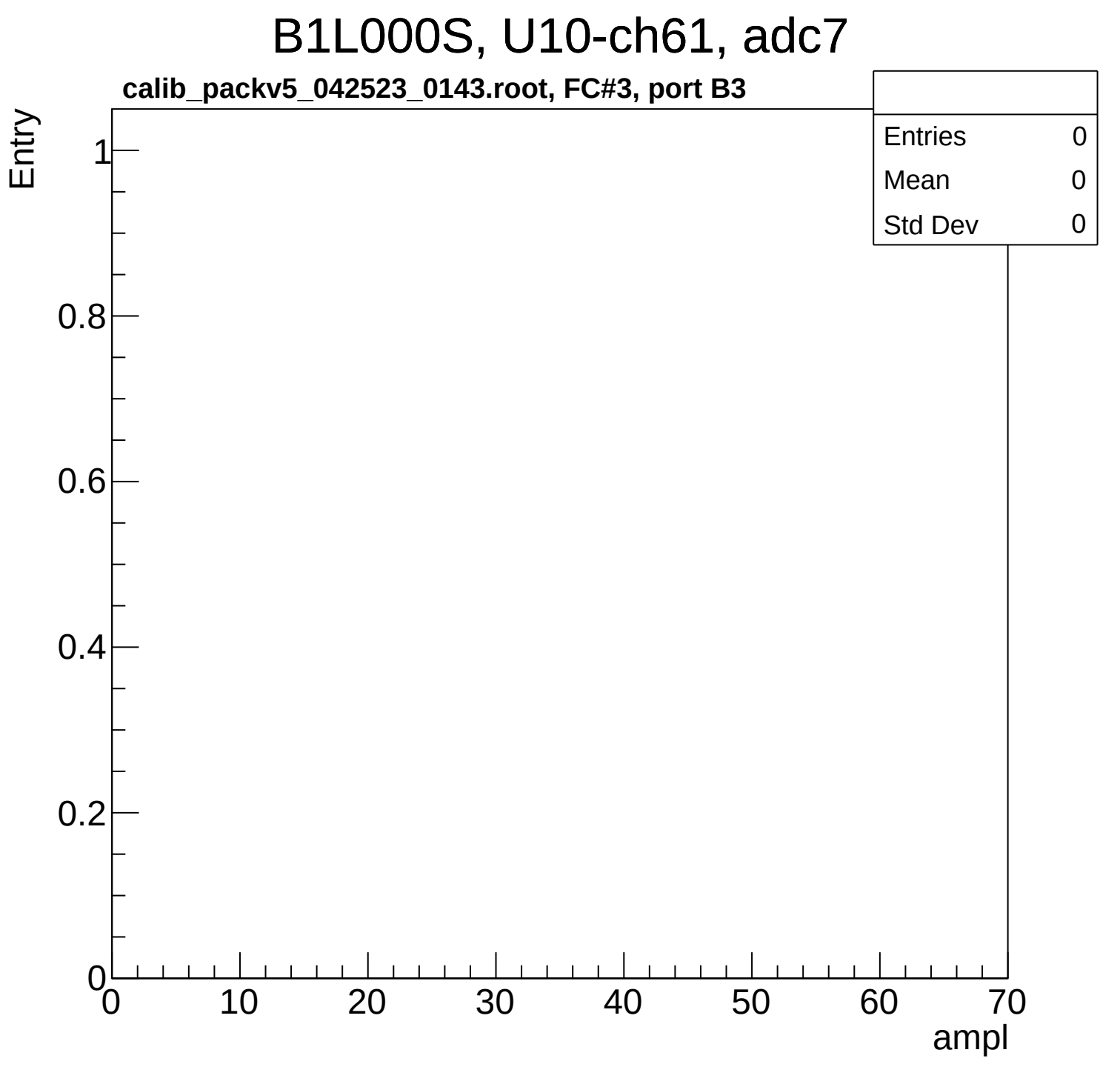
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch61, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch61, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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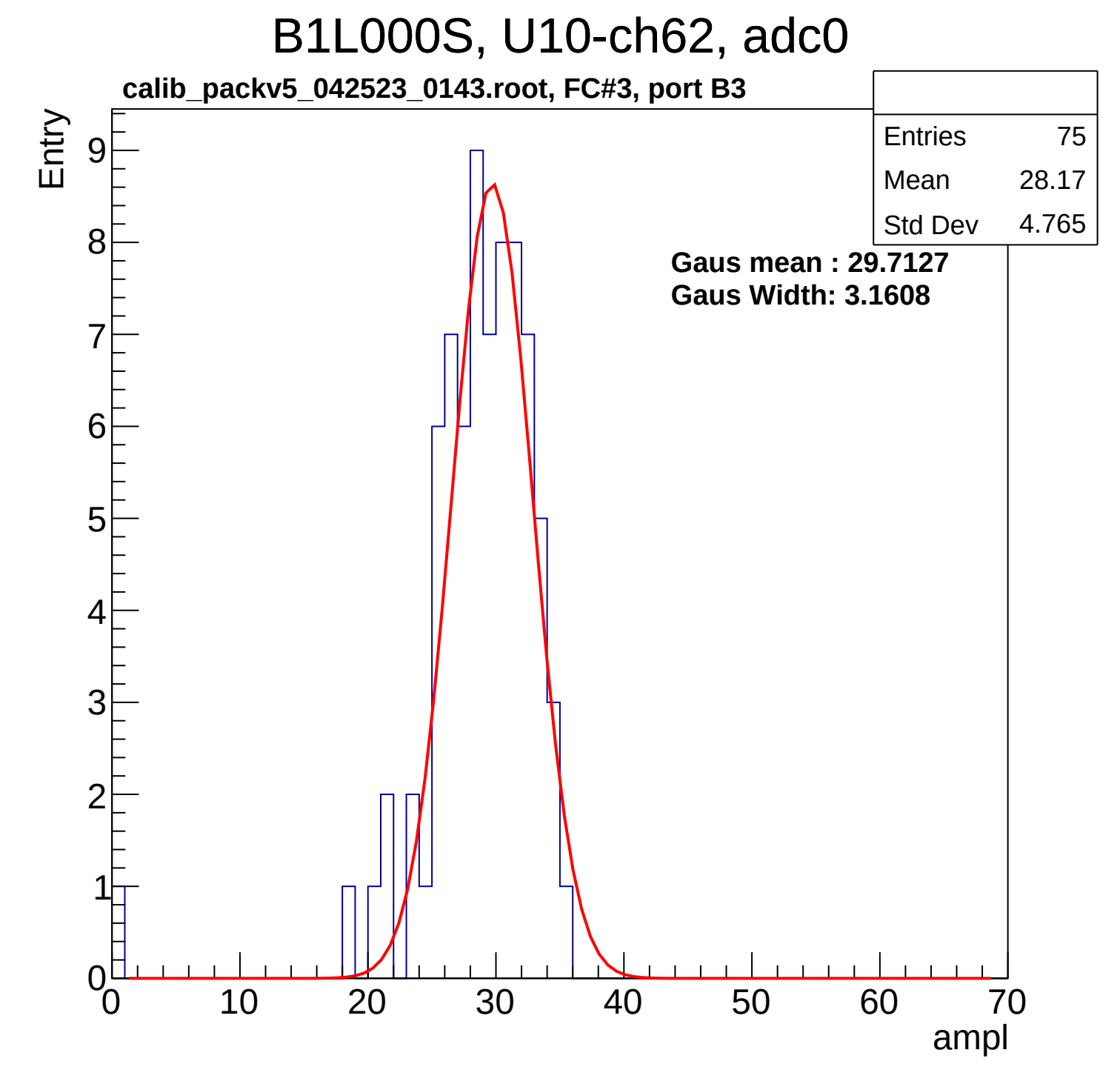
50
60
70
ampl


B1L000S, U10-ch62, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.17
Std Dev 4.765
Gaus mean : 29.7127
Gaus Width: 3.1608
Entry
ampl
0
1
2
3
4
5
6
7
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0
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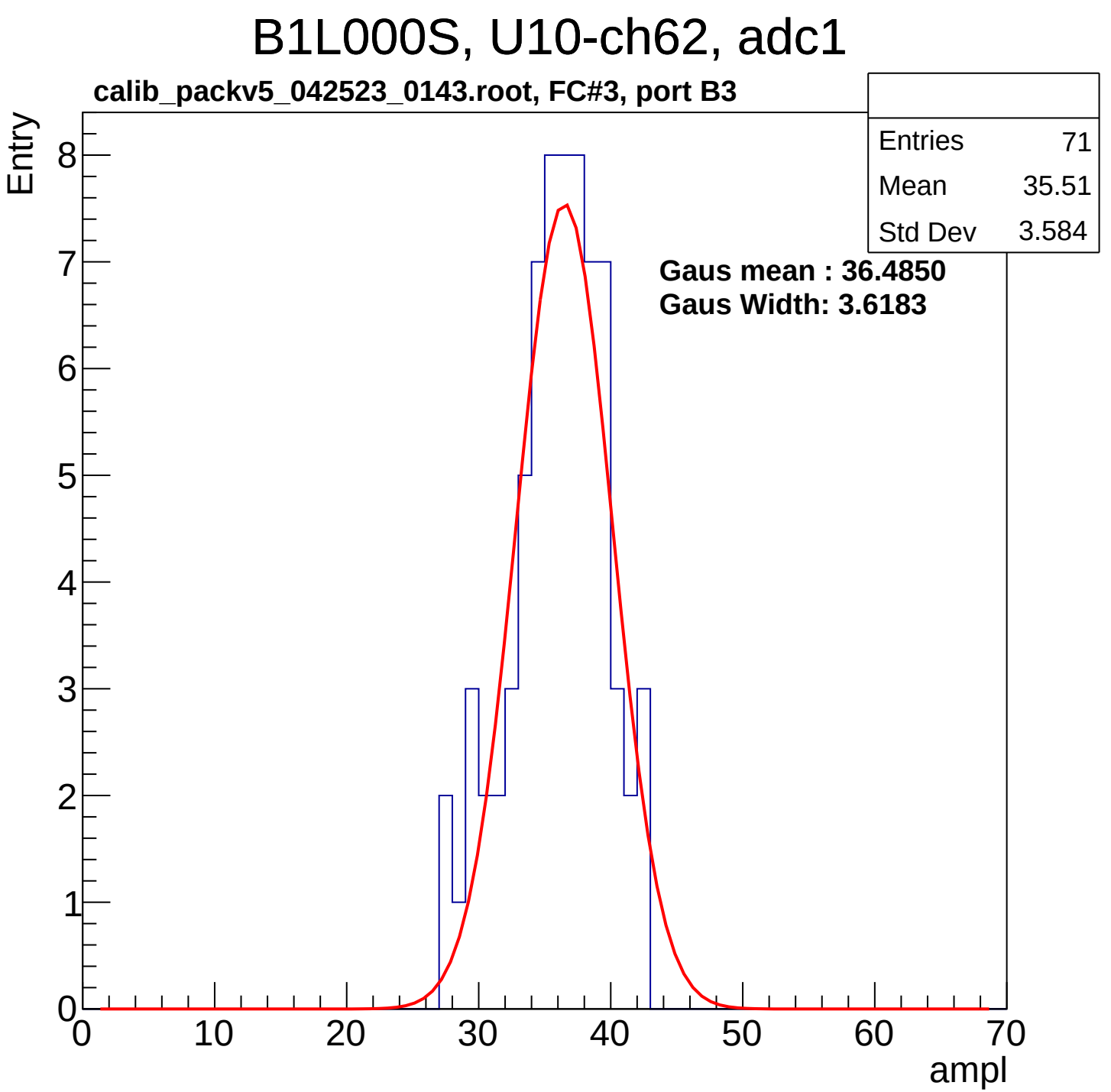
B1L000S, U10-ch62, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.51
Std Dev 3.584
Gaus mean : 36.4850
Gaus Width: 3.6183
Entry
ampl
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1
2
3
4
5
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0
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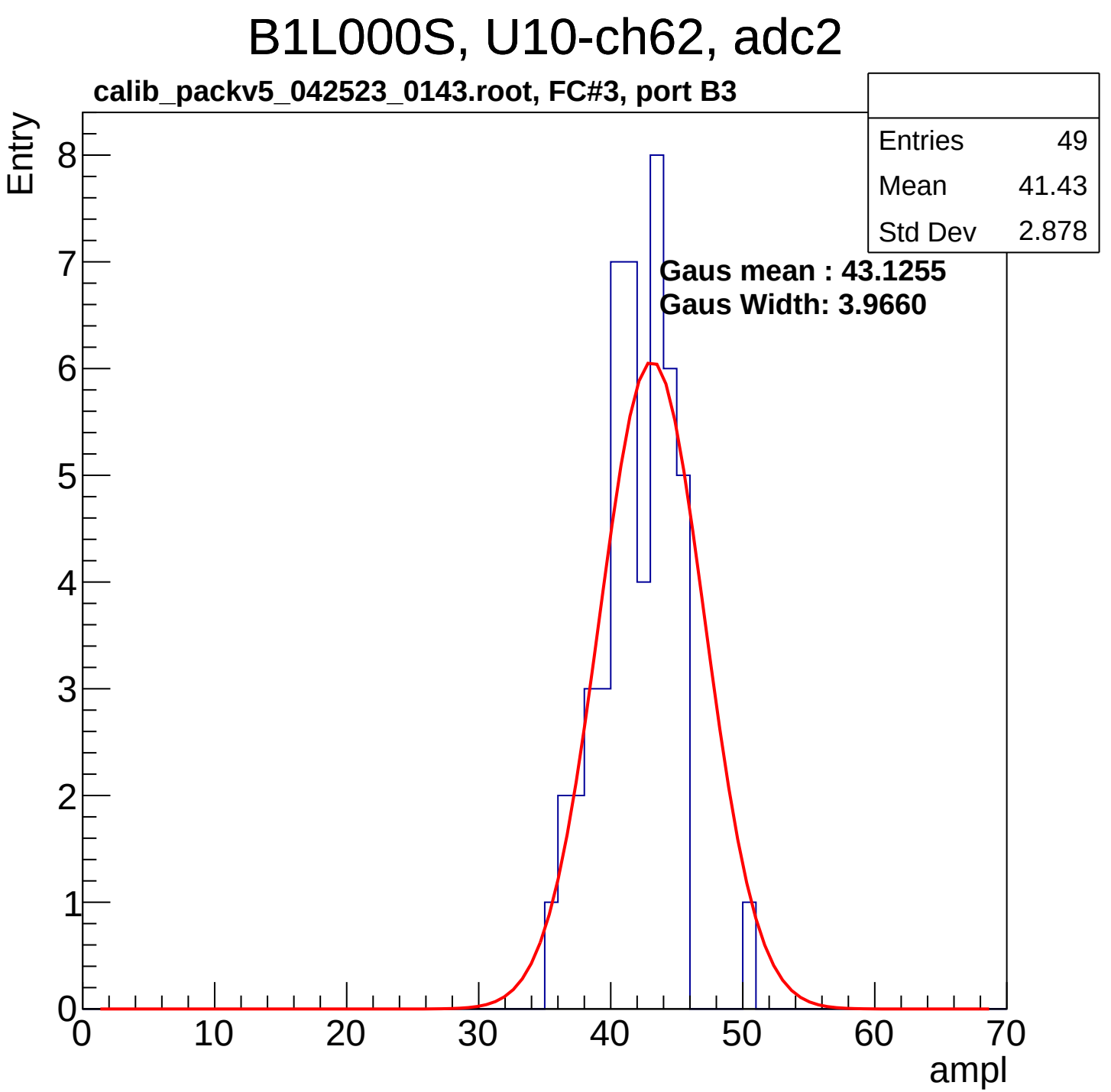
B1L000S, U10-ch62, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 41.43
Std Dev 2.878
Gaus mean : 43.1255
Gaus Width: 3.9660
Entry
ampl
0
1
2
3
4
5
6
7
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0
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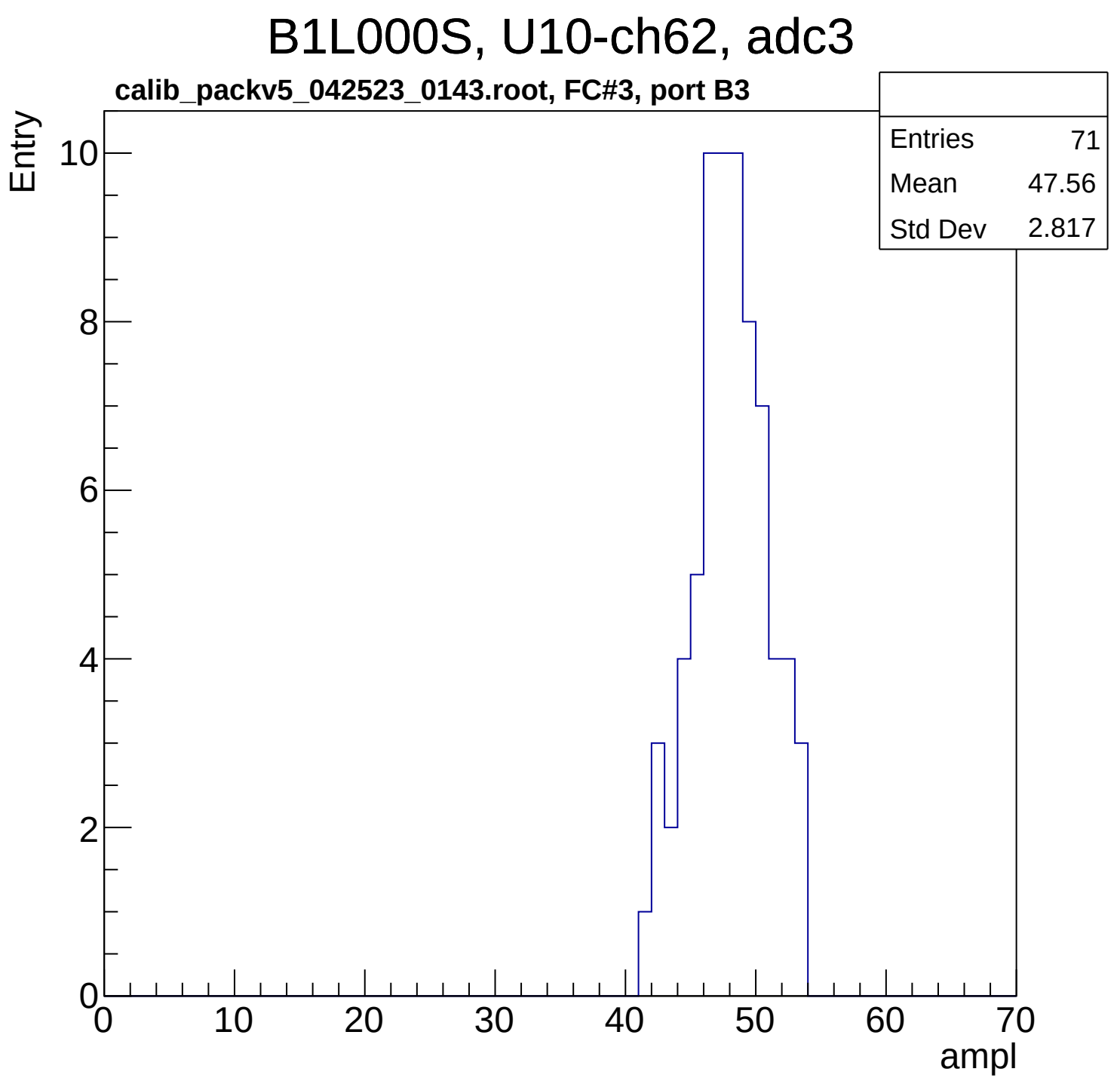
B1L000S, U10-ch62, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 47.56
Std Dev 2.817
Entry
0
2
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6
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10
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10
20
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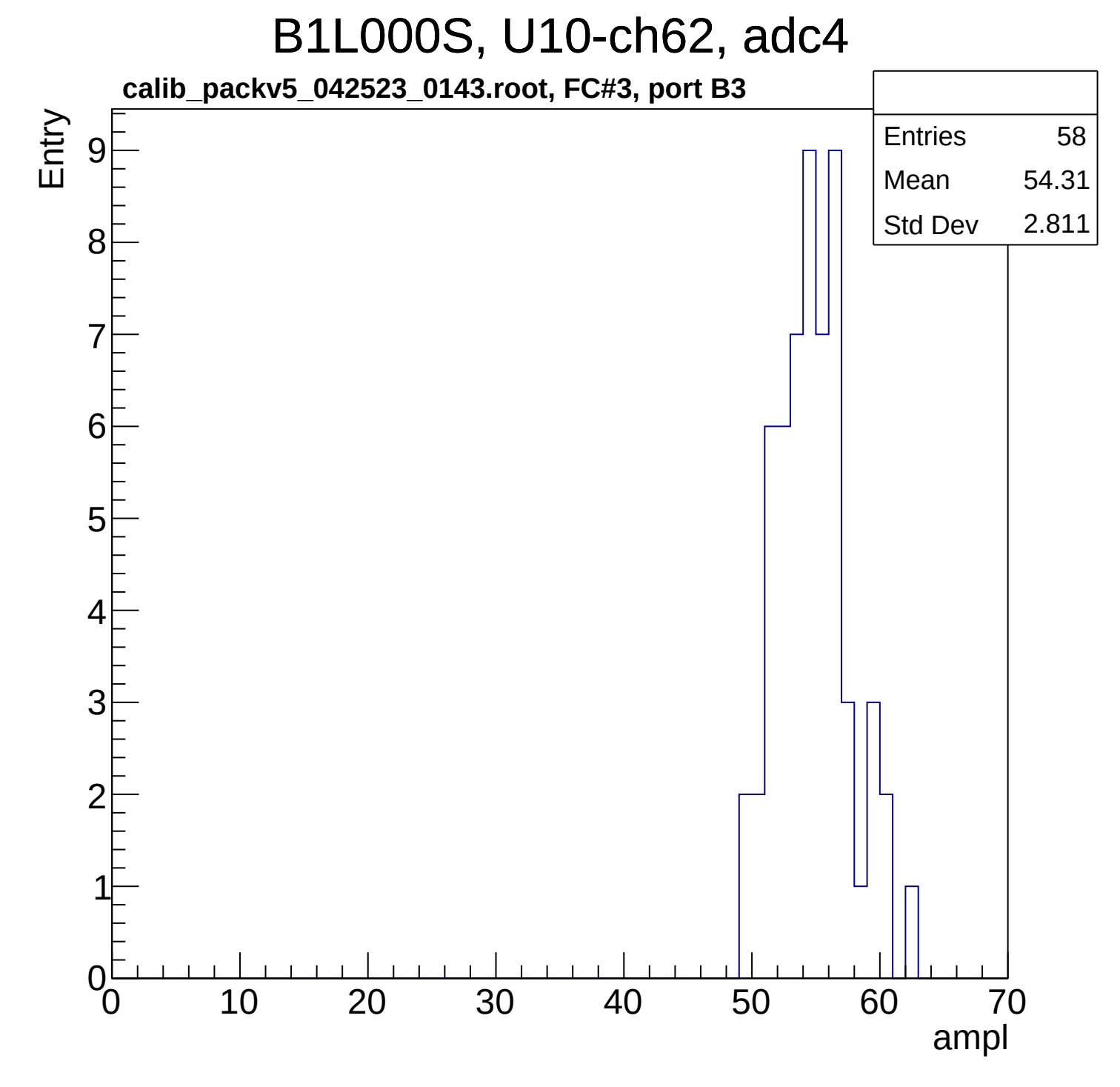
50
60
70
ampl


B1L000S, U10-ch62, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.31
Std Dev 2.811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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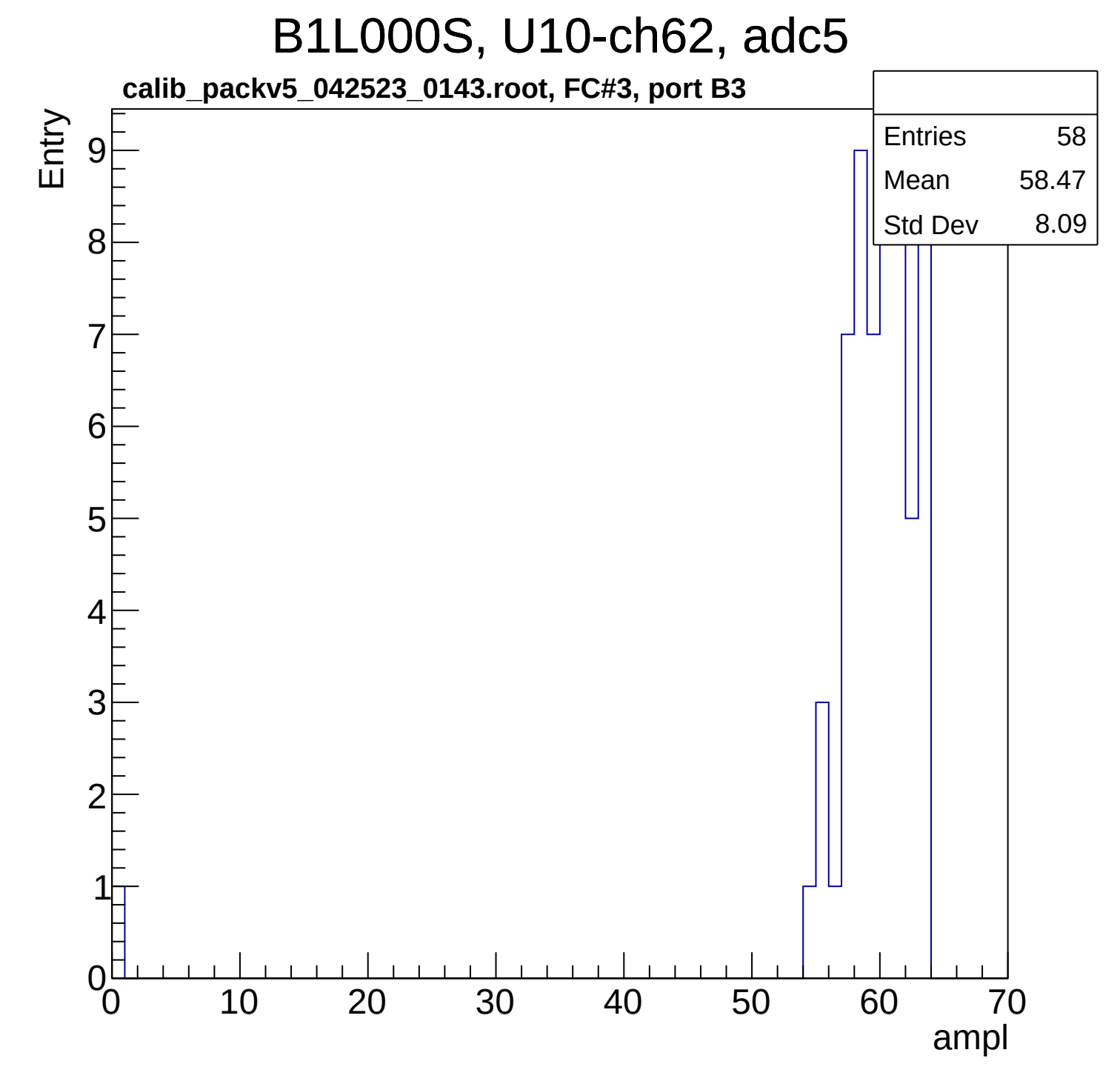
B1L000S, U10-ch62, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.47
Std Dev 8.09
Entry
ampl
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2
3
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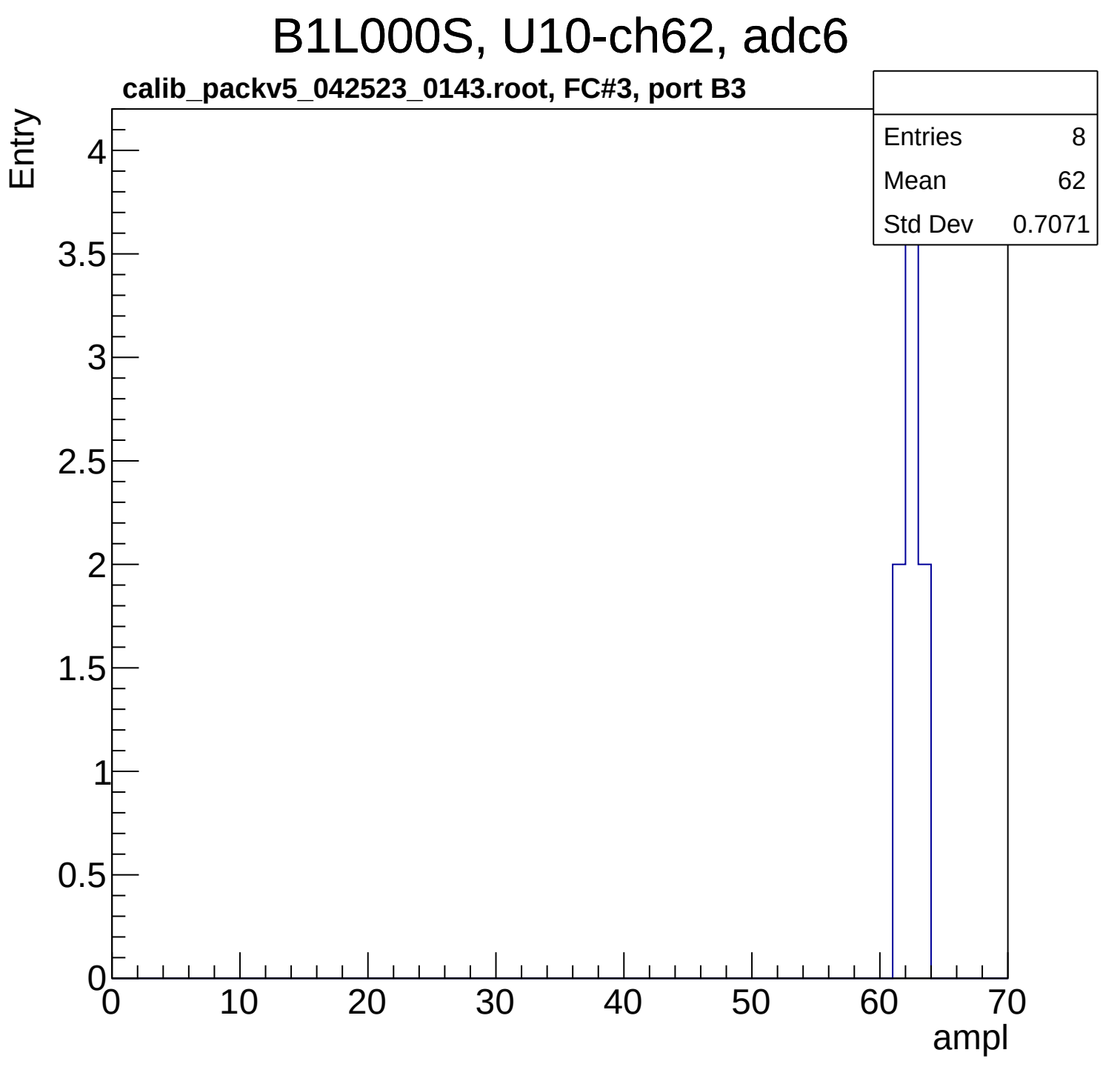
B1L000S, U10-ch62, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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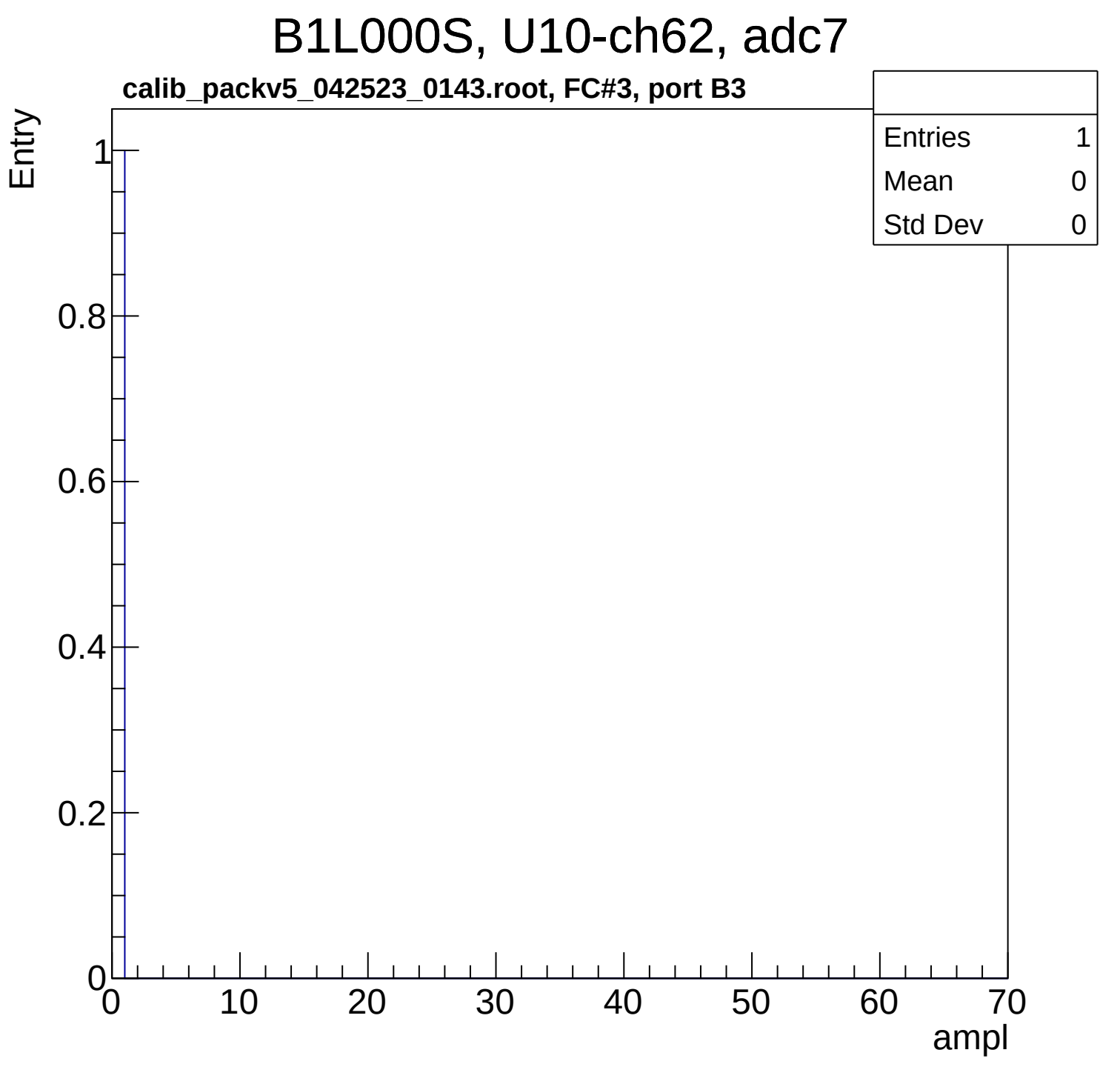
B1L000S, U10-ch62, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
30
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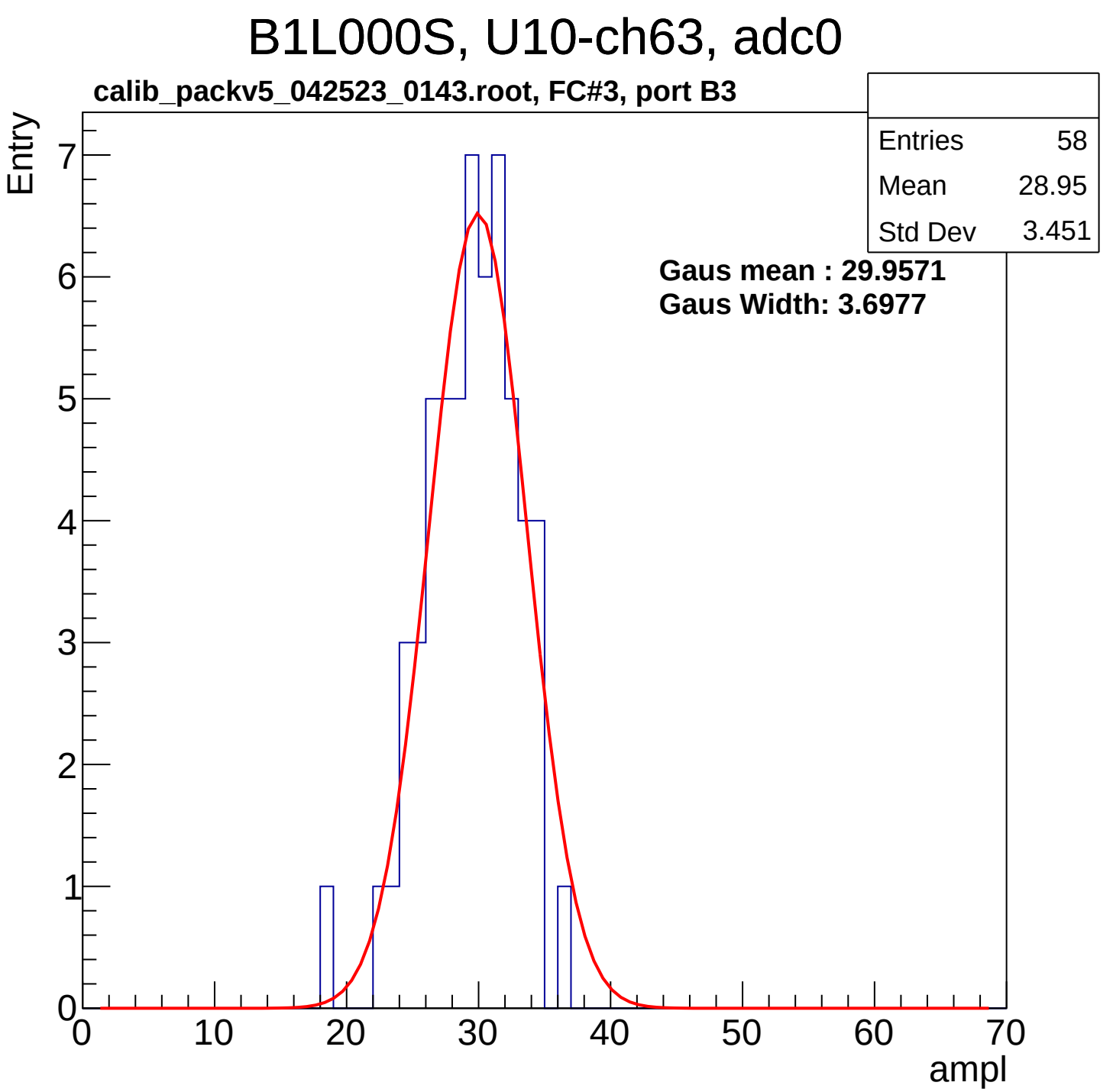
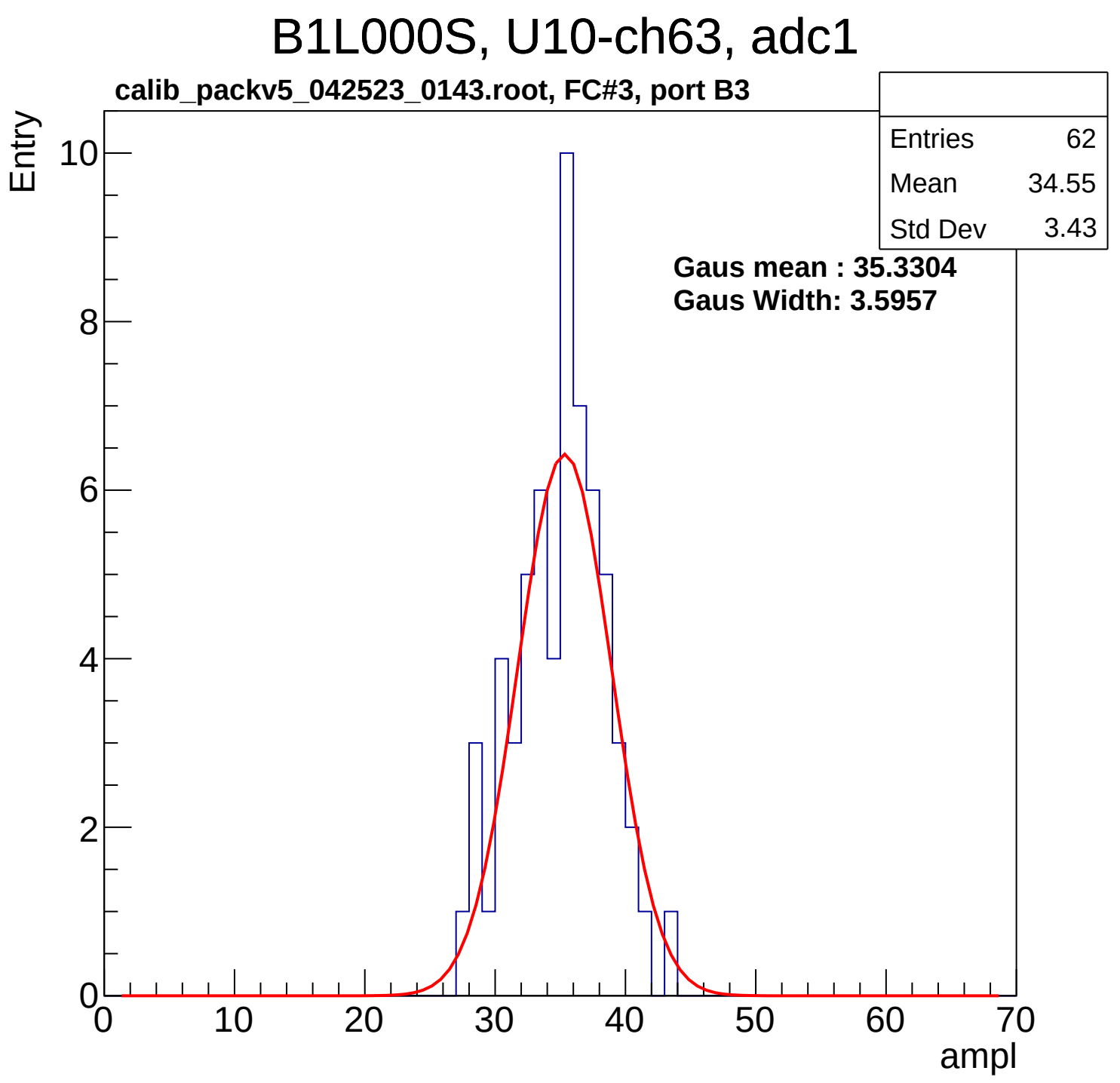
50
60
70
ampl


B1L000S, U10-ch63, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 28.95
Std Dev 3.451
Gaus mean : 29.9571
Gaus Width: 3.6977
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch63, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 34.55
Std Dev 3.43
Gaus mean : 35.3304
Gaus Width: 3.5957
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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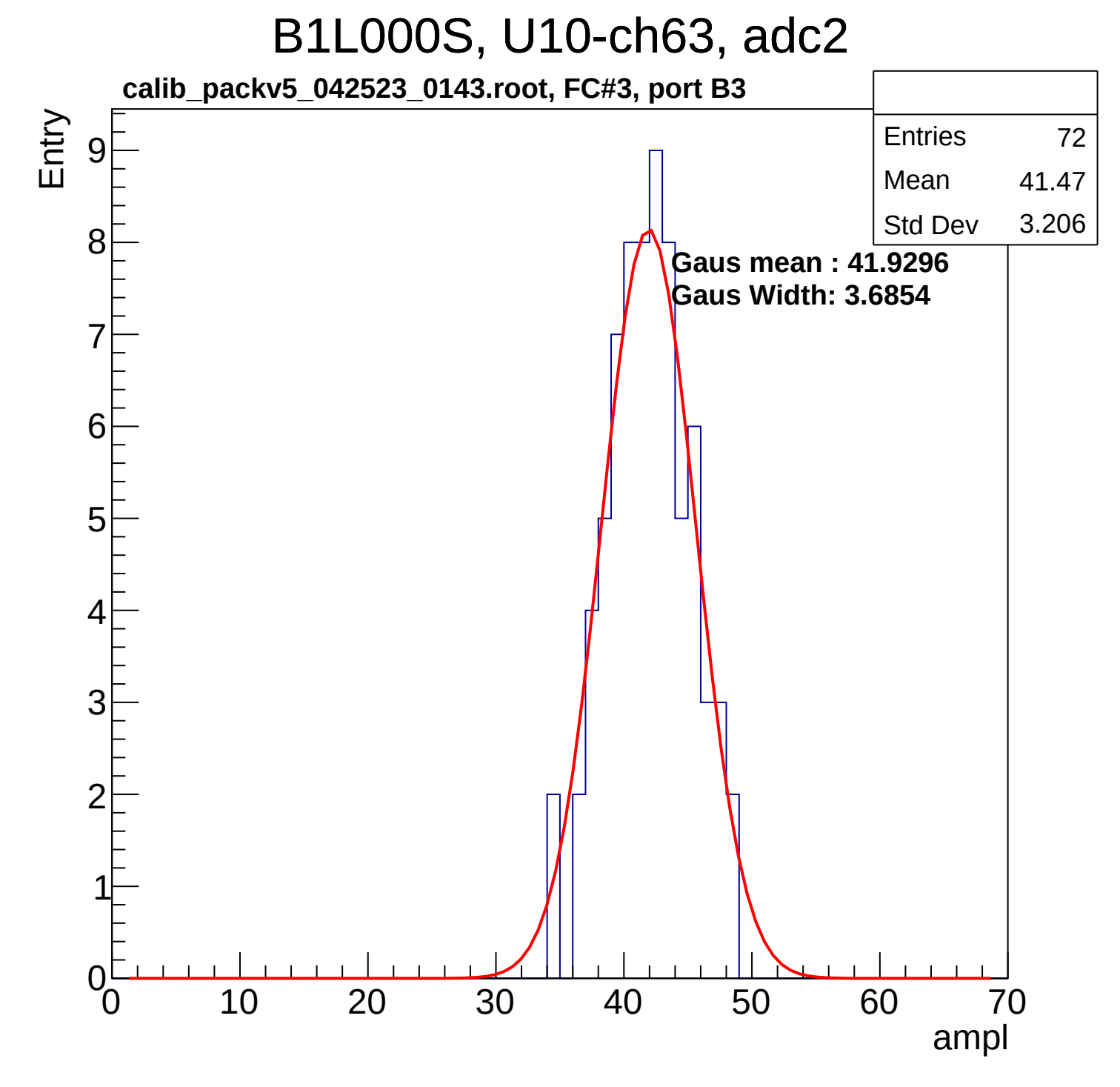
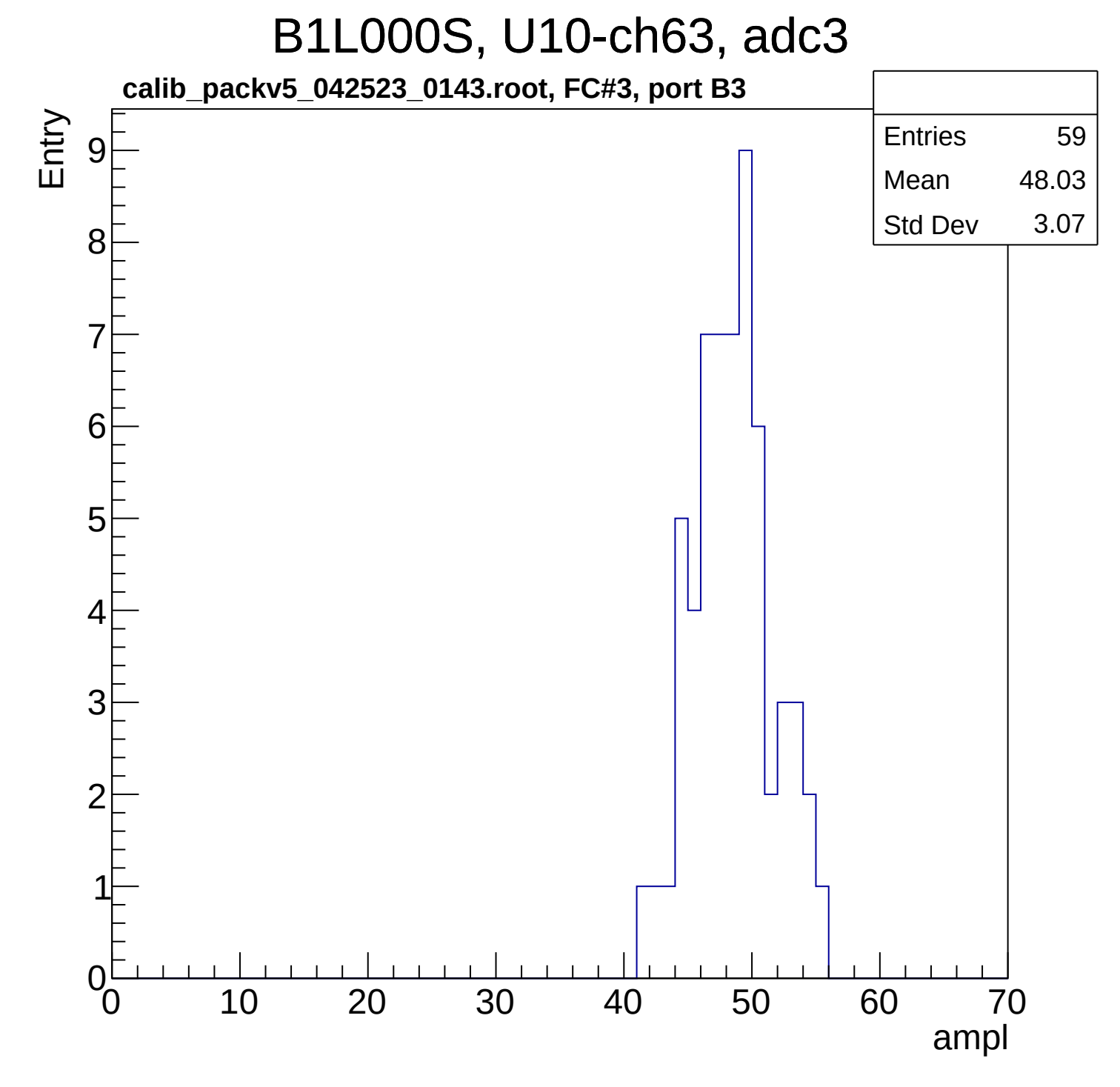

B1L000S, U10-ch63, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 41.47
Std Dev 3.206
Gaus mean : 41.9296
Gaus Width: 3.6854
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U10-ch63, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 48.03
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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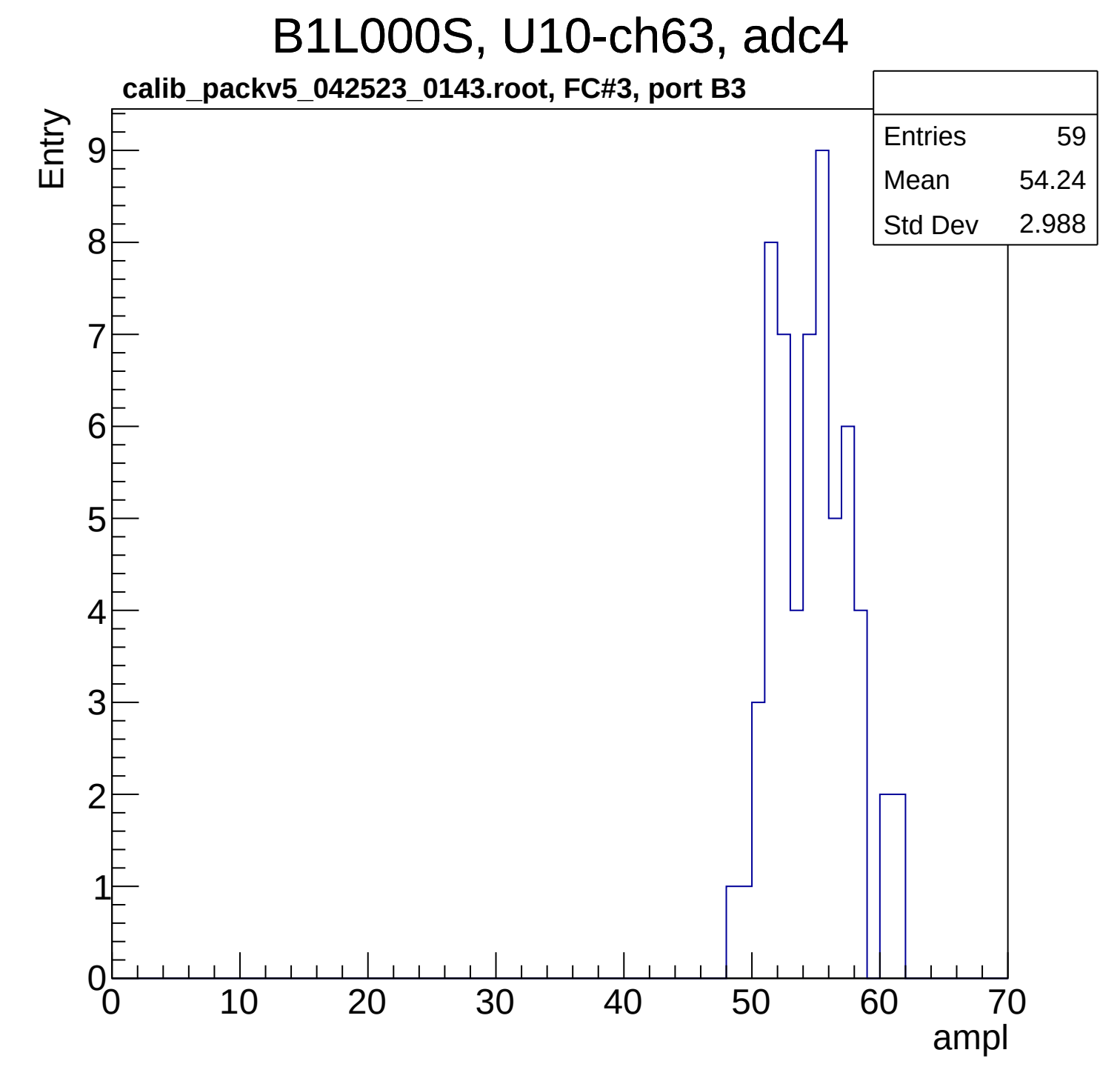
B1L000S, U10-ch63, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 54.24
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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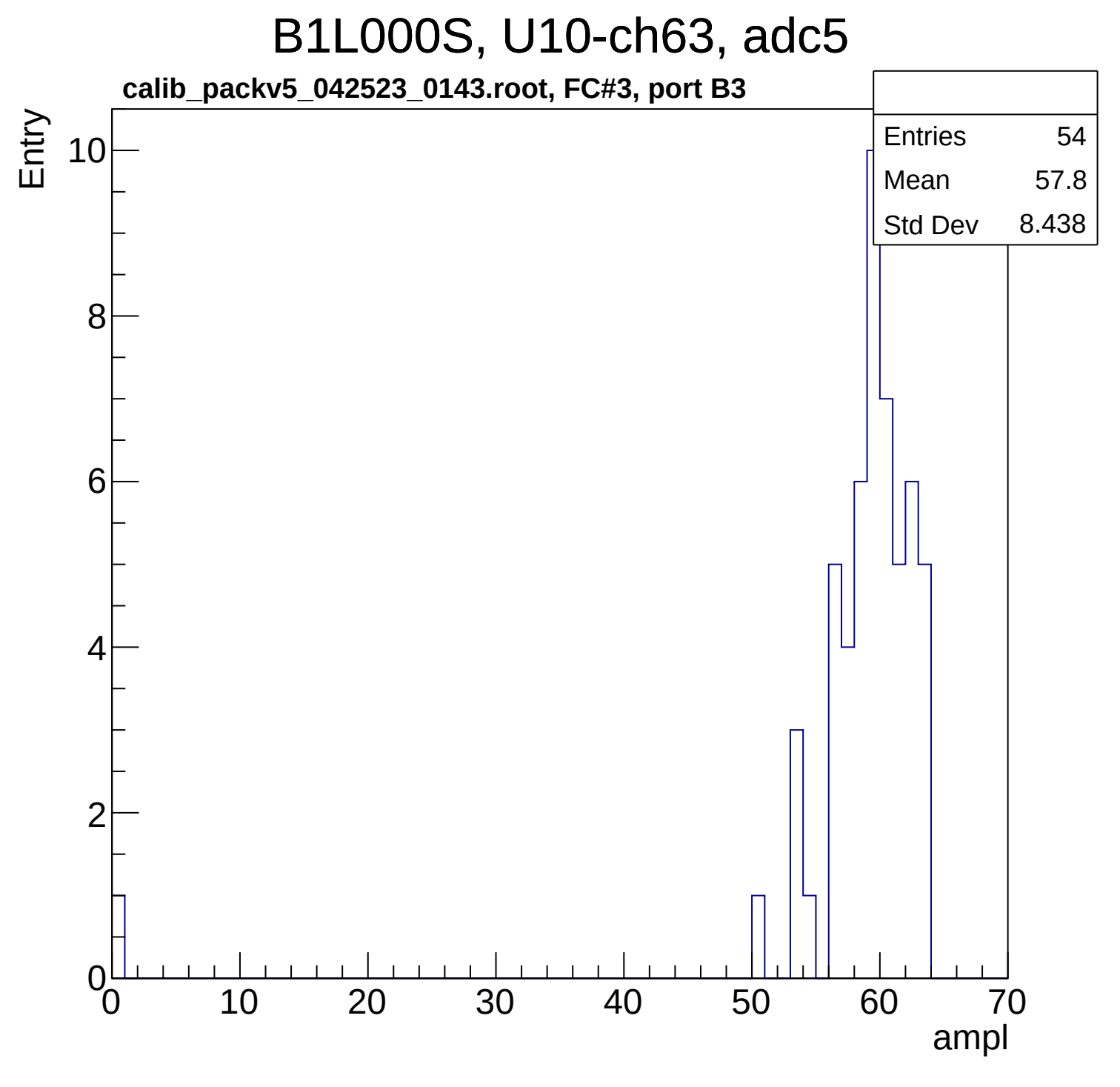
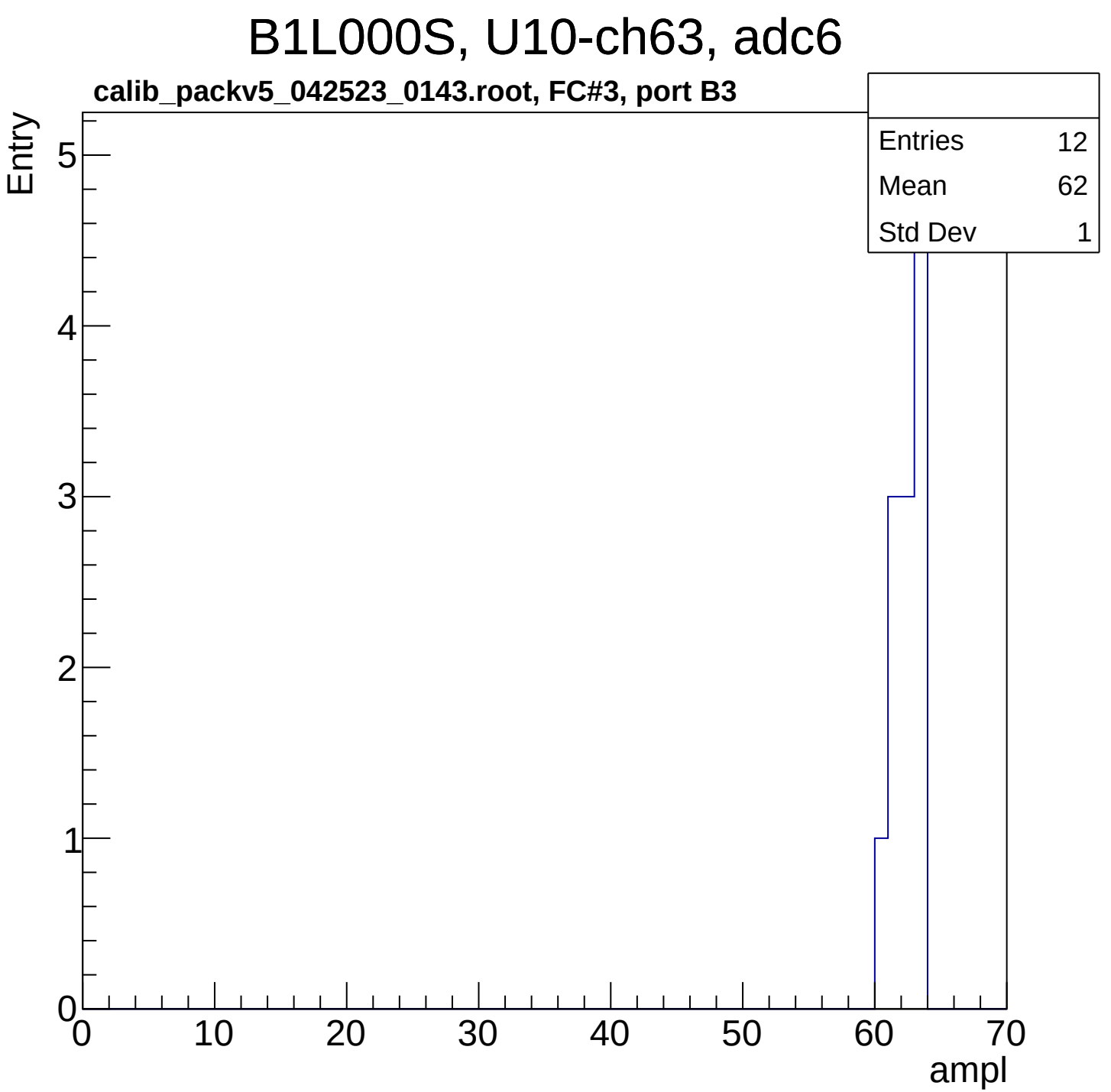
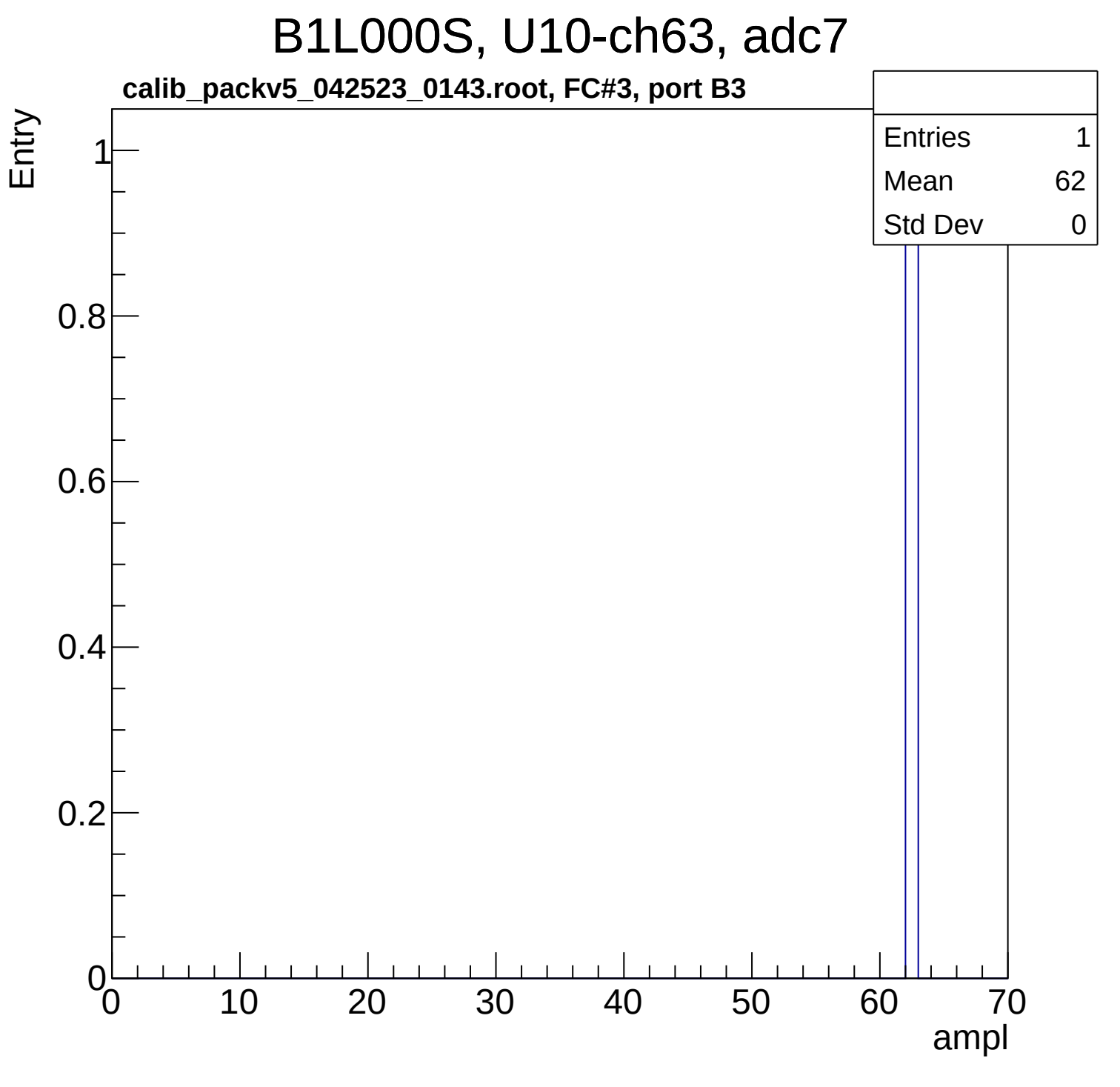

B1L000S, U10-ch63, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.8
Std Dev 8.438
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch63, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 62
Std Dev 1
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch63, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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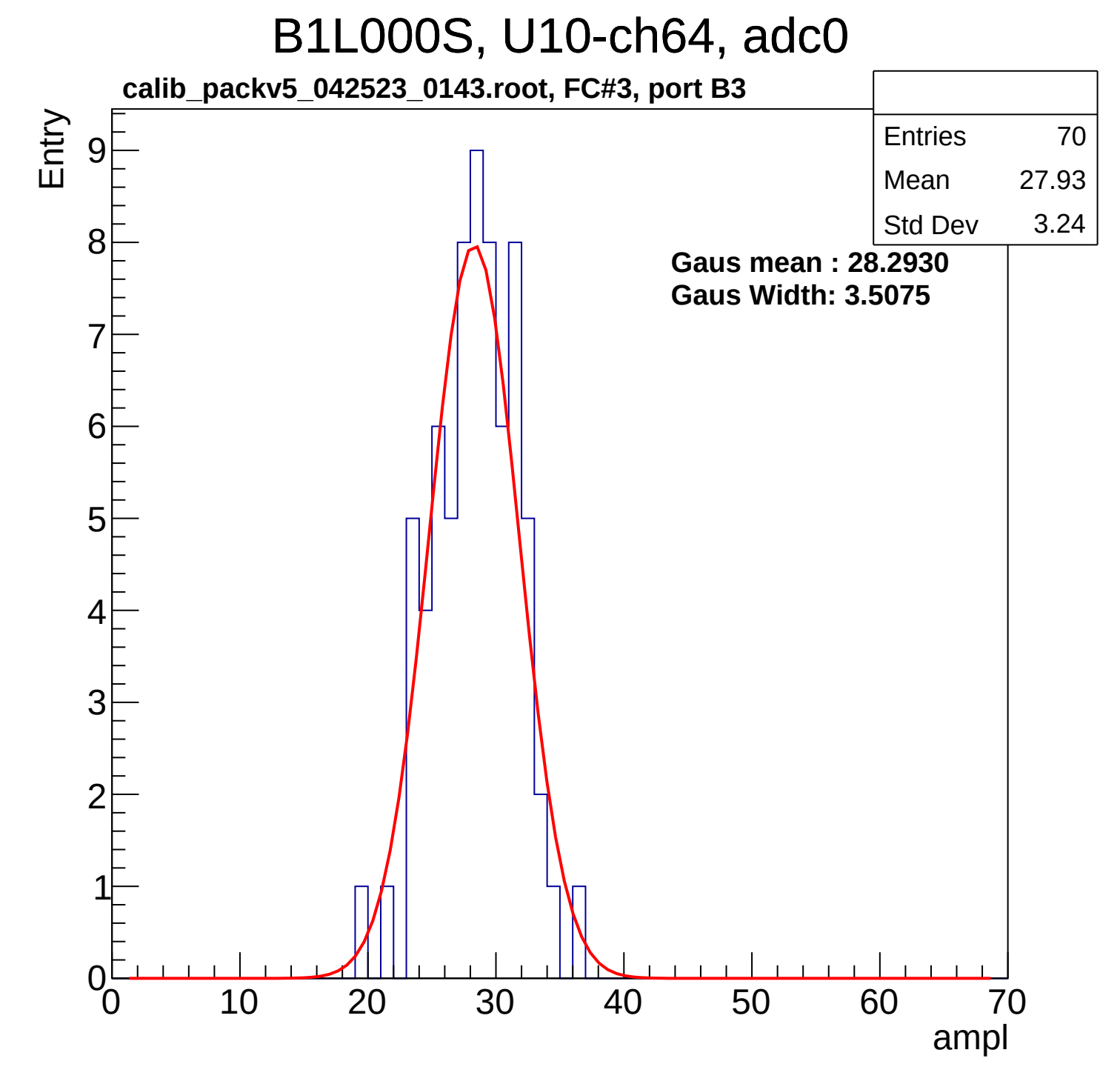
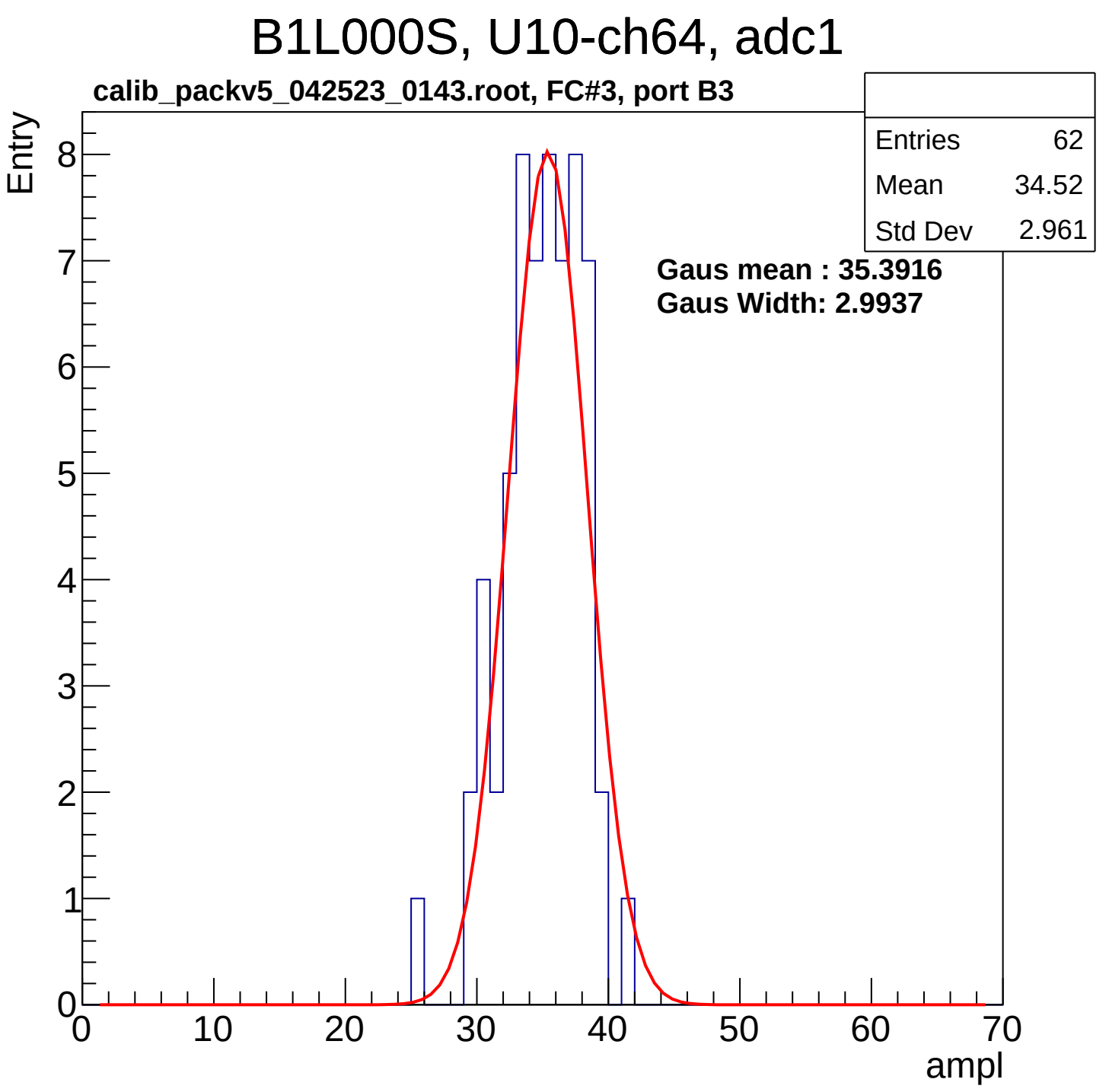
50
60
70
ampl


B1L000S, U10-ch64, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 27.93
Std Dev 3.24
Gaus mean : 28.2930
Gaus Width: 3.5075
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch64, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 34.52
Std Dev 2.961
Gaus mean : 35.3916
Gaus Width: 2.9937
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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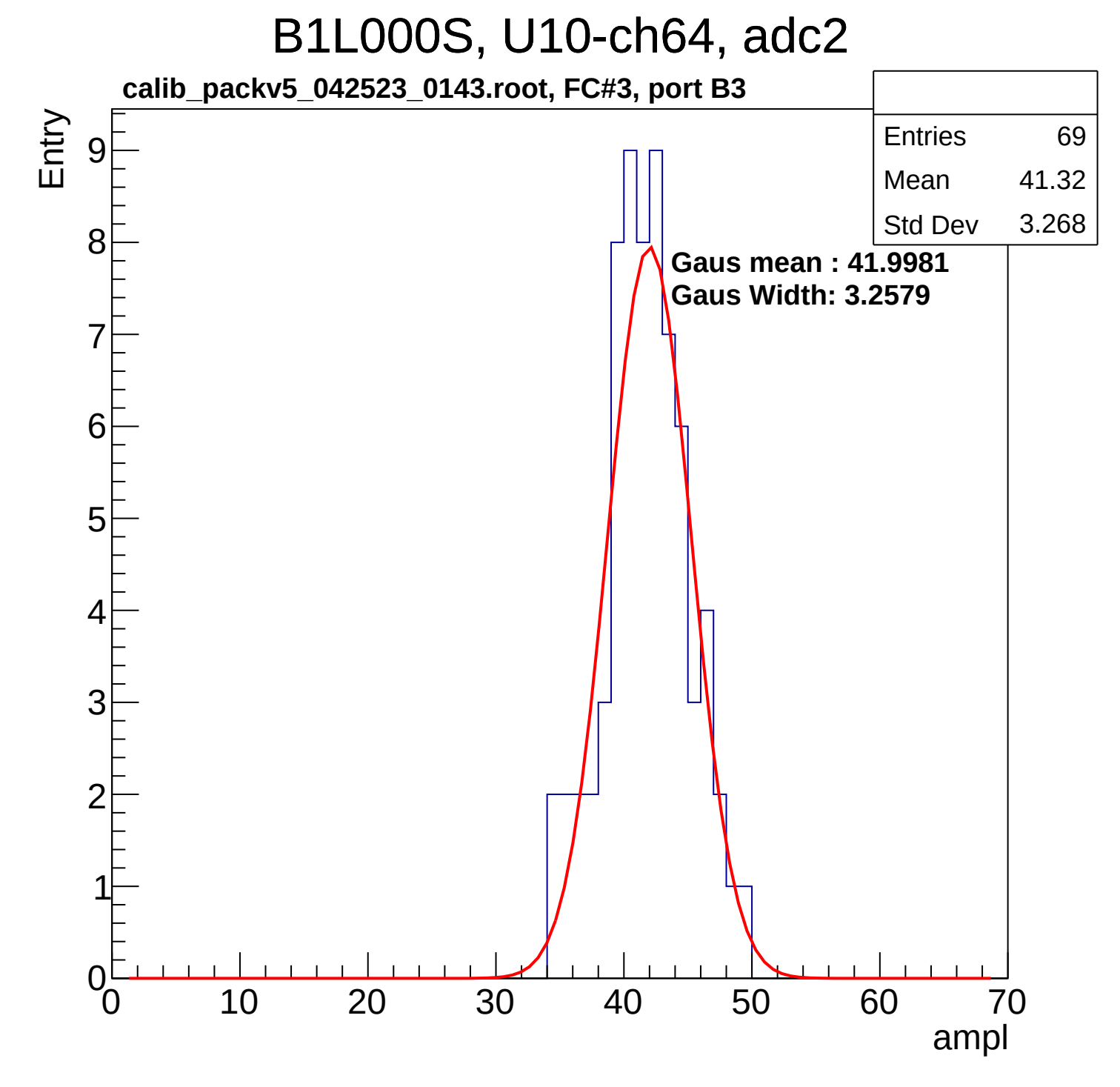
B1L000S, U10-ch64, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.32
Std Dev 3.268
Gaus mean : 41.9981
Gaus Width: 3.2579
Entry
ampl
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2
3
4
5
6
7
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0
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60
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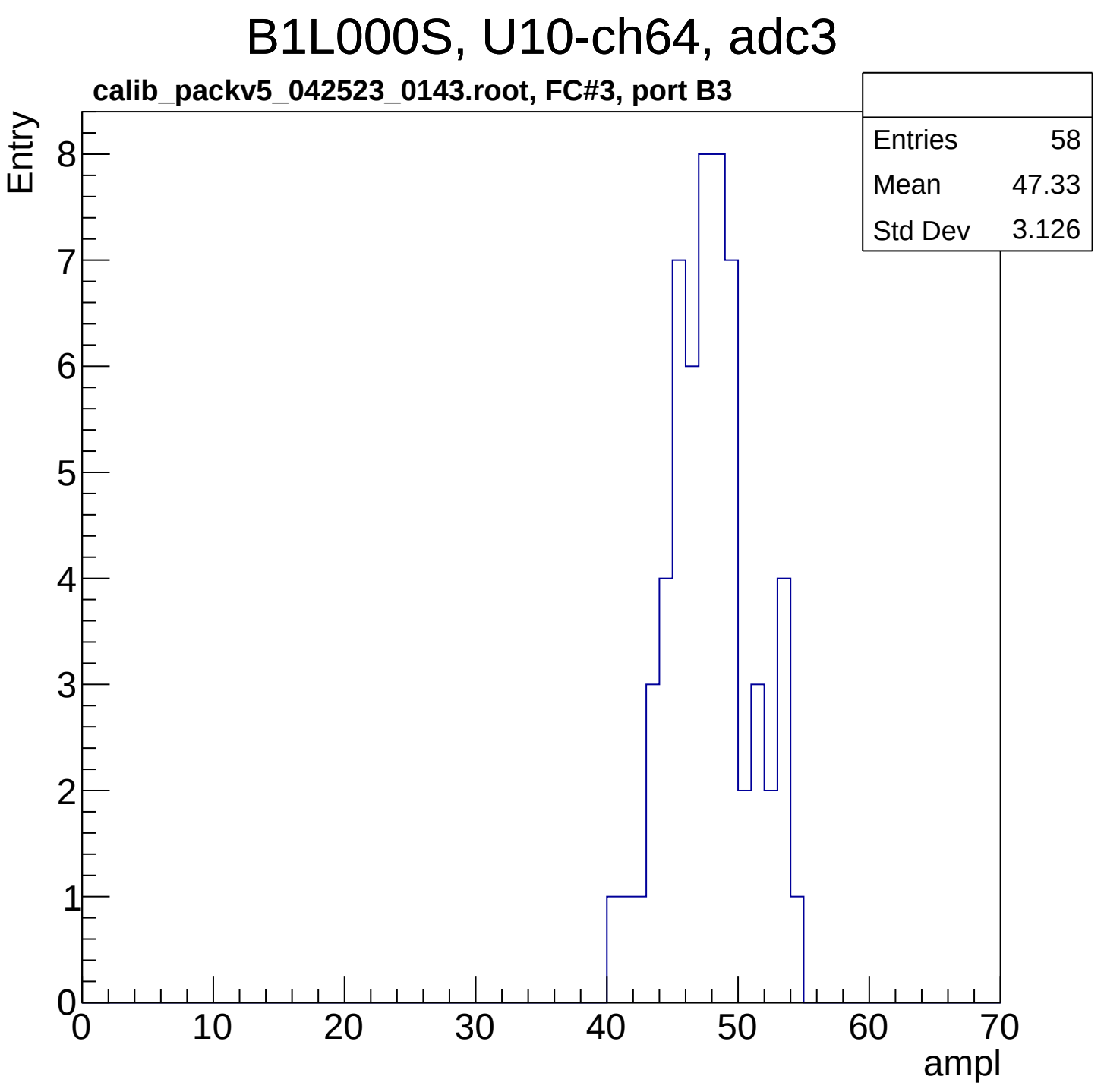
B1L000S, U10-ch64, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 47.33
Std Dev 3.126
Entry
ampl
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4
5
6
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60
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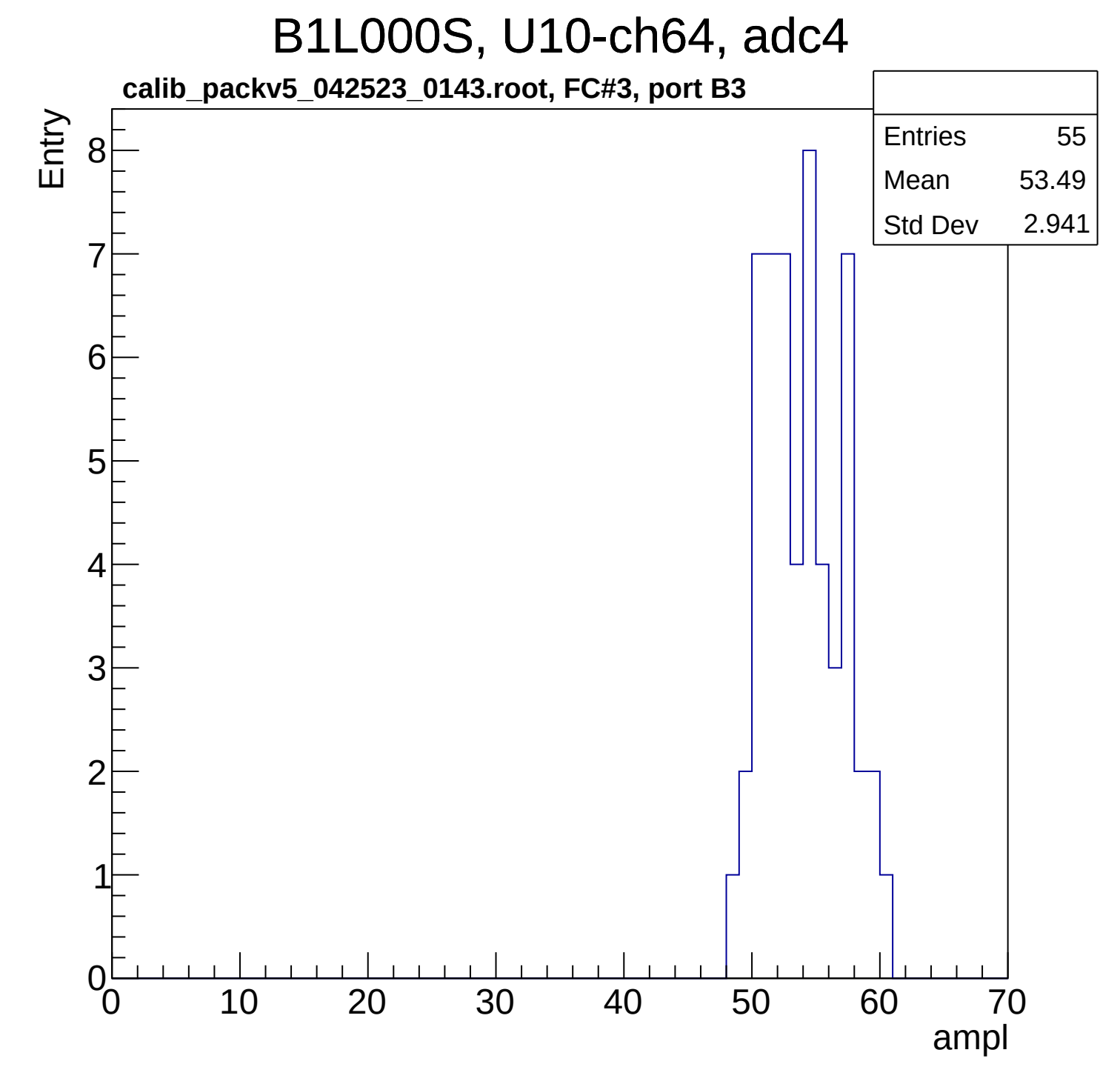
B1L000S, U10-ch64, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.49
Std Dev 2.941
Entry
ampl
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3
4
5
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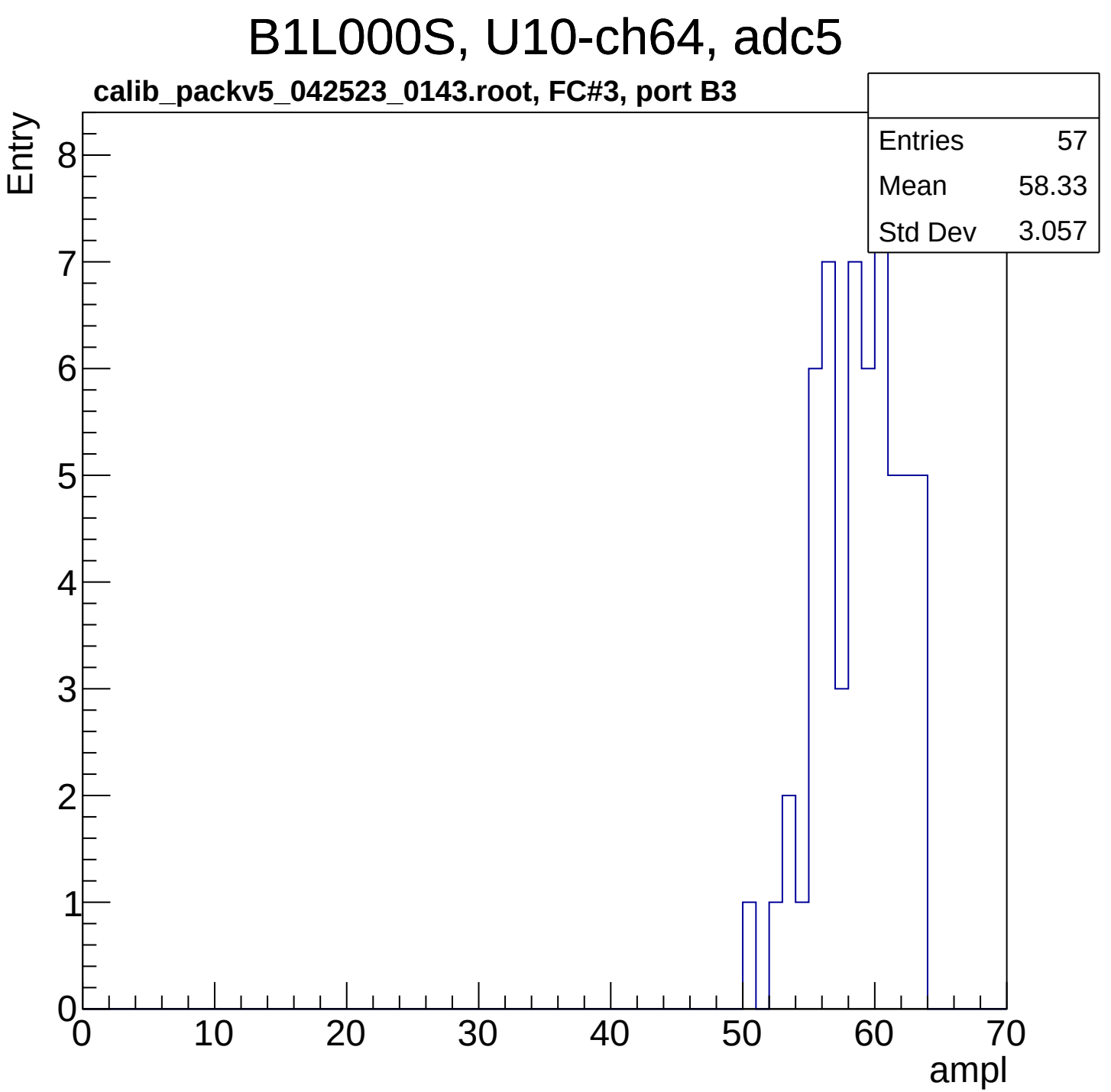
B1L000S, U10-ch64, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.33
Std Dev 3.057
Entry
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4
5
6
7
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10
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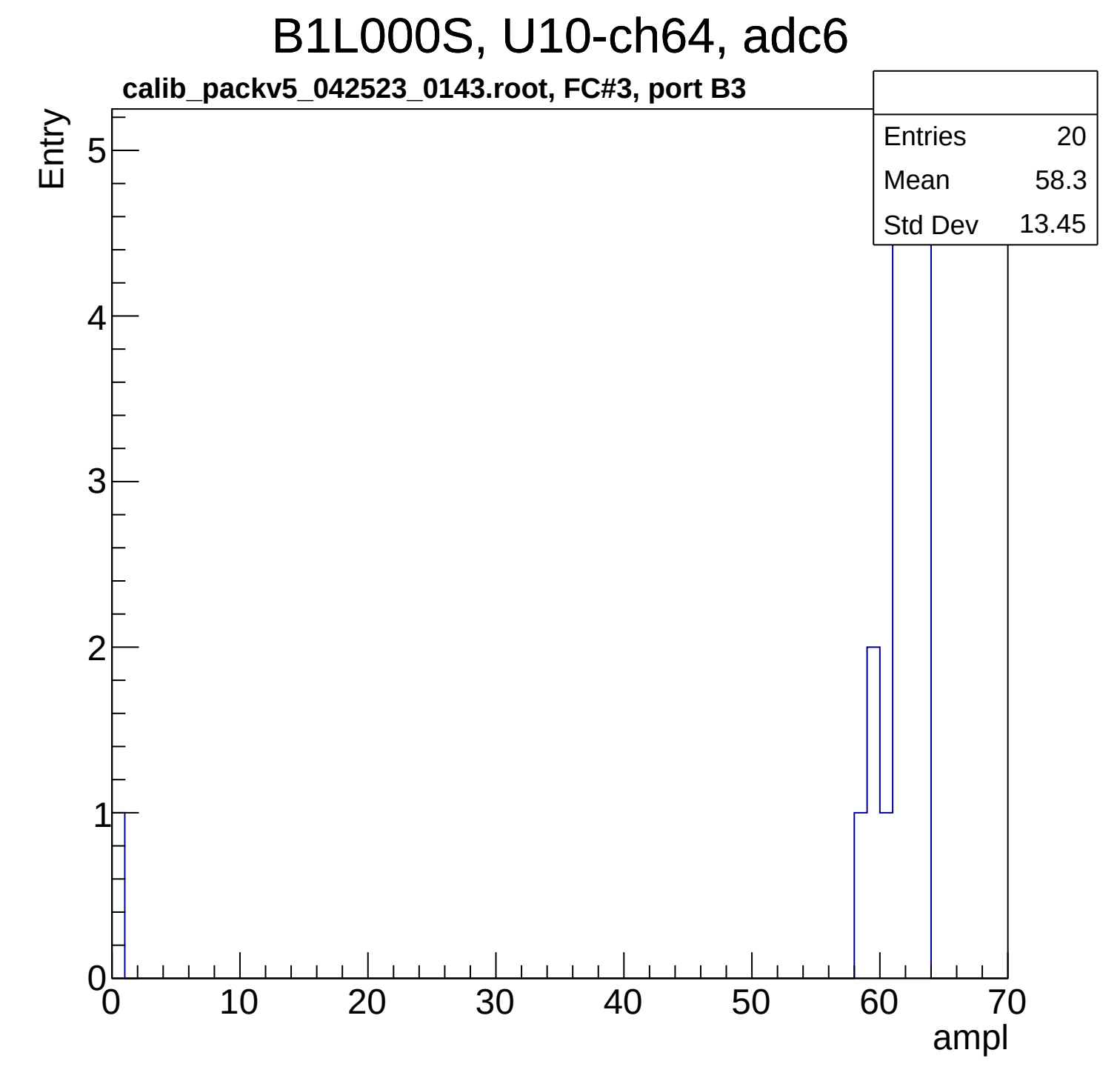
60
70
ampl


B1L000S, U10-ch64, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.3
Std Dev 13.45
Entry
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1
2
3
4
5
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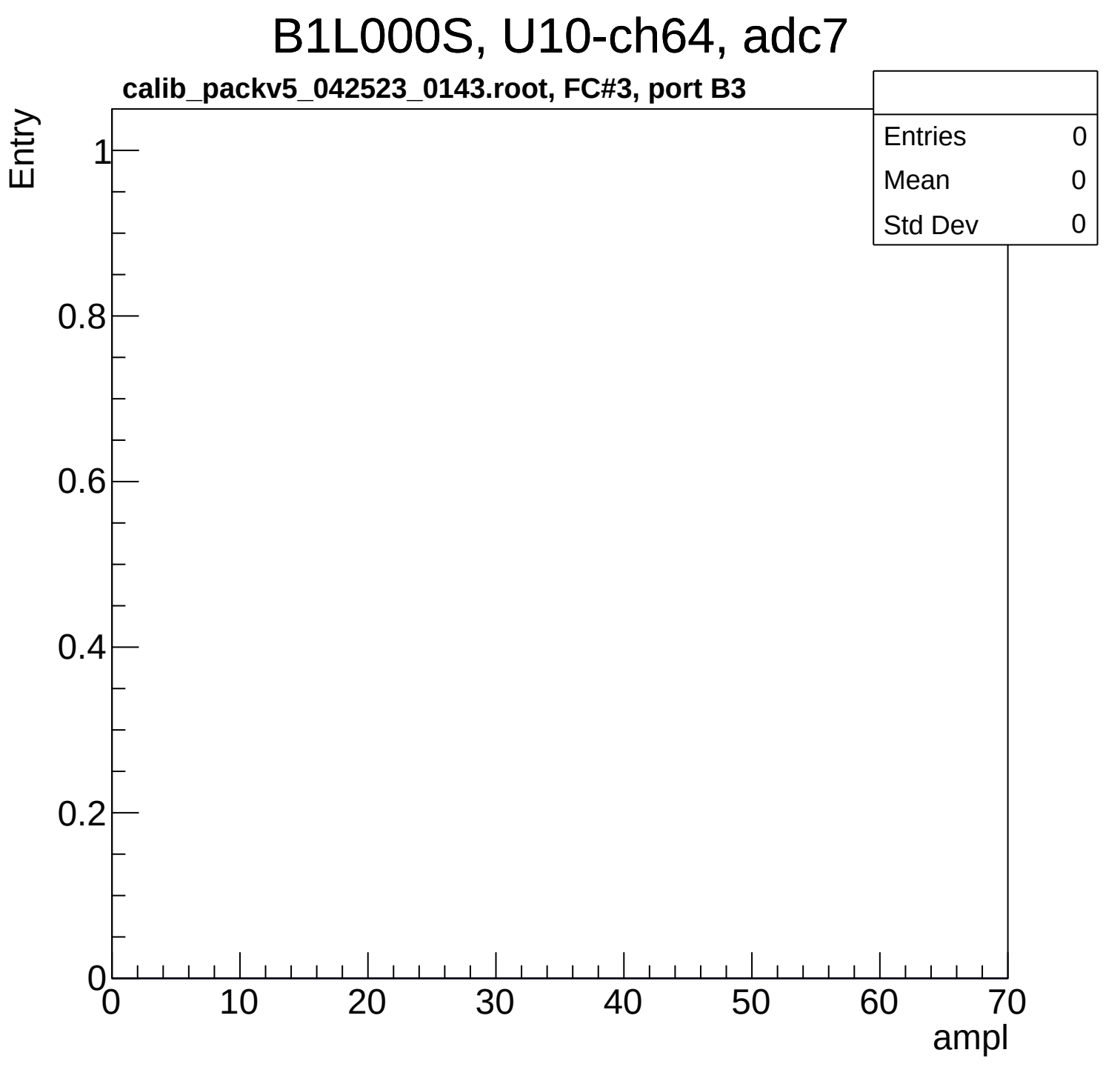
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch64, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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10
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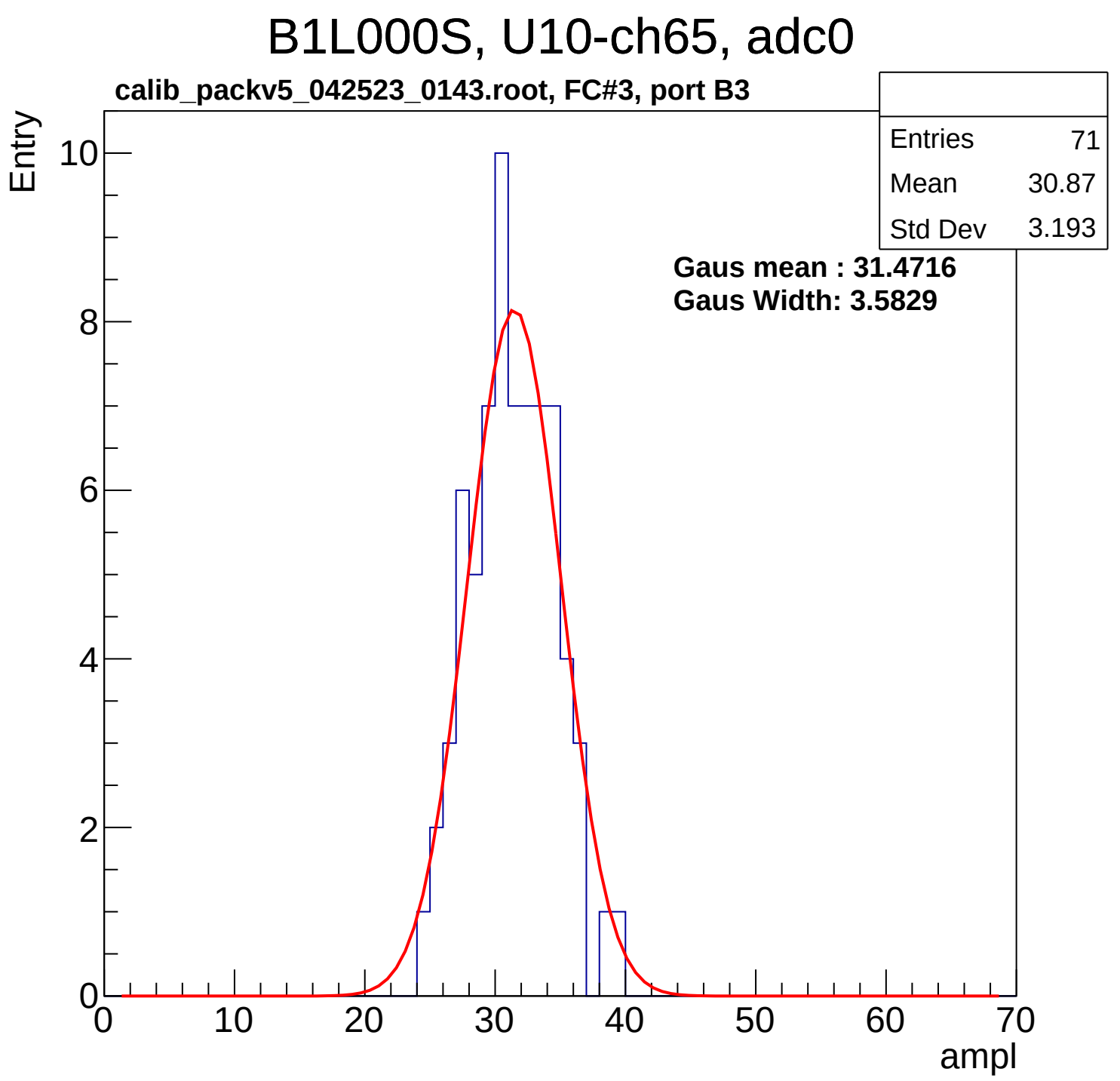
50
60
70
ampl


B1L000S, U10-ch65, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 30.87
Std Dev 3.193
Gaus mean : 31.4716
Gaus Width: 3.5829
Entry
ampl
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2
4
6
8
10
0
10
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40
50
60
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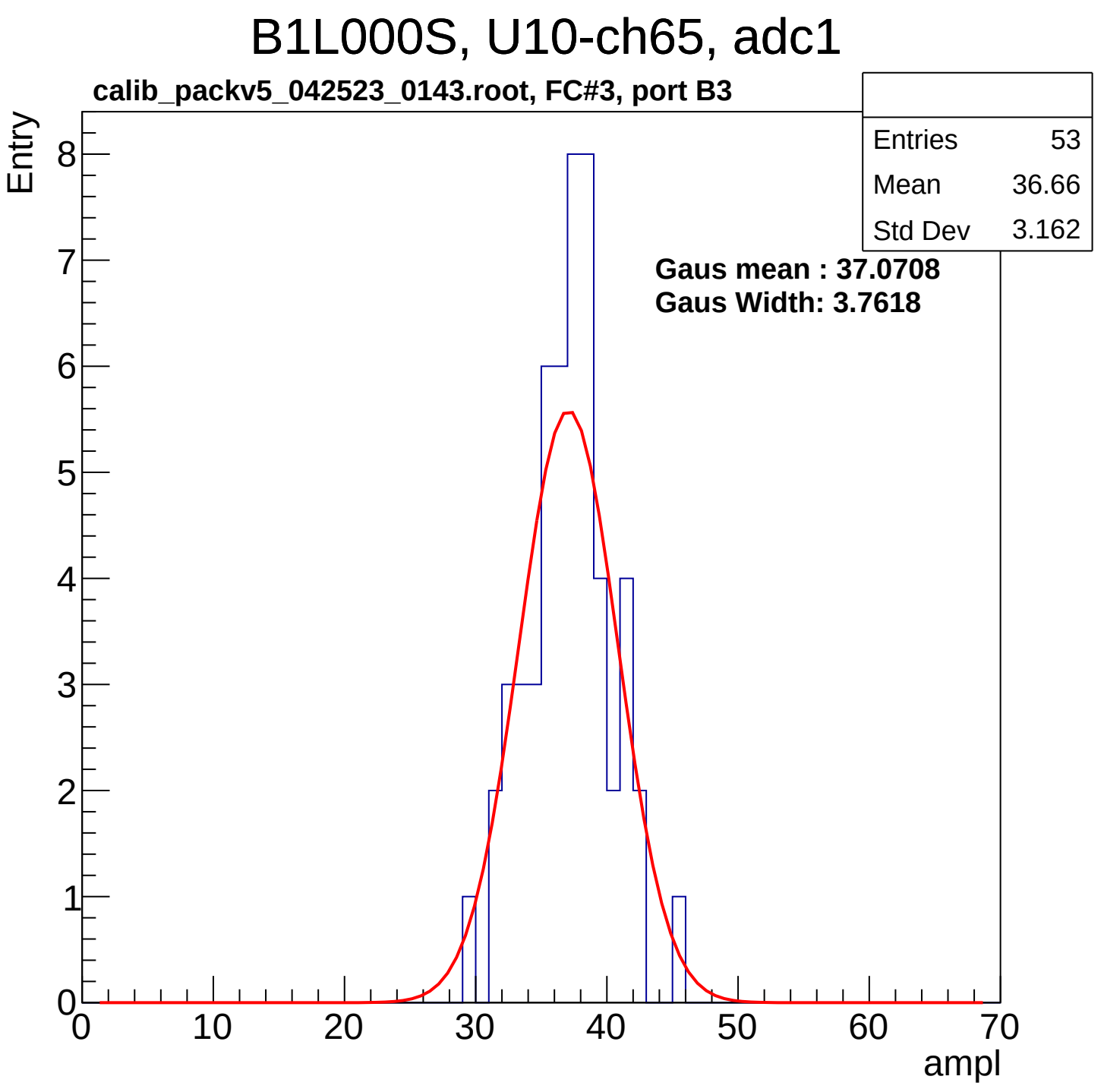
B1L000S, U10-ch65, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 36.66
Std Dev 3.162
Gaus mean : 37.0708
Gaus Width: 3.7618
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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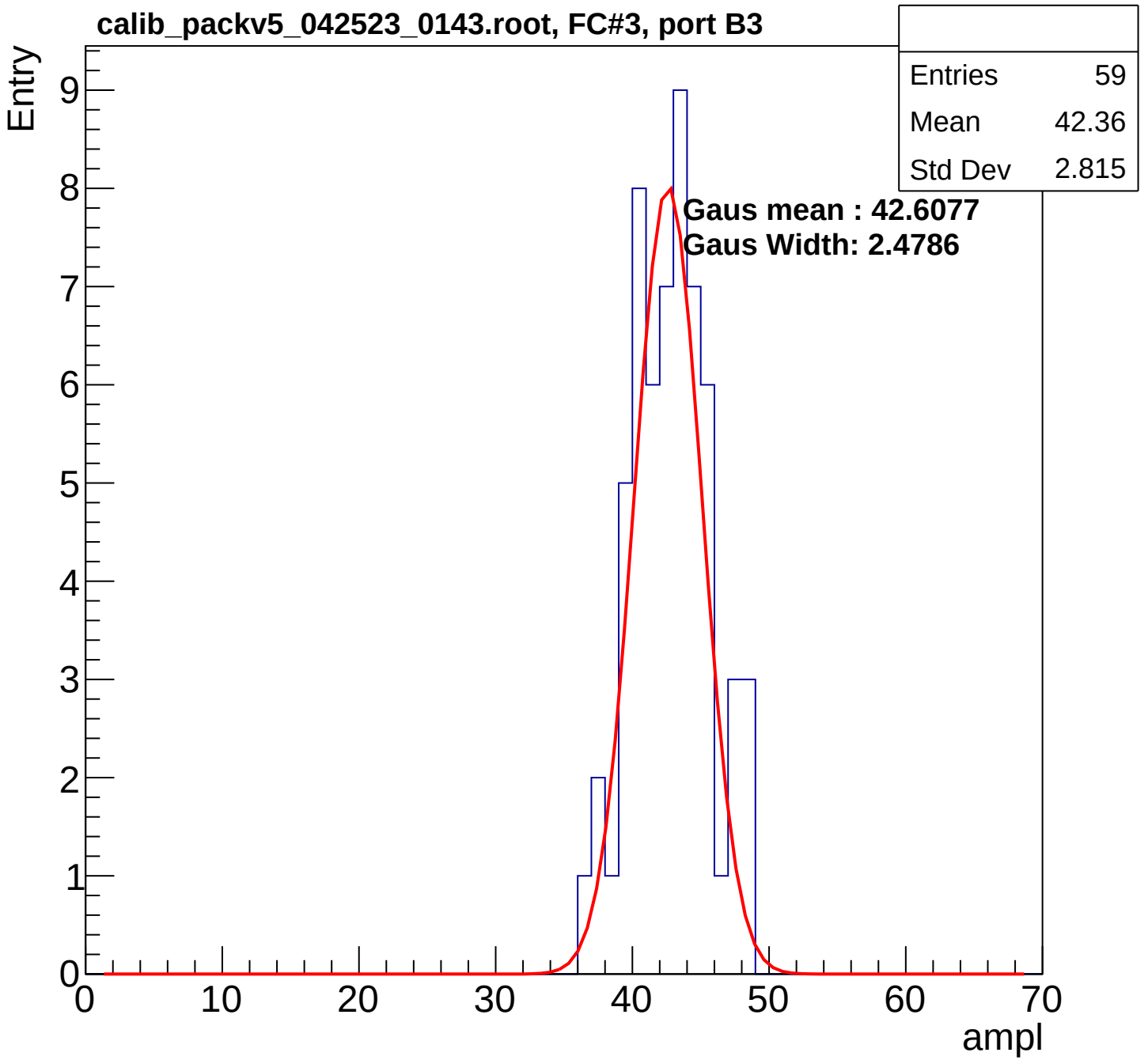
B1L000S, U10-ch65, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.36
Std Dev 2.815
Gaus mean : 42.6077
Gaus Width: 2.4786
Entry
ampl
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10
20
30
40
50
60
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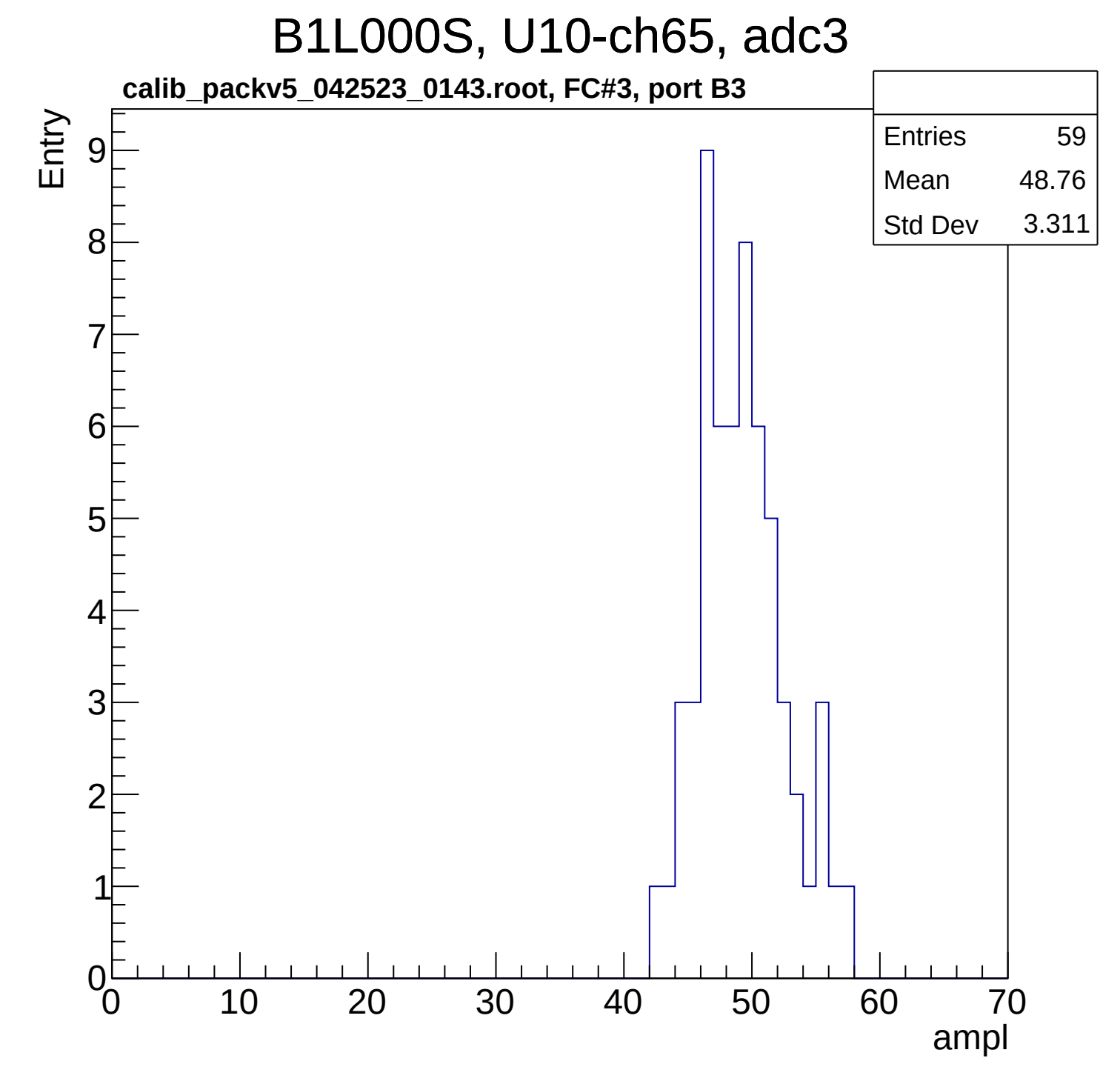
1
2
3
4
5
6
7
8
9


B1L000S, U10-ch65, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 48.76
Std Dev 3.311
Entry
ampl
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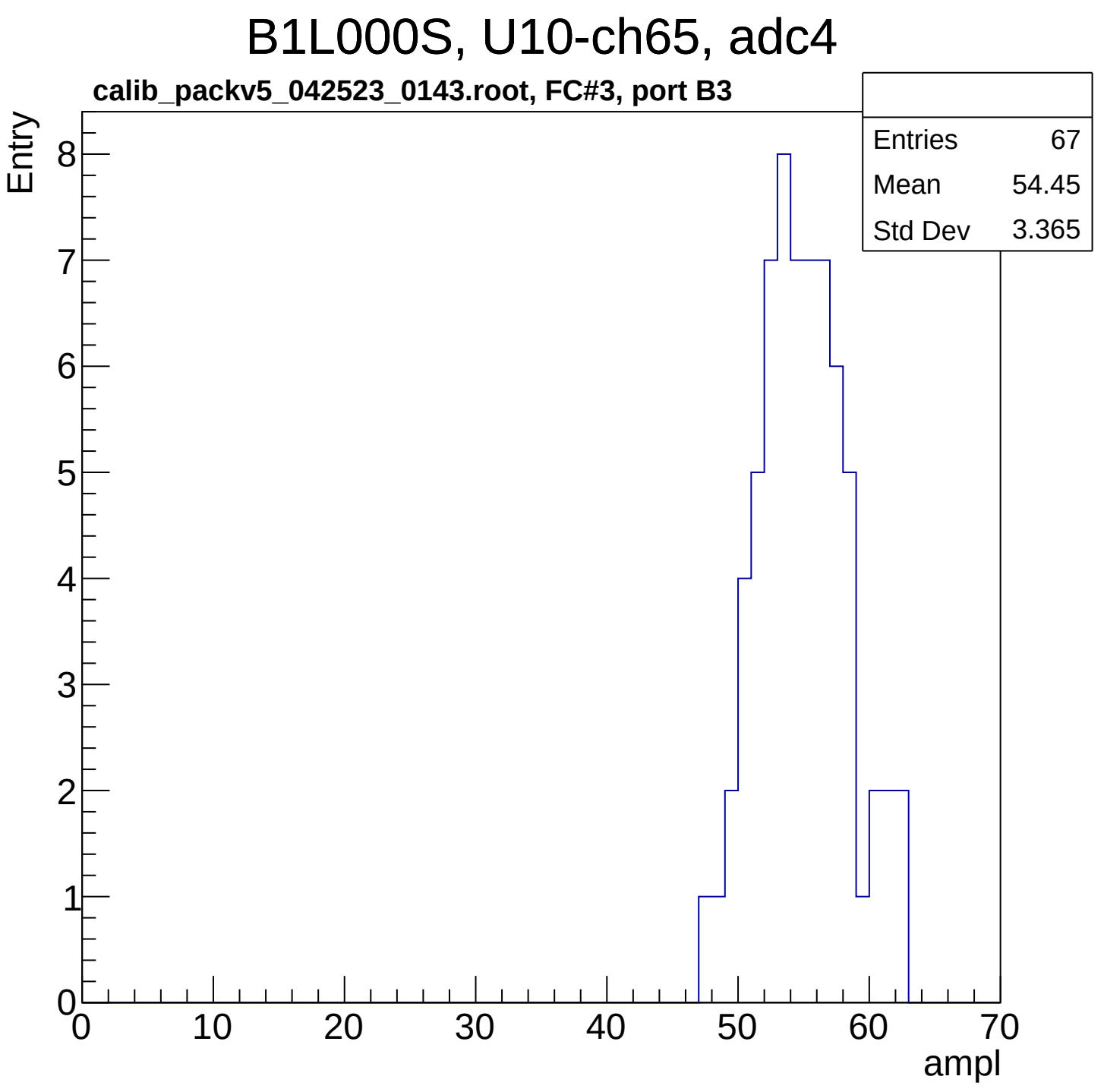
B1L000S, U10-ch65, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 54.45
Std Dev 3.365
Entry
ampl
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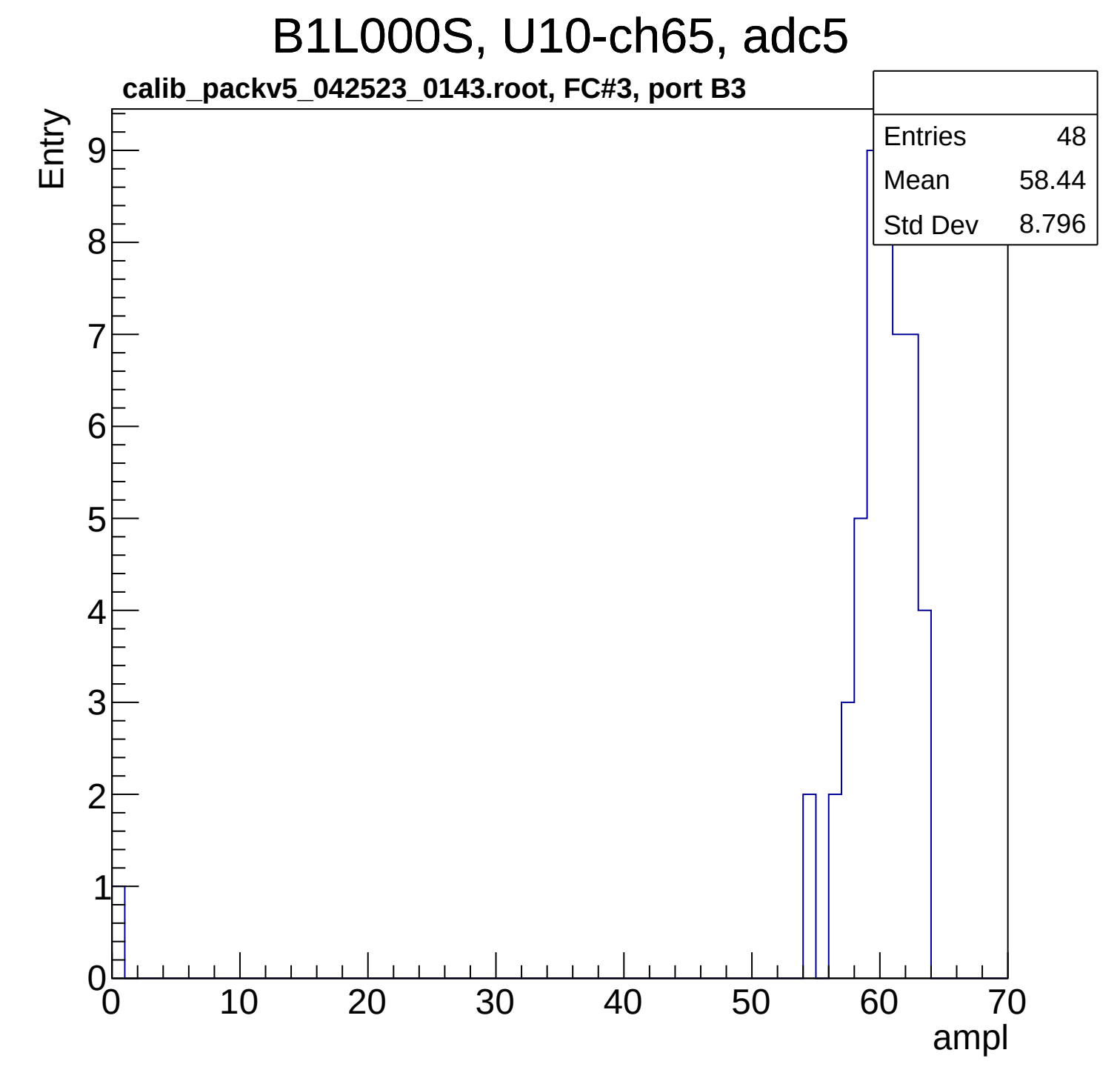
B1L000S, U10-ch65, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.44
Std Dev 8.796
Entry
ampl
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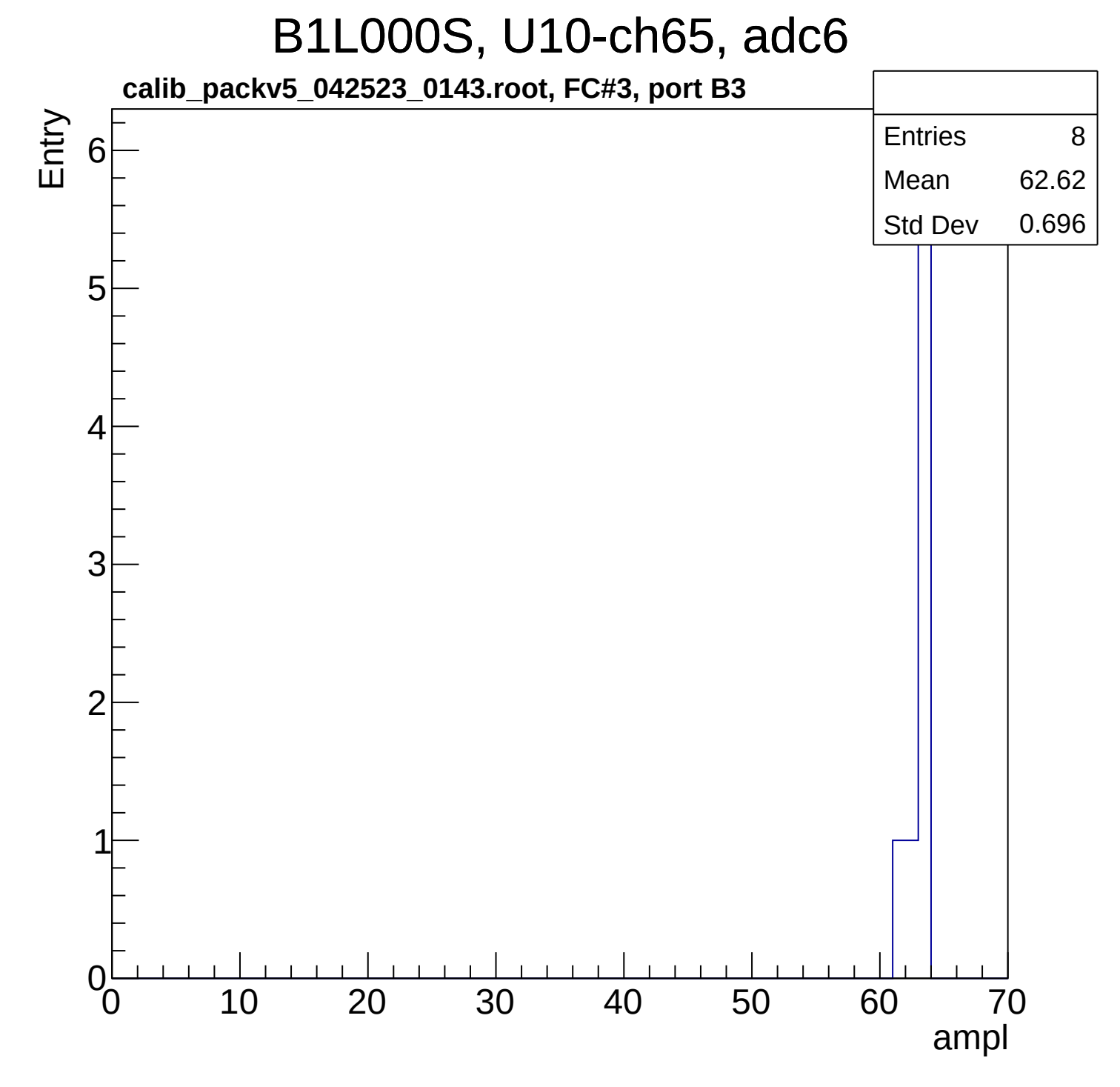
B1L000S, U10-ch65, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.62
Std Dev 0.696
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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60
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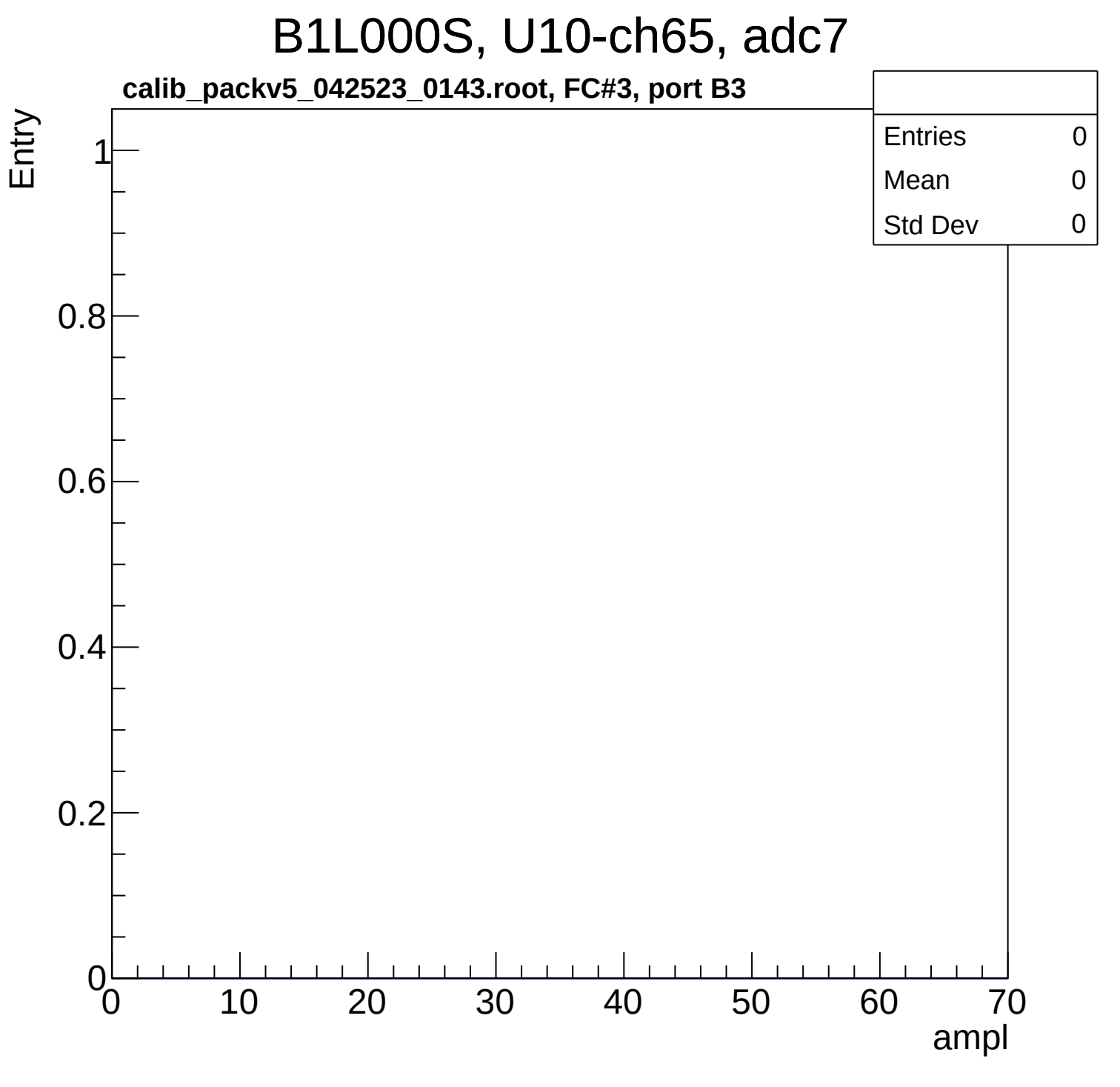
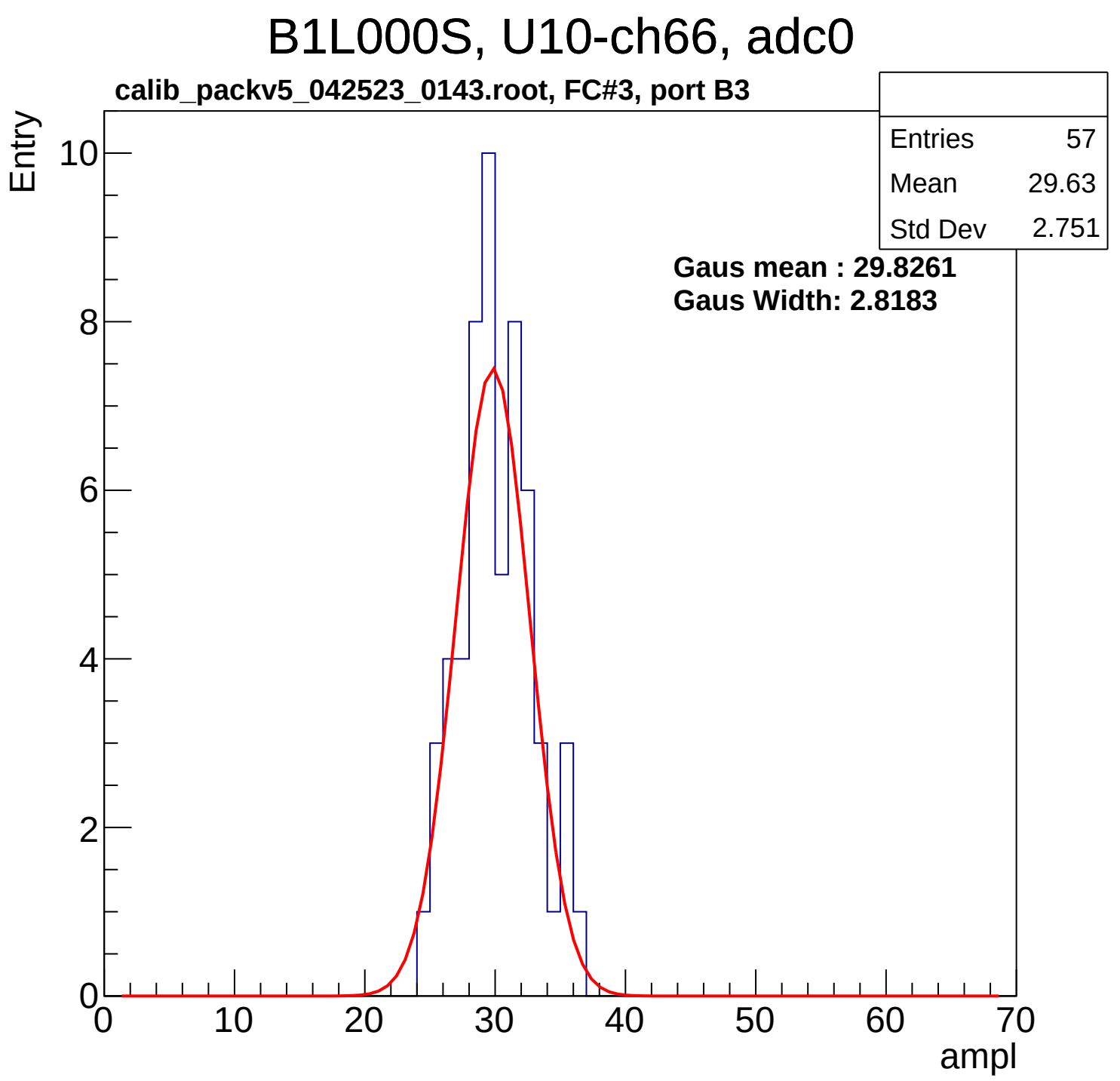

B1L000S, U10-ch65, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch66, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 29.63
Std Dev 2.751
Gaus mean : 29.8261
Gaus Width: 2.8183
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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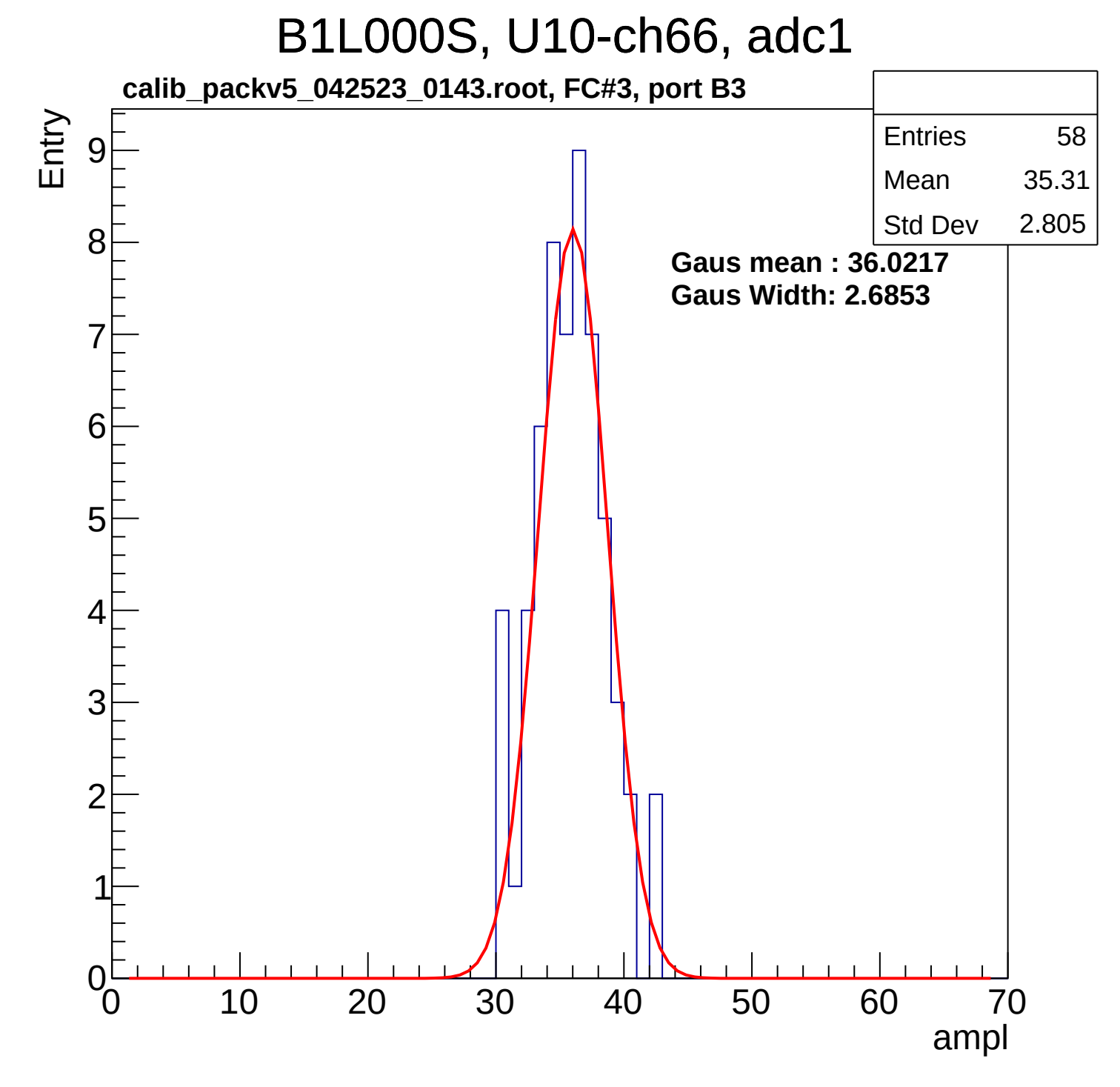
B1L000S, U10-ch66, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.31
Std Dev 2.805
Gaus mean : 36.0217
Gaus Width: 2.6853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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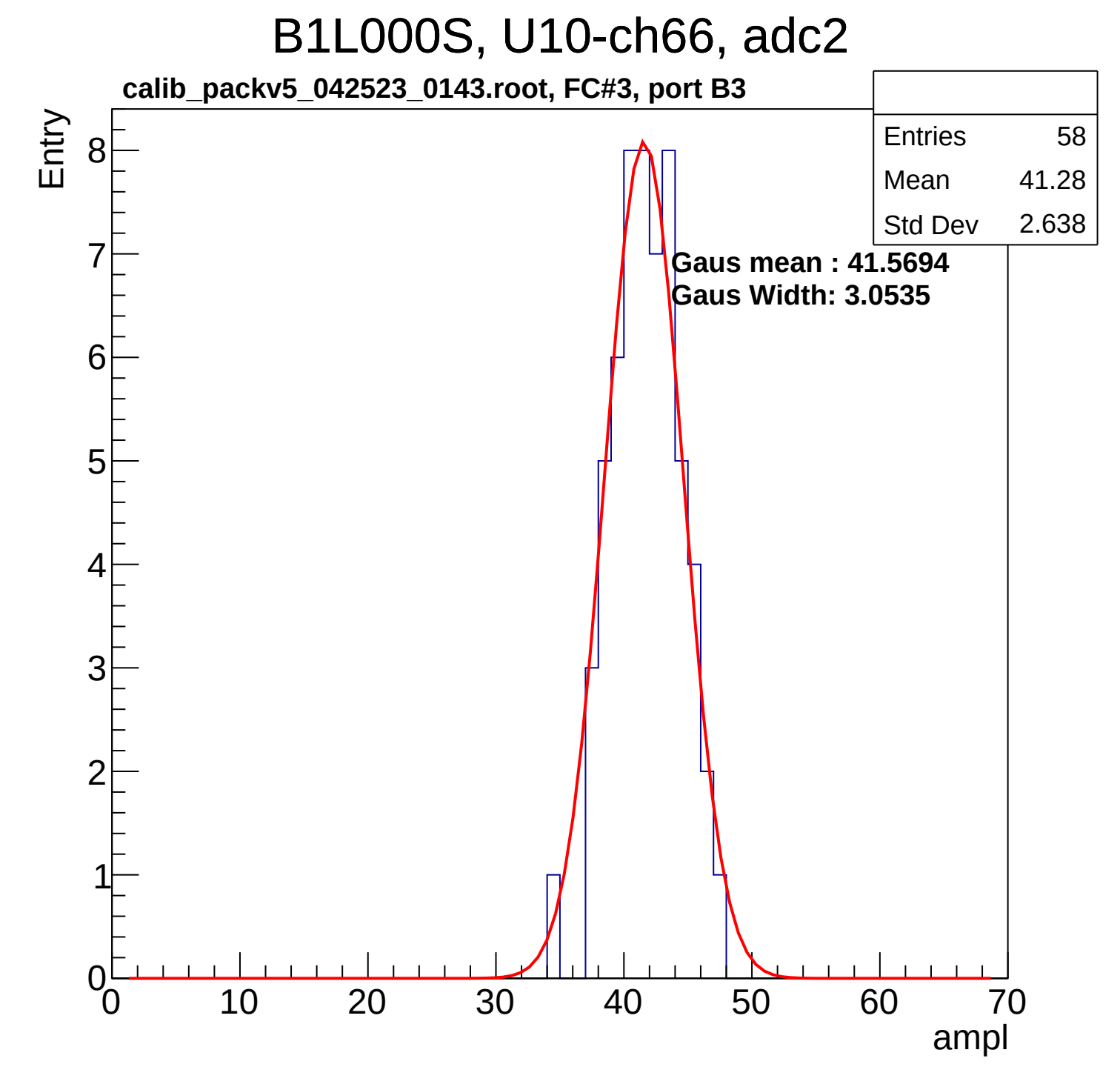
B1L000S, U10-ch66, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.28
Std Dev 2.638
Gaus mean : 41.5694
Gaus Width: 3.0535
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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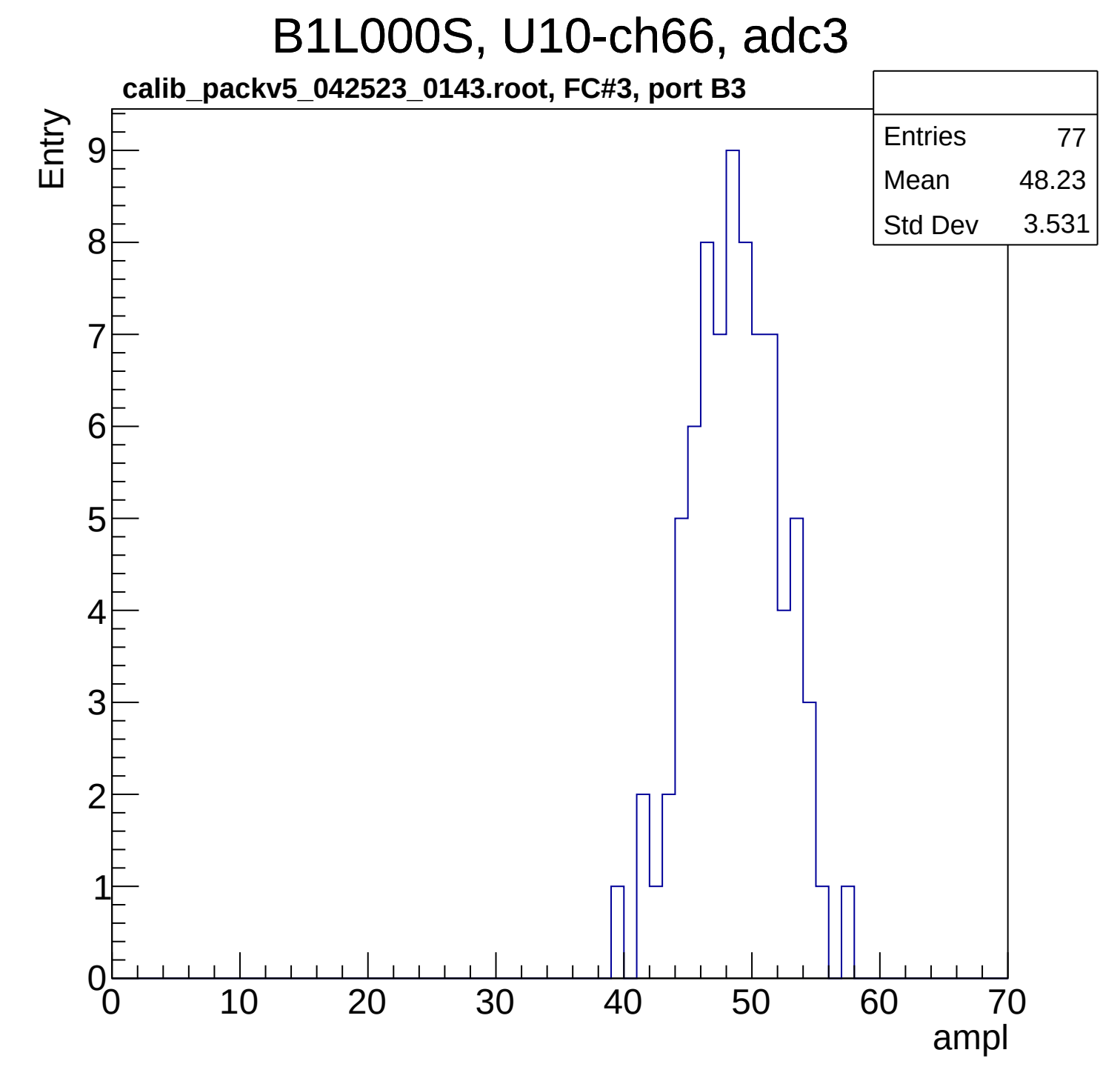
B1L000S, U10-ch66, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 48.23
Std Dev 3.531
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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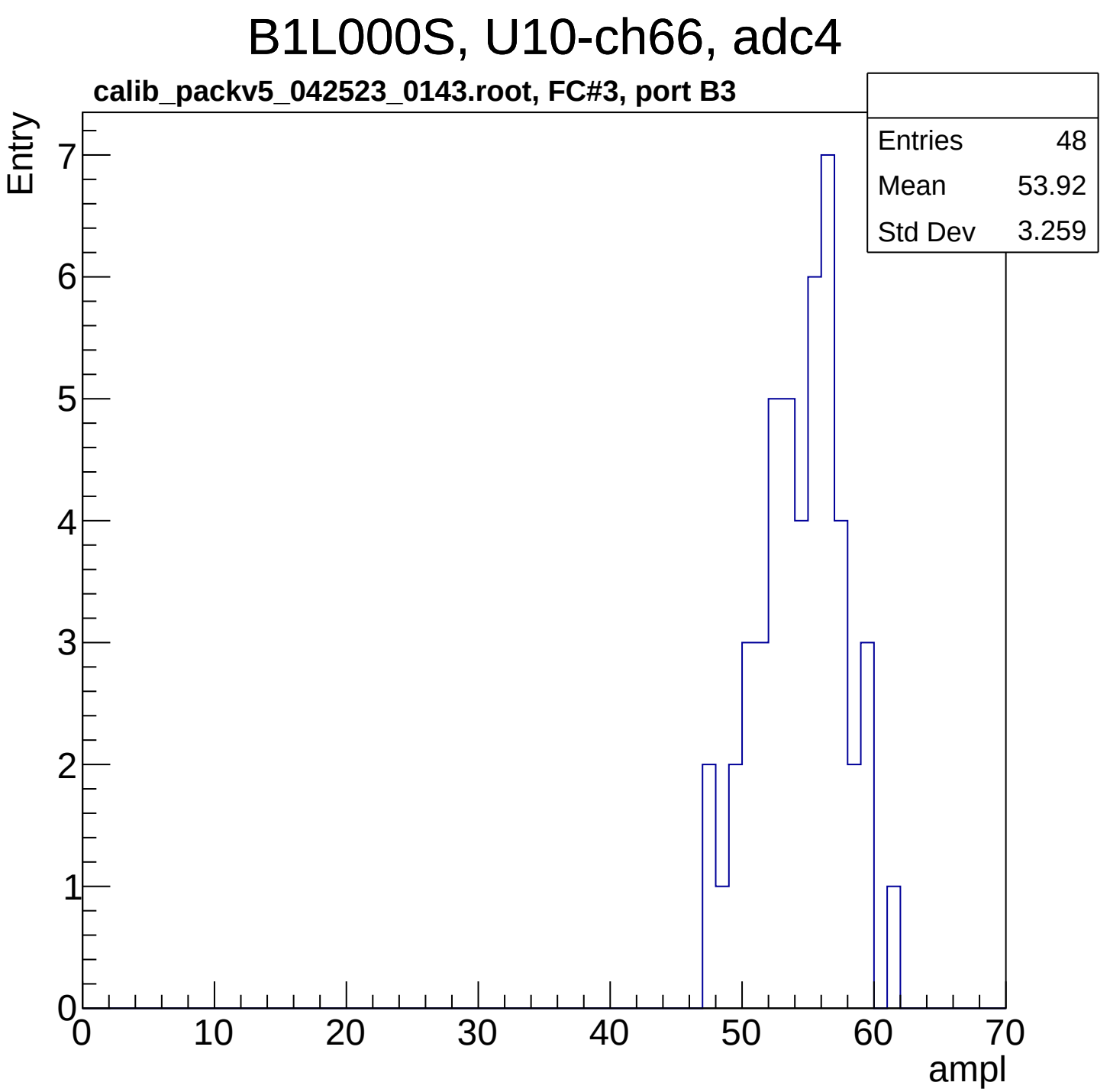
B1L000S, U10-ch66, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.92
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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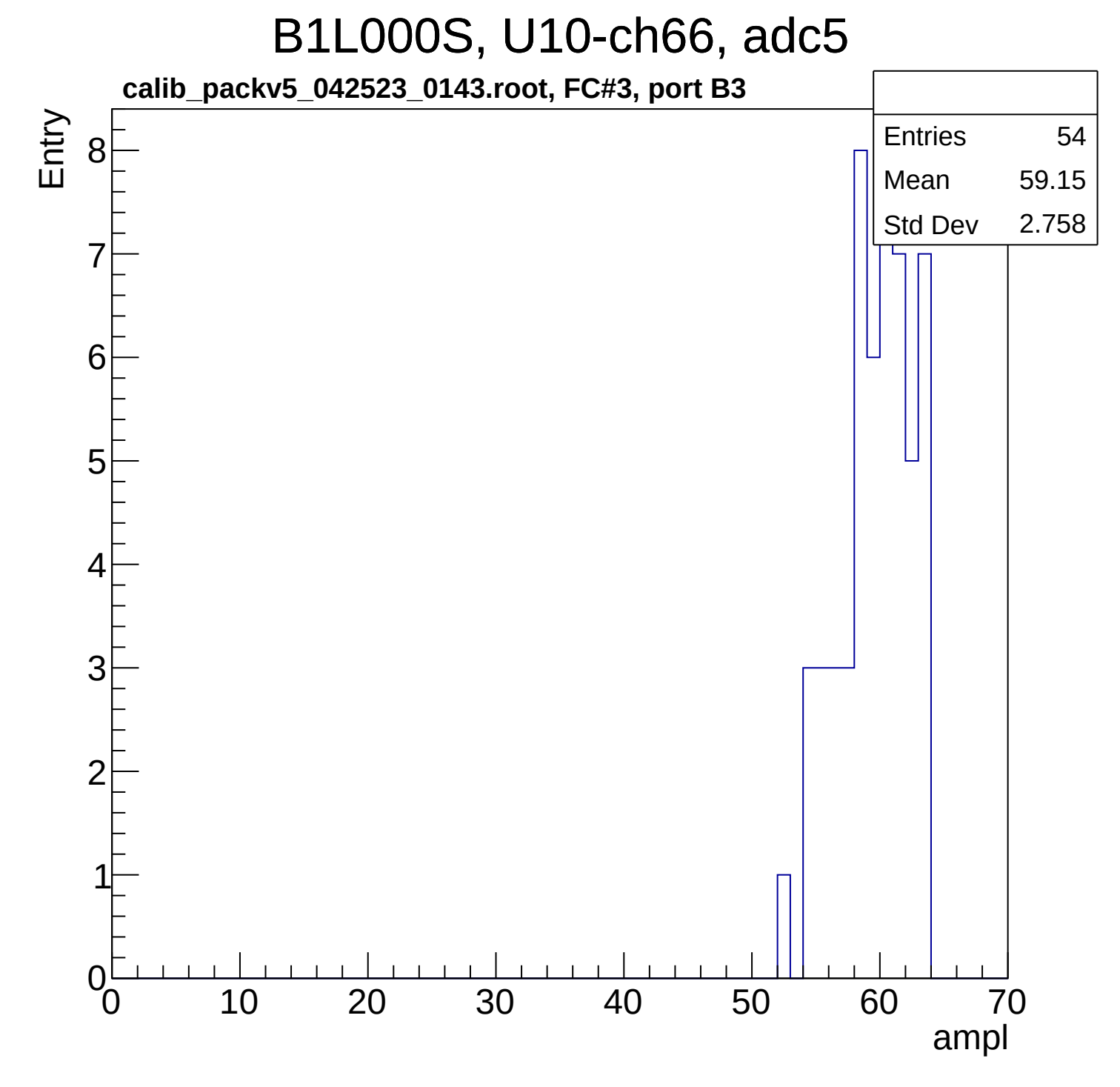
B1L000S, U10-ch66, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 59.15
Std Dev 2.758
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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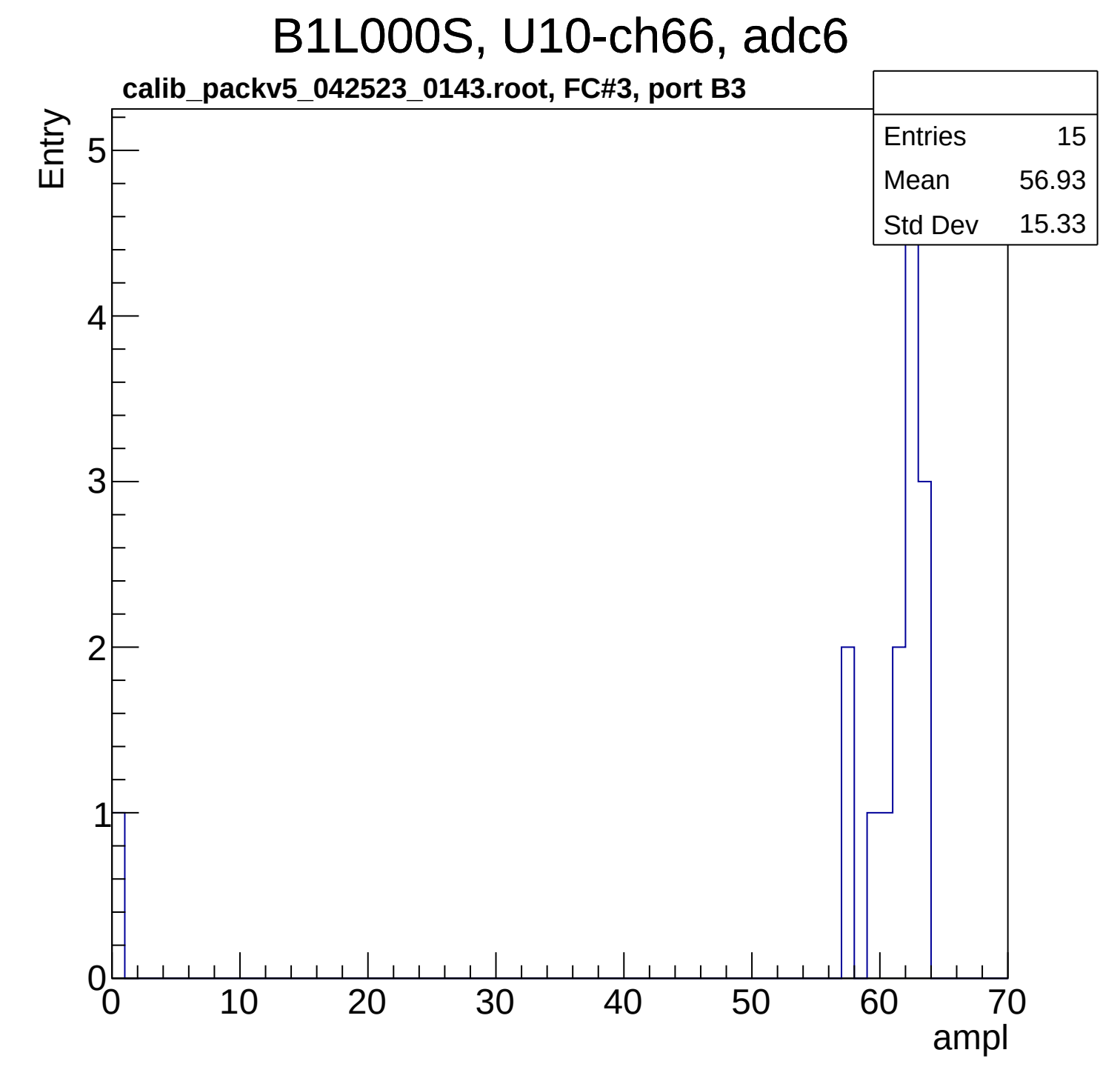
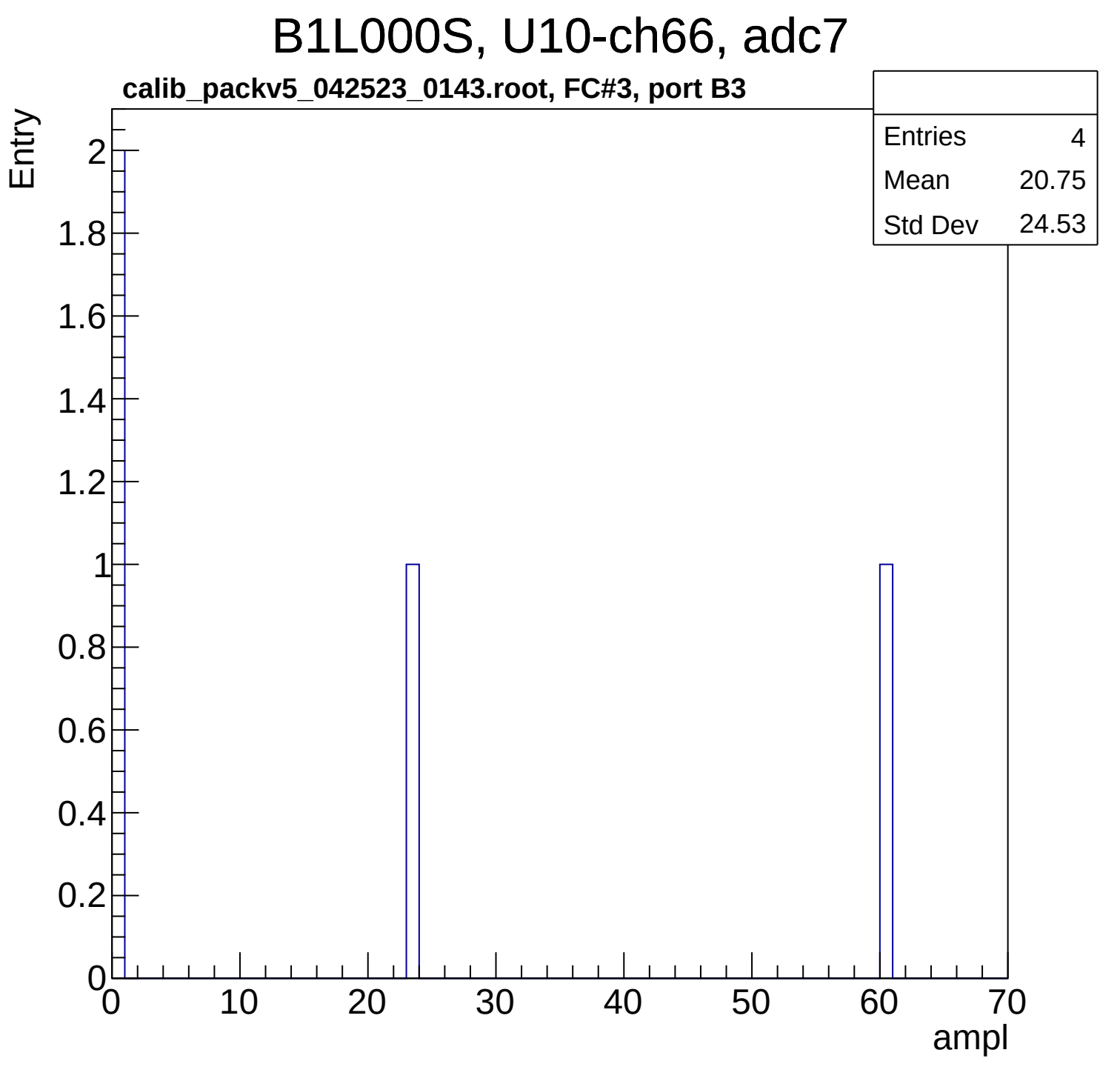
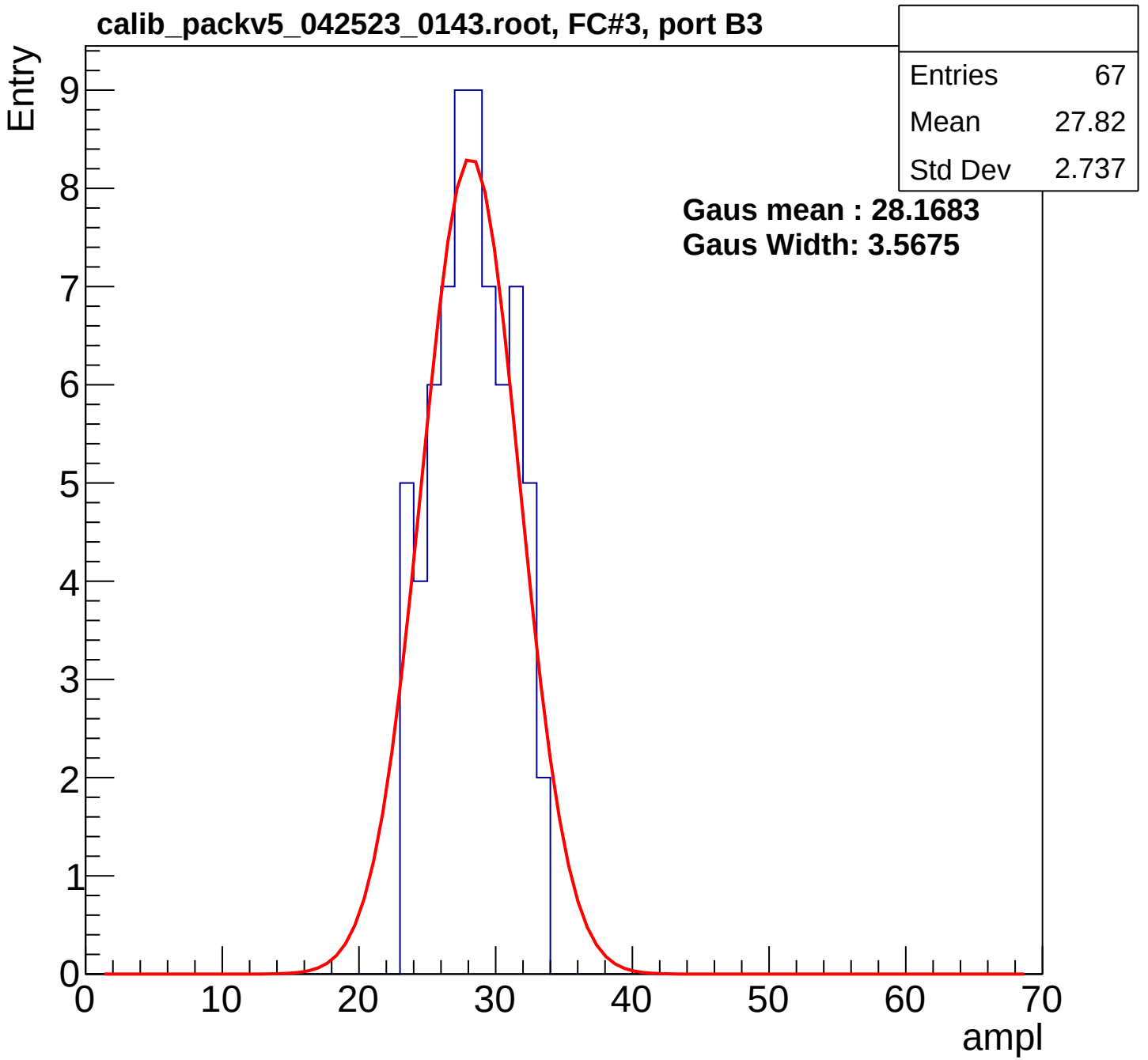

B1L000S, U10-ch66, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 56.93
Std Dev 15.33
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch66, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 20.75
Std Dev 24.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch67, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.82
Std Dev 2.737
Gaus mean : 28.1683
Gaus Width: 3.5675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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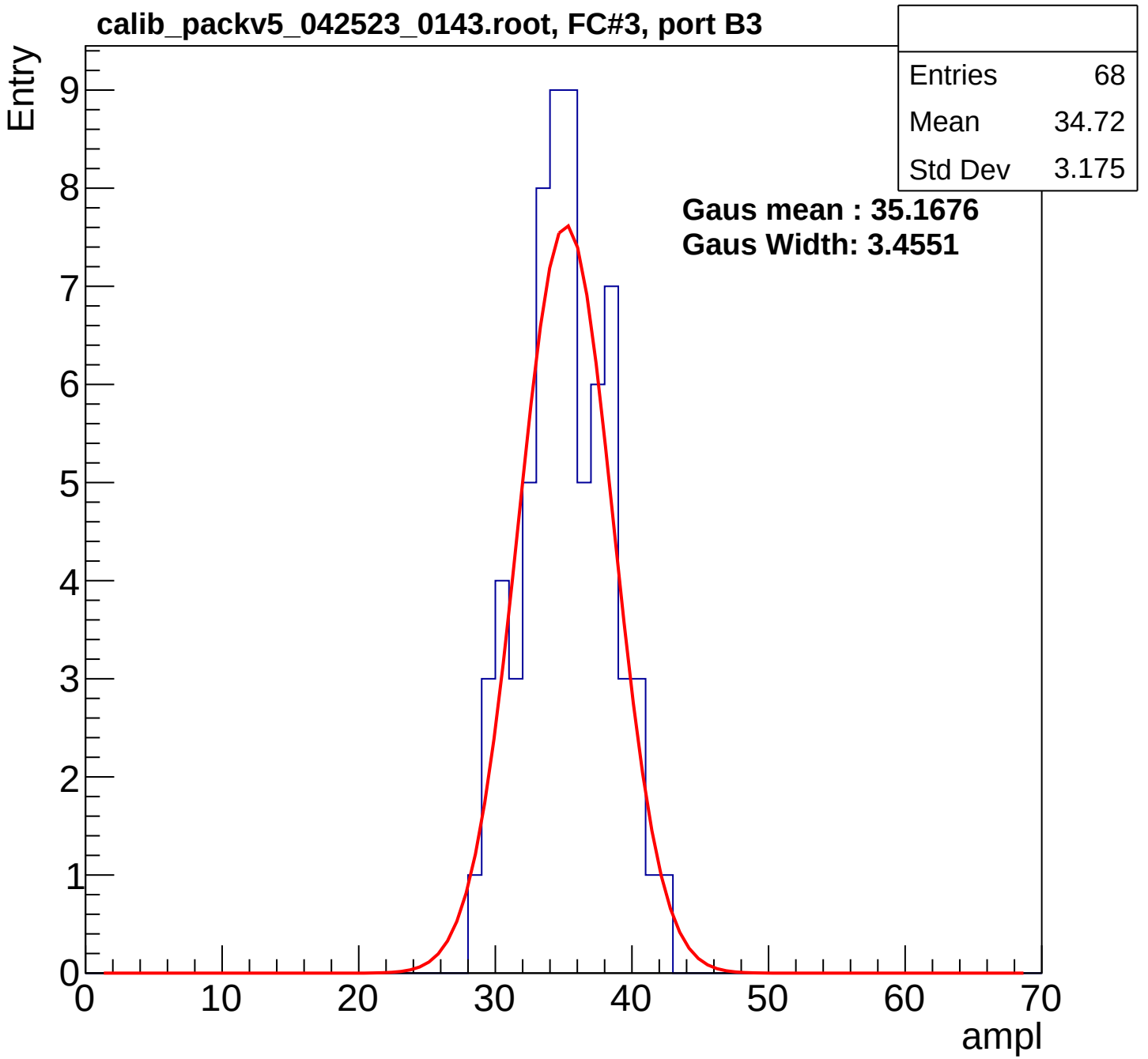
B1L000S, U10-ch67, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 34.72
Std Dev 3.175
Gaus mean : 35.1676
Gaus Width: 3.4551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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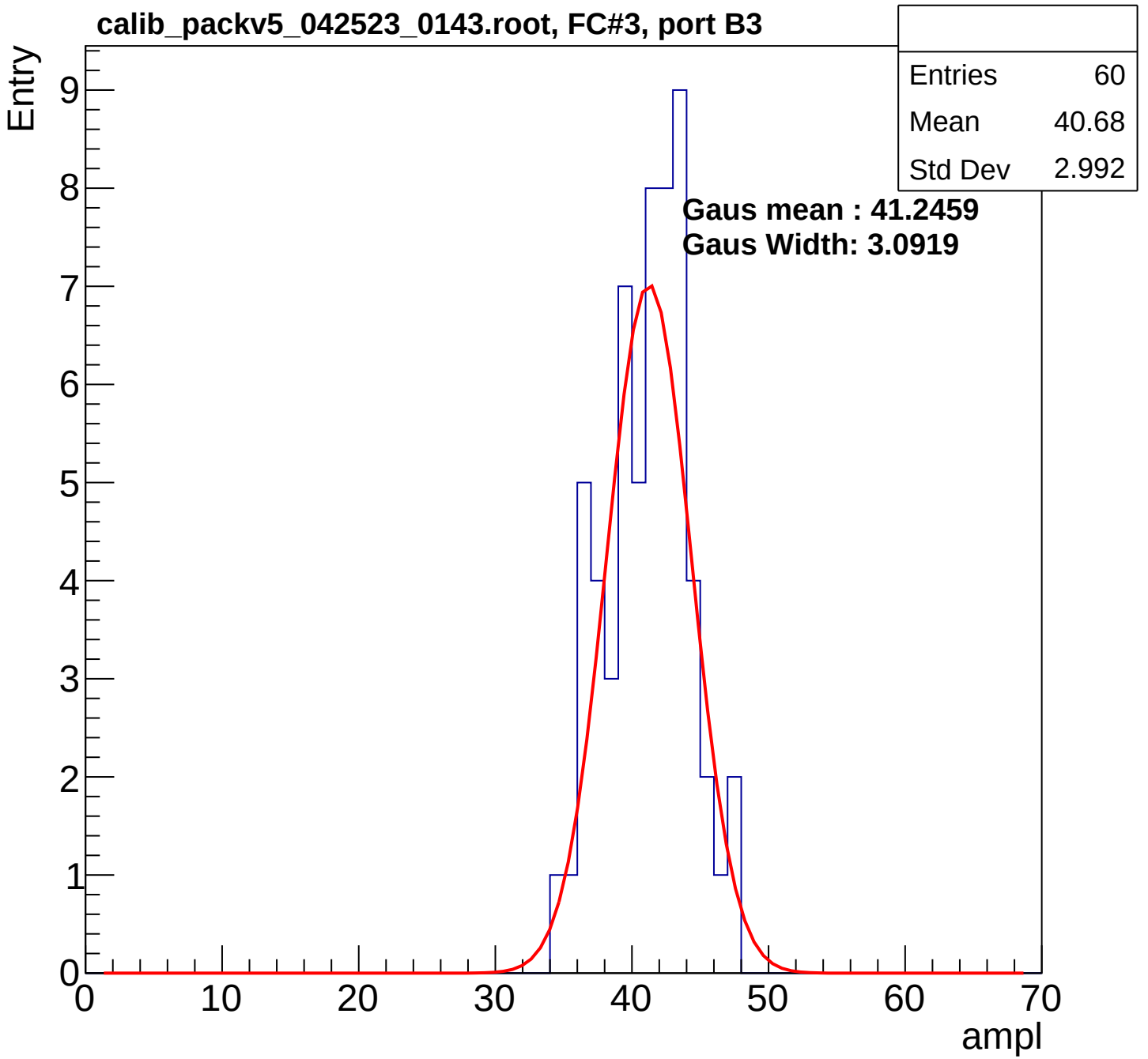
B1L000S, U10-ch67, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 40.68
Std Dev 2.992
Gaus mean : 41.2459
Gaus Width: 3.0919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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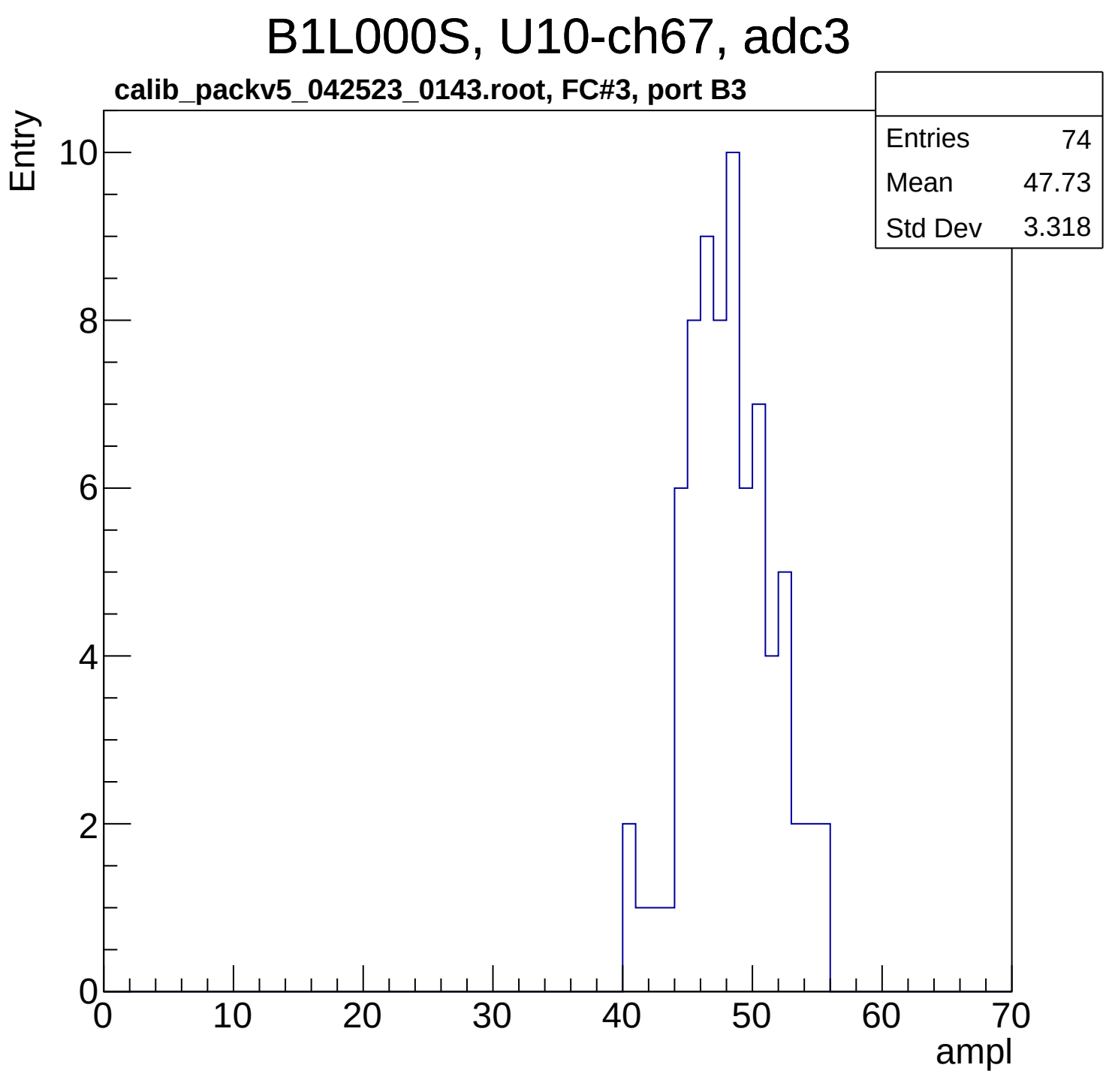
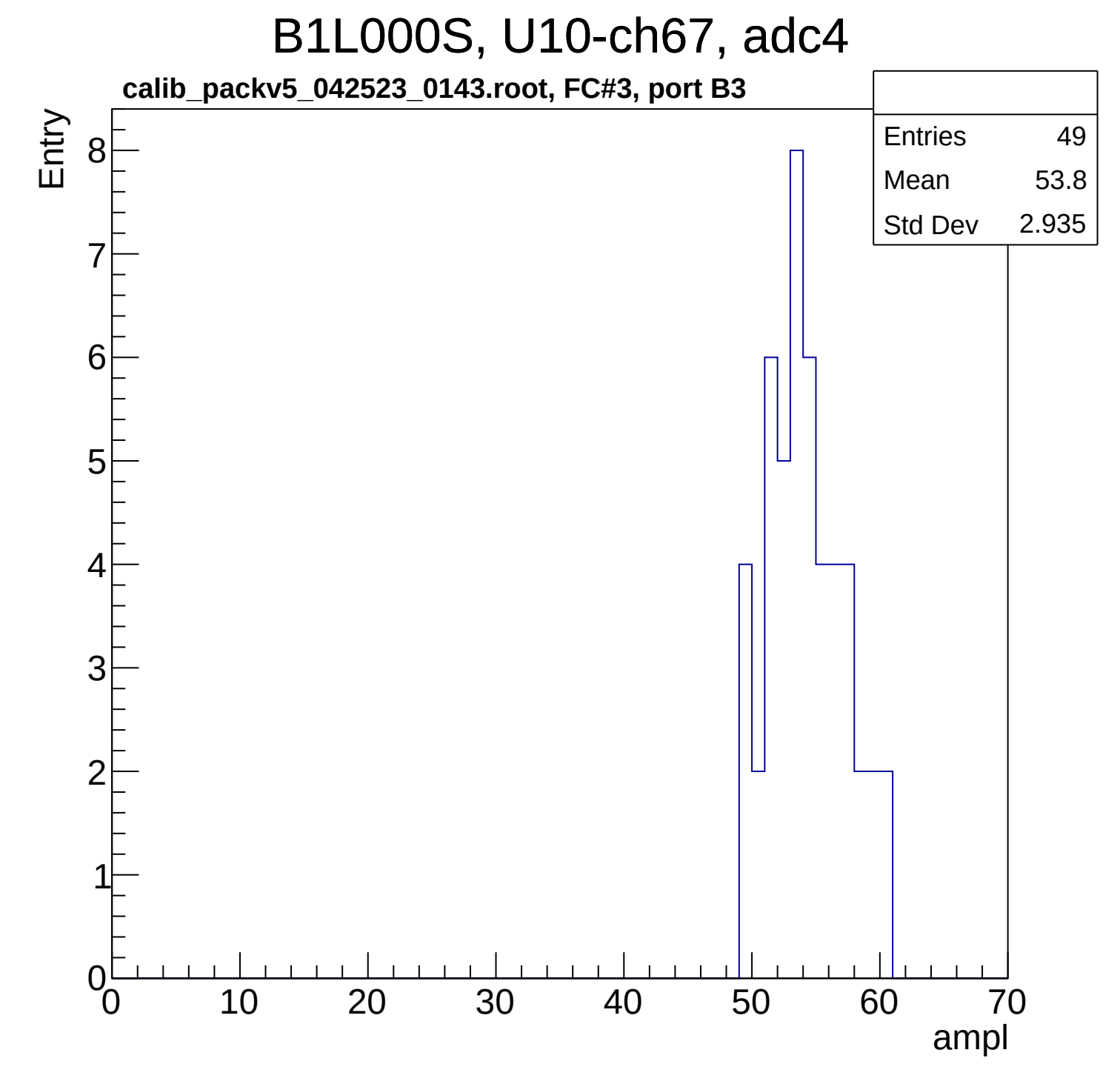
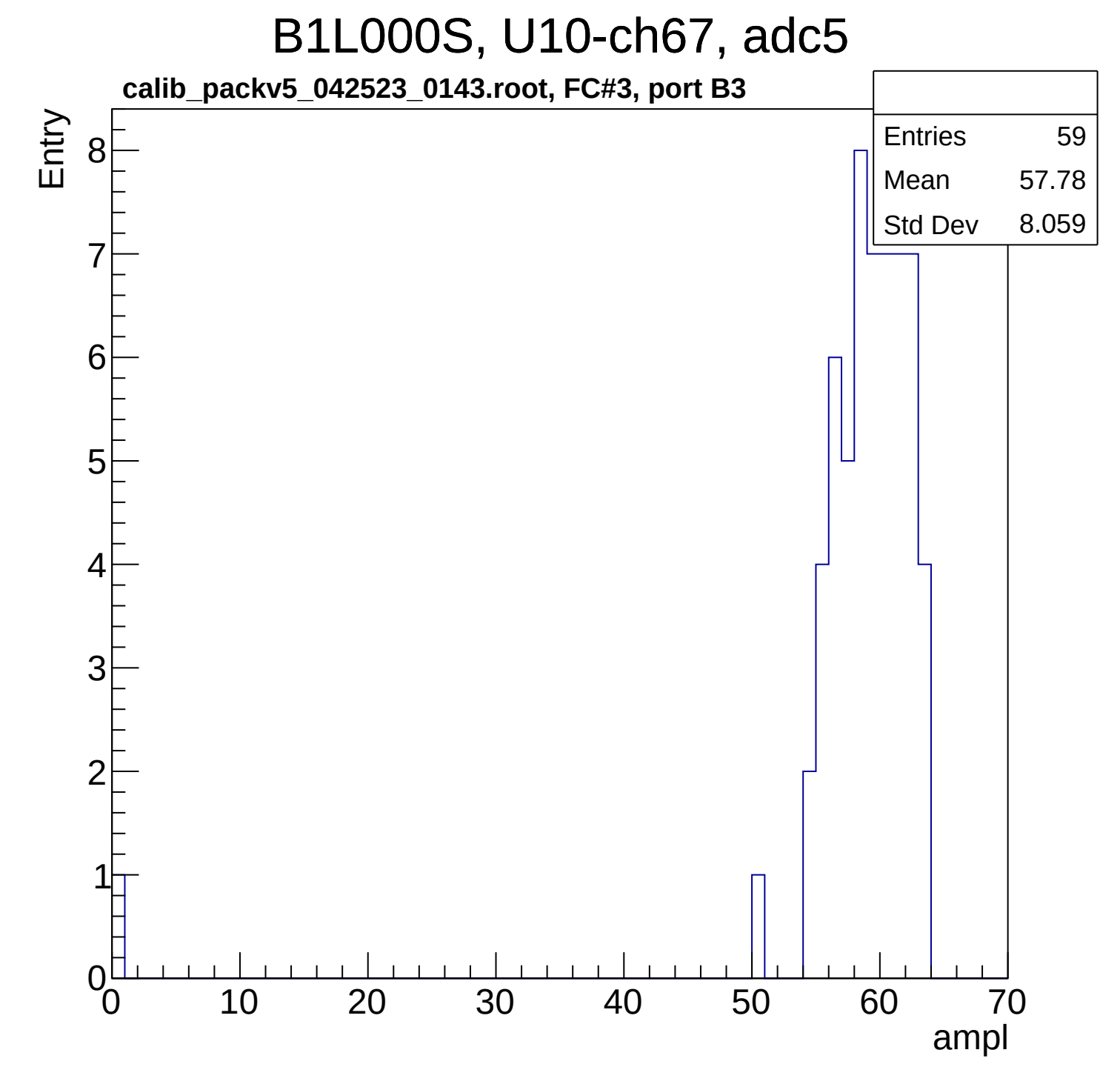
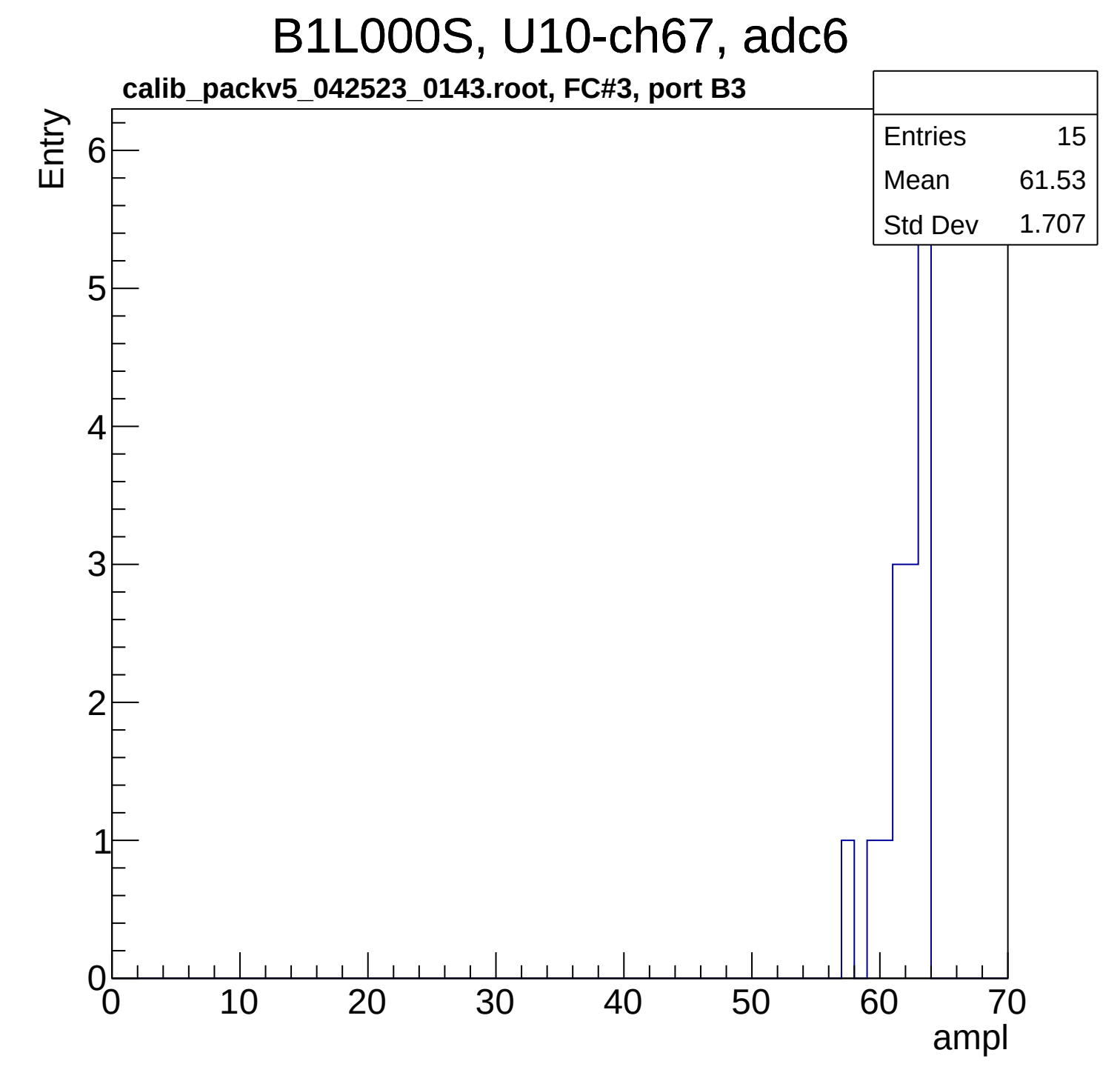
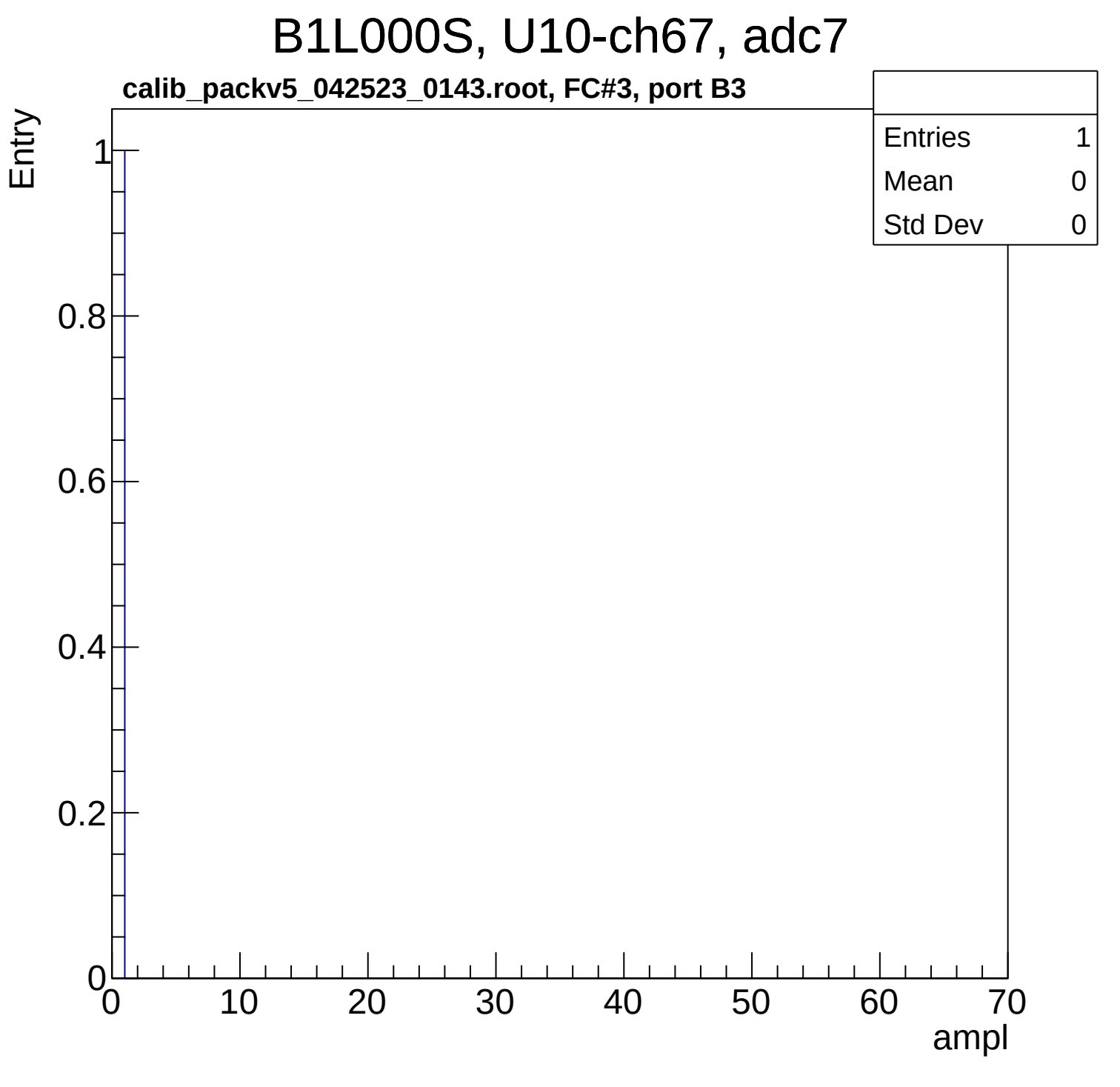
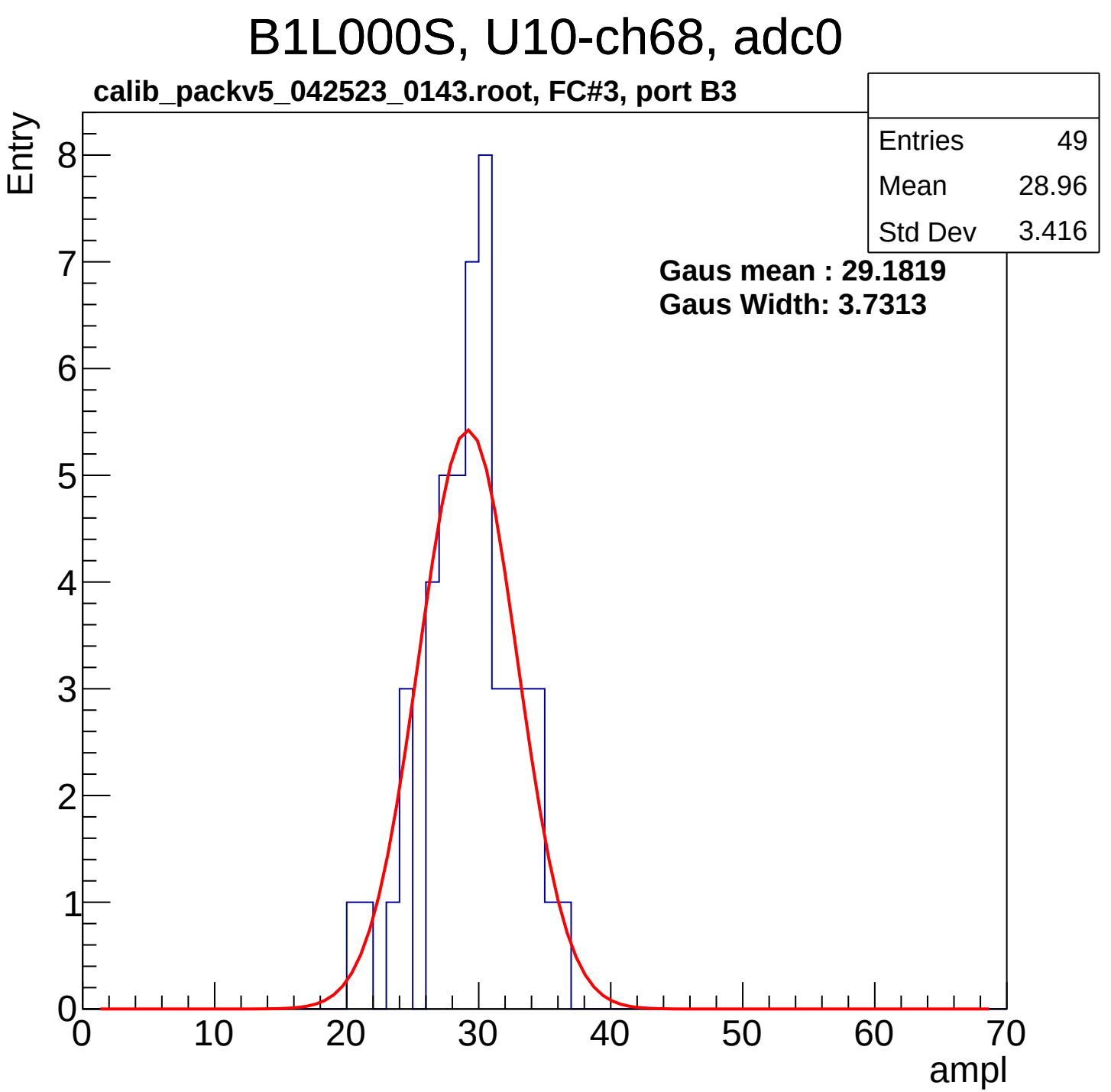

B1L000S, U10-ch67, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 47.73
Std Dev 3.318
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch67, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.8
Std Dev 2.935
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch67, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.78
Std Dev 8.059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch67, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.53
Std Dev 1.707
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch67, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch68, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 28.96
Std Dev 3.416
Gaus mean : 29.1819
Gaus Width: 3.7313
Entry
ampl
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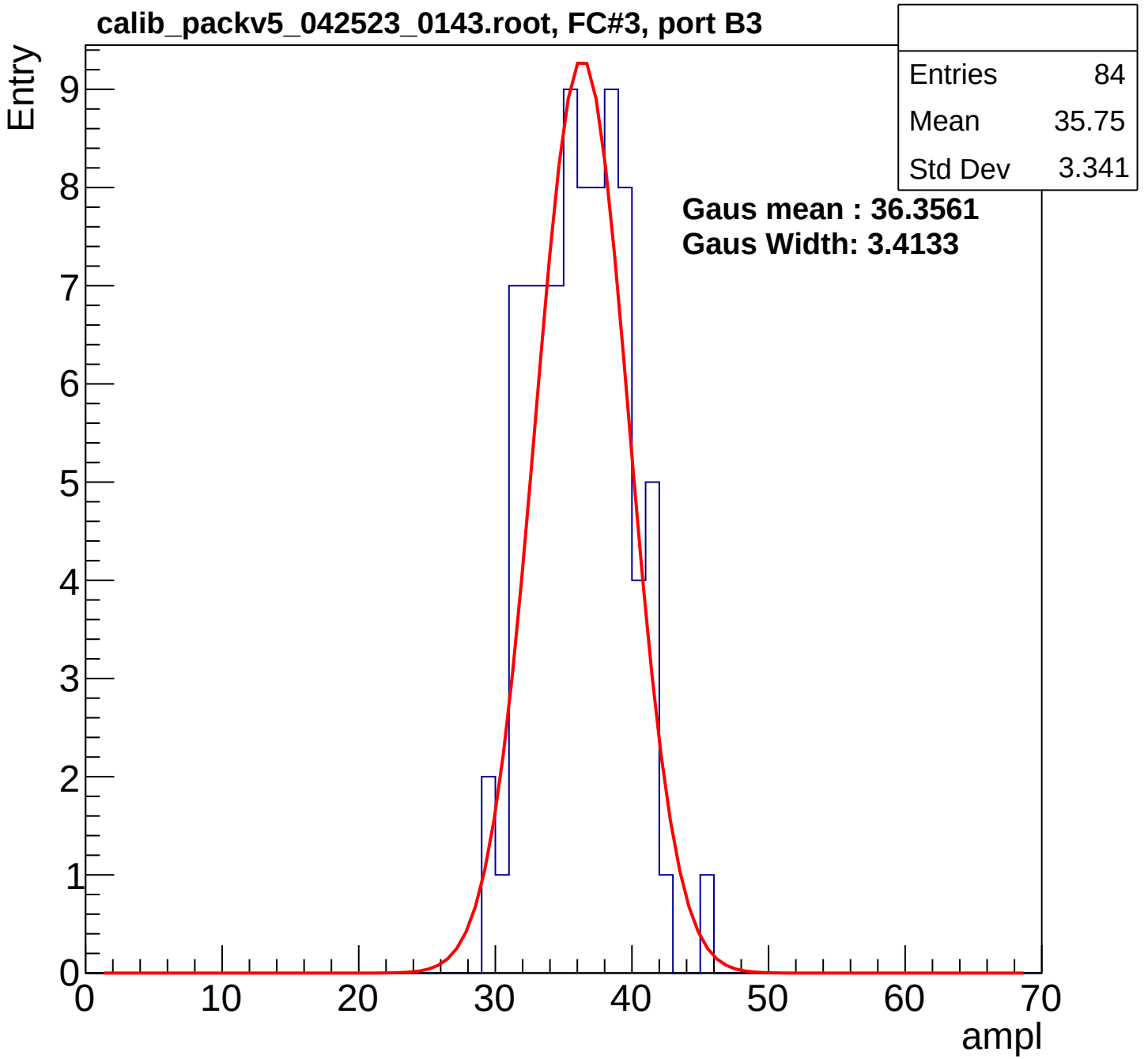
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch68, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 35.75
Std Dev 3.341
Gaus mean : 36.3561
Gaus Width: 3.4133
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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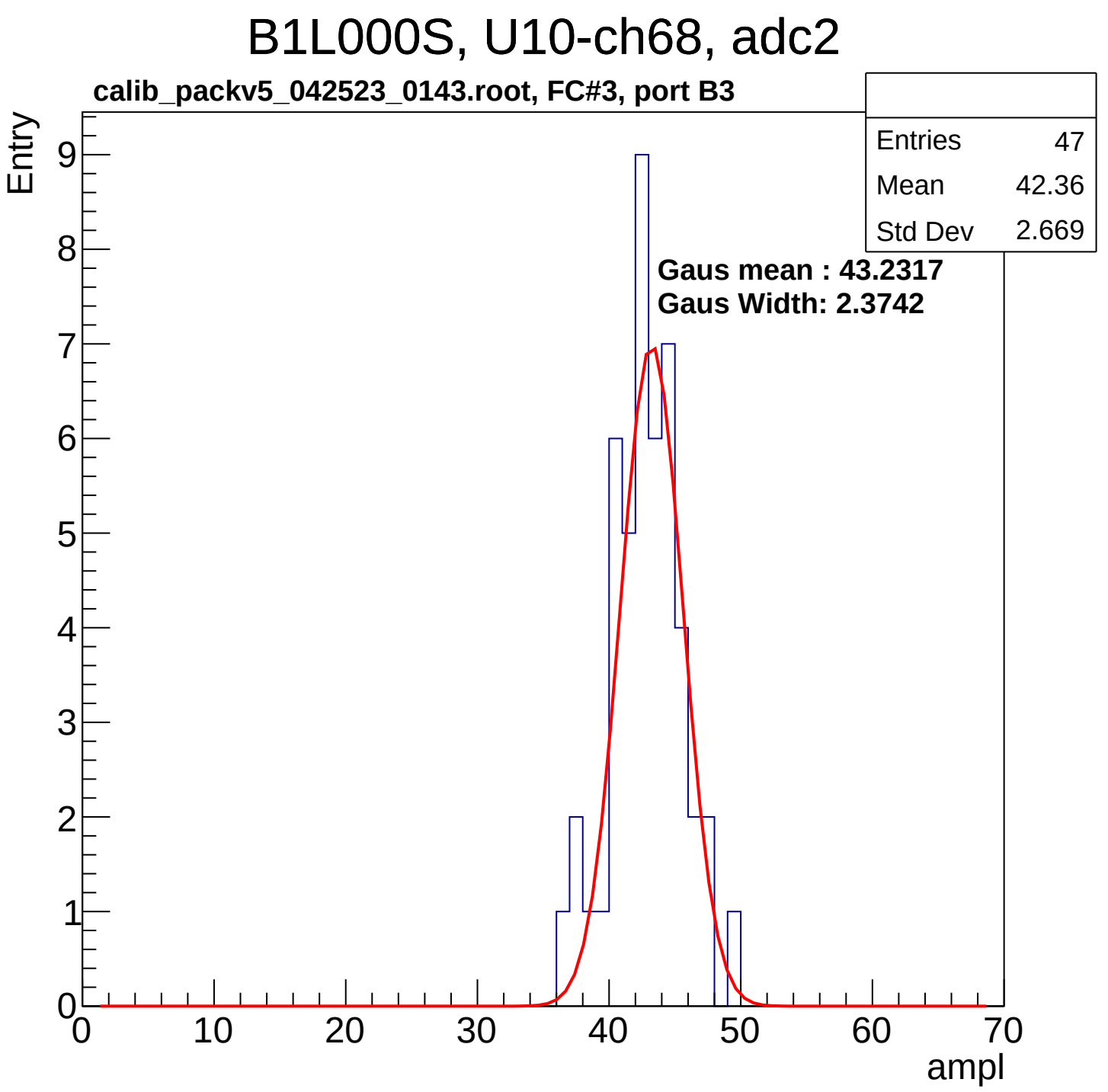
B1L000S, U10-ch68, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 42.36
Std Dev 2.669
Gaus mean : 43.2317
Gaus Width: 2.3742
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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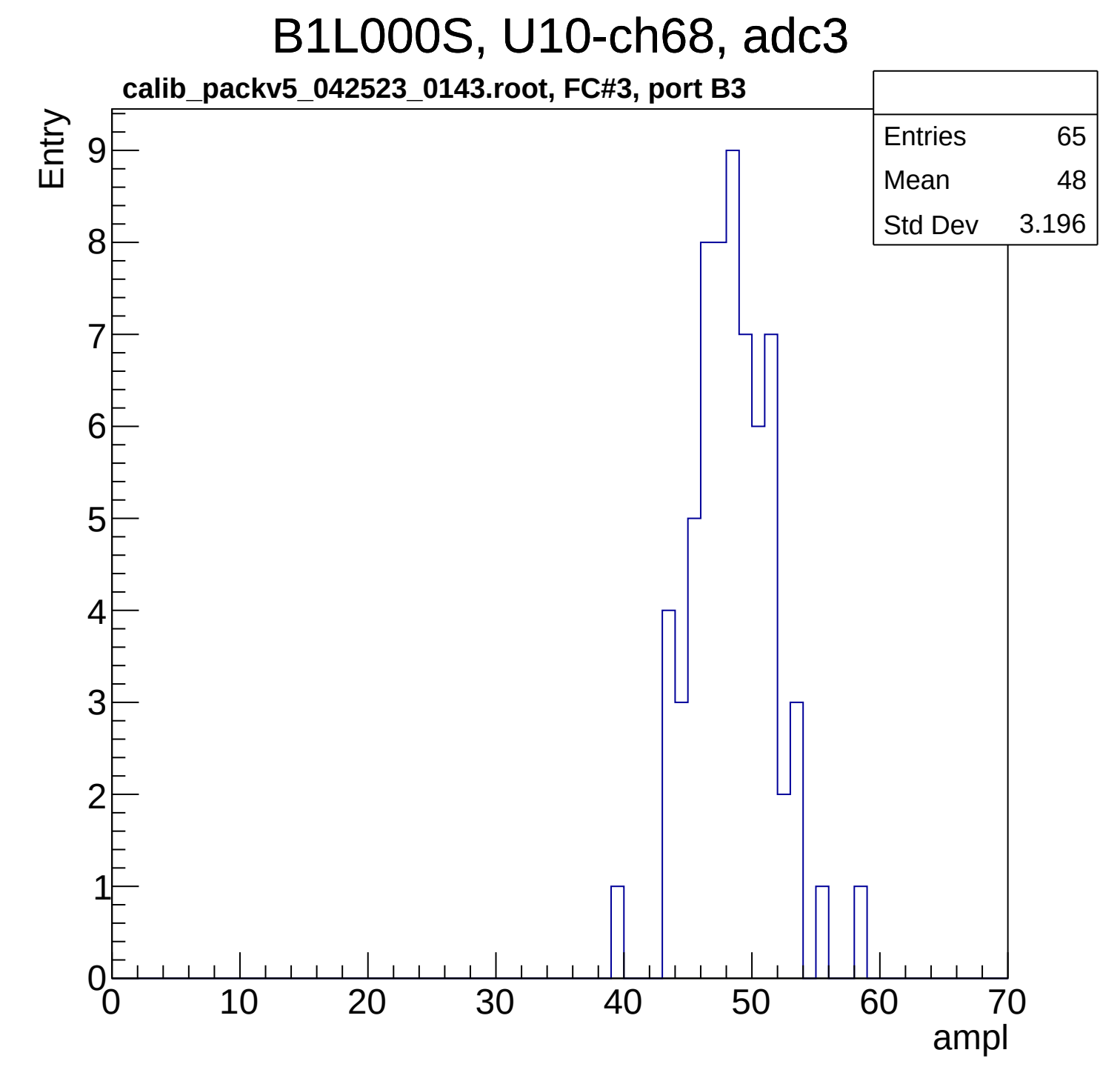
B1L000S, U10-ch68, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48
Std Dev 3.196
Entry
ampl
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4
5
6
7
8
9
0
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20
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60
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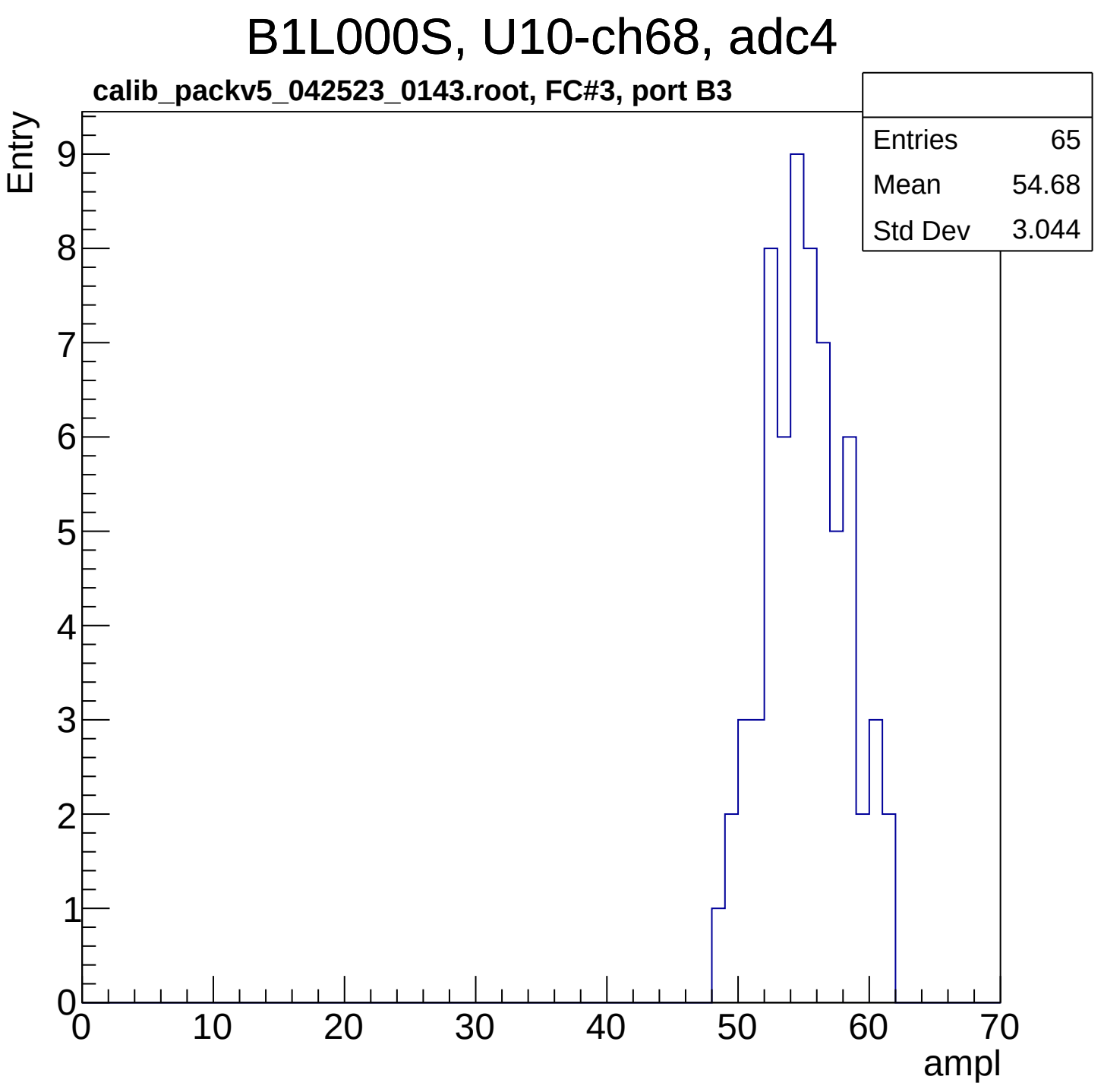
B1L000S, U10-ch68, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 54.68
Std Dev 3.044
Entry
ampl
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3
4
5
6
7
8
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0
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60
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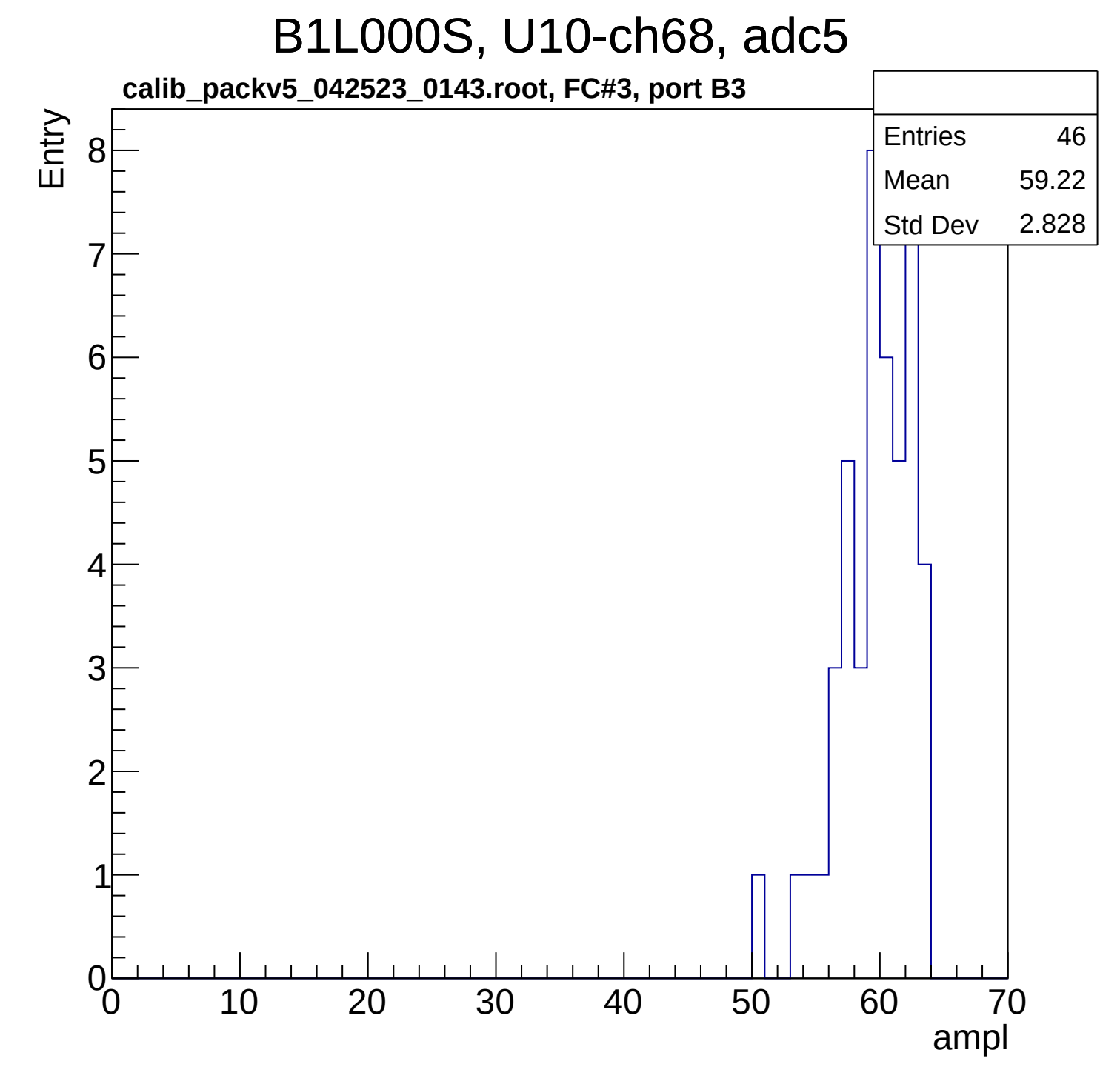
B1L000S, U10-ch68, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 59.22
Std Dev 2.828
Entry
ampl
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1
2
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4
5
6
7
8
0
10
20
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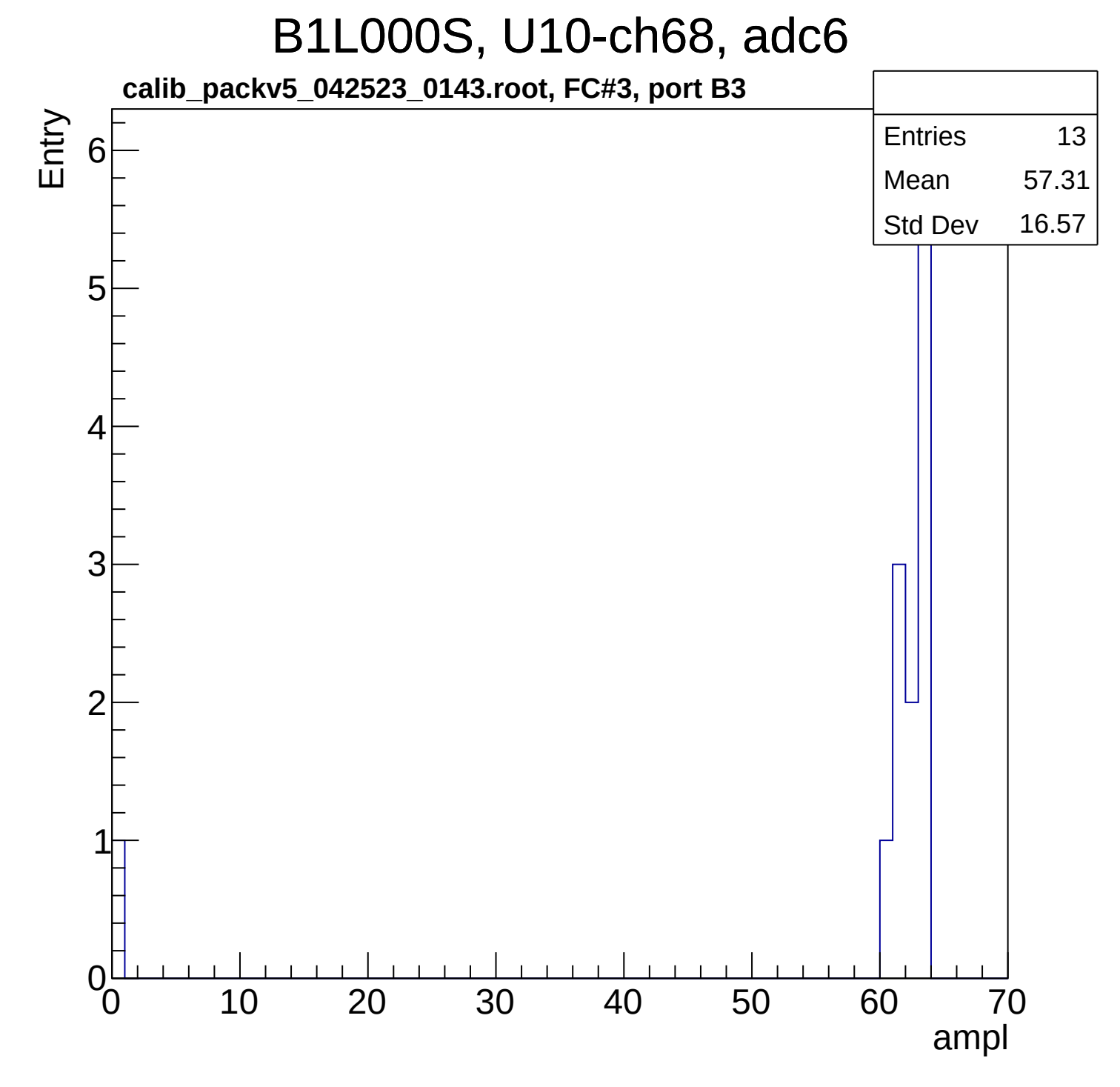
40
50
60
70


B1L000S, U10-ch68, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.31
Std Dev 16.57
Entry
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2
3
4
5
6
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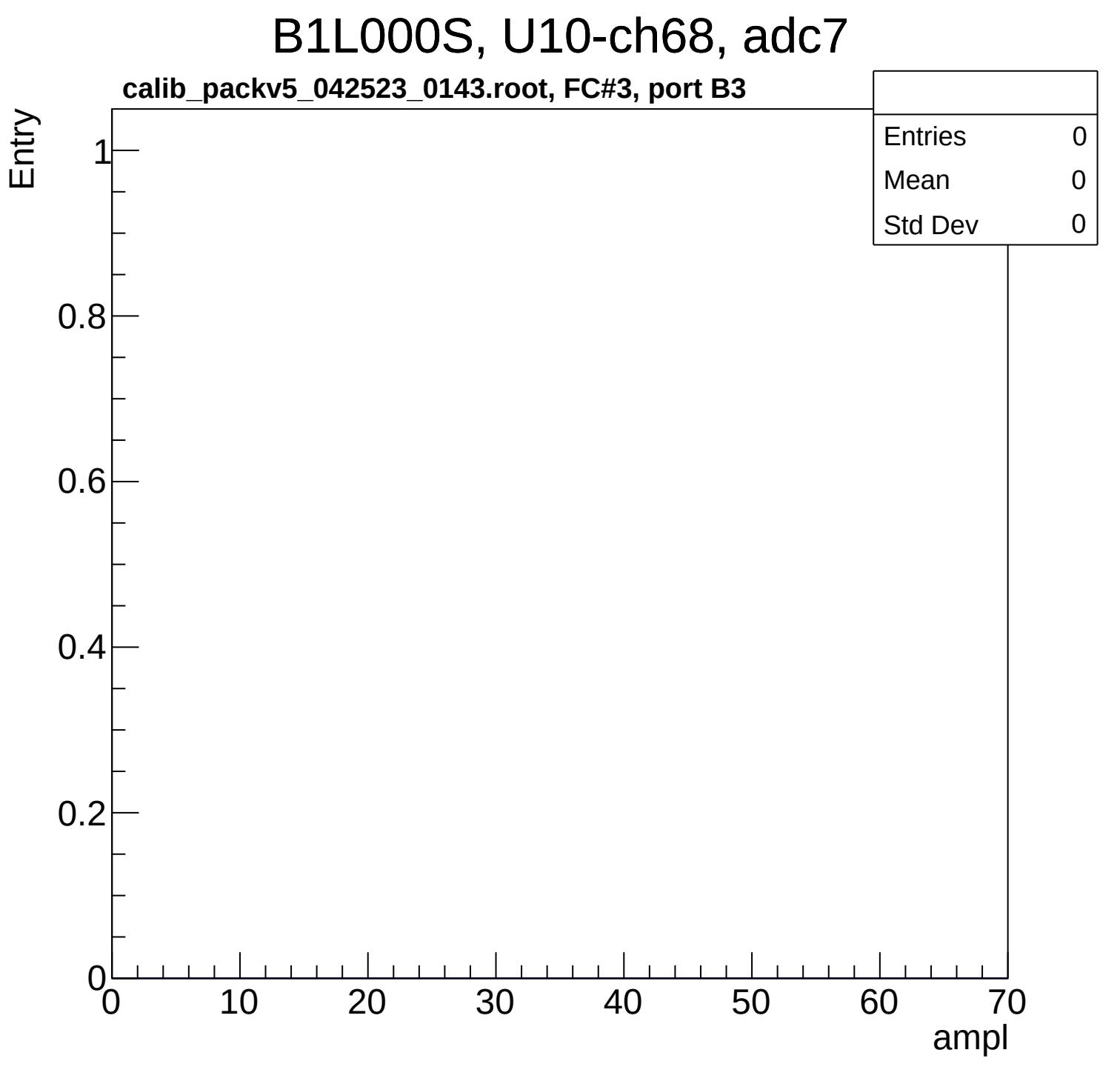
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch68, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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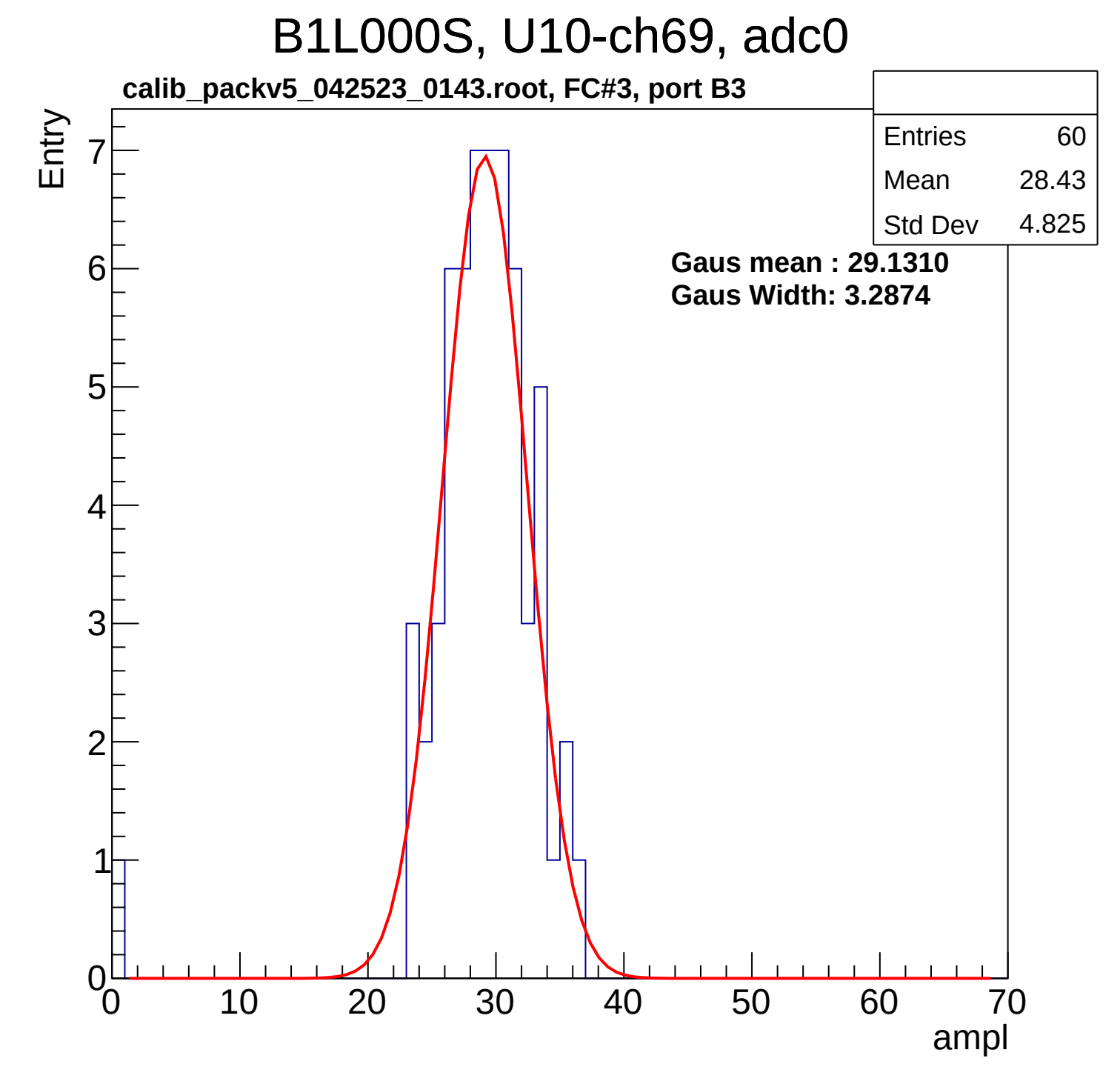
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch69, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.43
Std Dev 4.825
Gaus mean : 29.1310
Gaus Width: 3.2874
Entry
ampl
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3
4
5
6
7
0
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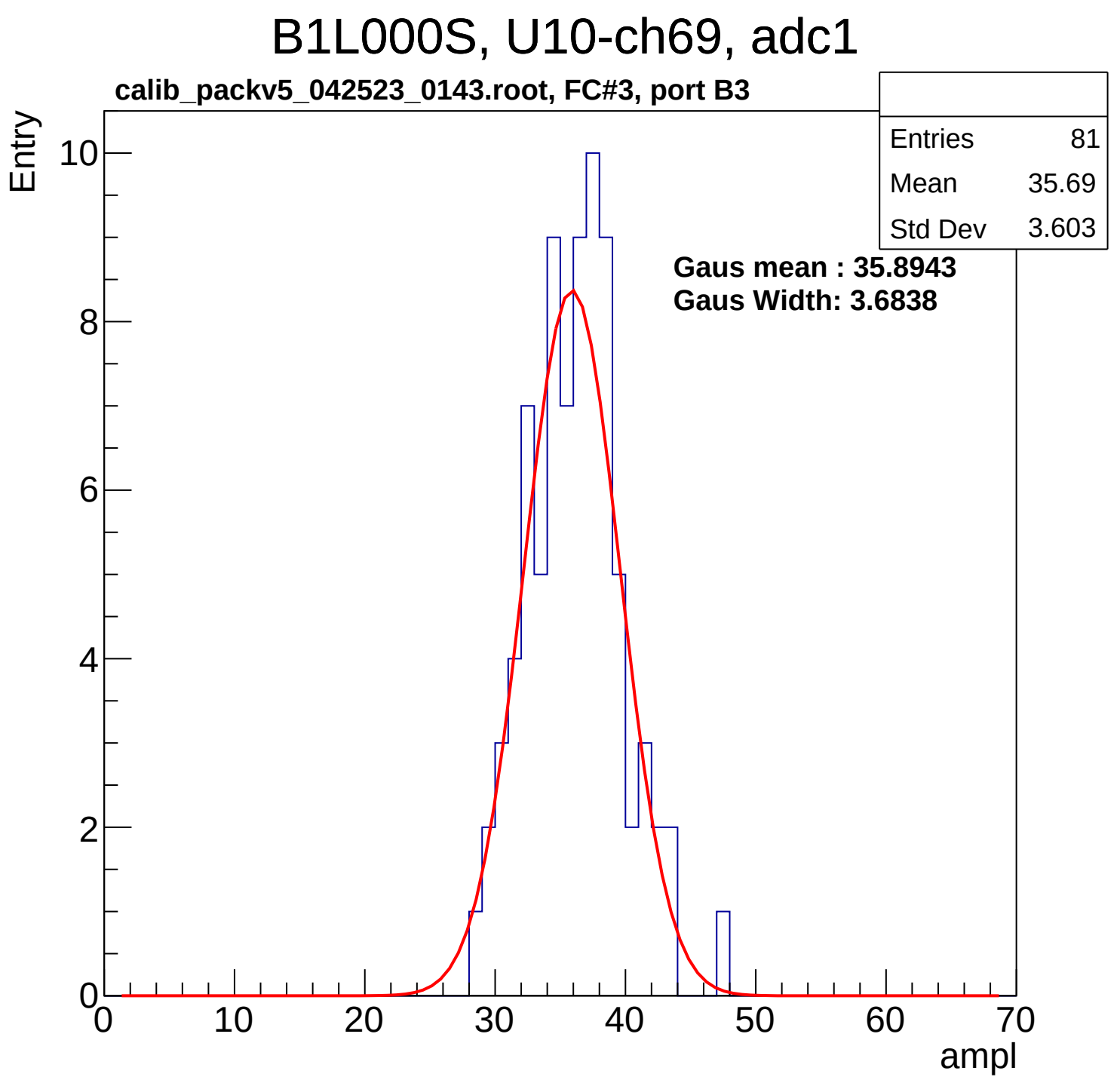
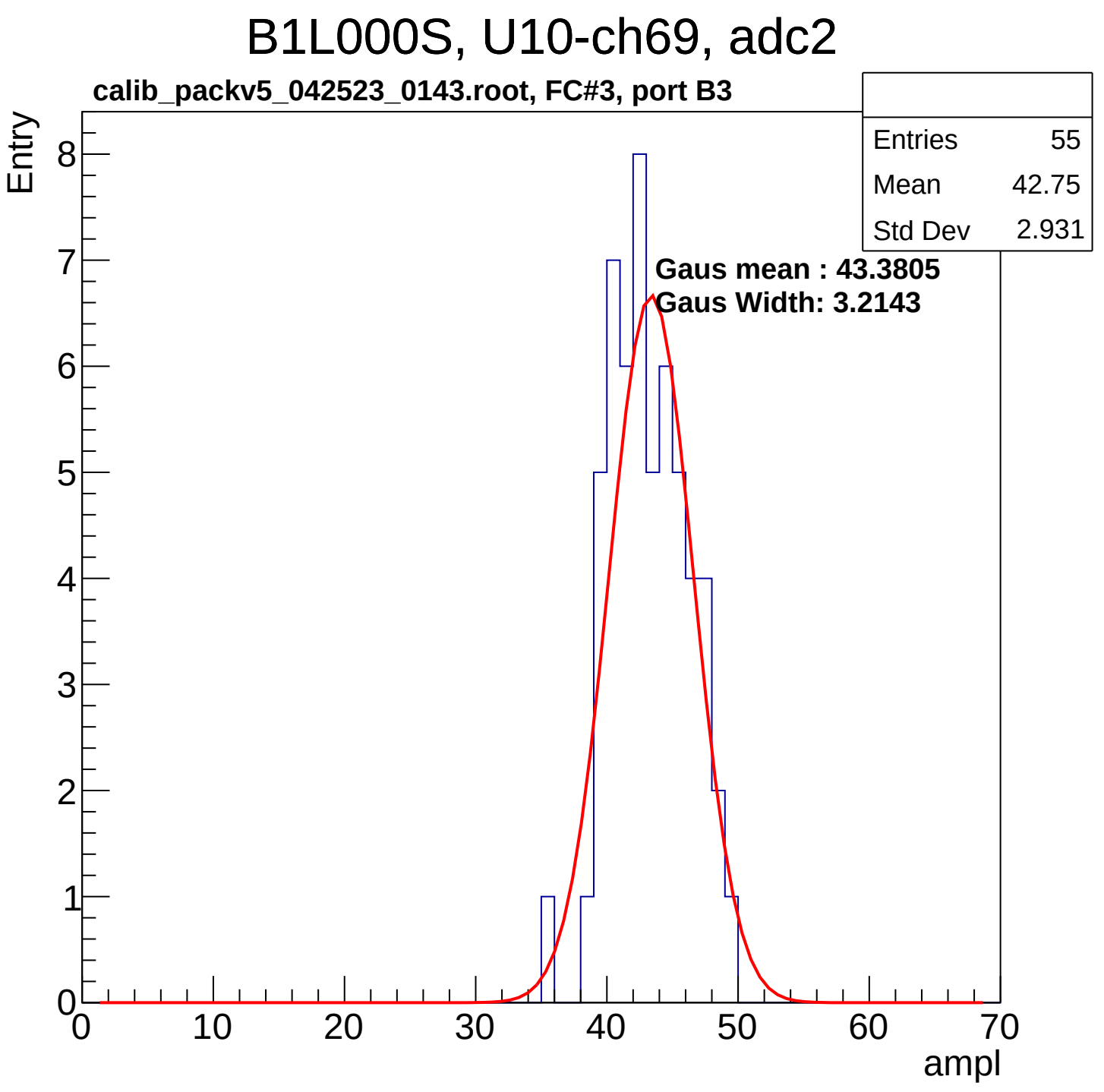

B1L000S, U10-ch69, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 35.69
Std Dev 3.603
Gaus mean : 35.8943
Gaus Width: 3.6838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch69, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 42.75
Std Dev 2.931
Gaus mean : 43.3805
Gaus Width: 3.2143
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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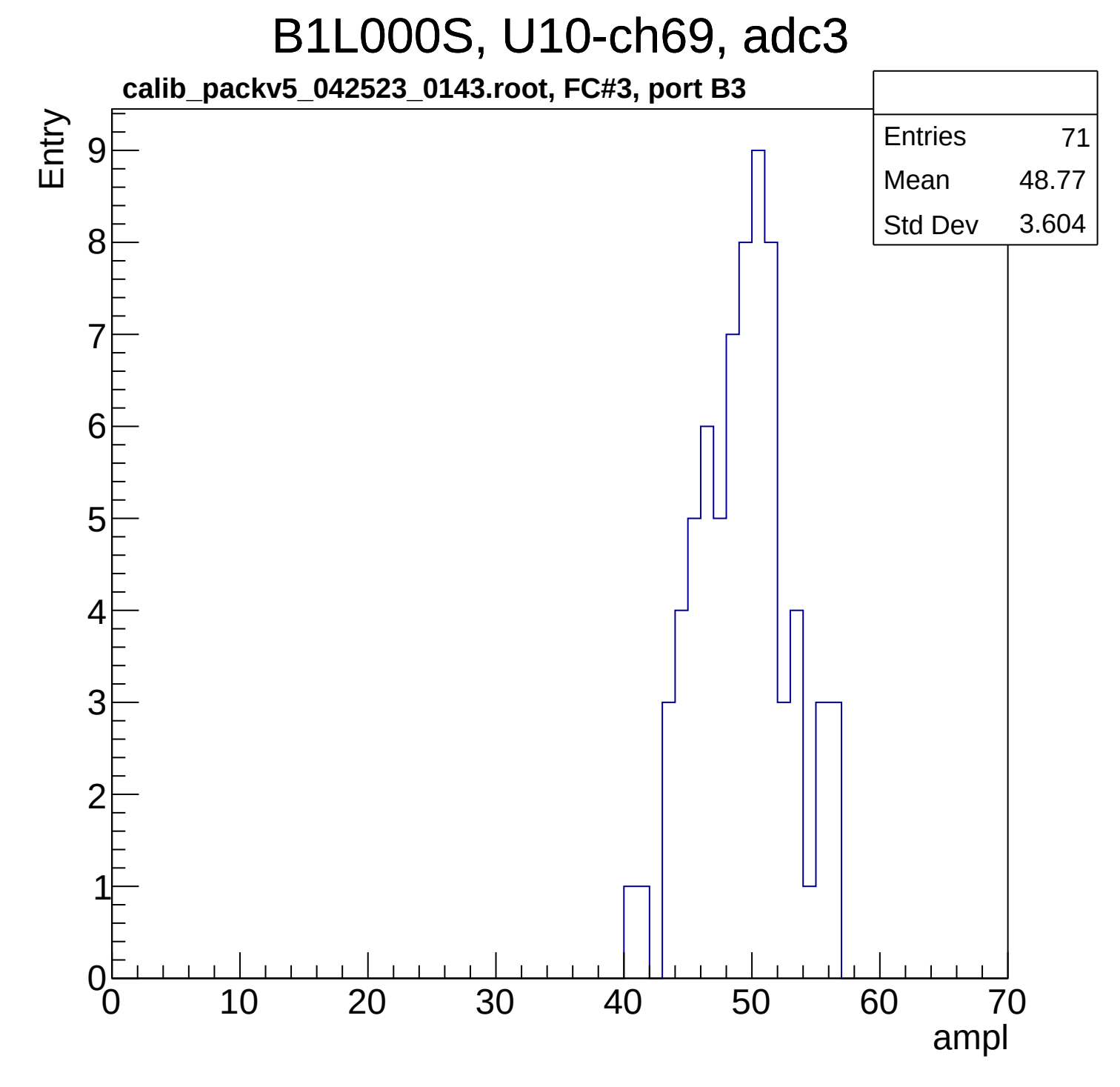
B1L000S, U10-ch69, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 48.77
Std Dev 3.604
Entry
ampl
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2
3
4
5
6
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8
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0
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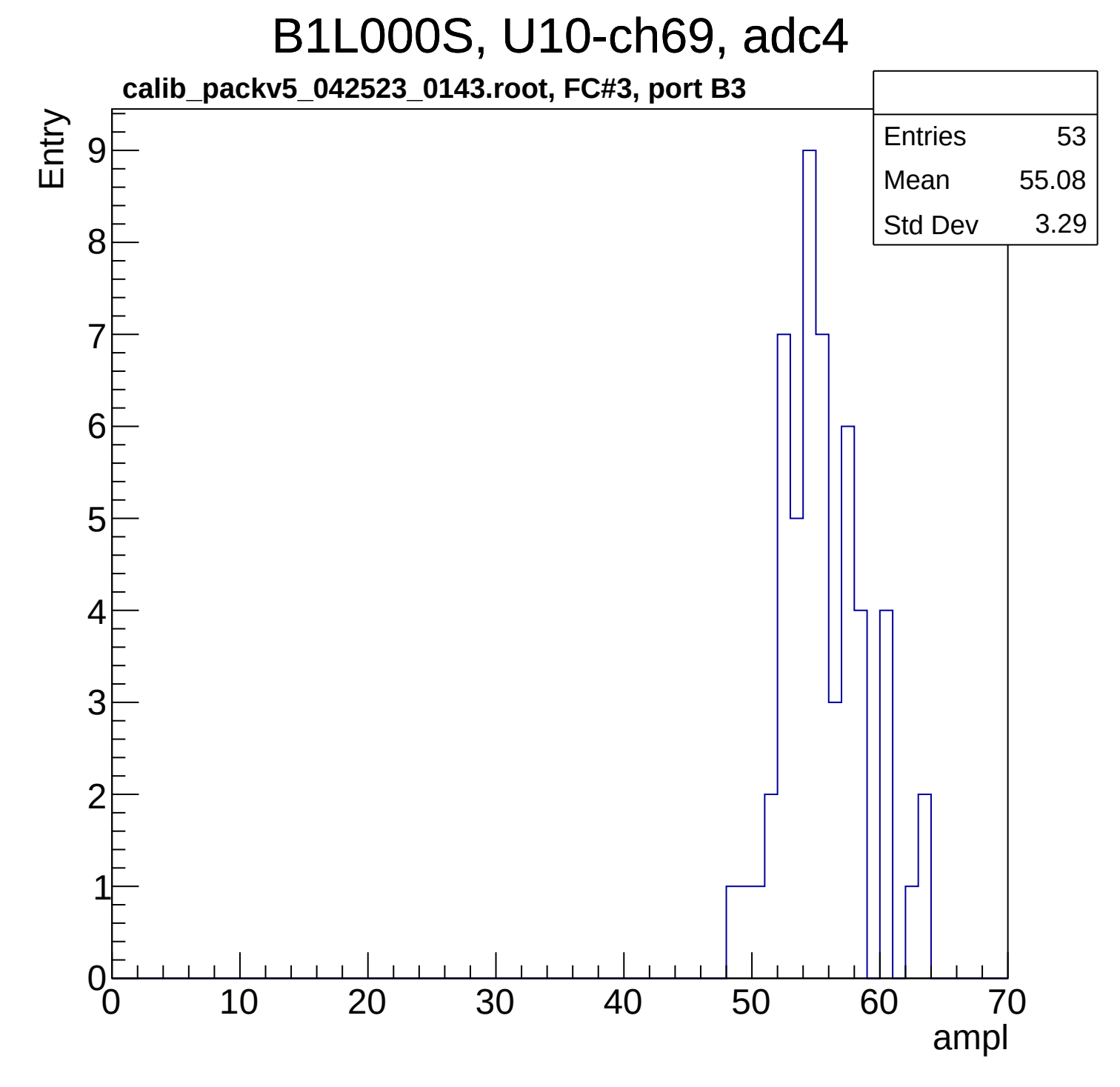
B1L000S, U10-ch69, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 55.08
Std Dev 3.29
Entry
ampl
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40
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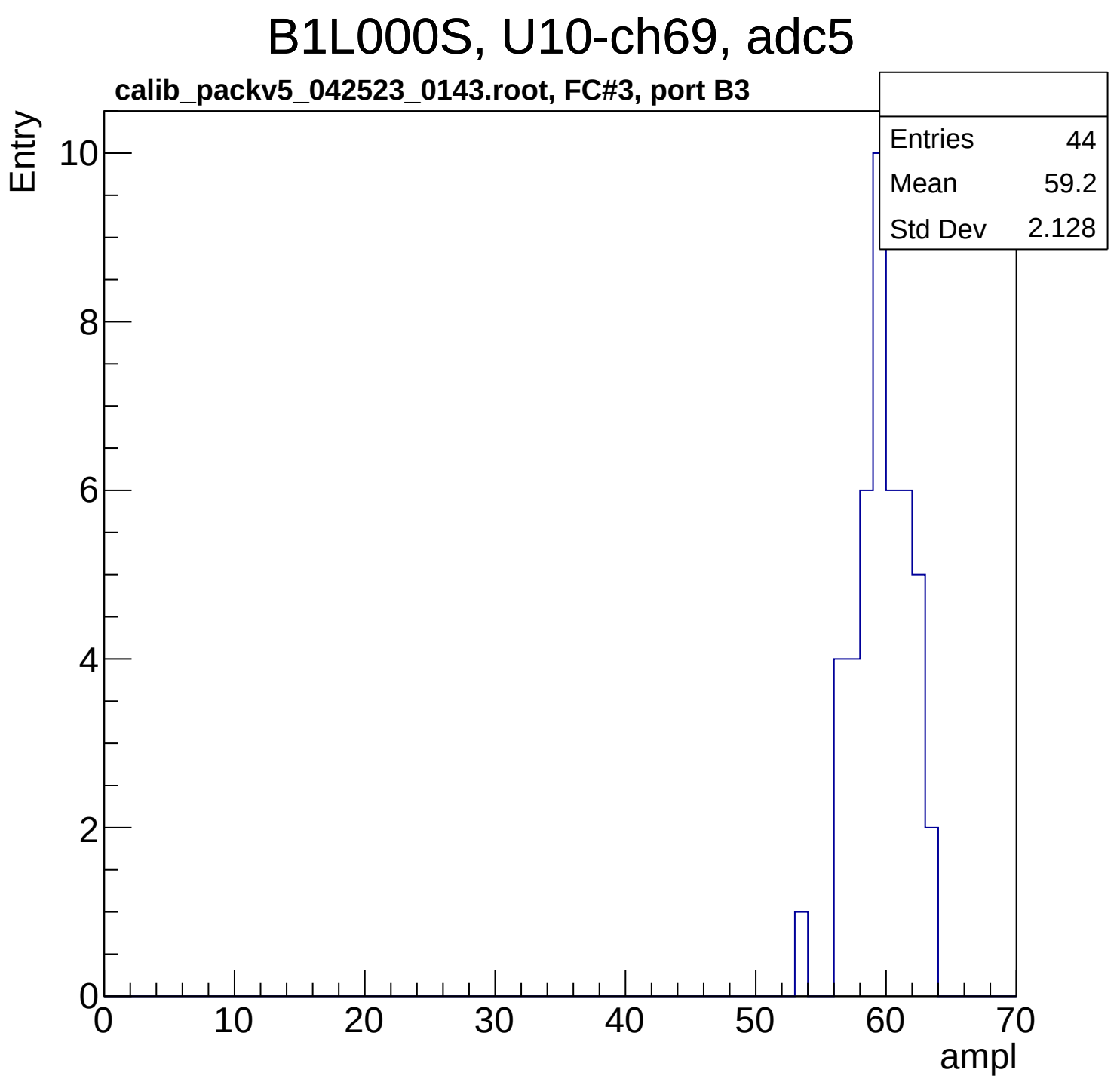
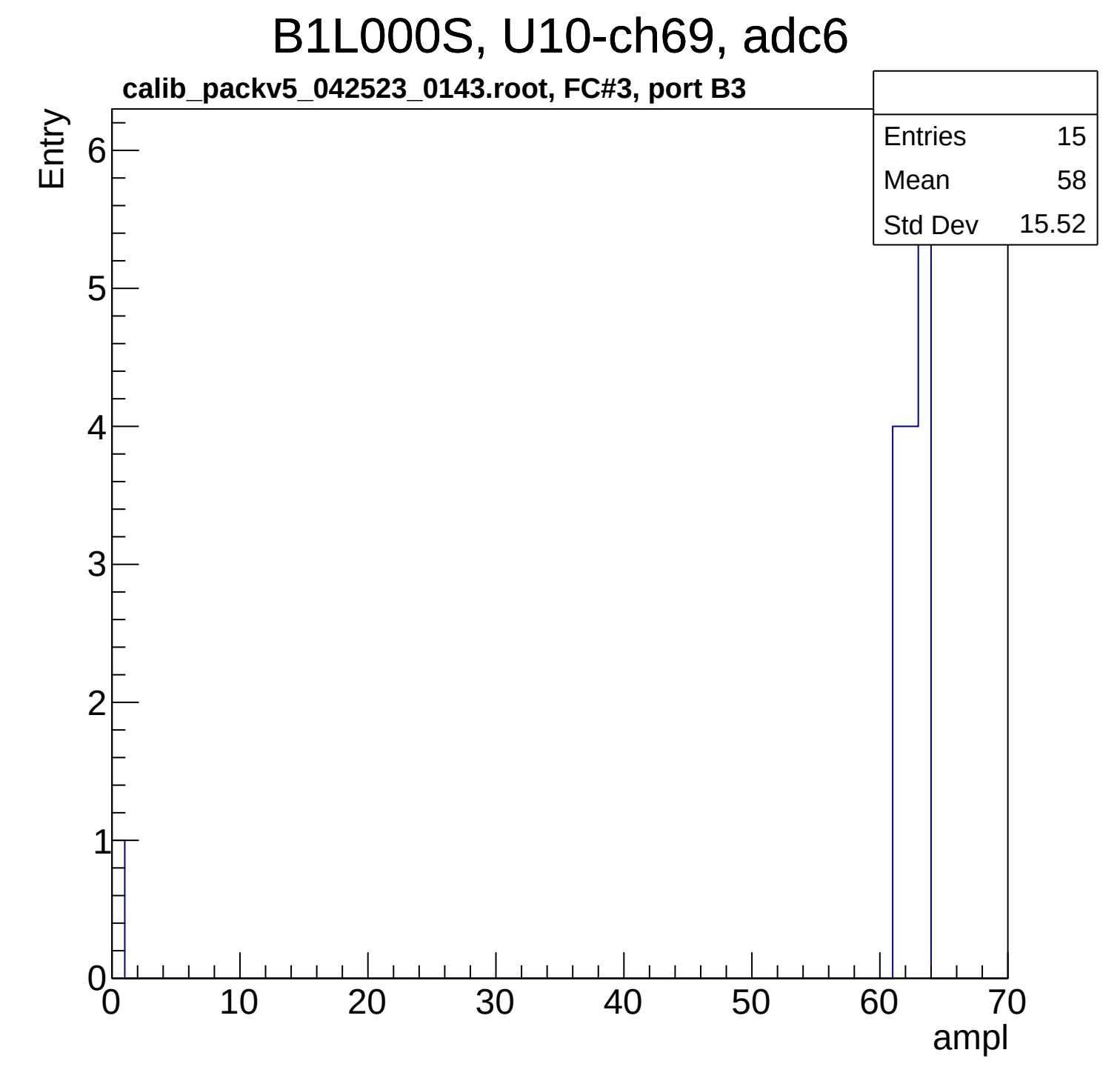

B1L000S, U10-ch69, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.2
Std Dev 2.128
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch69, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 58
Std Dev 15.52
Entry
0
1
2
3
4
5
6
0
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20
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50
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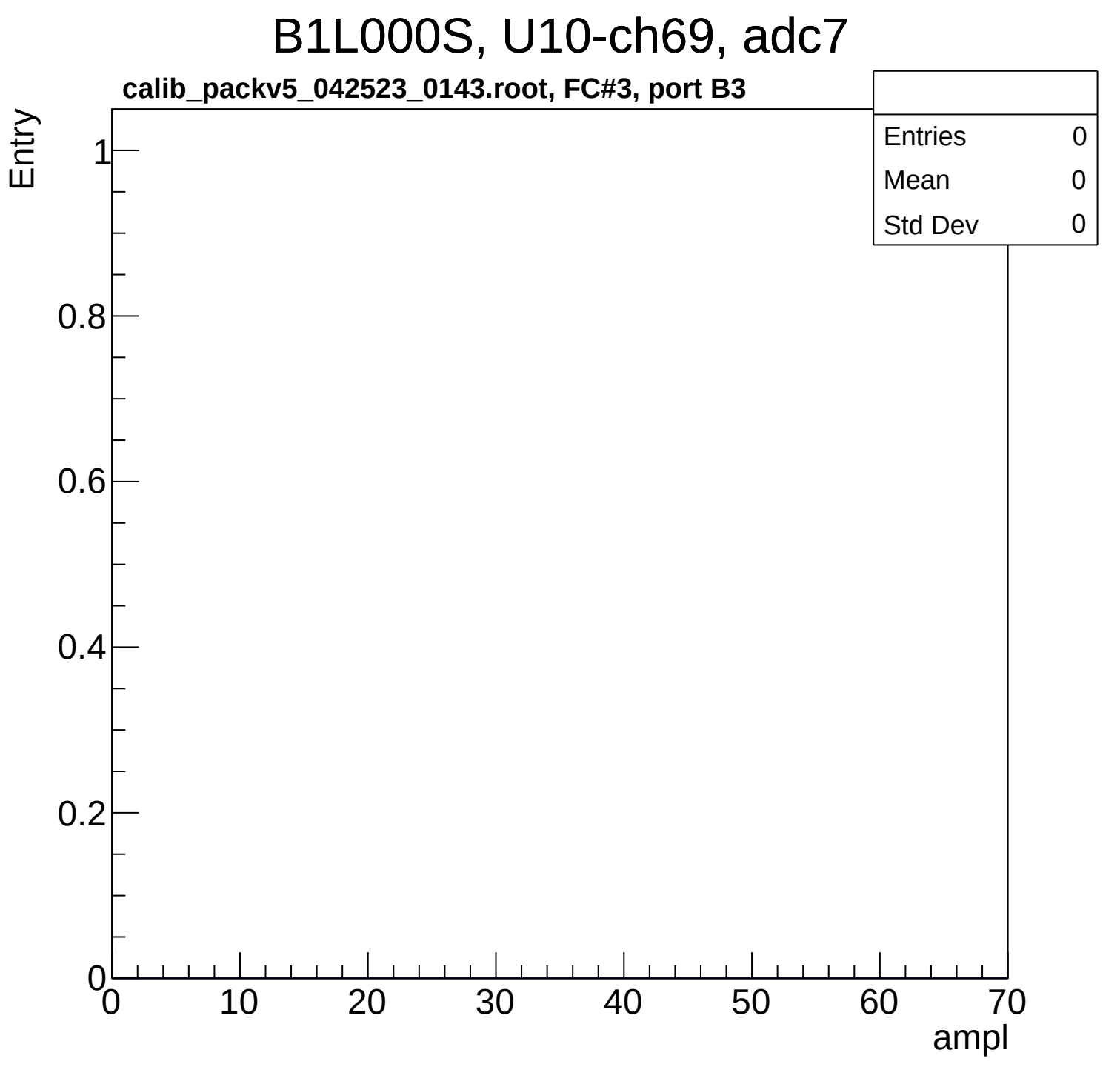
70
ampl


B1L000S, U10-ch69, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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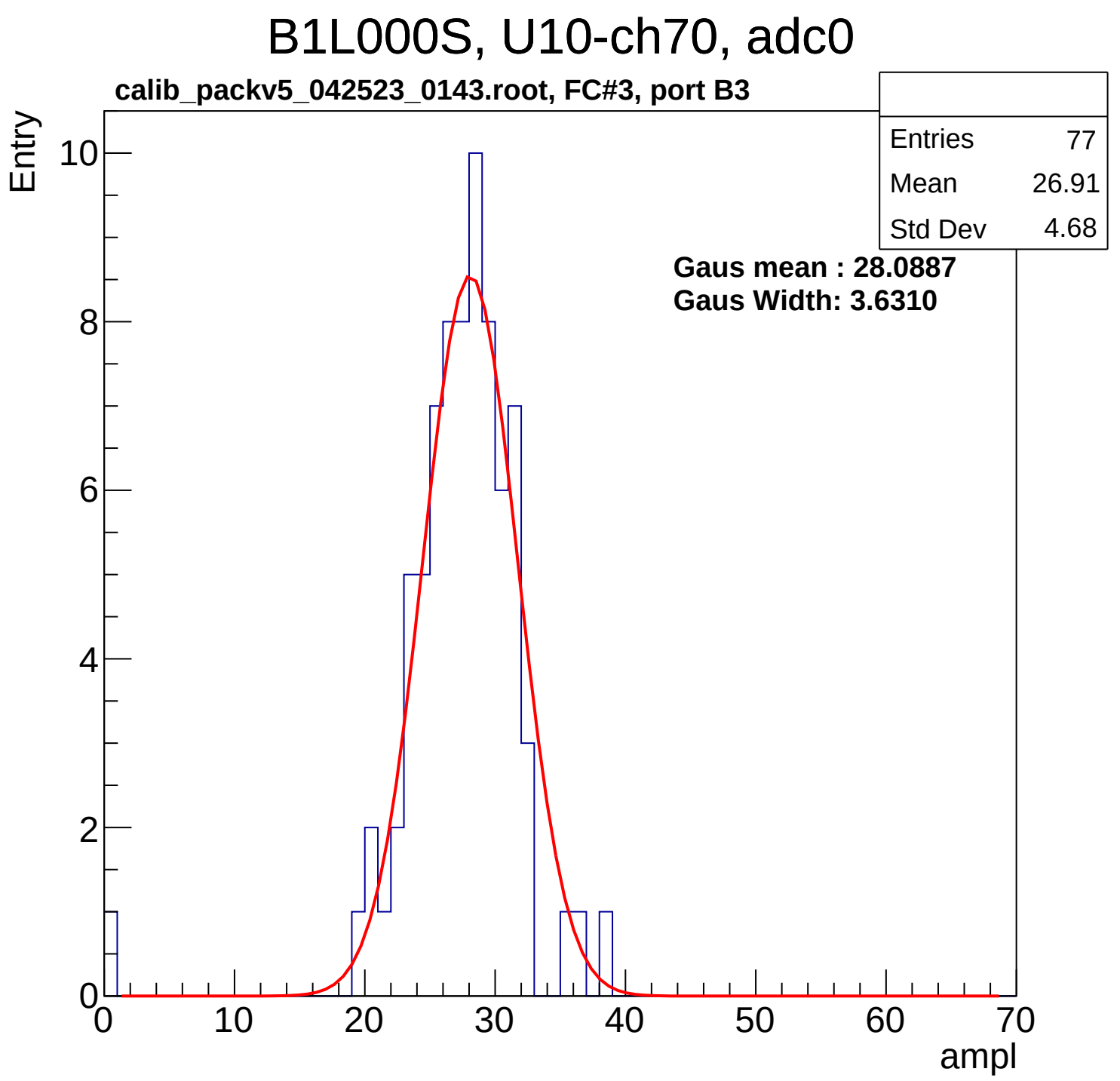
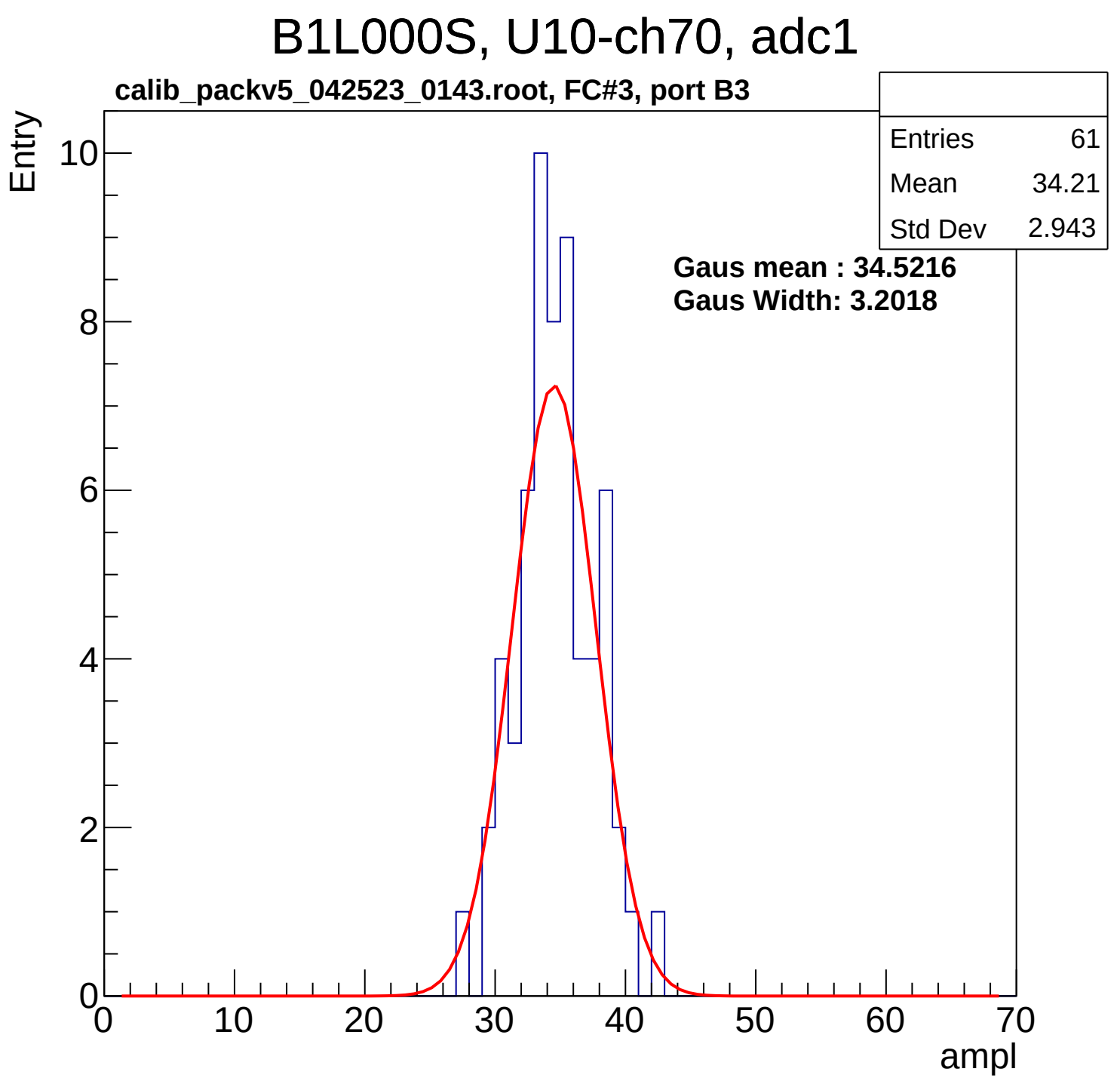
50
60
70
ampl


B1L000S, U10-ch70, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 26.91
Std Dev 4.68
Gaus mean : 28.0887
Gaus Width: 3.6310
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch70, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 34.21
Std Dev 2.943
Gaus mean : 34.5216
Gaus Width: 3.2018
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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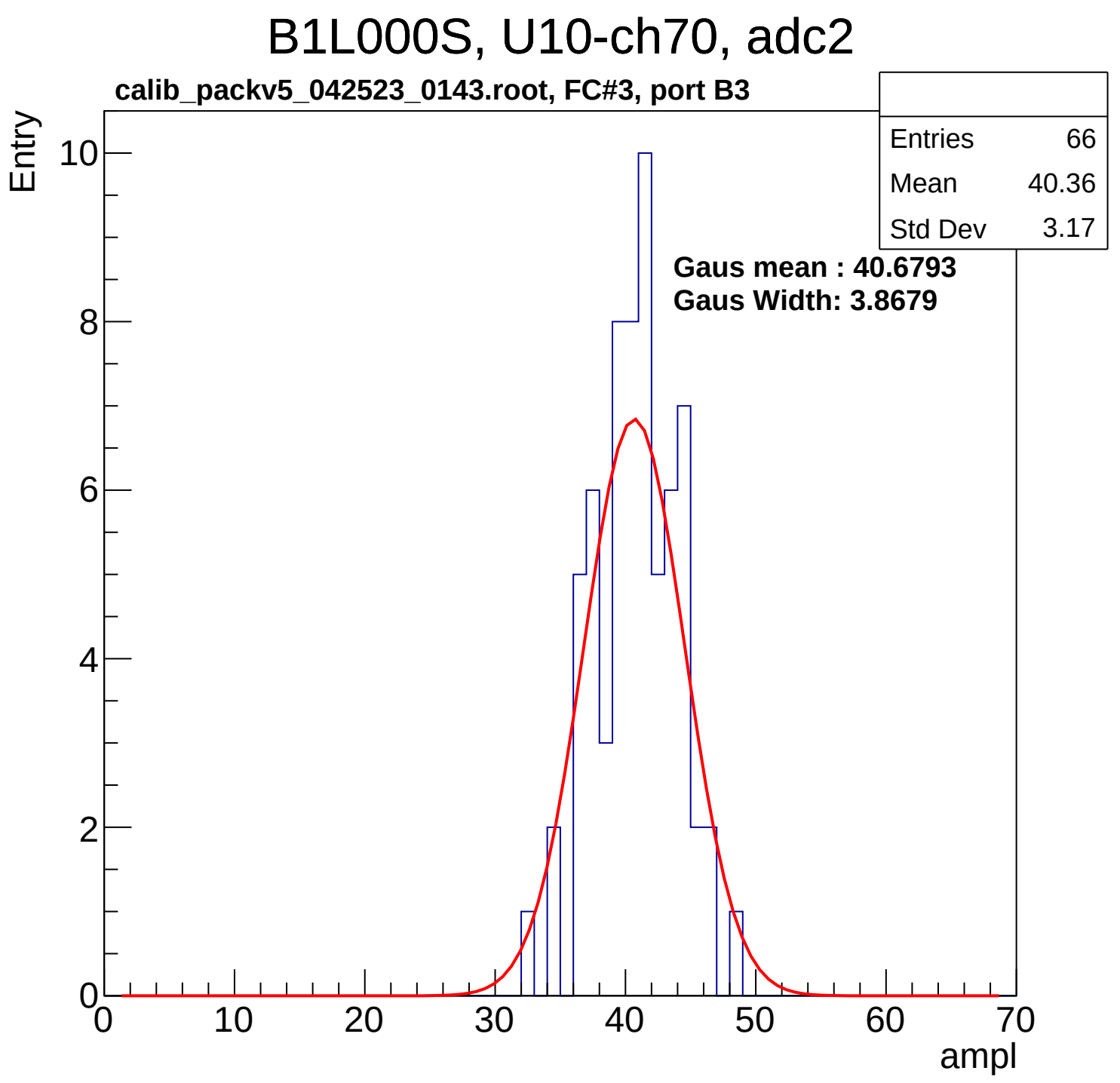
B1L000S, U10-ch70, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 40.36
Std Dev 3.17
Gaus mean : 40.6793
Gaus Width: 3.8679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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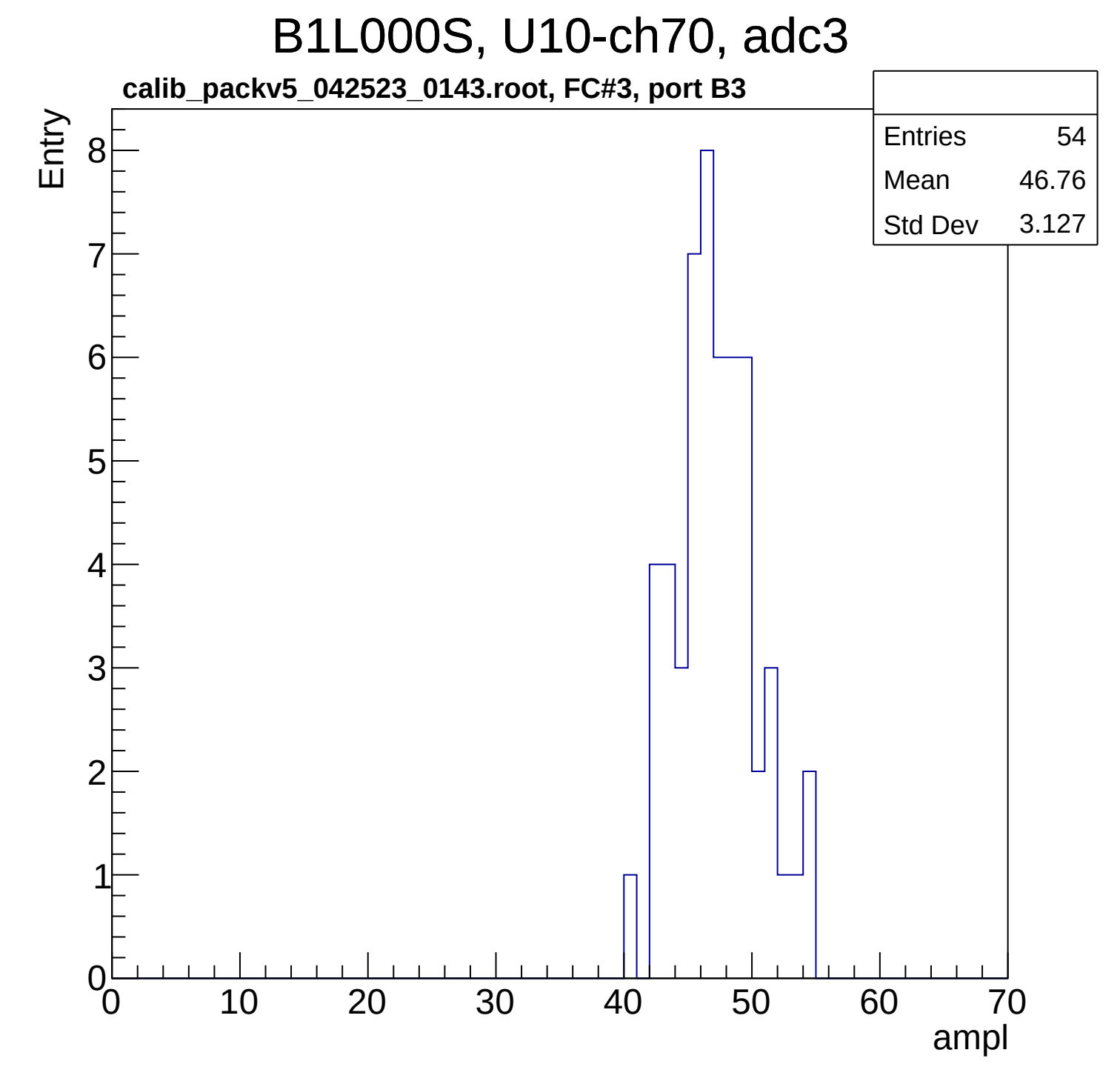
B1L000S, U10-ch70, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 46.76
Std Dev 3.127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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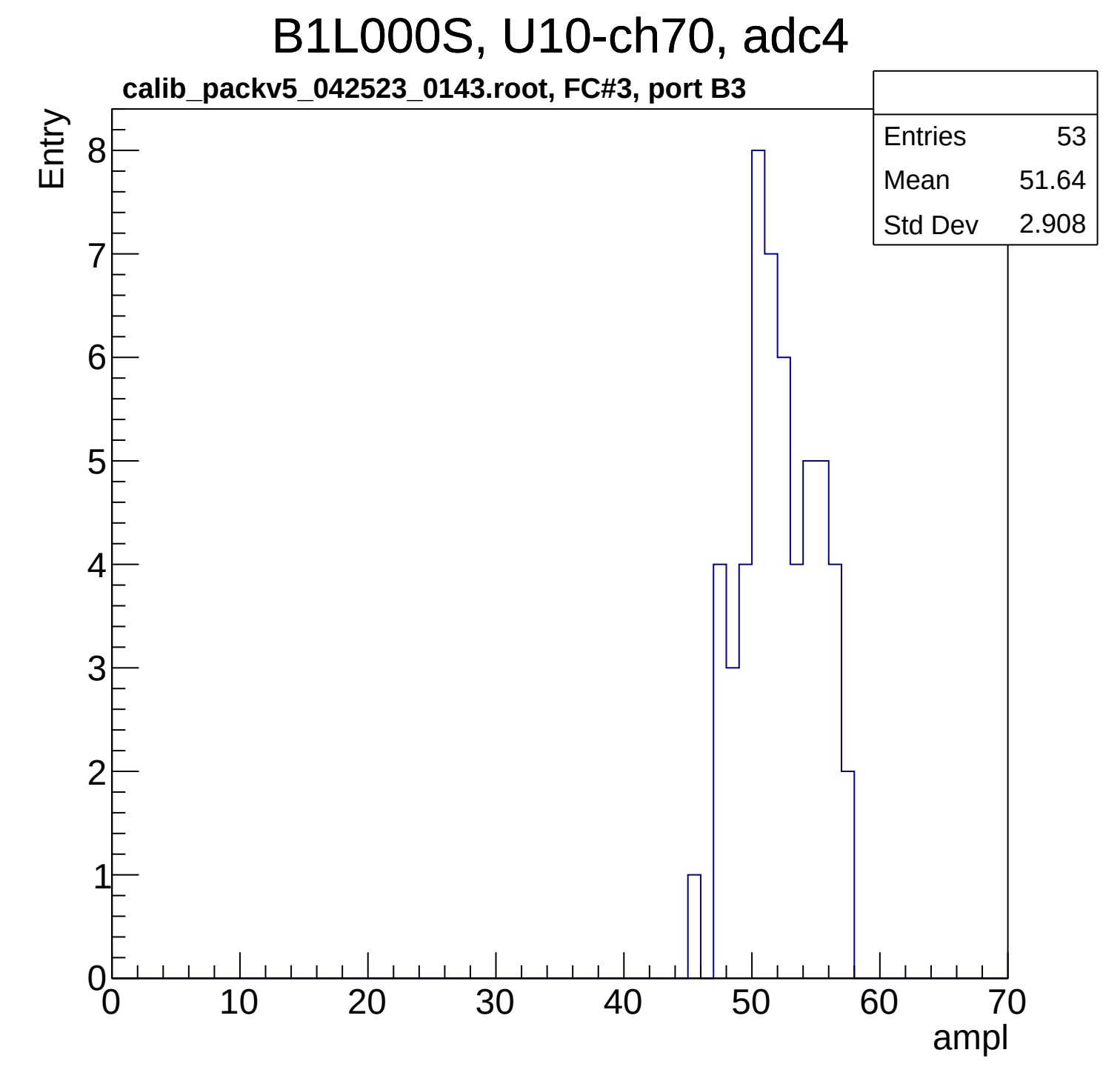
B1L000S, U10-ch70, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 51.64
Std Dev 2.908
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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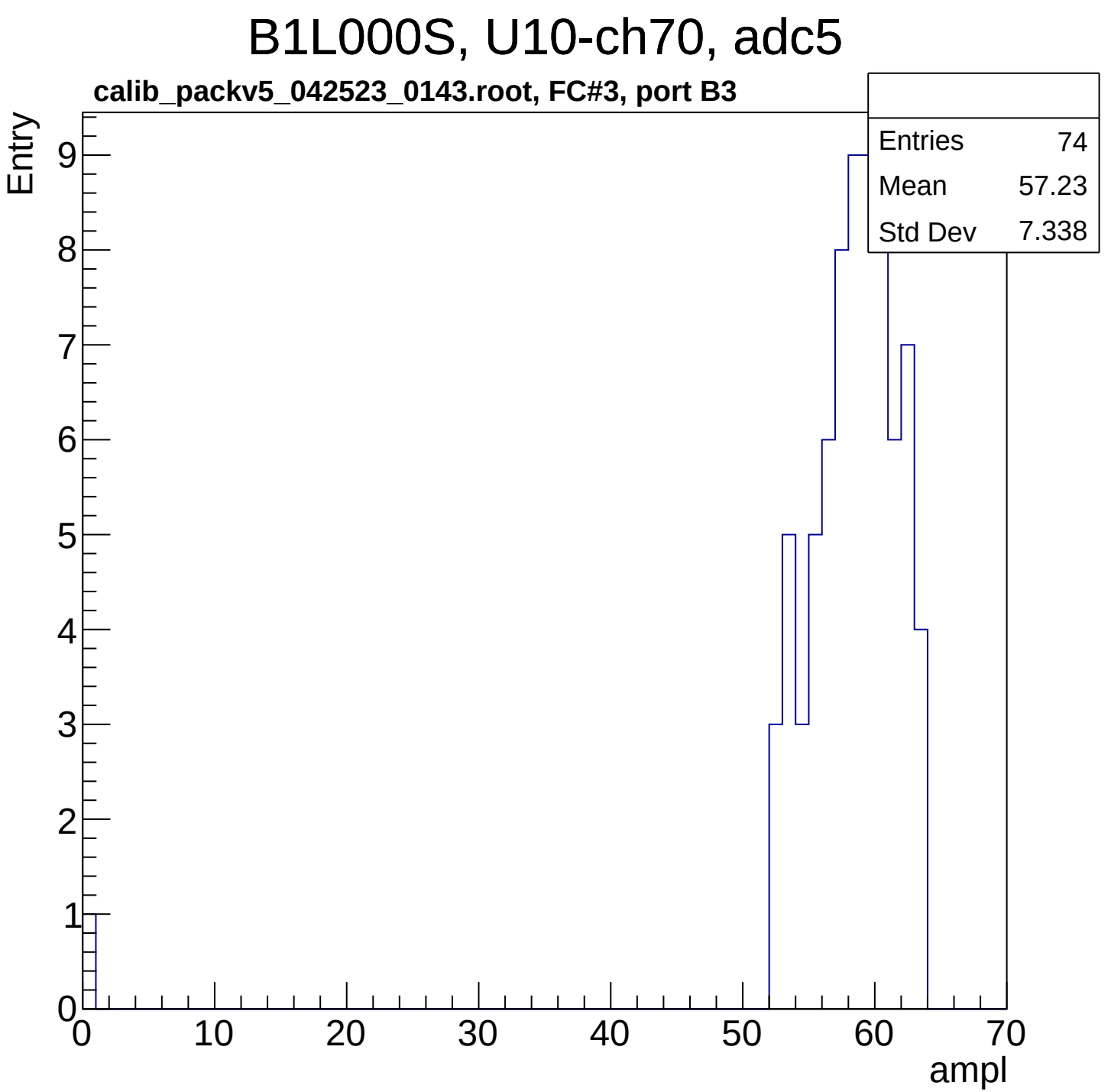
B1L000S, U10-ch70, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 57.23
Std Dev 7.338
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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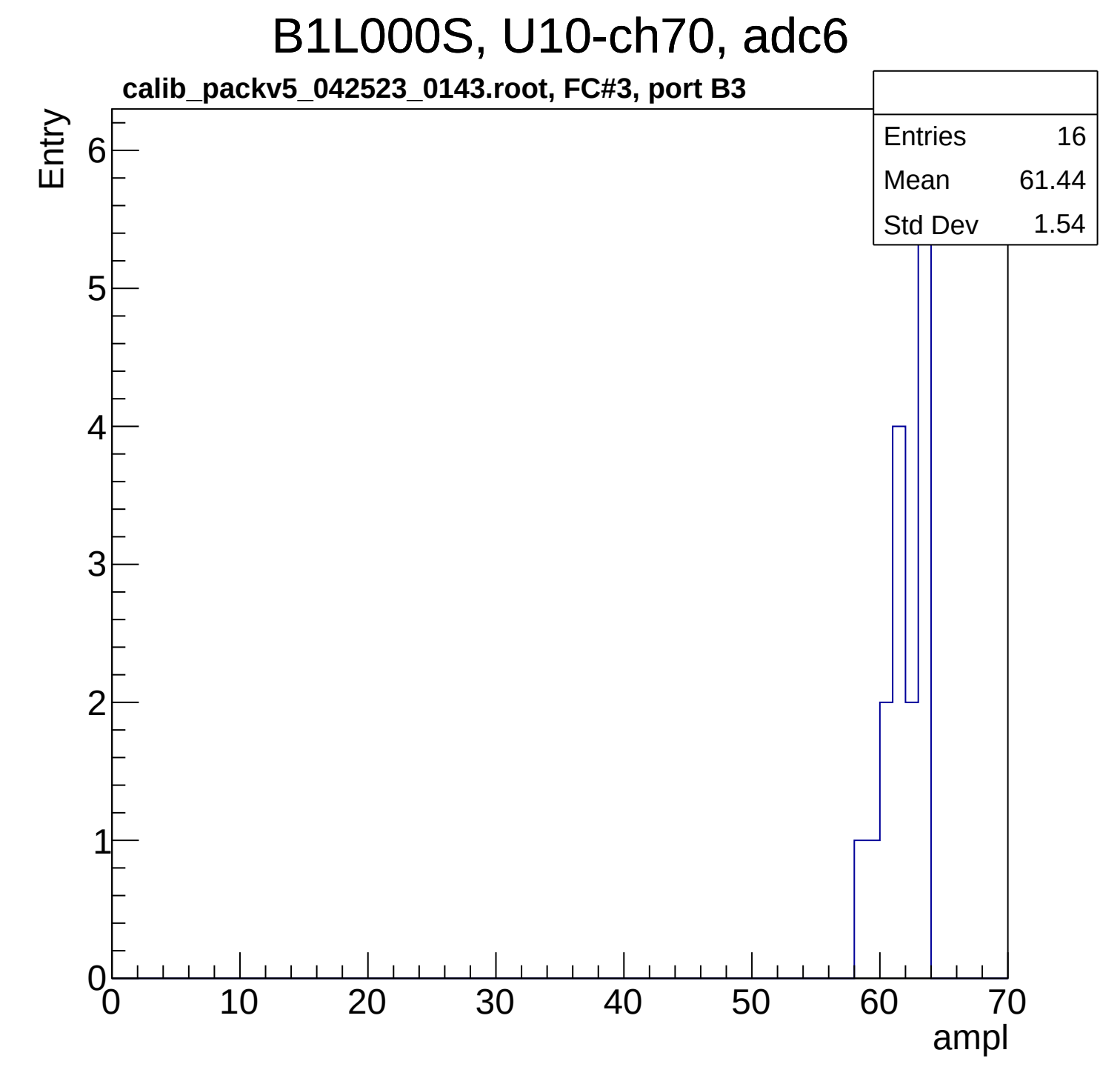
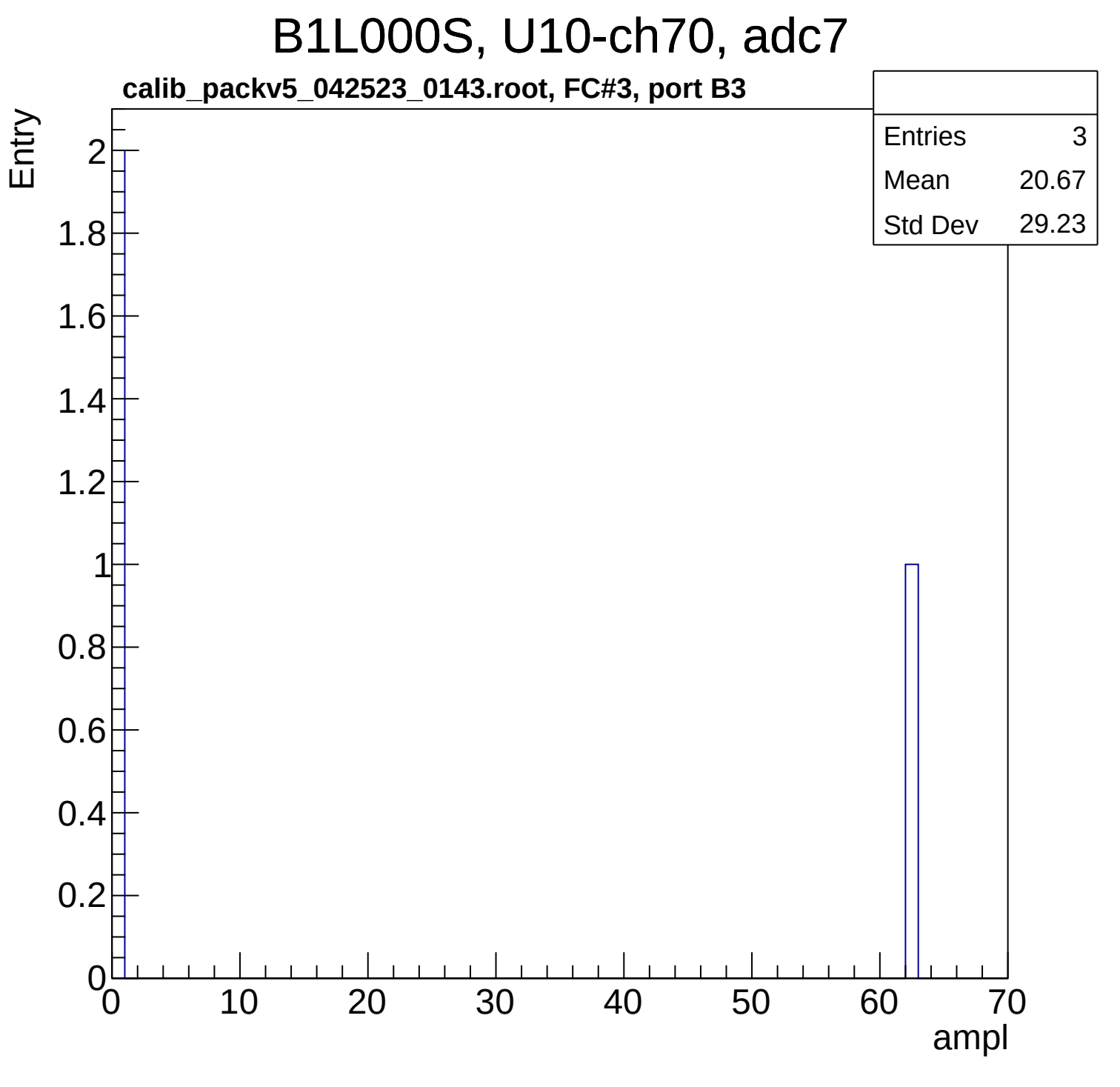
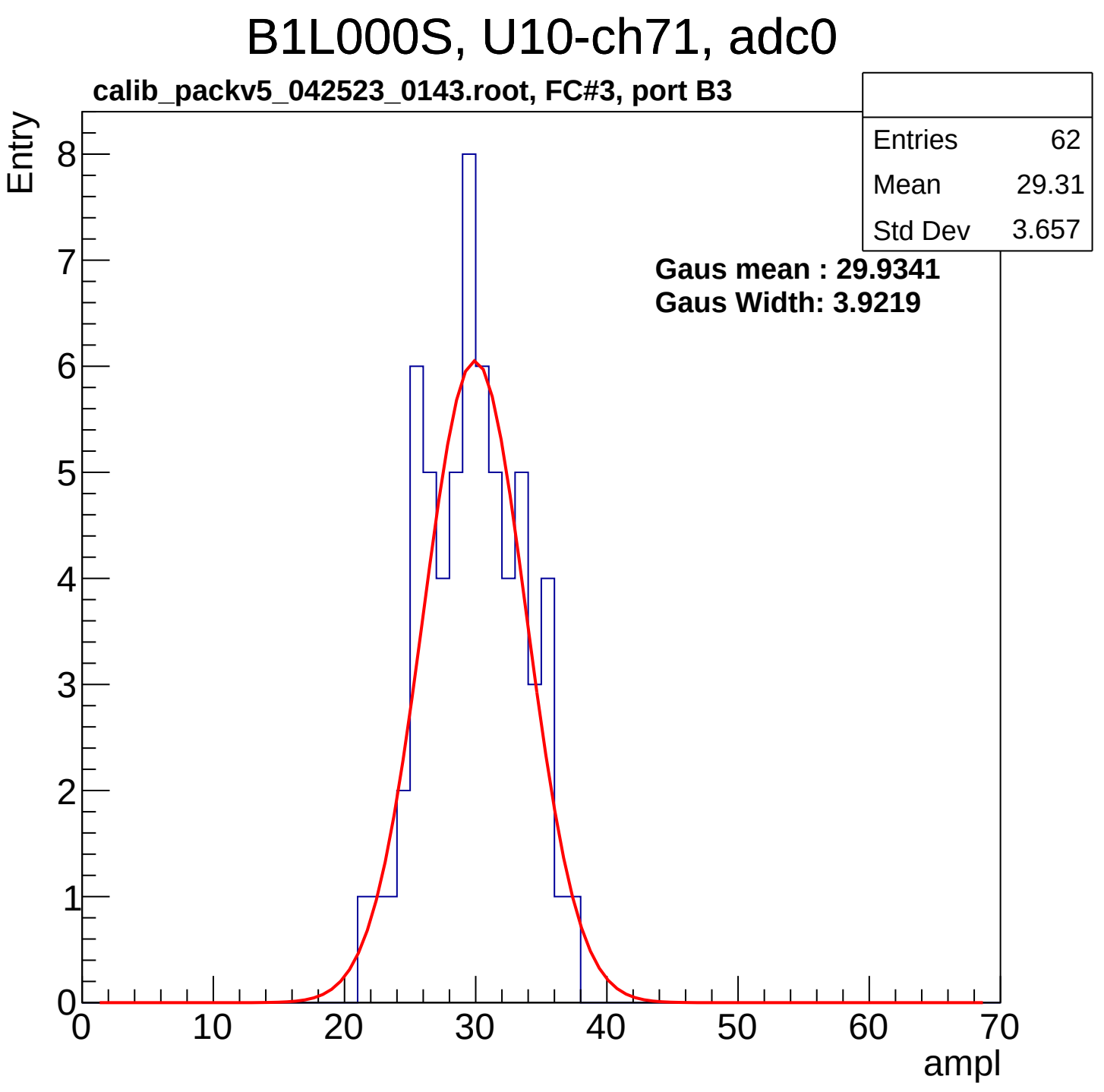
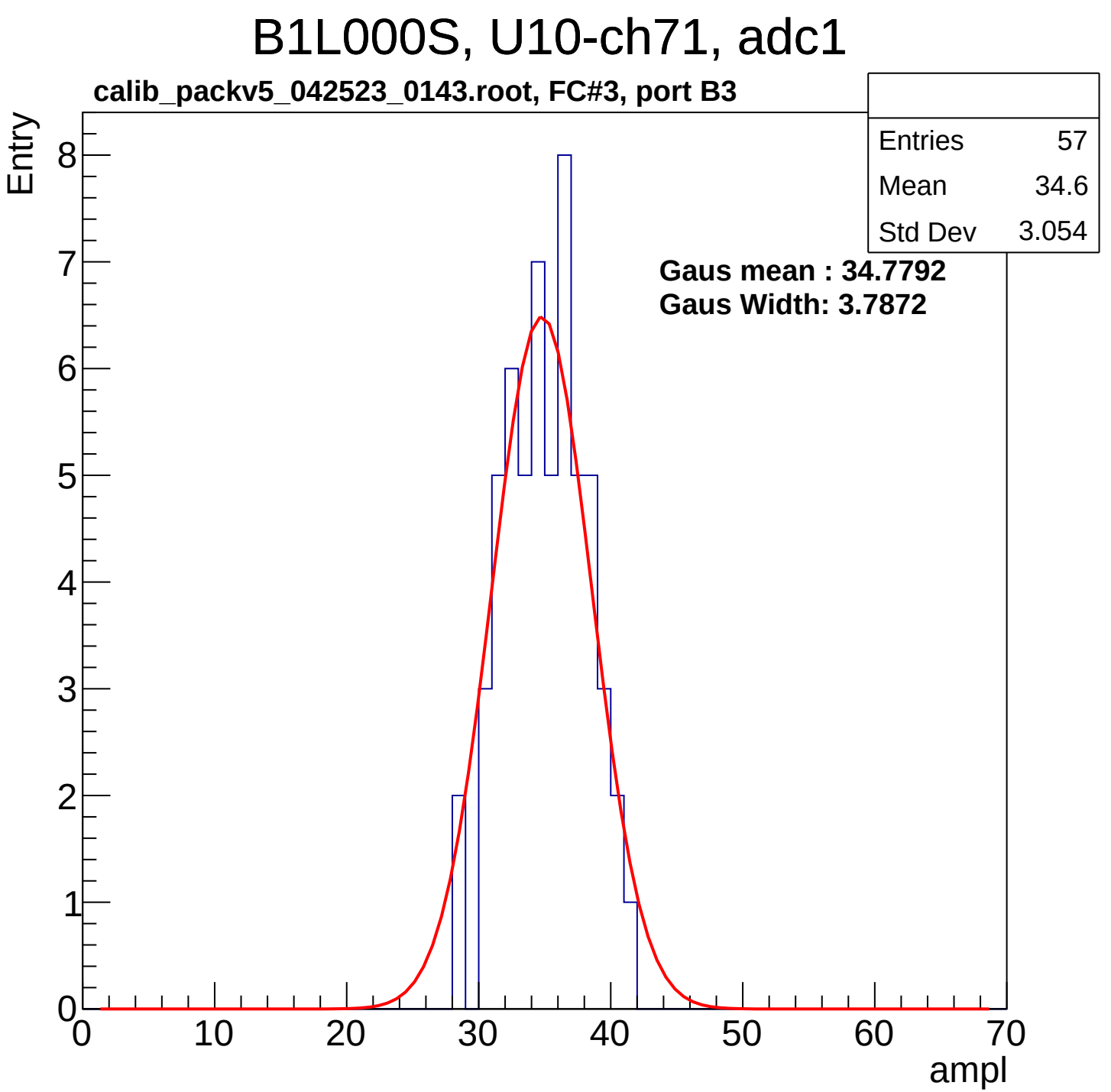
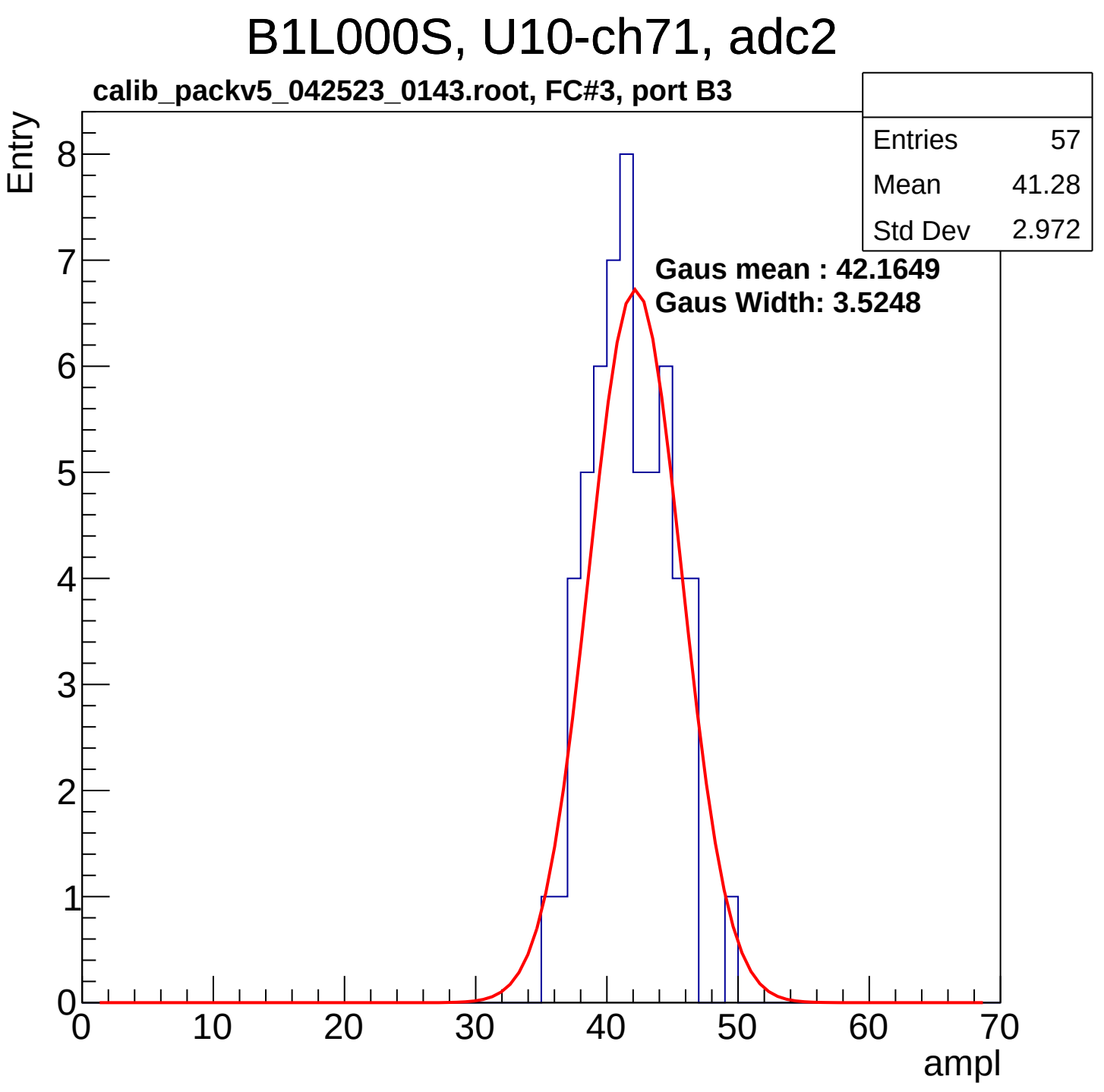
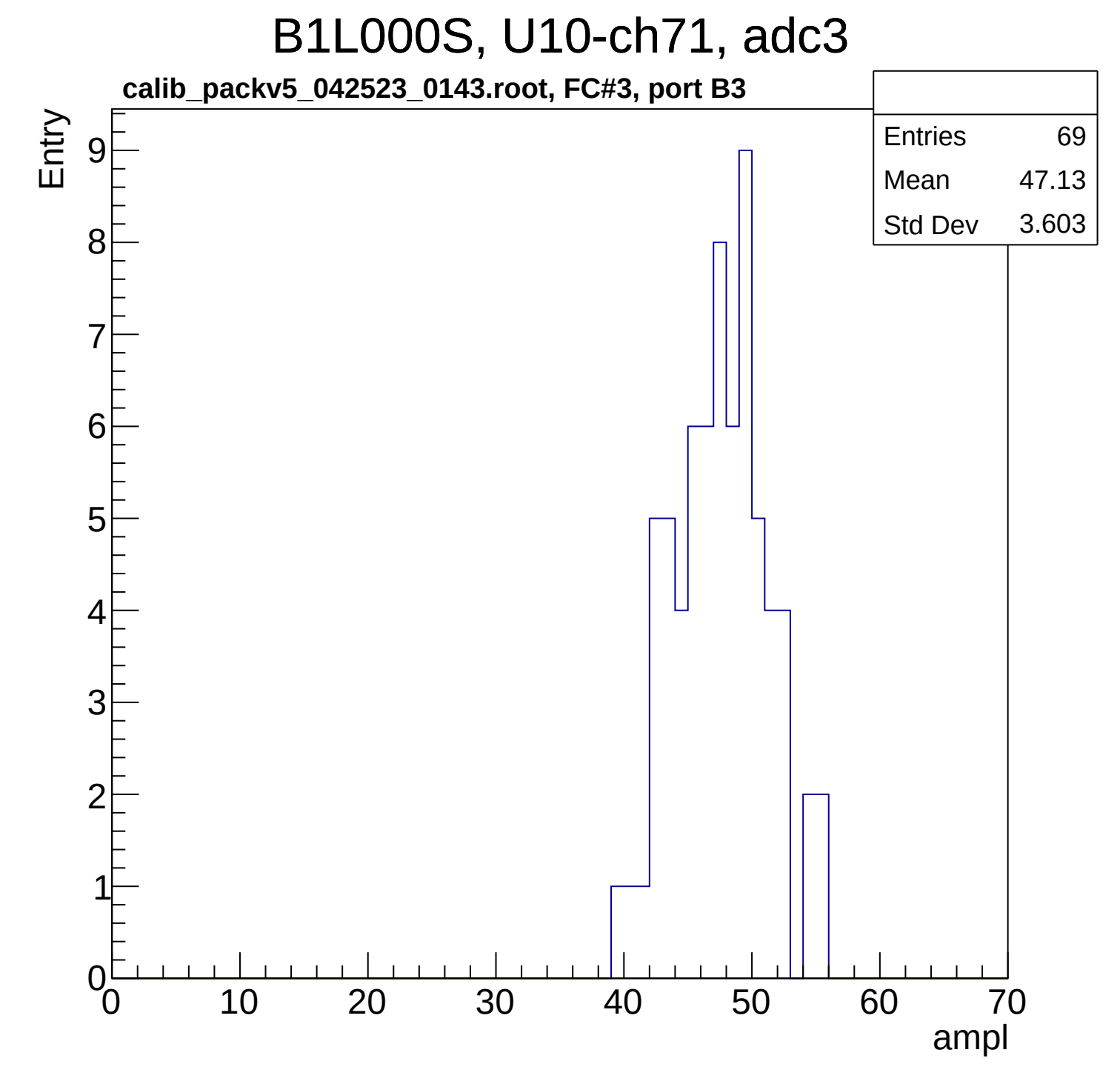

B1L000S, U10-ch70, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 61.44
Std Dev 1.54
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

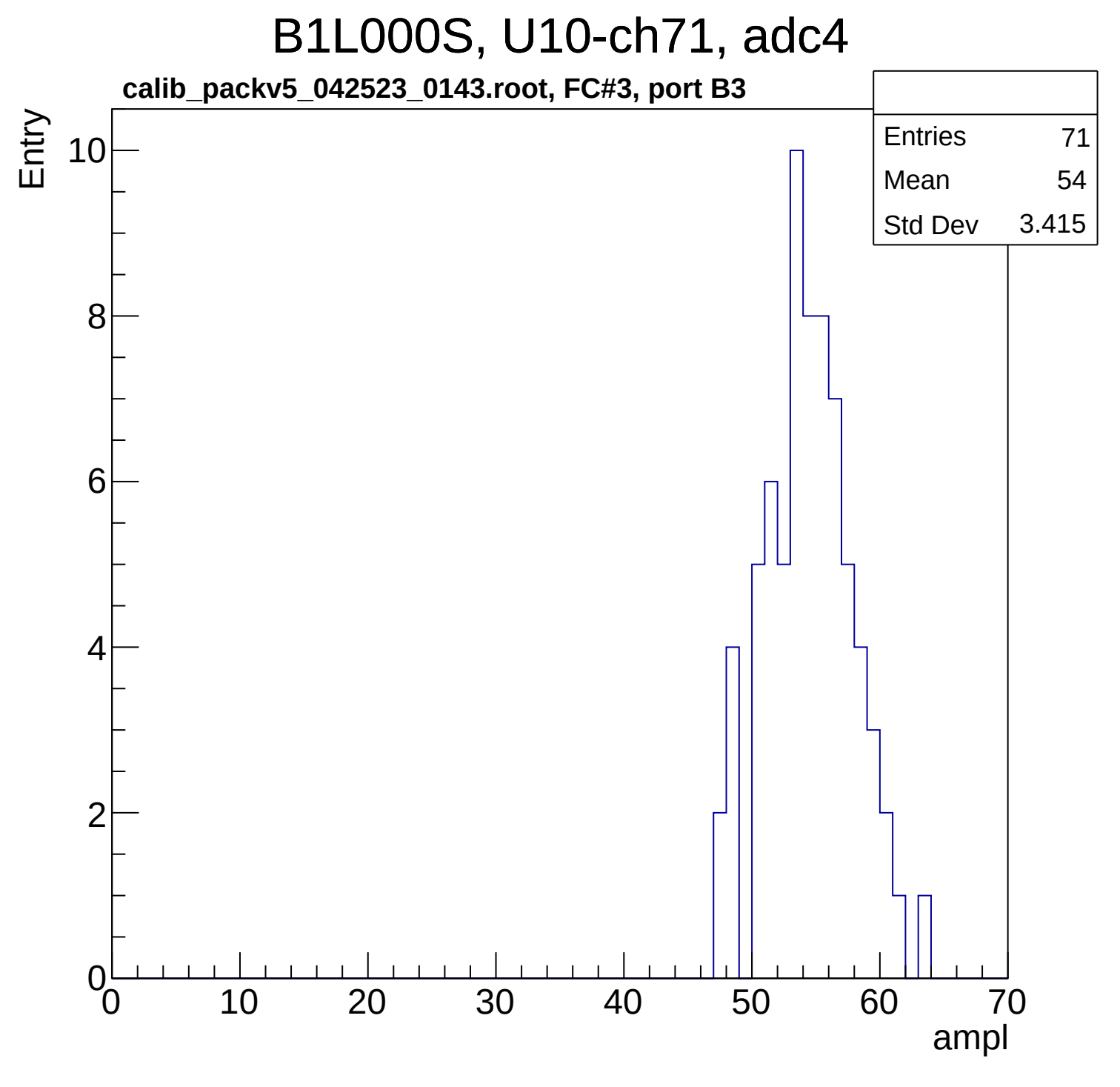

B1L000S, U10-ch70, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

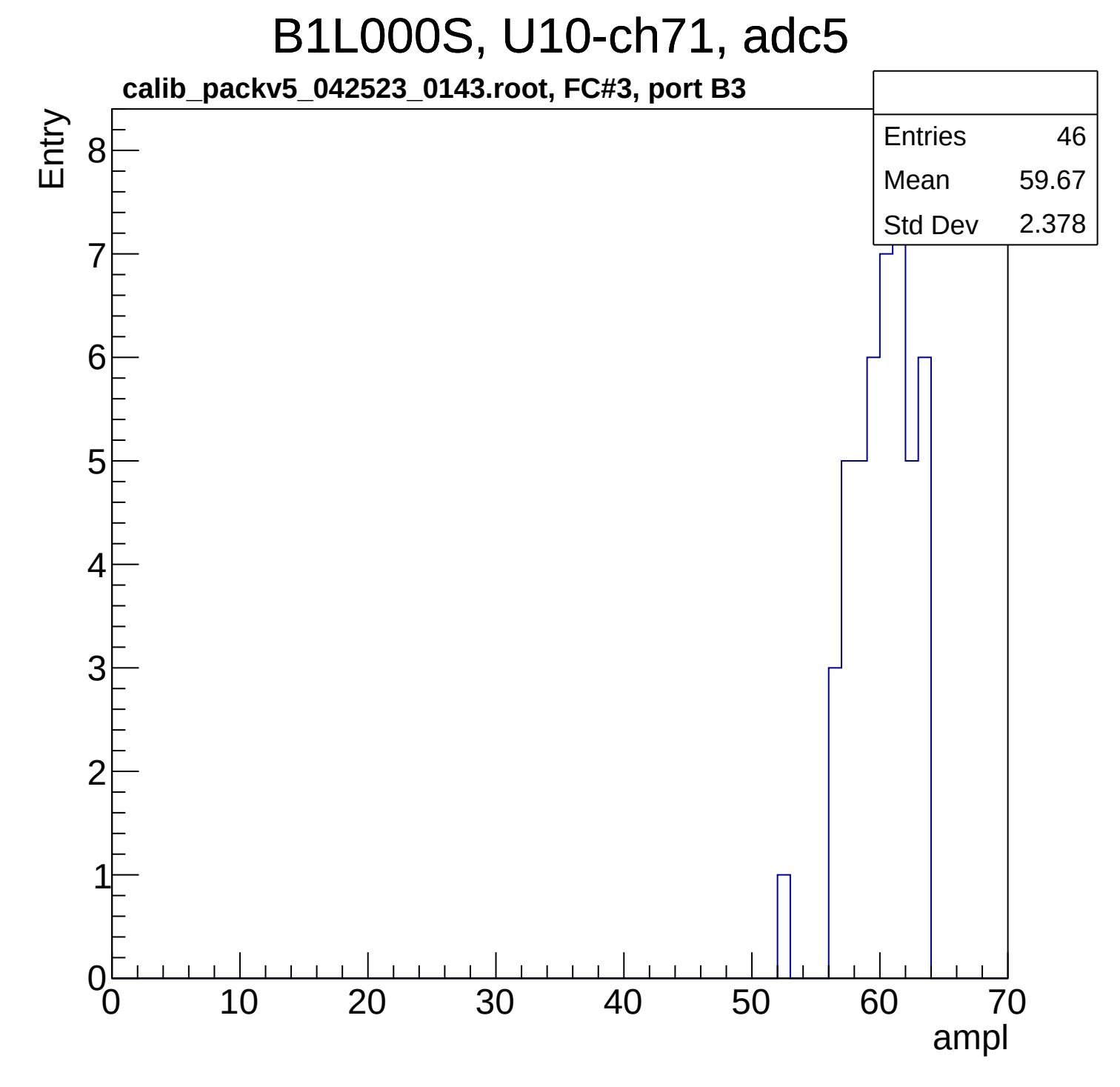

B1L000S, U10-ch71, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 29.31
Std Dev 3.657
Gaus mean : 29.9341
Gaus Width: 3.9219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch71, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 34.6
Std Dev 3.054
Gaus mean : 34.7792
Gaus Width: 3.7872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch71, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.28
Std Dev 2.972
Gaus mean : 42.1649
Gaus Width: 3.5248
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch71, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 47.13
Std Dev 3.603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch71, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 54
Std Dev 3.415
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch71, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 59.67
Std Dev 2.378
Entry
ampl
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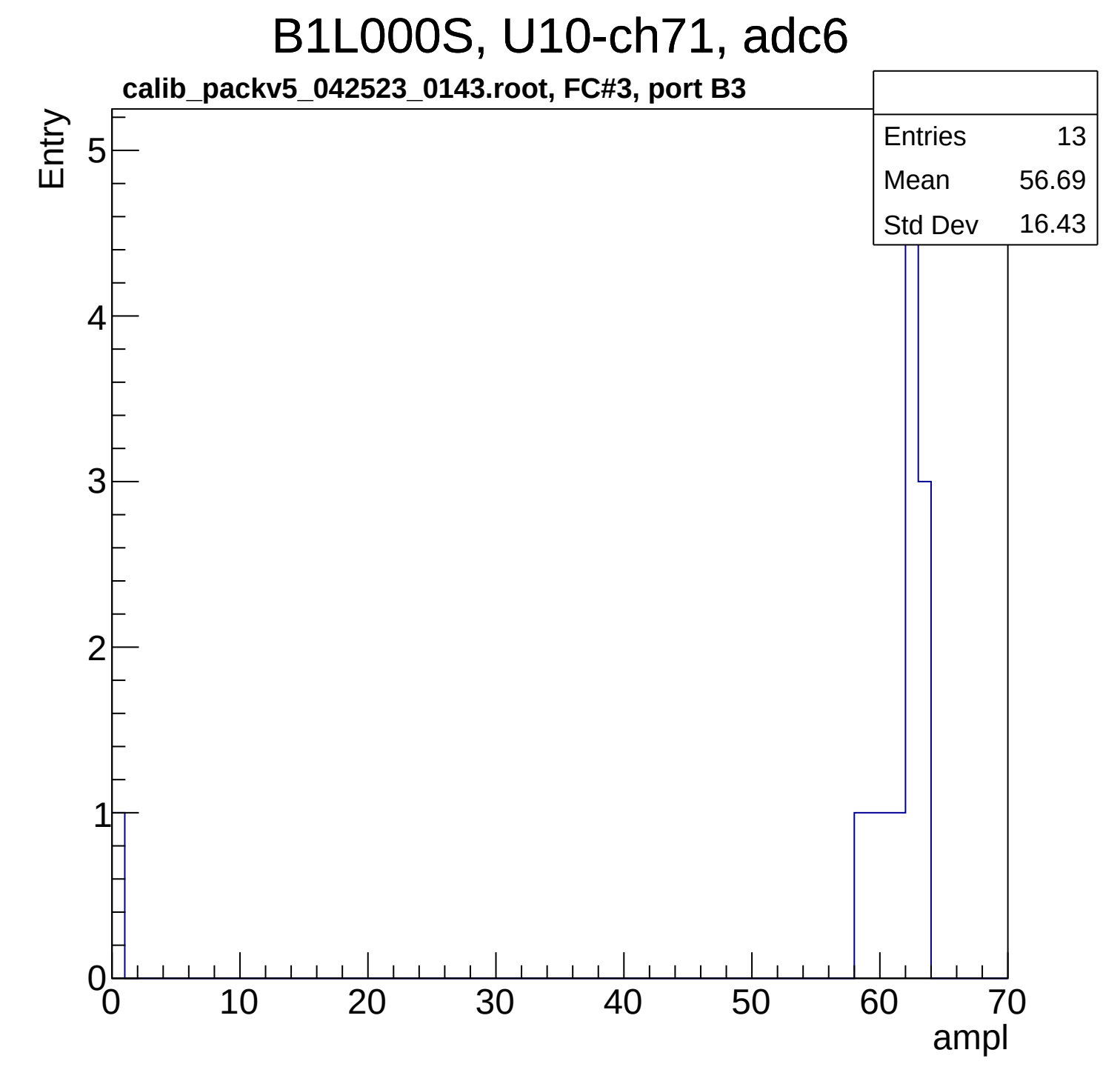
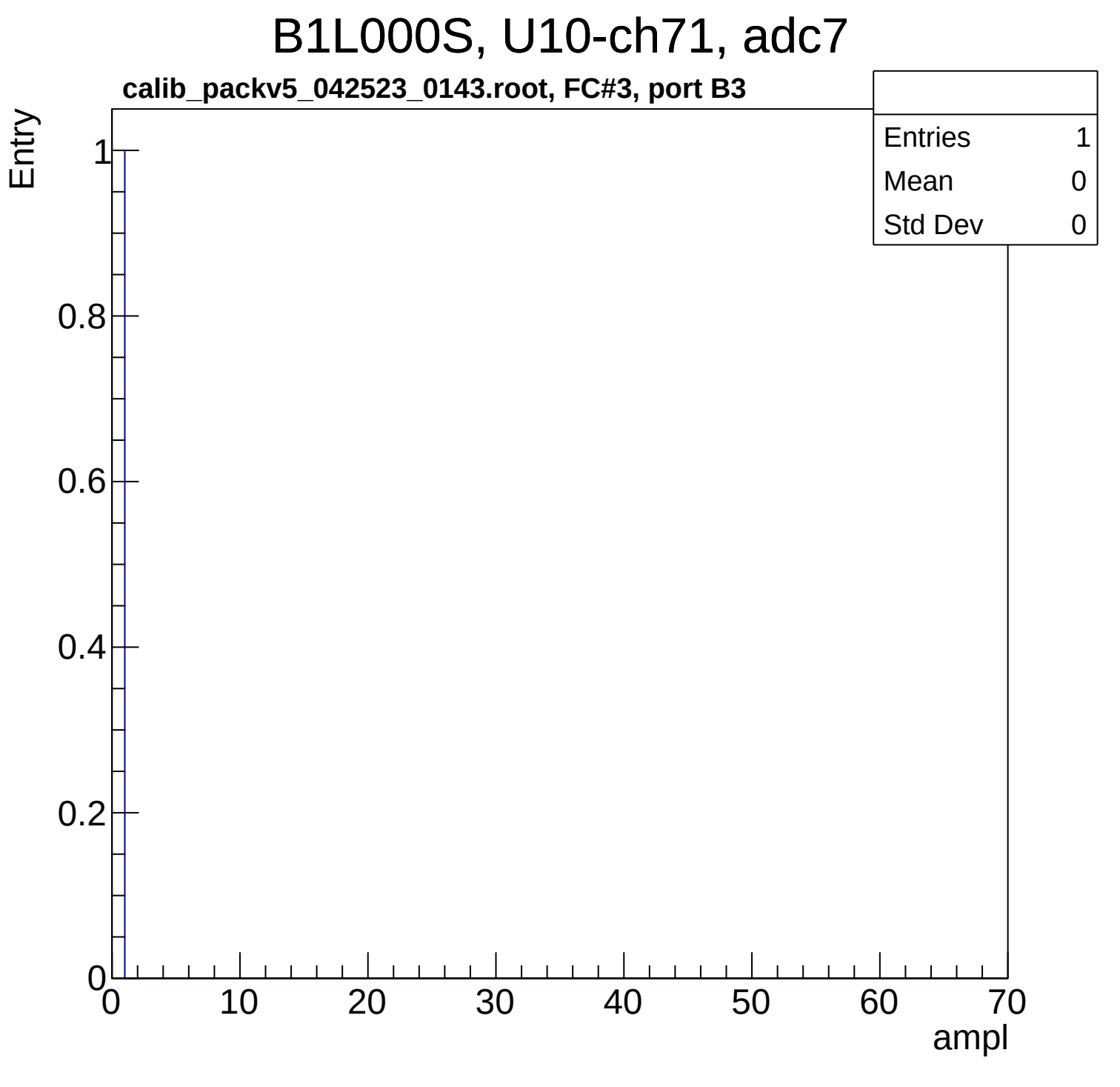
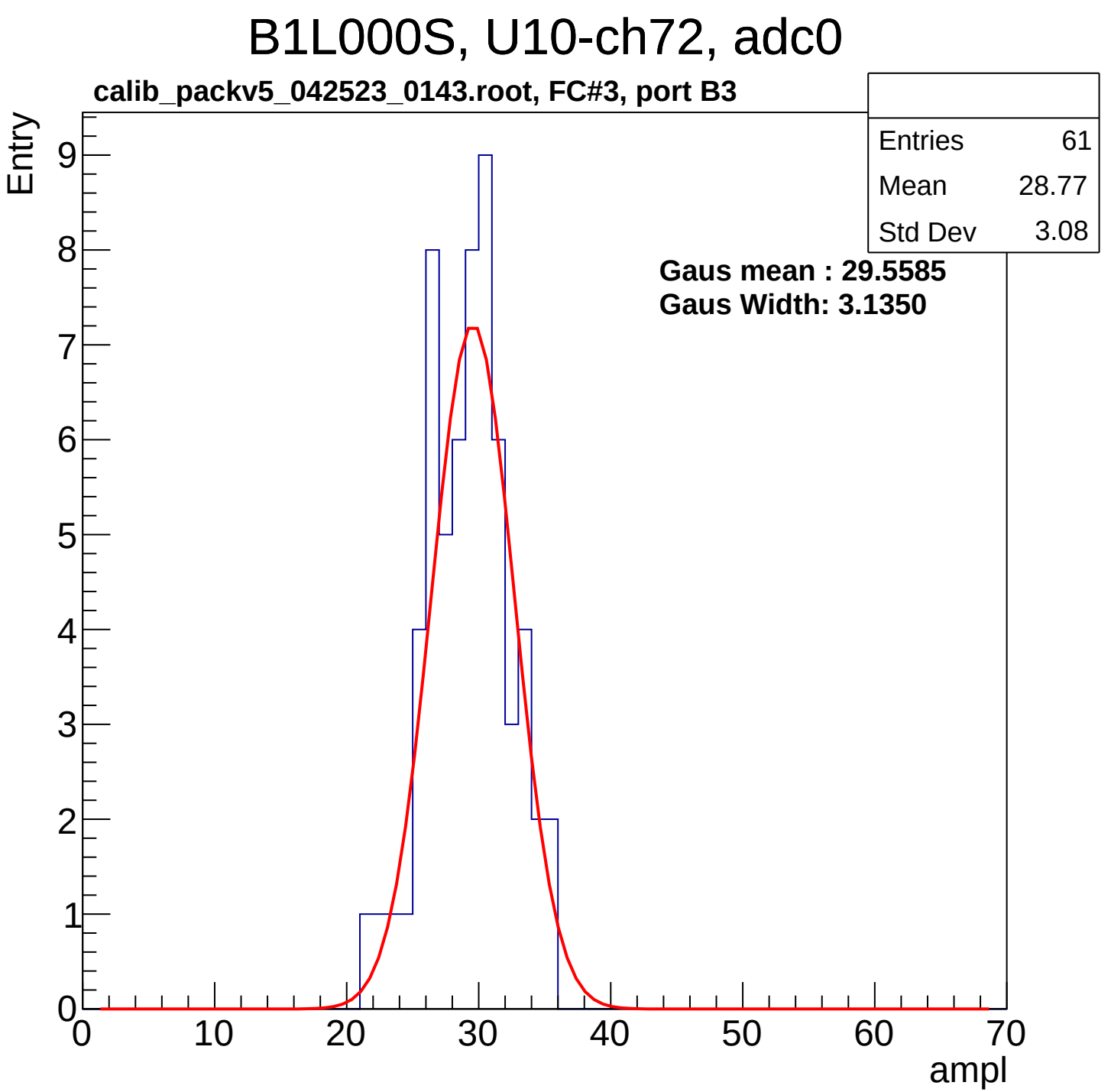
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch71, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.69
Std Dev 16.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch71, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch72, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.77
Std Dev 3.08
Gaus mean : 29.5585
Gaus Width: 3.1350
Entry
ampl
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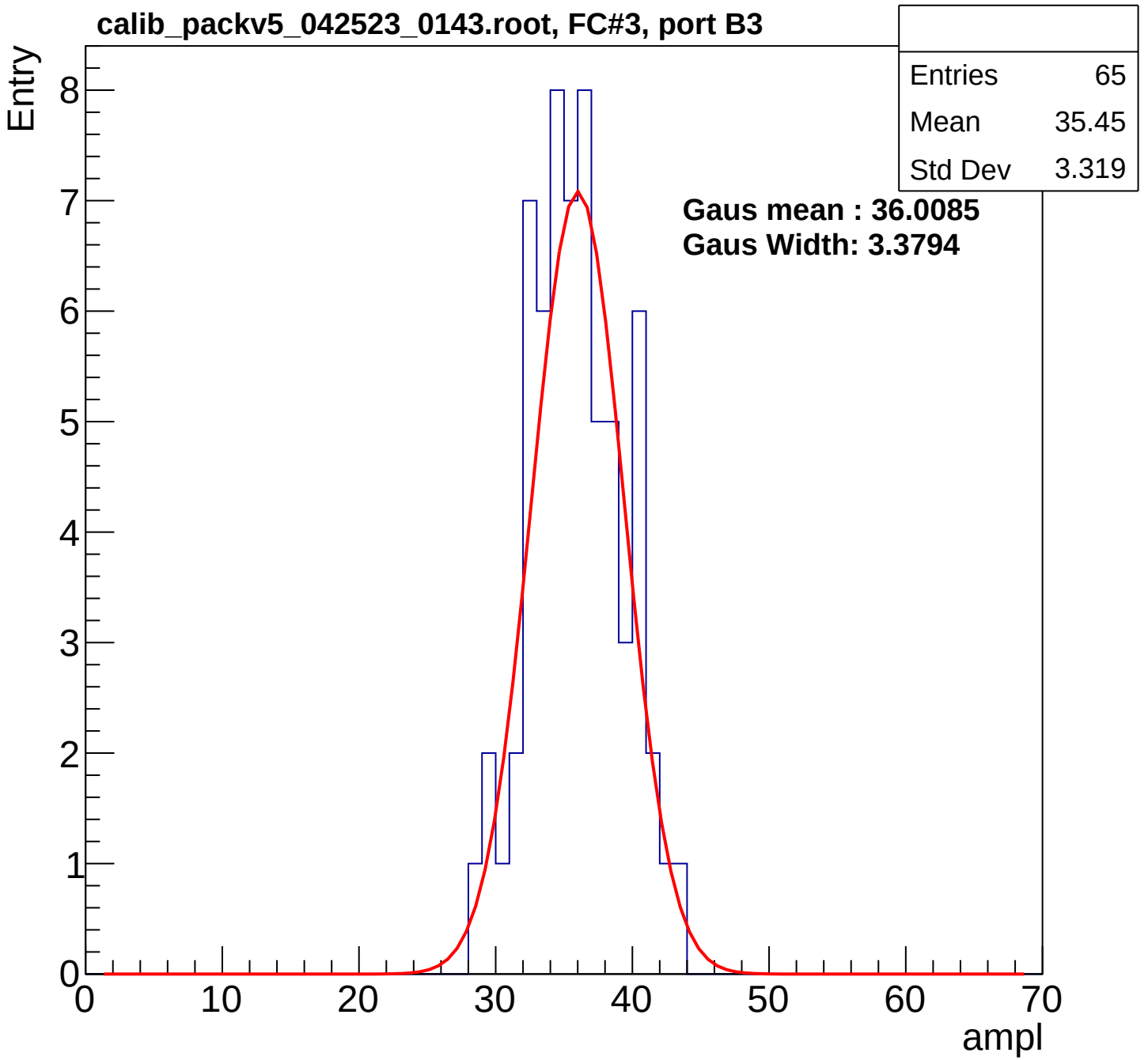
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch72, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.45
Std Dev 3.319
Gaus mean : 36.0085
Gaus Width: 3.3794
Entry
ampl
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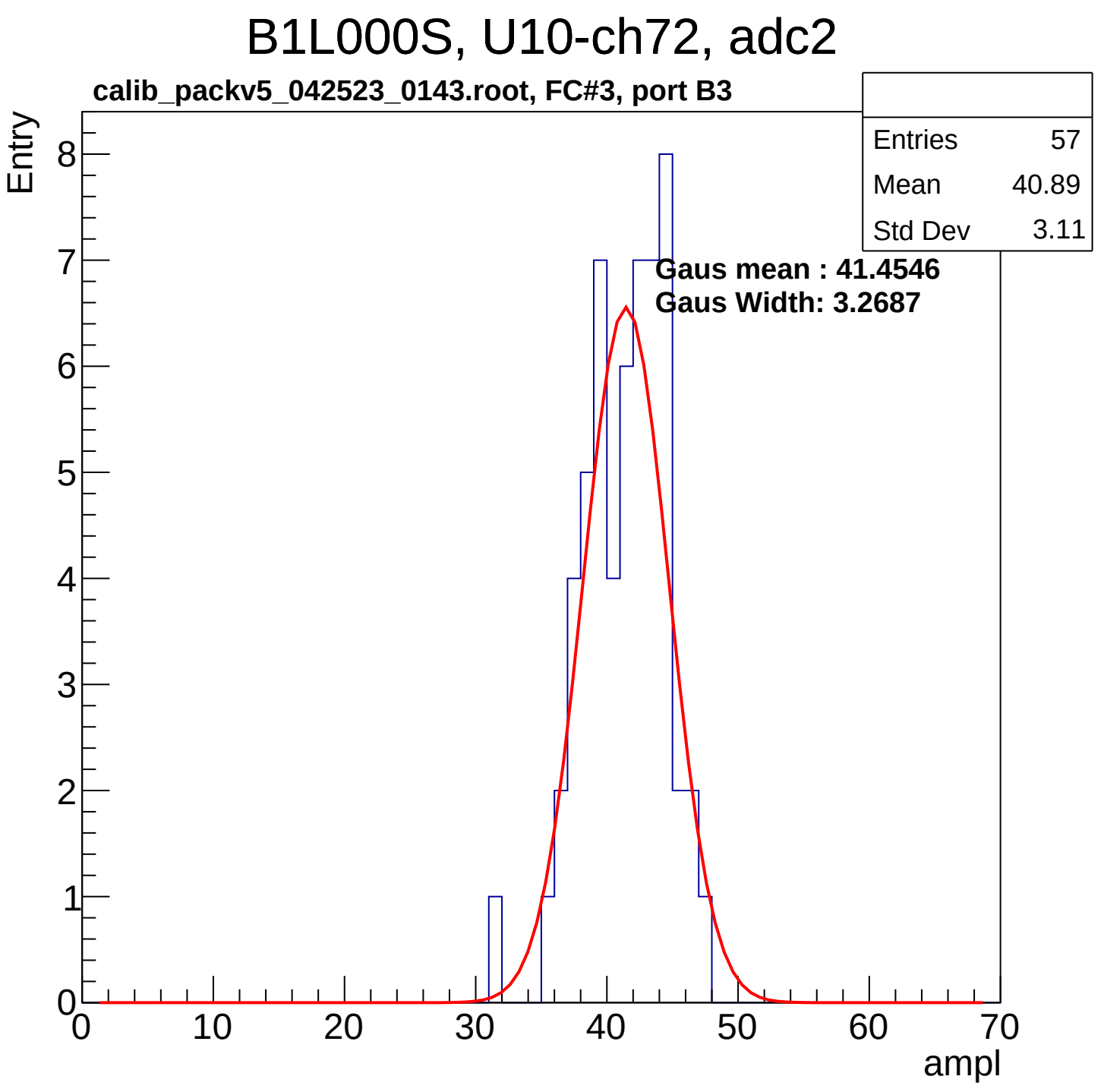
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch72, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 40.89
Std Dev 3.11
Gaus mean : 41.4546
Gaus Width: 3.2687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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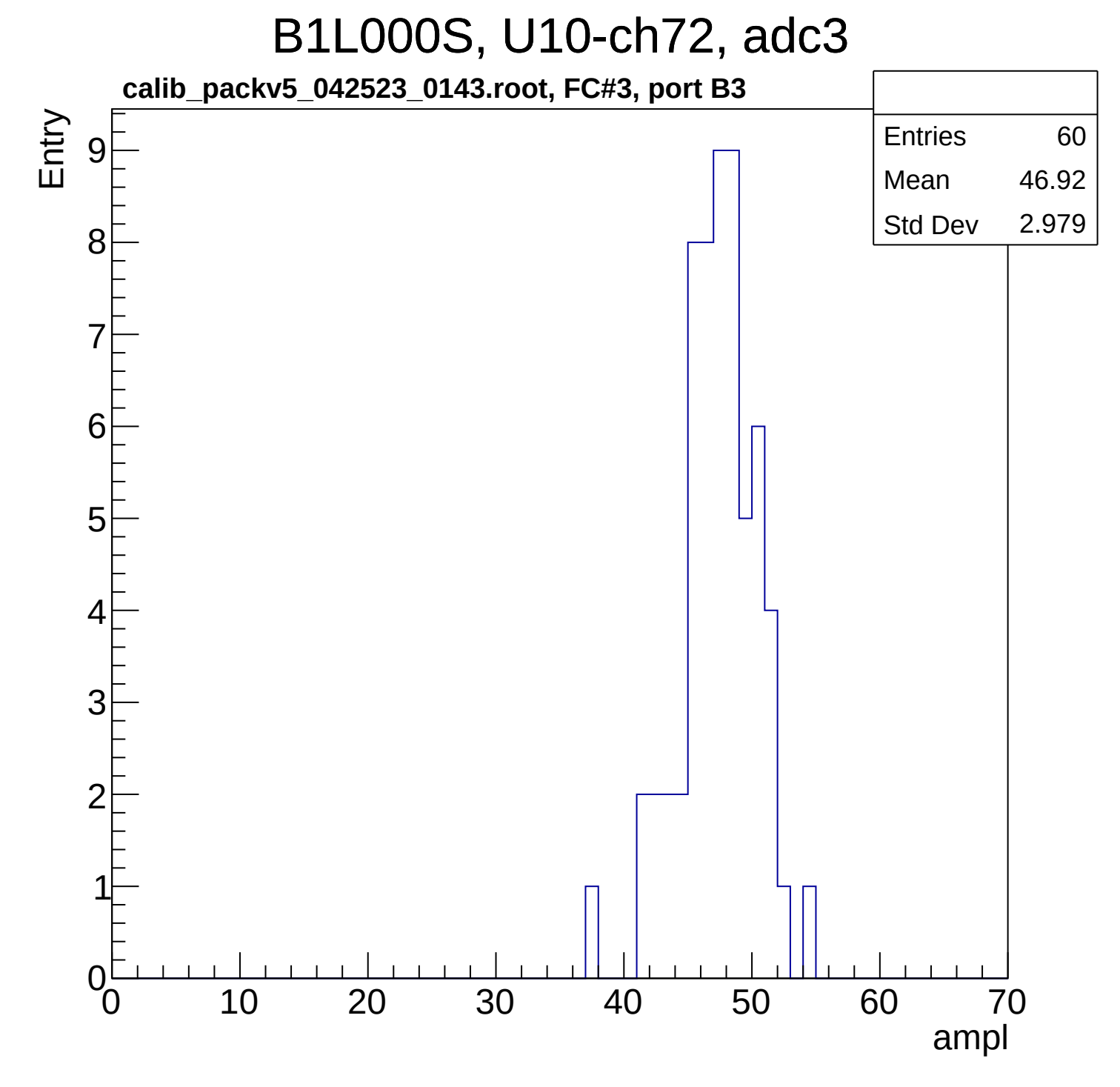
40
50
60
70


B1L000S, U10-ch72, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 46.92
Std Dev 2.979
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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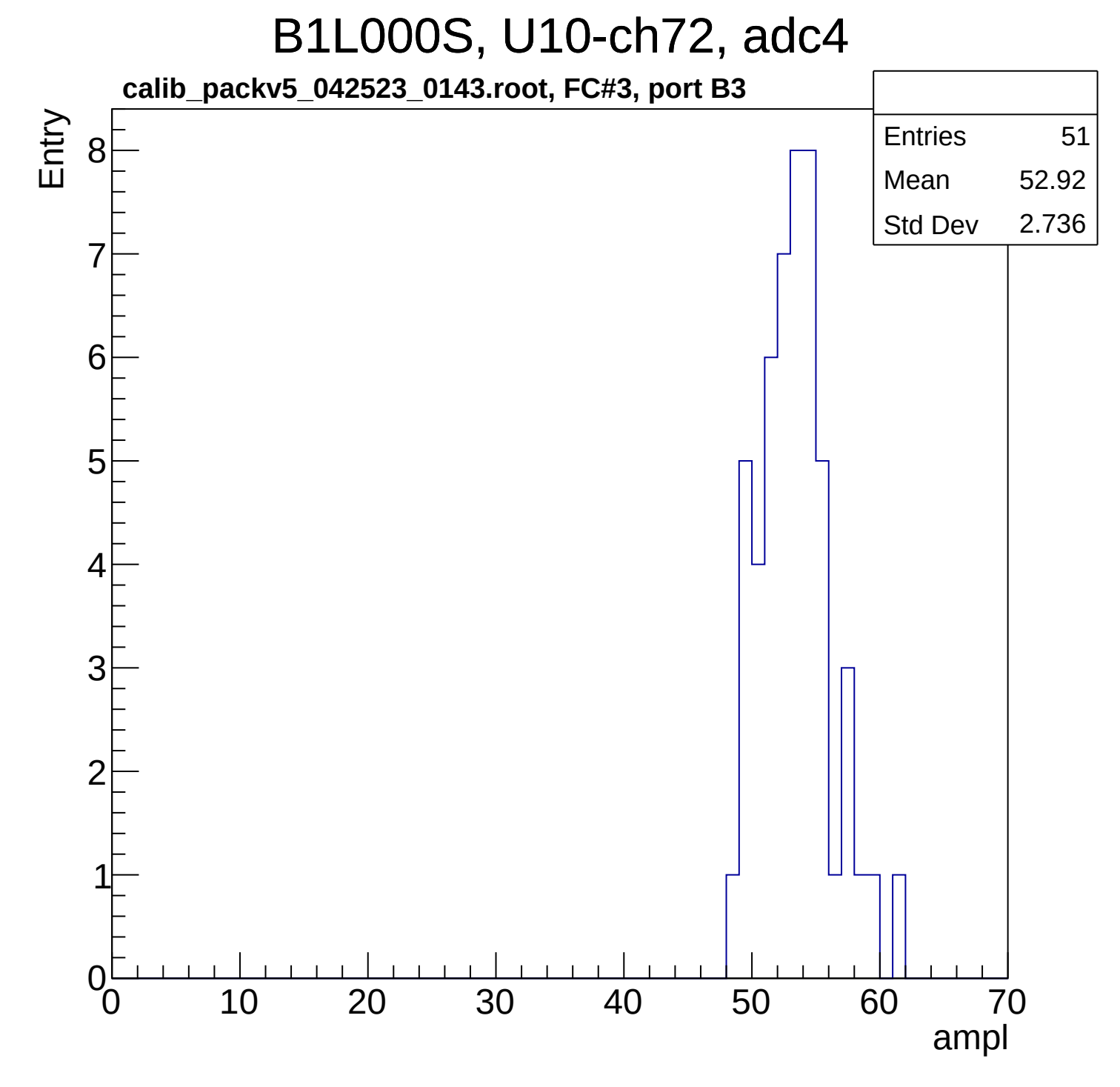
60
70


B1L000S, U10-ch72, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 52.92
Std Dev 2.736
Entry
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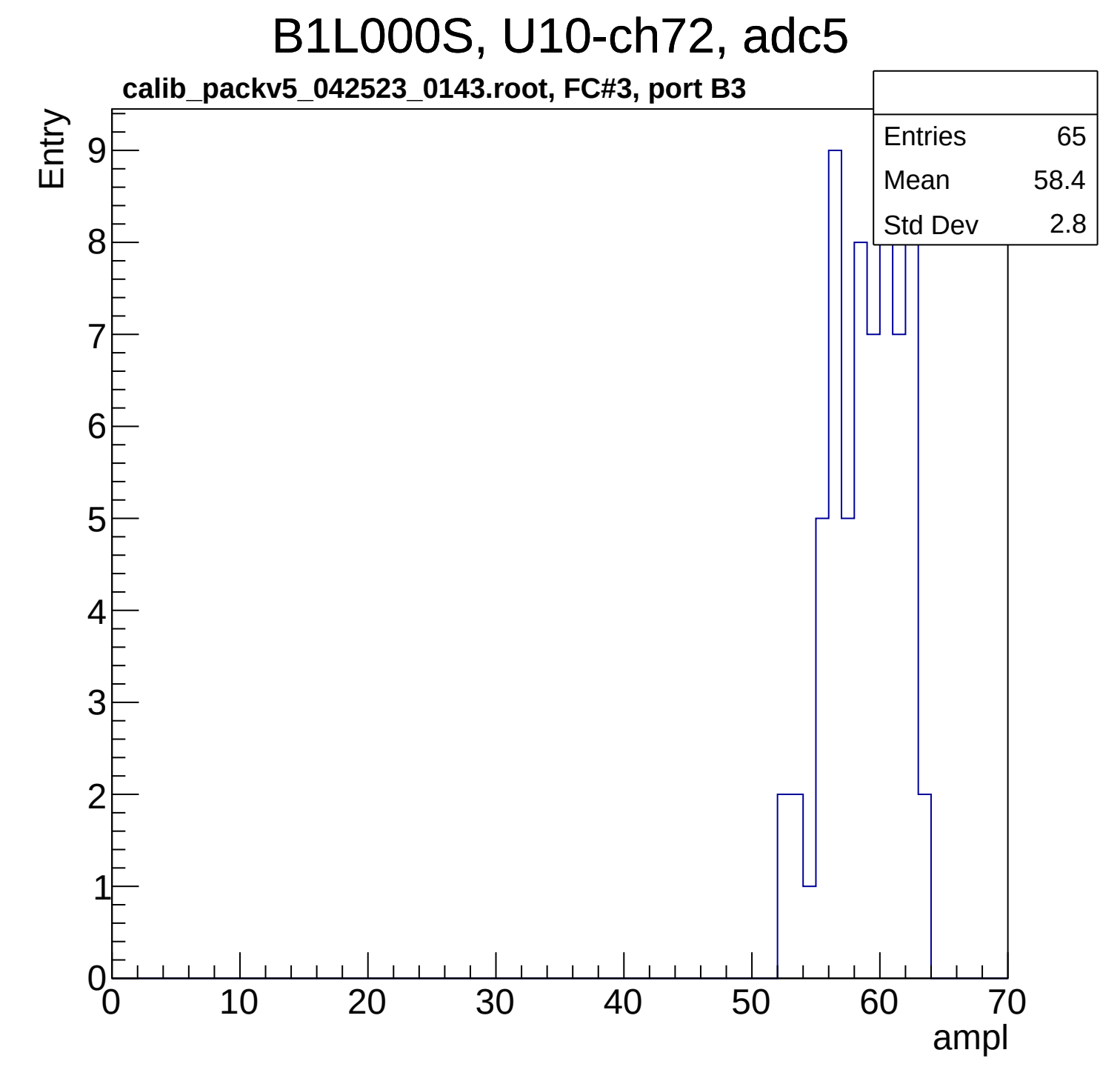
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch72, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 58.4
Std Dev 2.8
Entry
ampl
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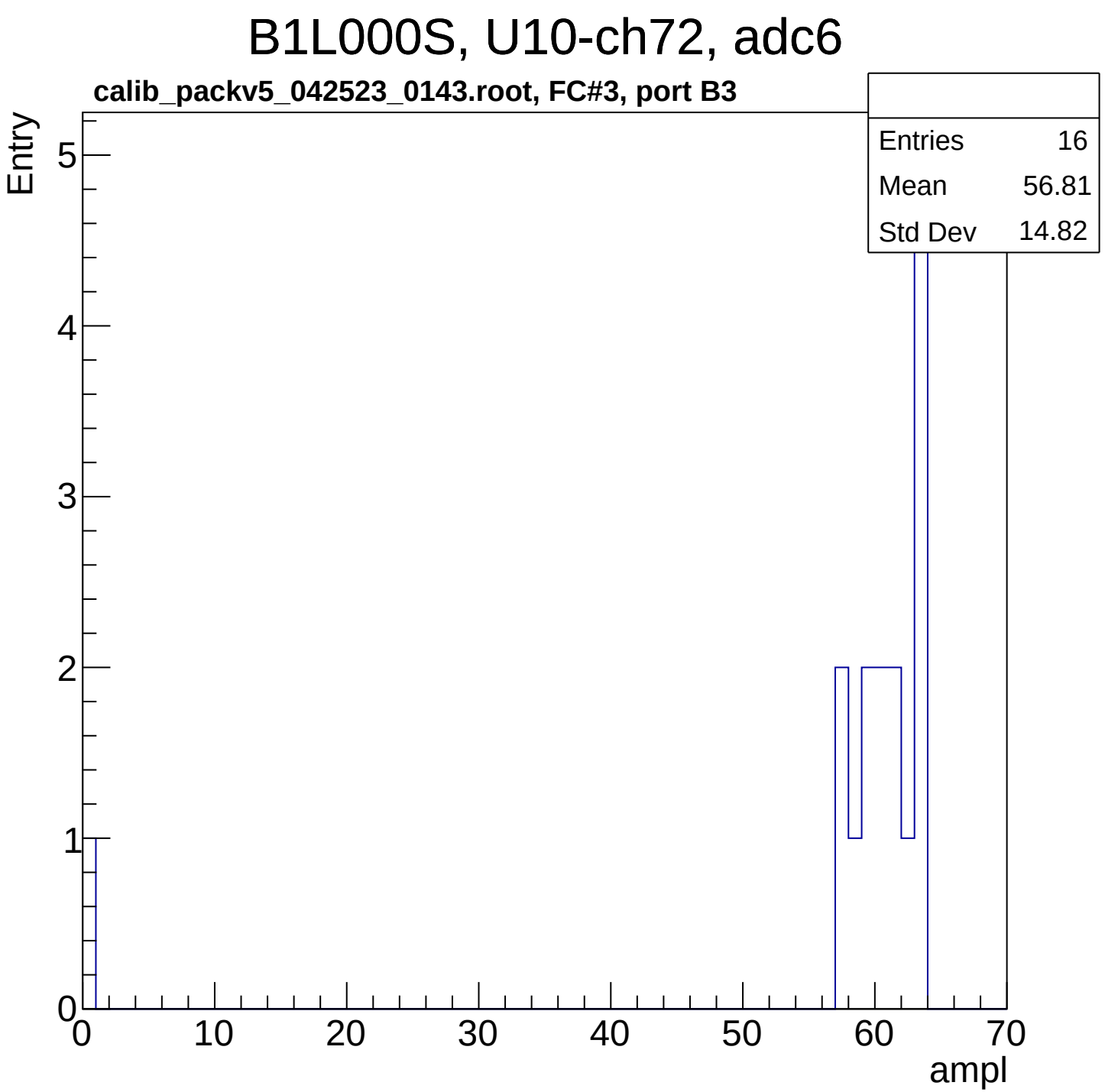
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch72, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 56.81
Std Dev 14.82
Entry
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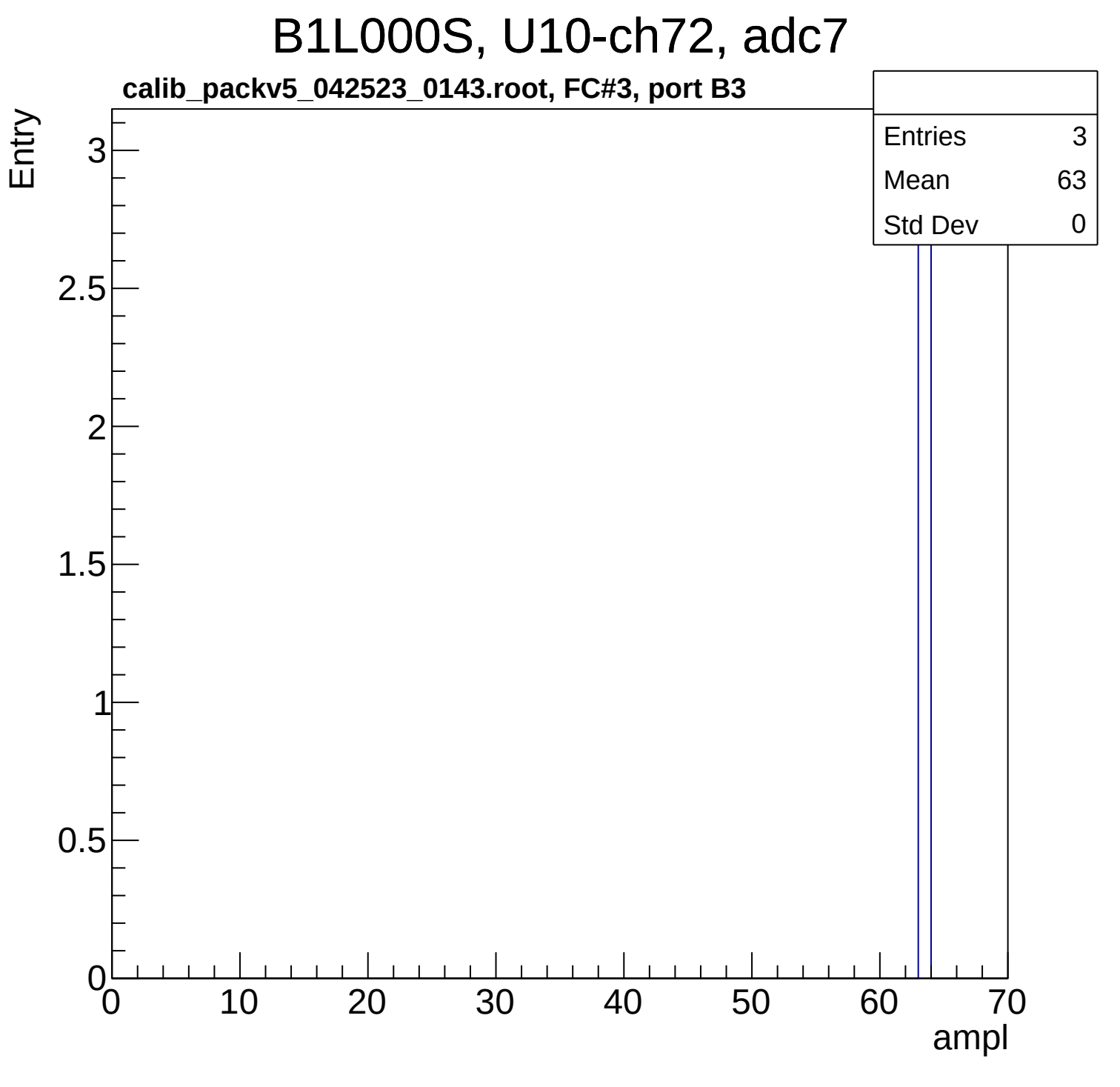
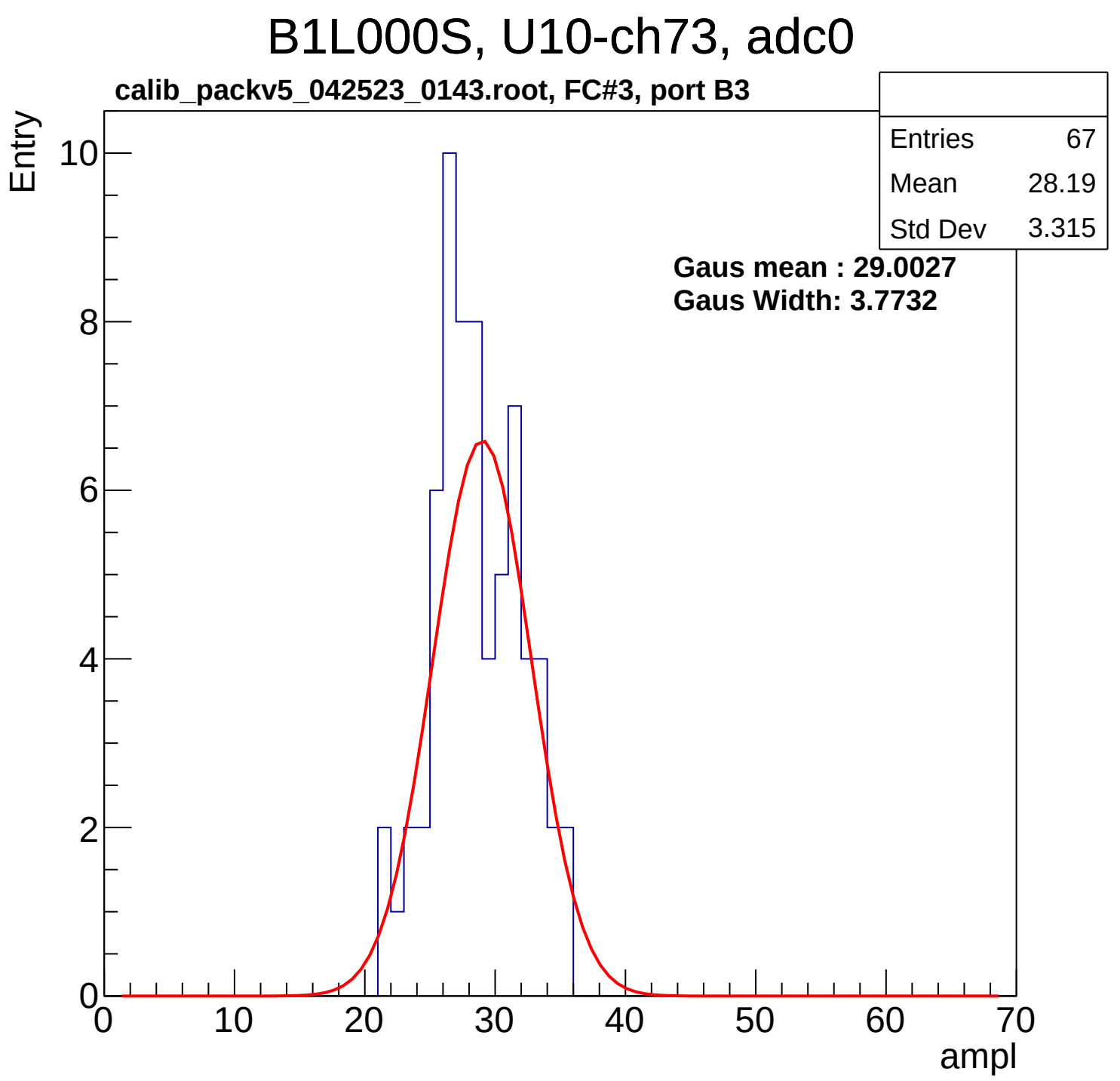
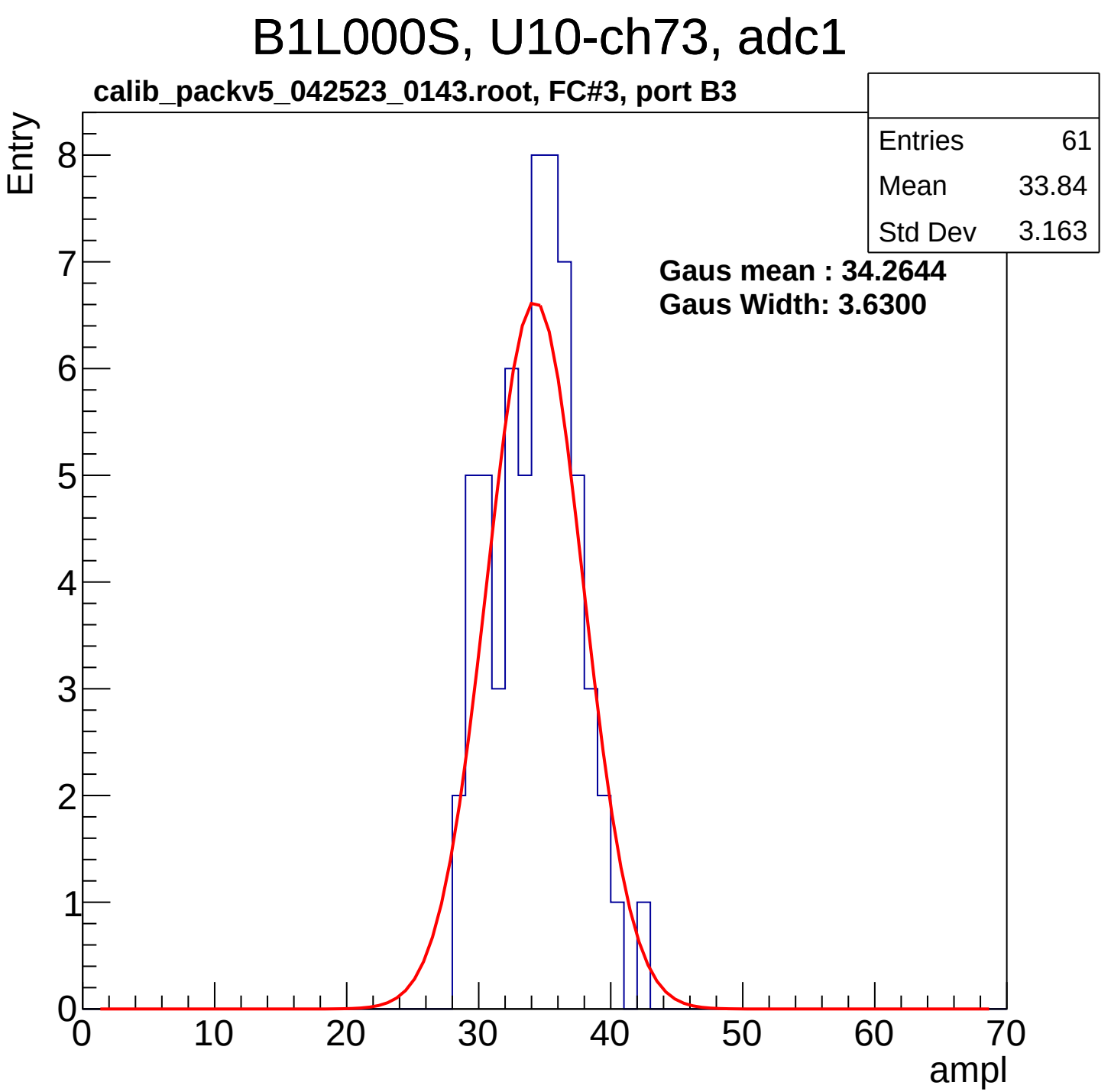
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch72, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch73, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 28.19
Std Dev 3.315
Gaus mean : 29.0027
Gaus Width: 3.7732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch73, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 33.84
Std Dev 3.163
Gaus mean : 34.2644
Gaus Width: 3.6300
Entry
ampl
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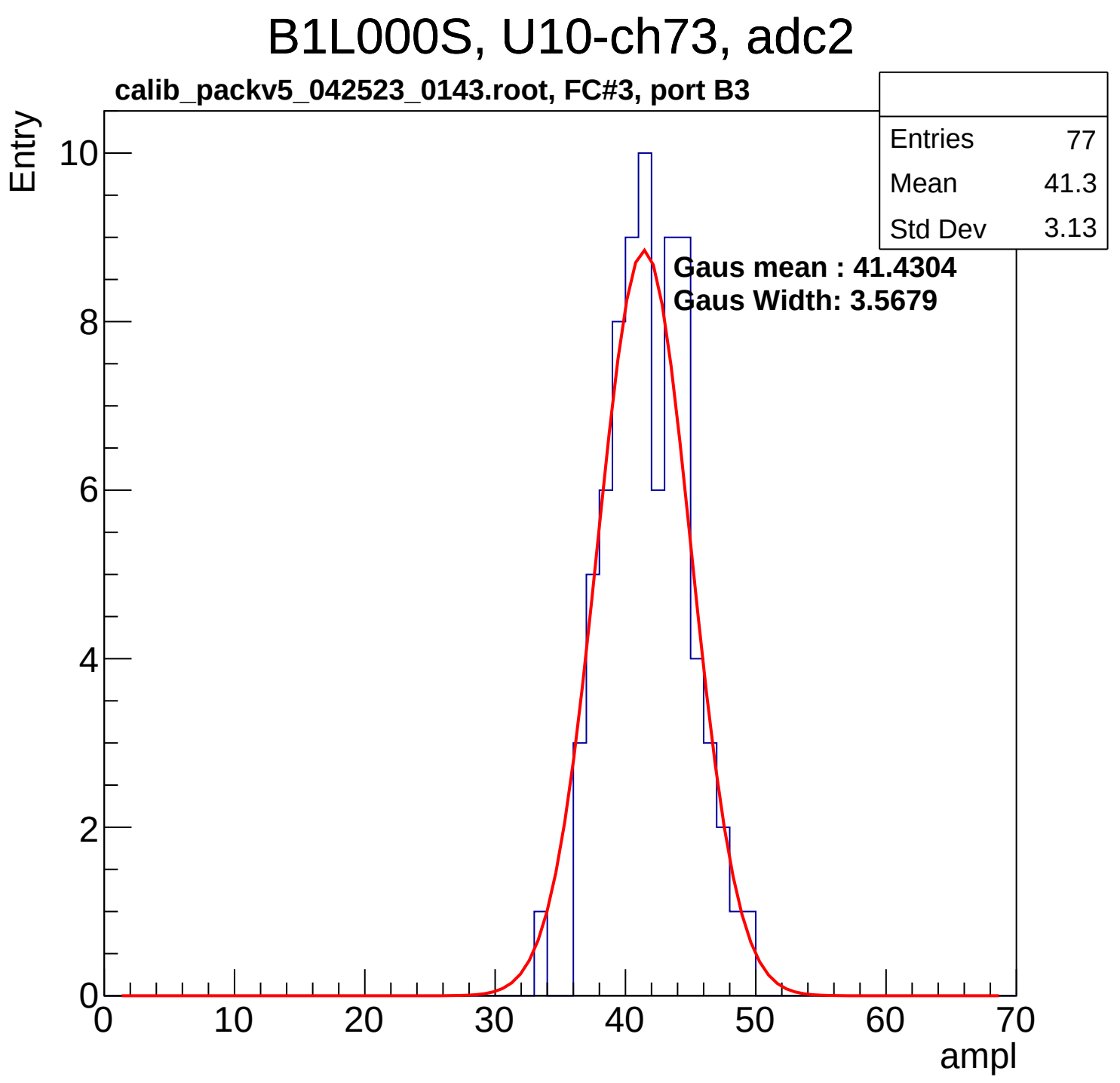
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch73, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 41.3
Std Dev 3.13
Gaus mean : 41.4304
Gaus Width: 3.5679
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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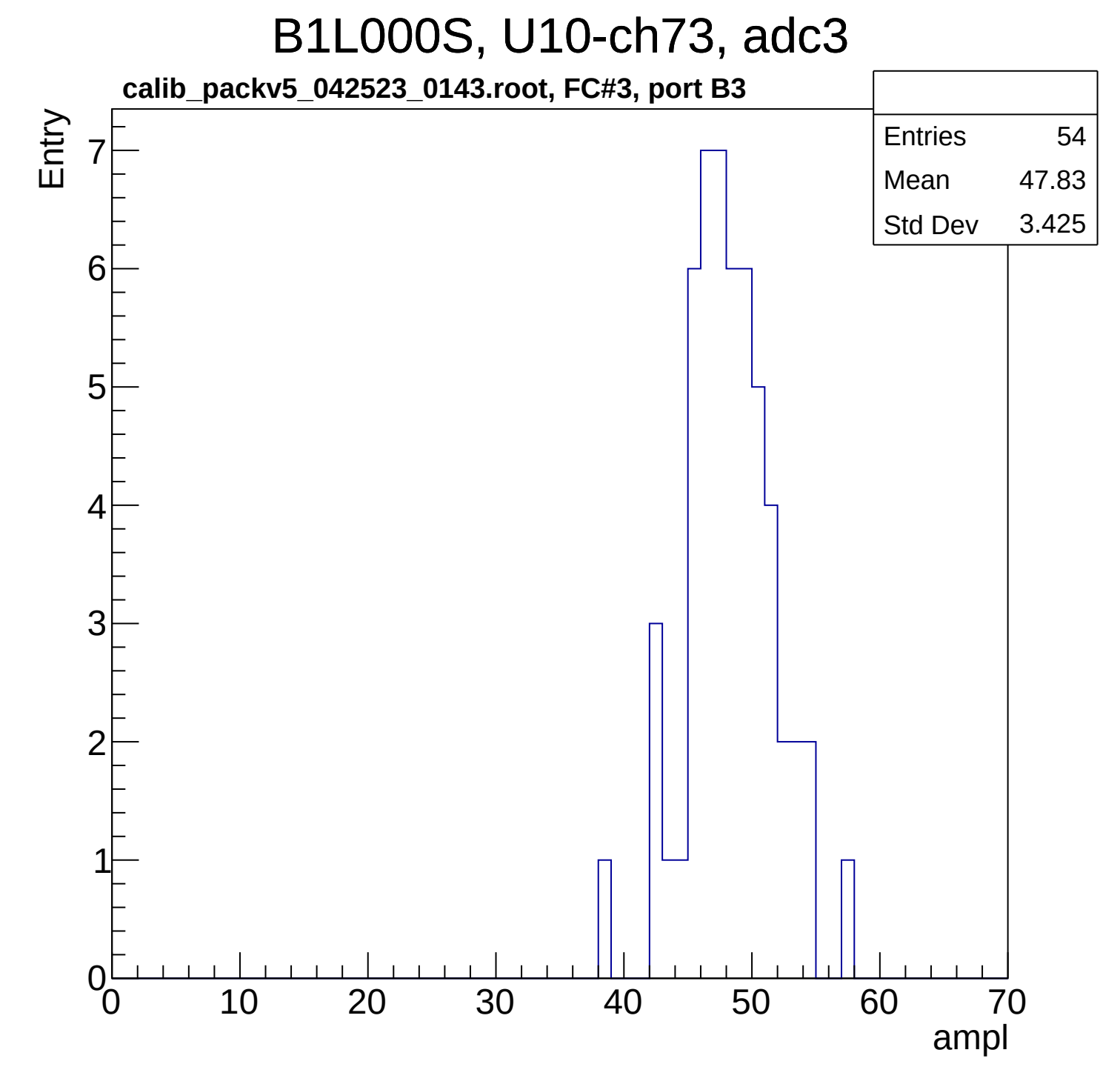
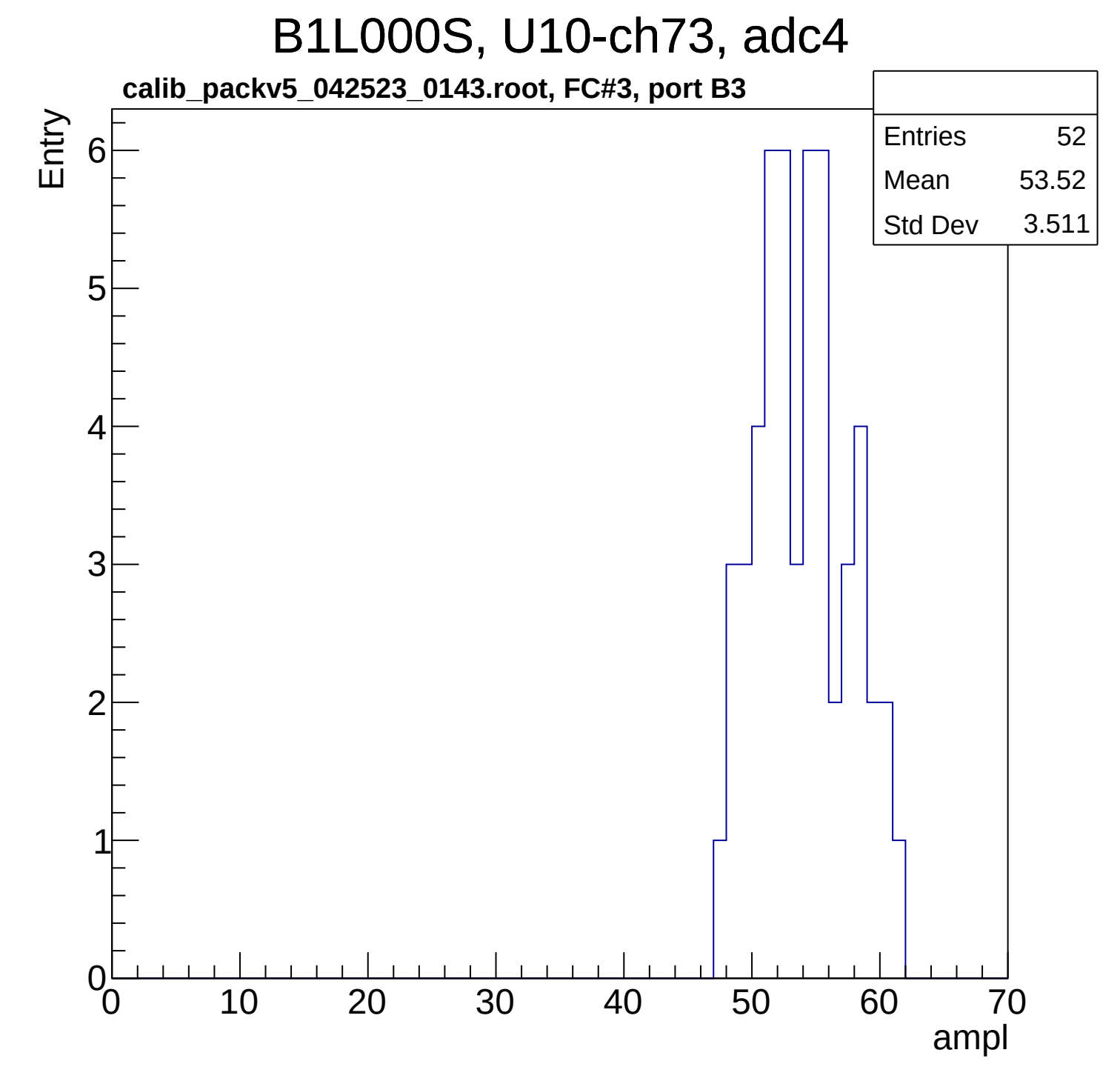
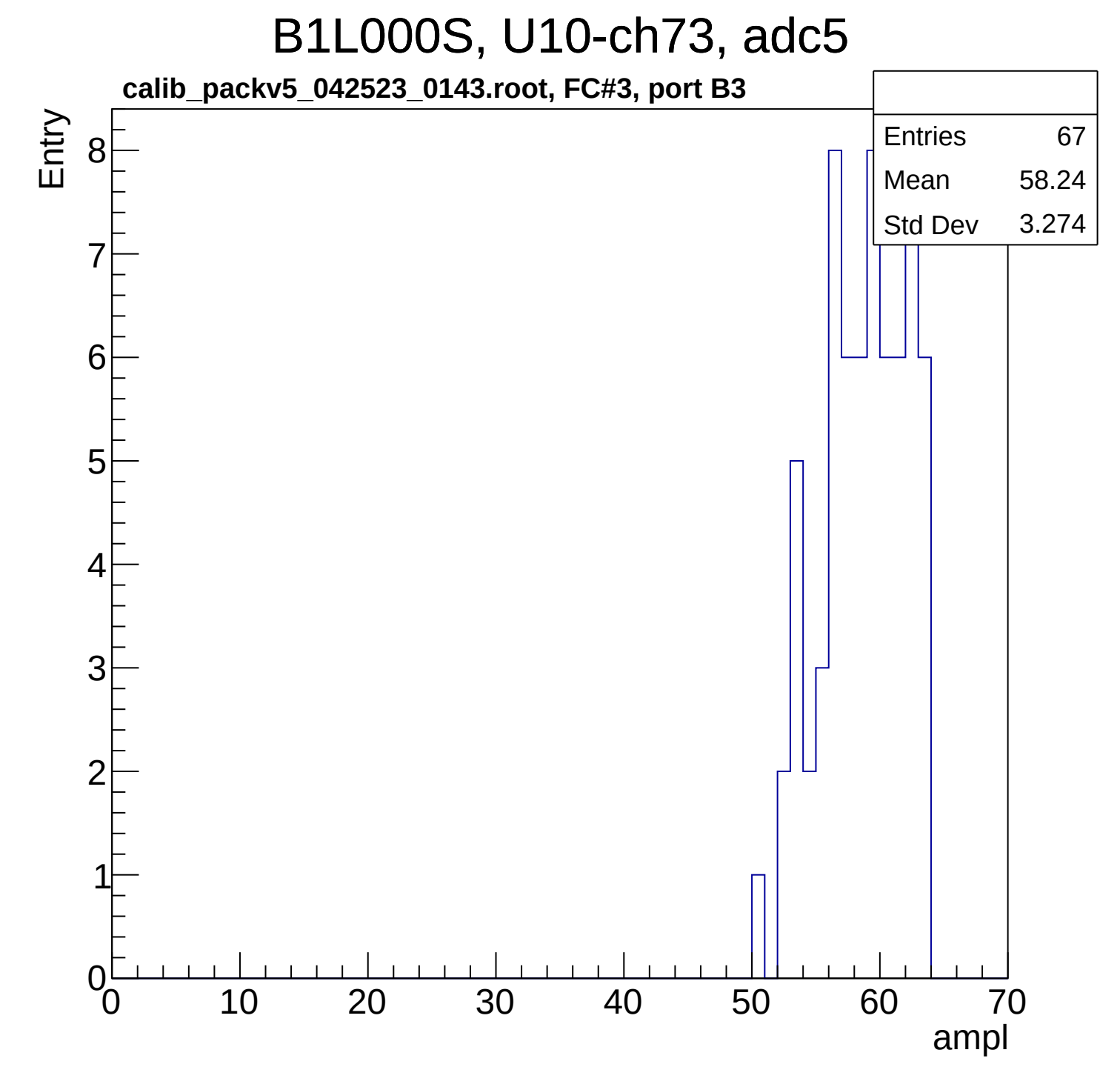

B1L000S, U10-ch73, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.83
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch73, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 53.52
Std Dev 3.511
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch73, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 58.24
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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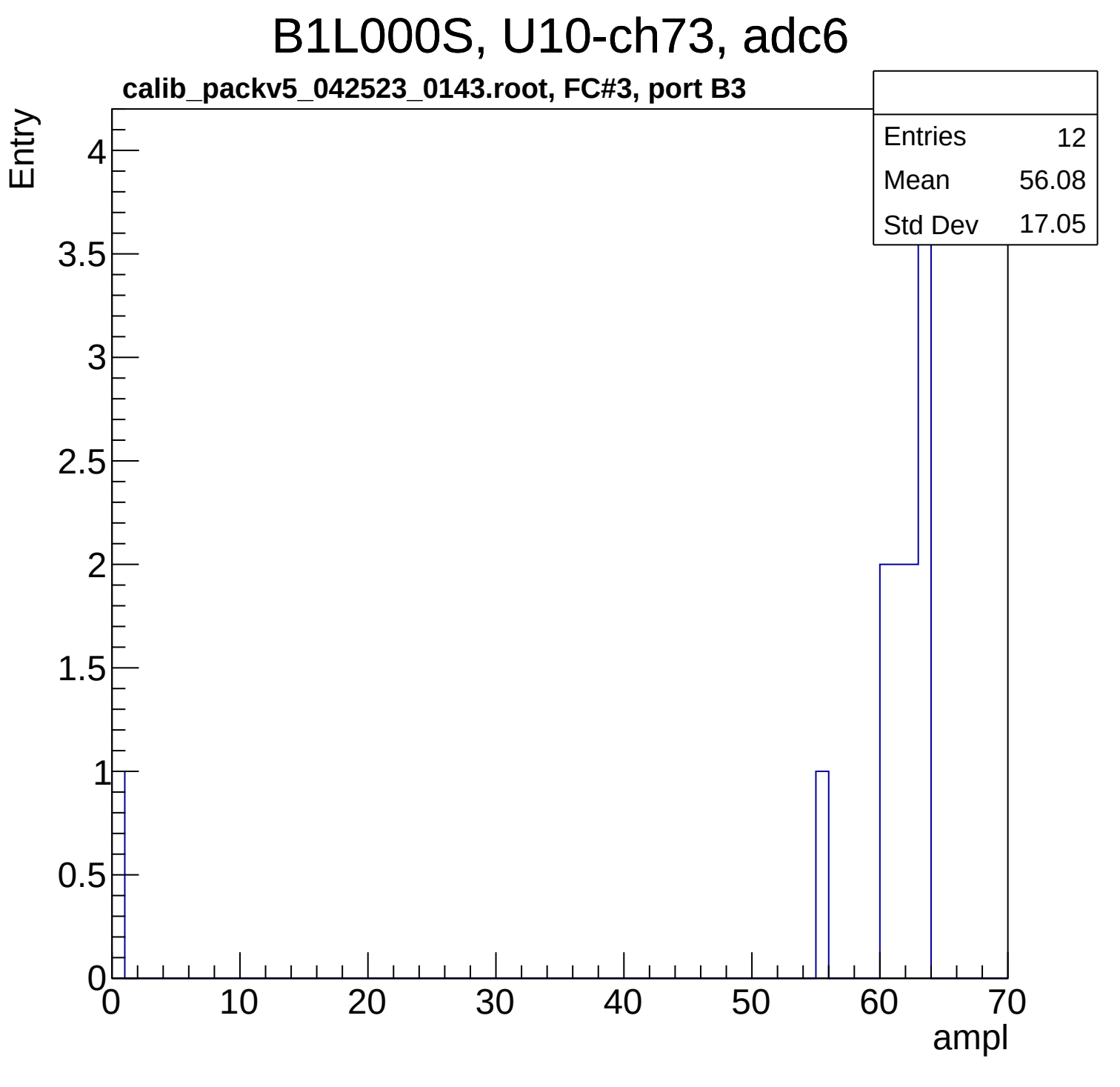
B1L000S, U10-ch73, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.08
Std Dev 17.05
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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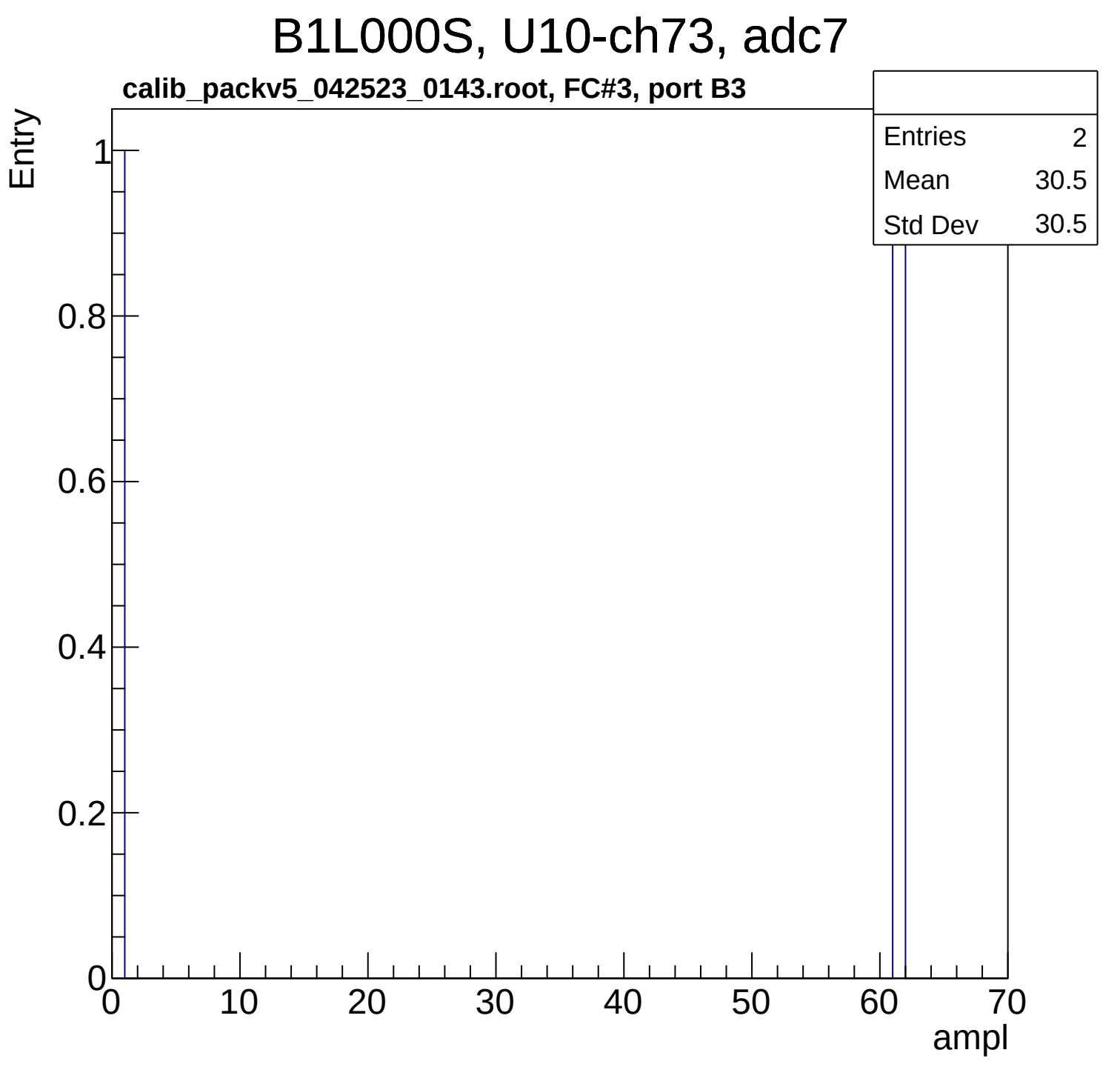
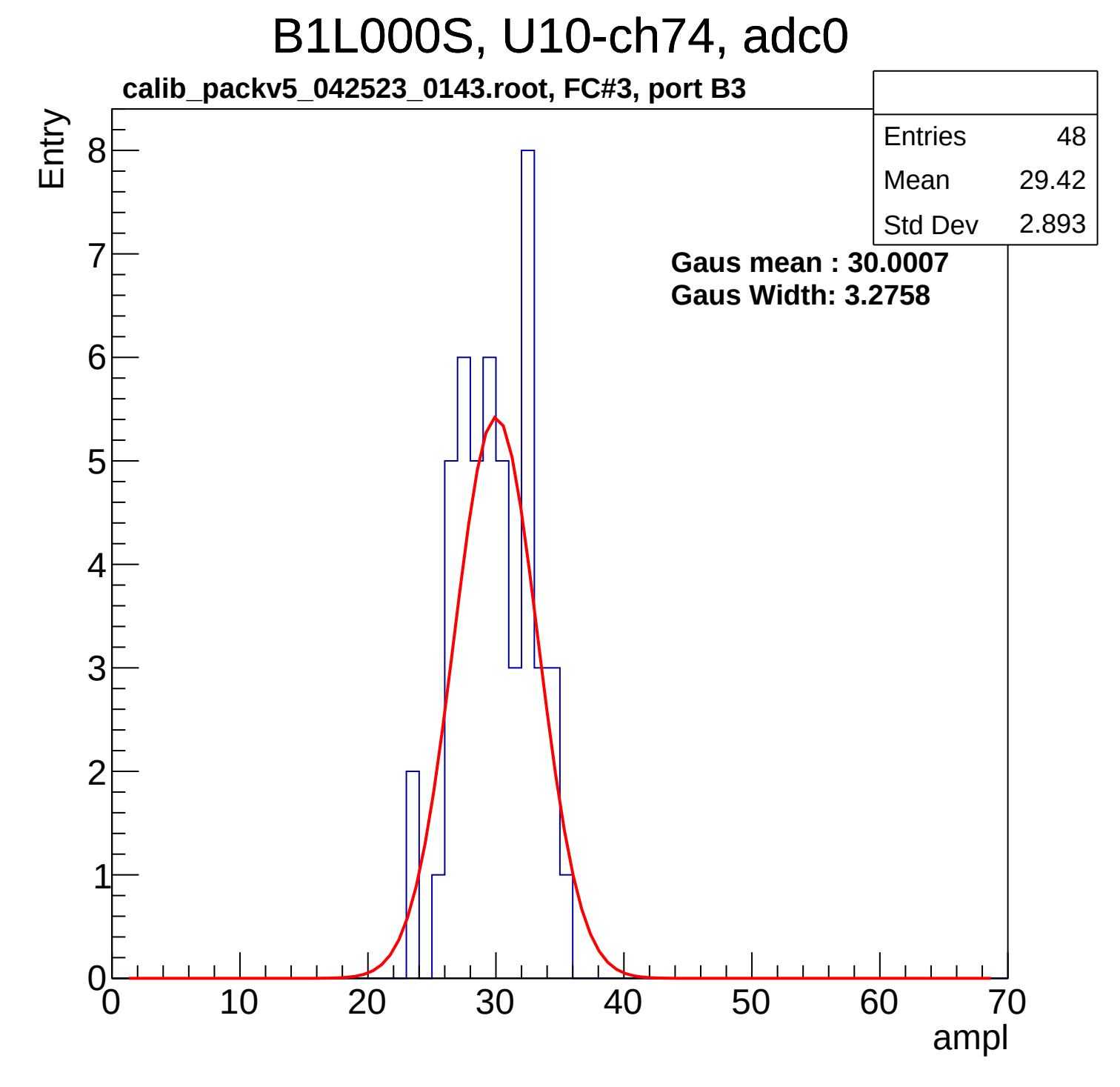

B1L000S, U10-ch73, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch74, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 29.42
Std Dev 2.893
Gaus mean : 30.0007
Gaus Width: 3.2758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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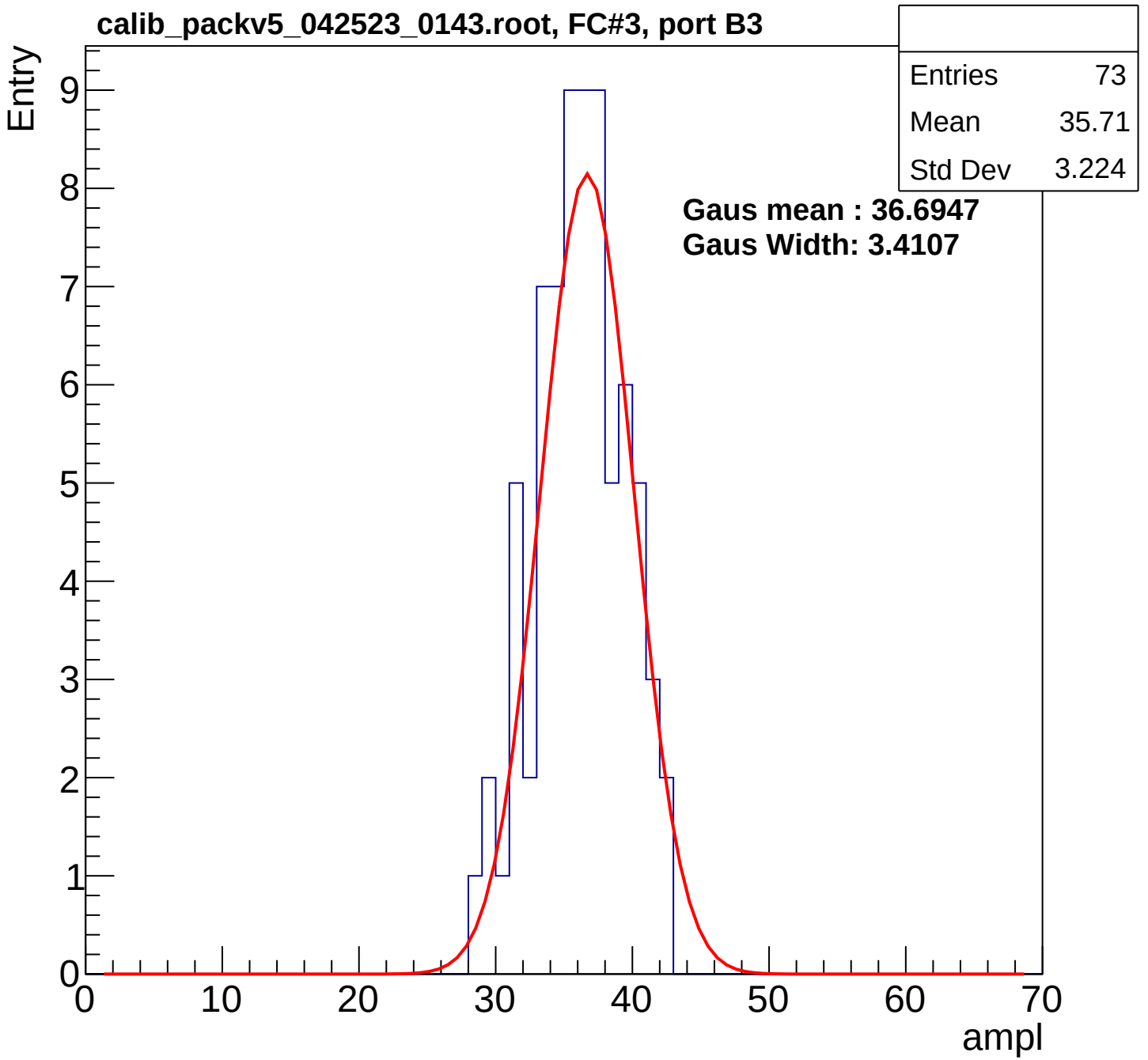
B1L000S, U10-ch74, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 35.71
Std Dev 3.224
Gaus mean : 36.6947
Gaus Width: 3.4107
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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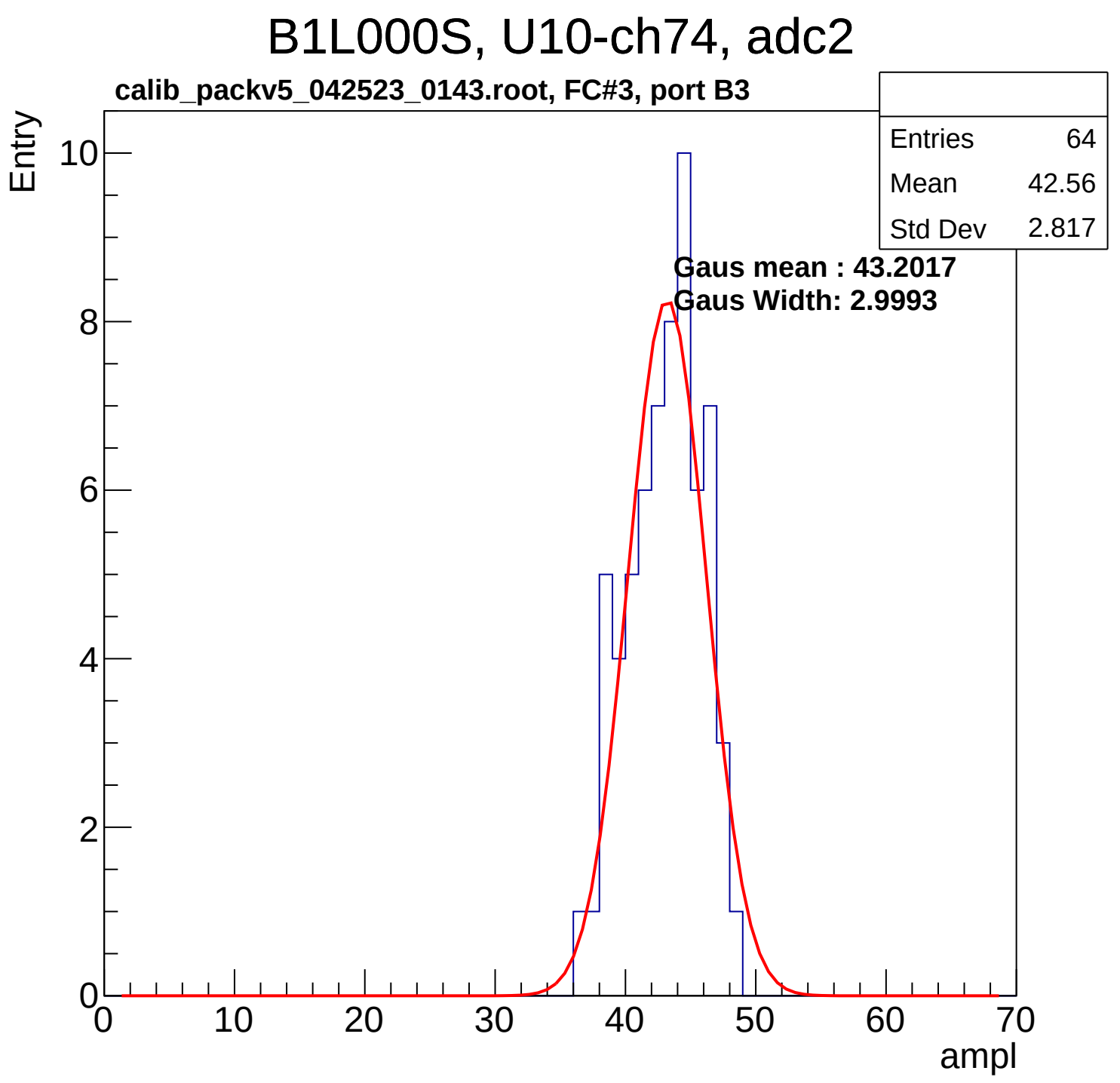
B1L000S, U10-ch74, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 42.56
Std Dev 2.817
Gaus mean : 43.2017
Gaus Width: 2.9993
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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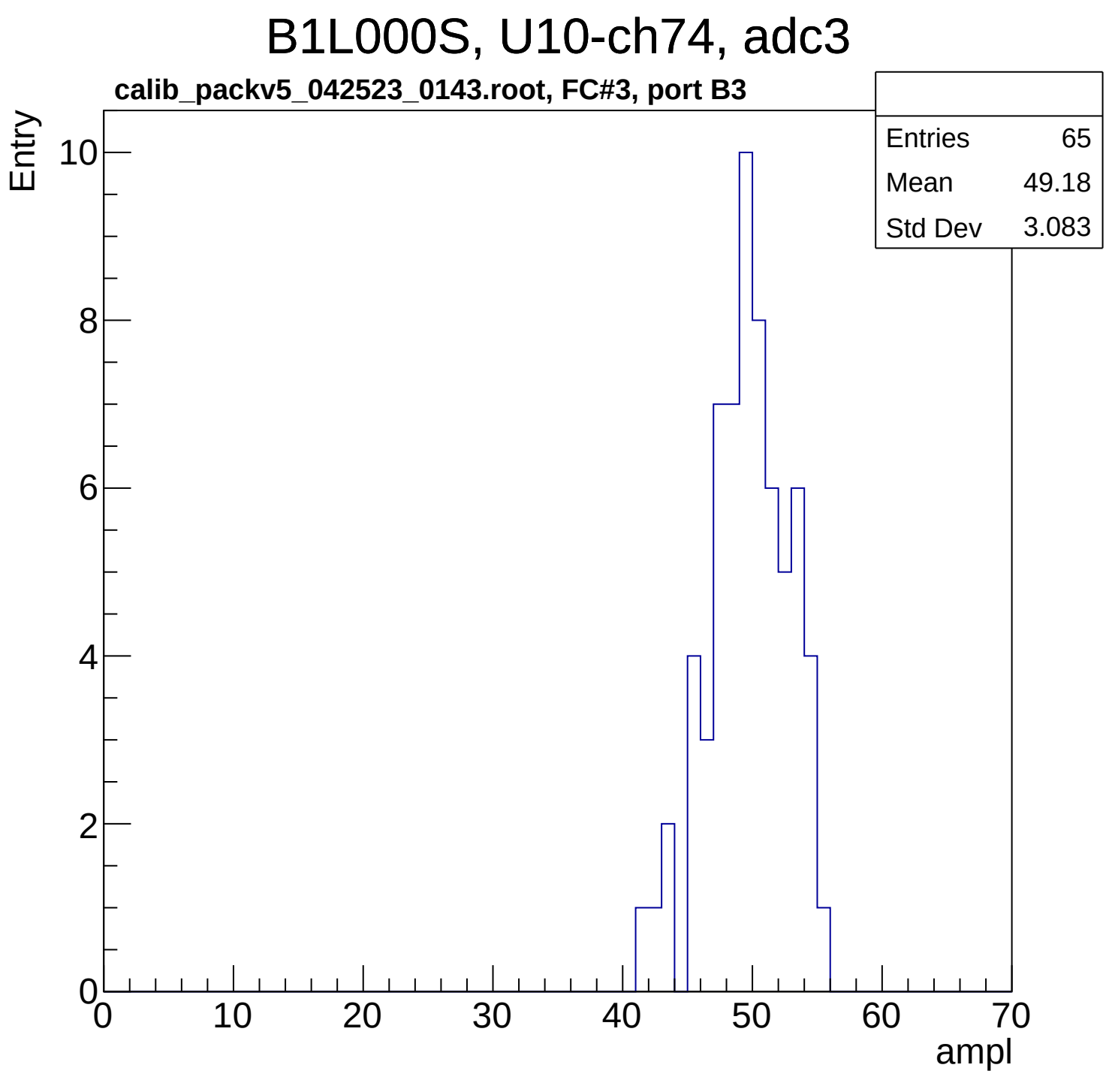
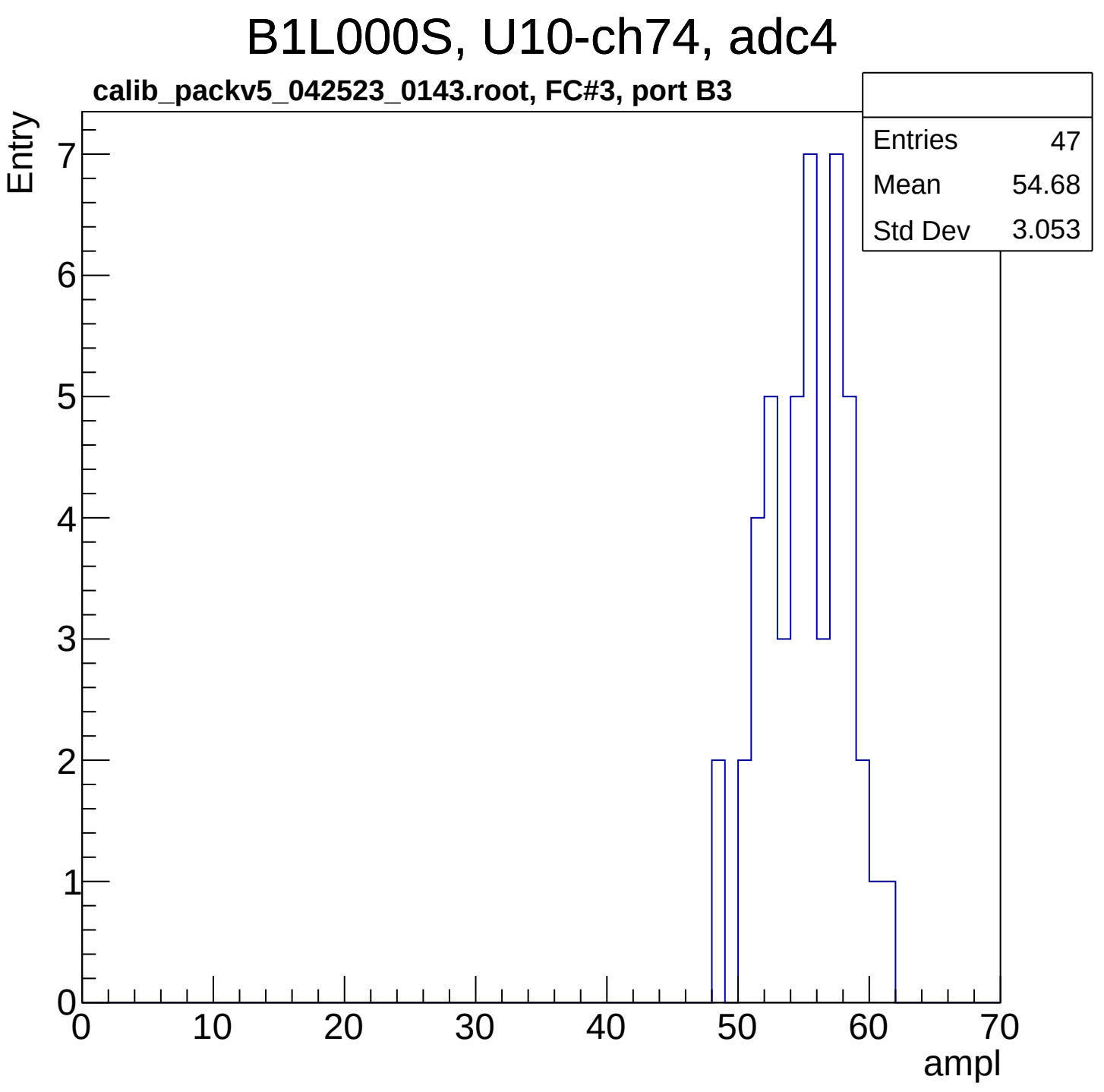
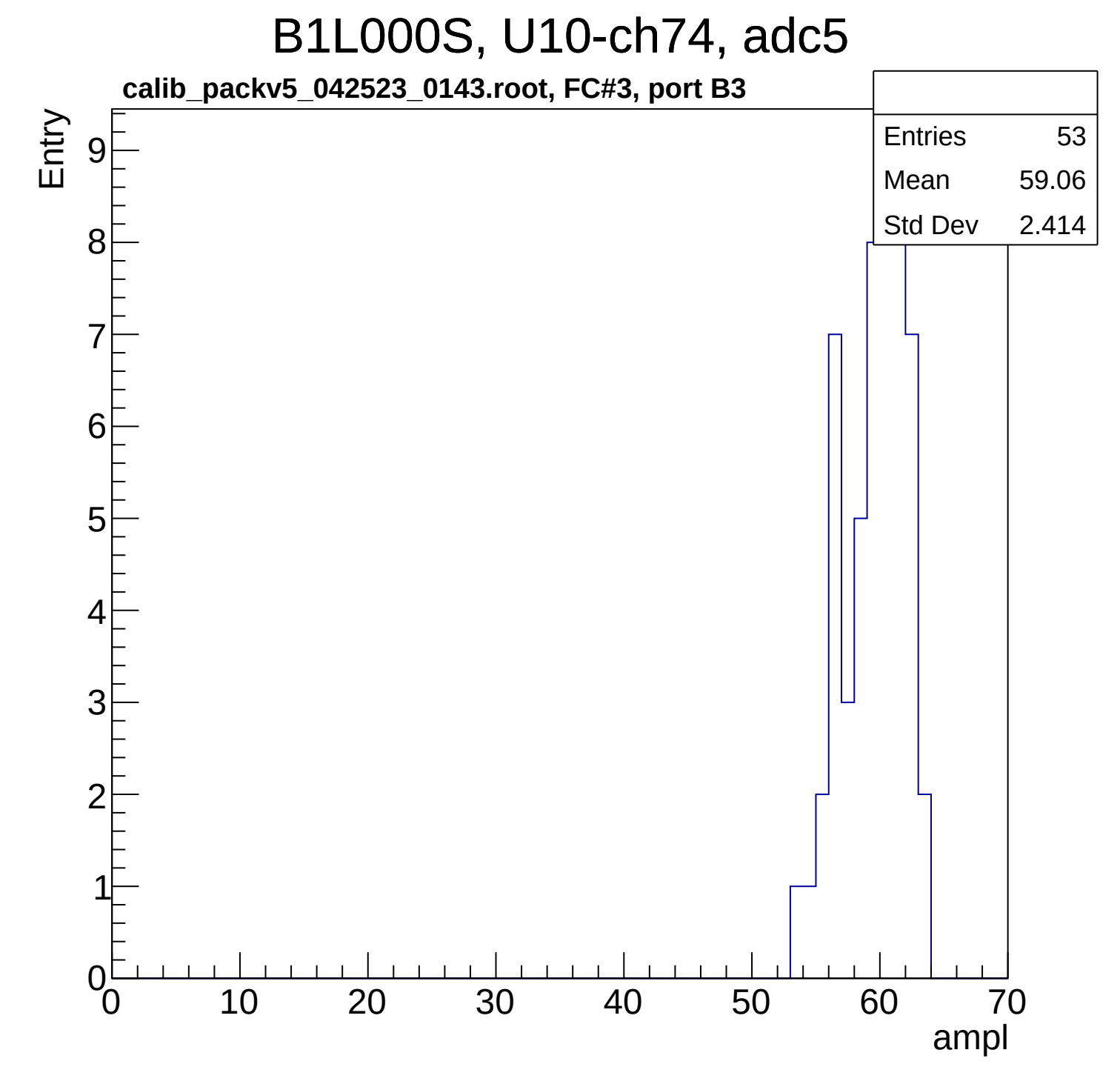

B1L000S, U10-ch74, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 49.18
Std Dev 3.083
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch74, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 54.68
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch74, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 59.06
Std Dev 2.414
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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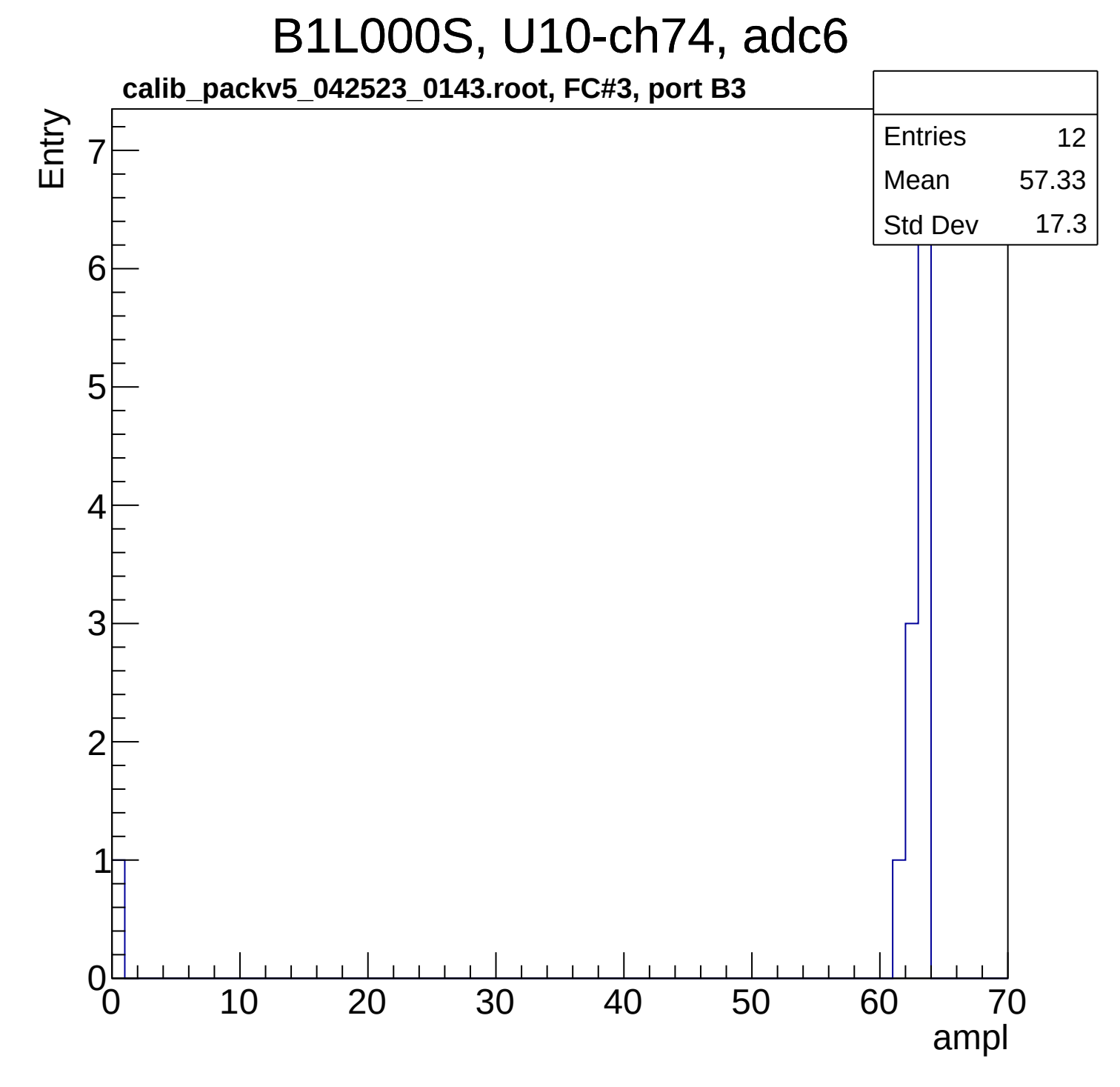
B1L000S, U10-ch74, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 57.33
Std Dev 17.3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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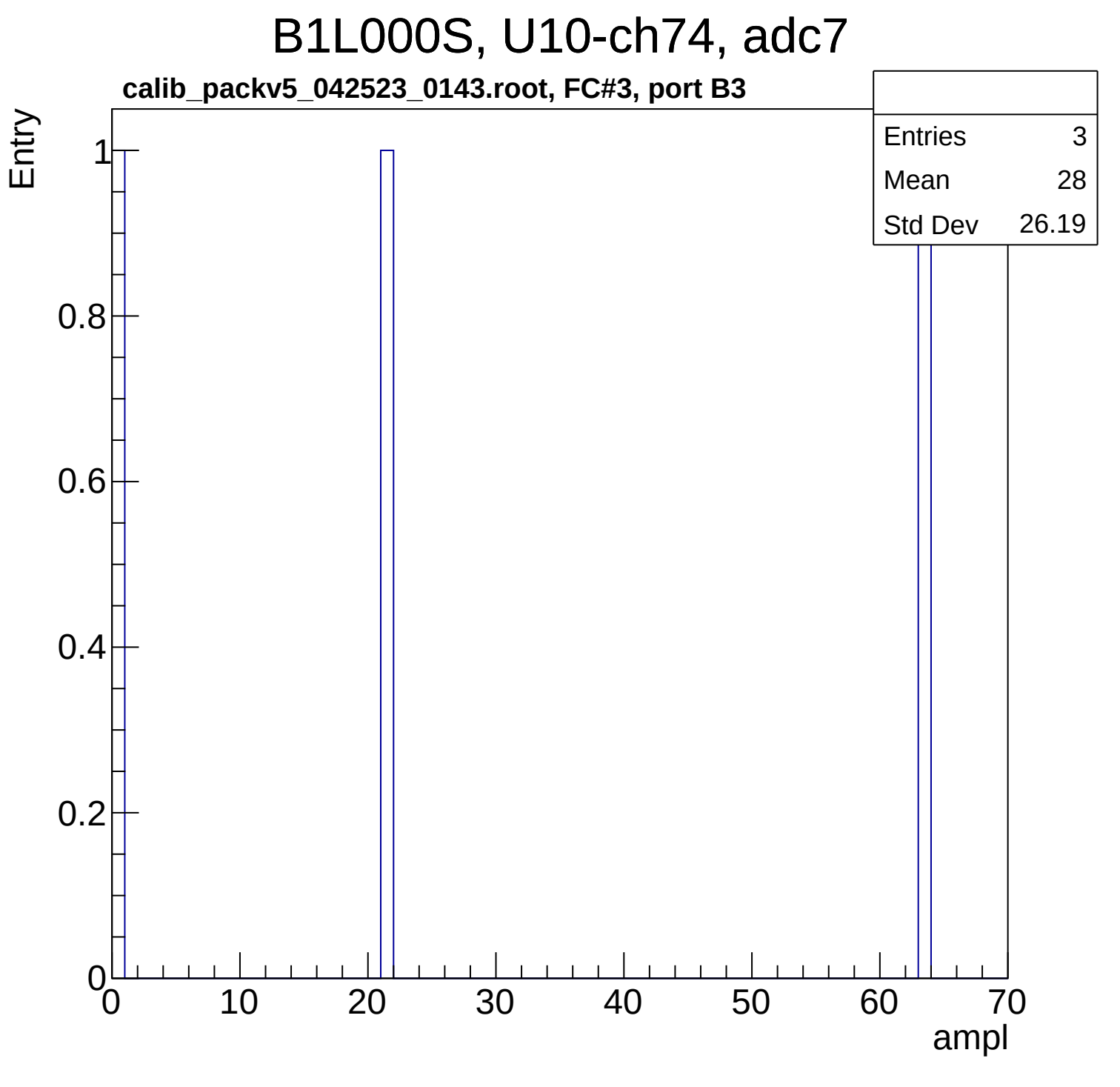
B1L000S, U10-ch74, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
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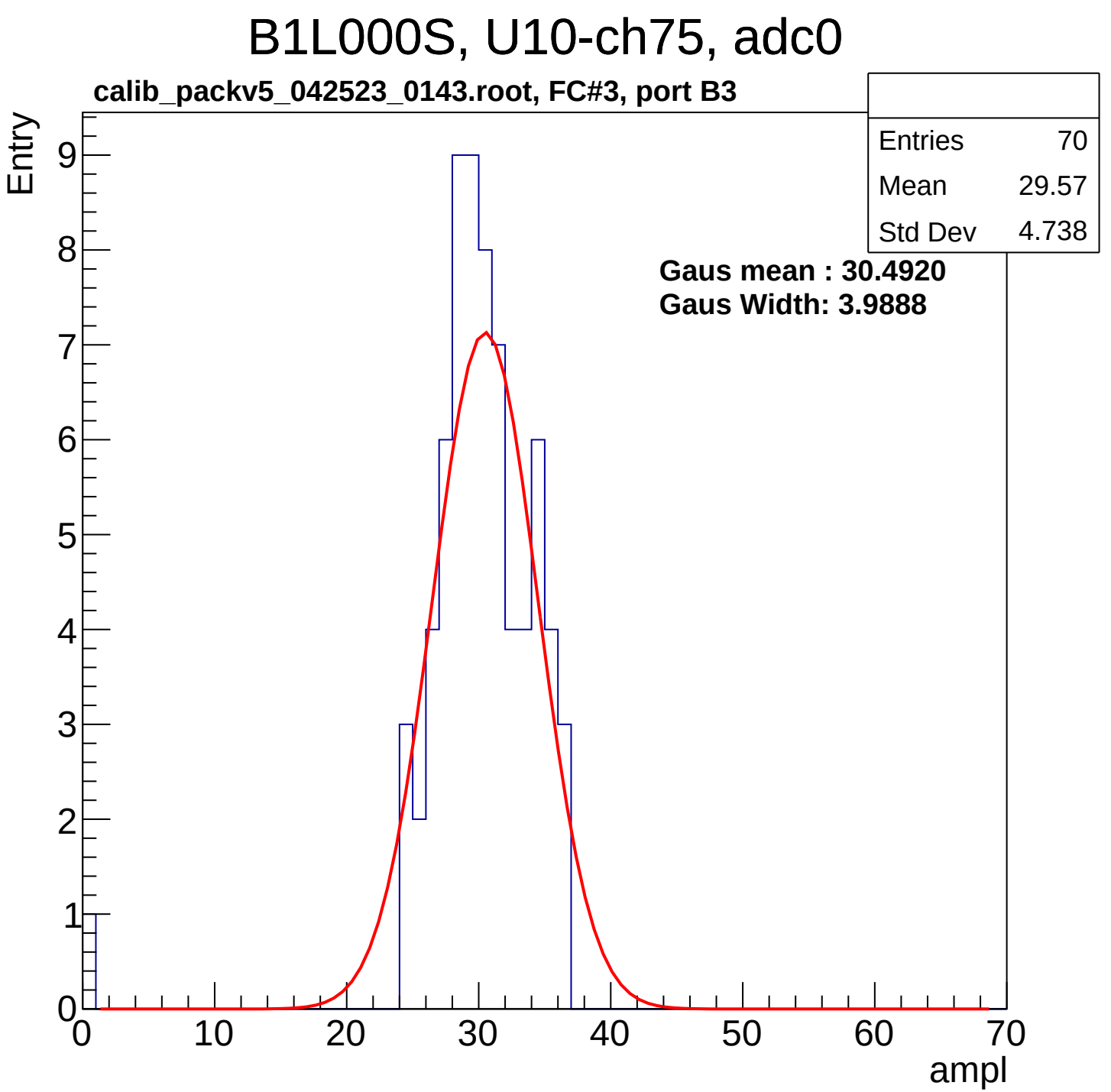
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U10-ch75, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 29.57
Std Dev 4.738
Gaus mean : 30.4920
Gaus Width: 3.9888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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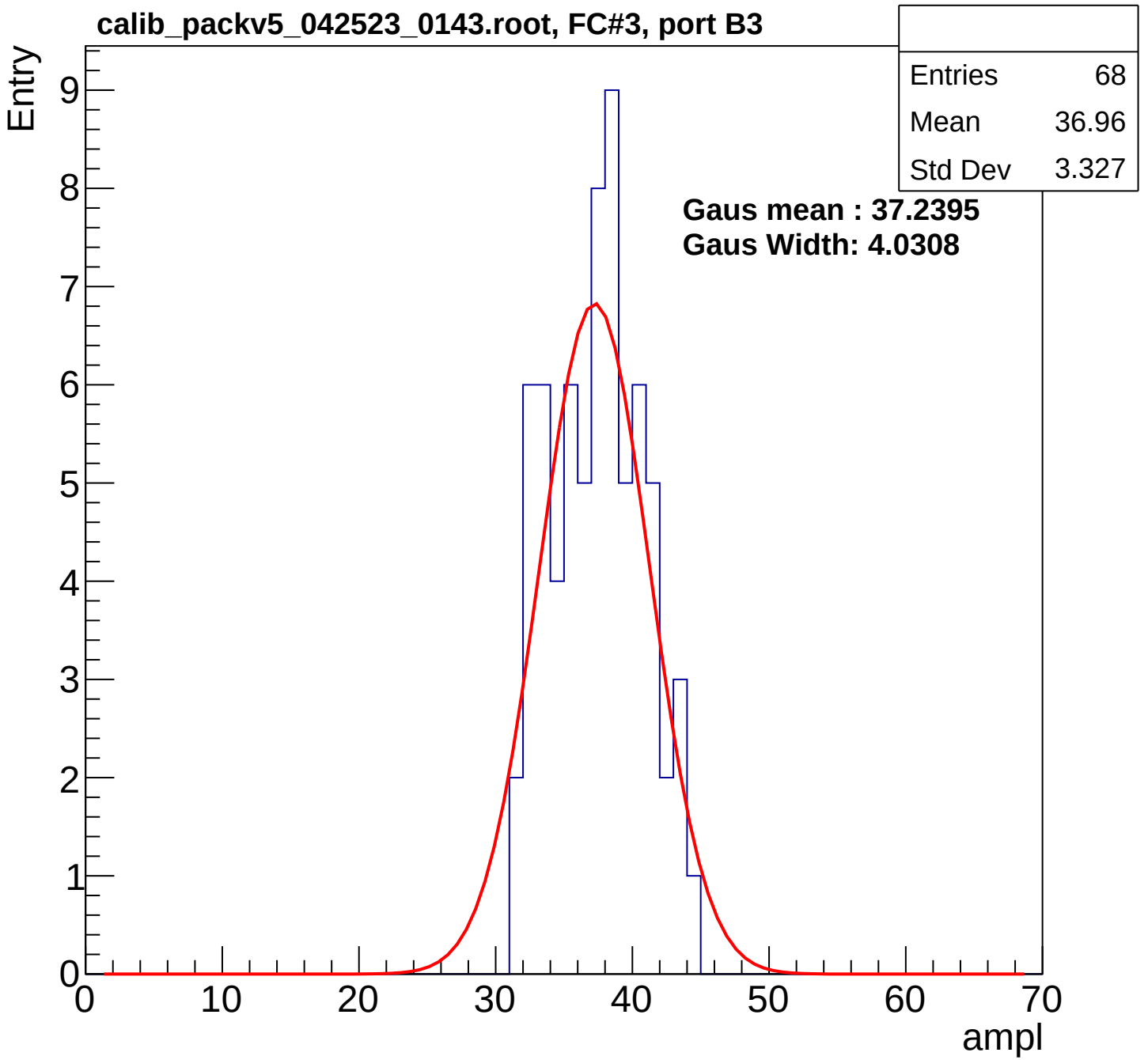
B1L000S, U10-ch75, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 36.96
Std Dev 3.327
Gaus mean : 37.2395
Gaus Width: 4.0308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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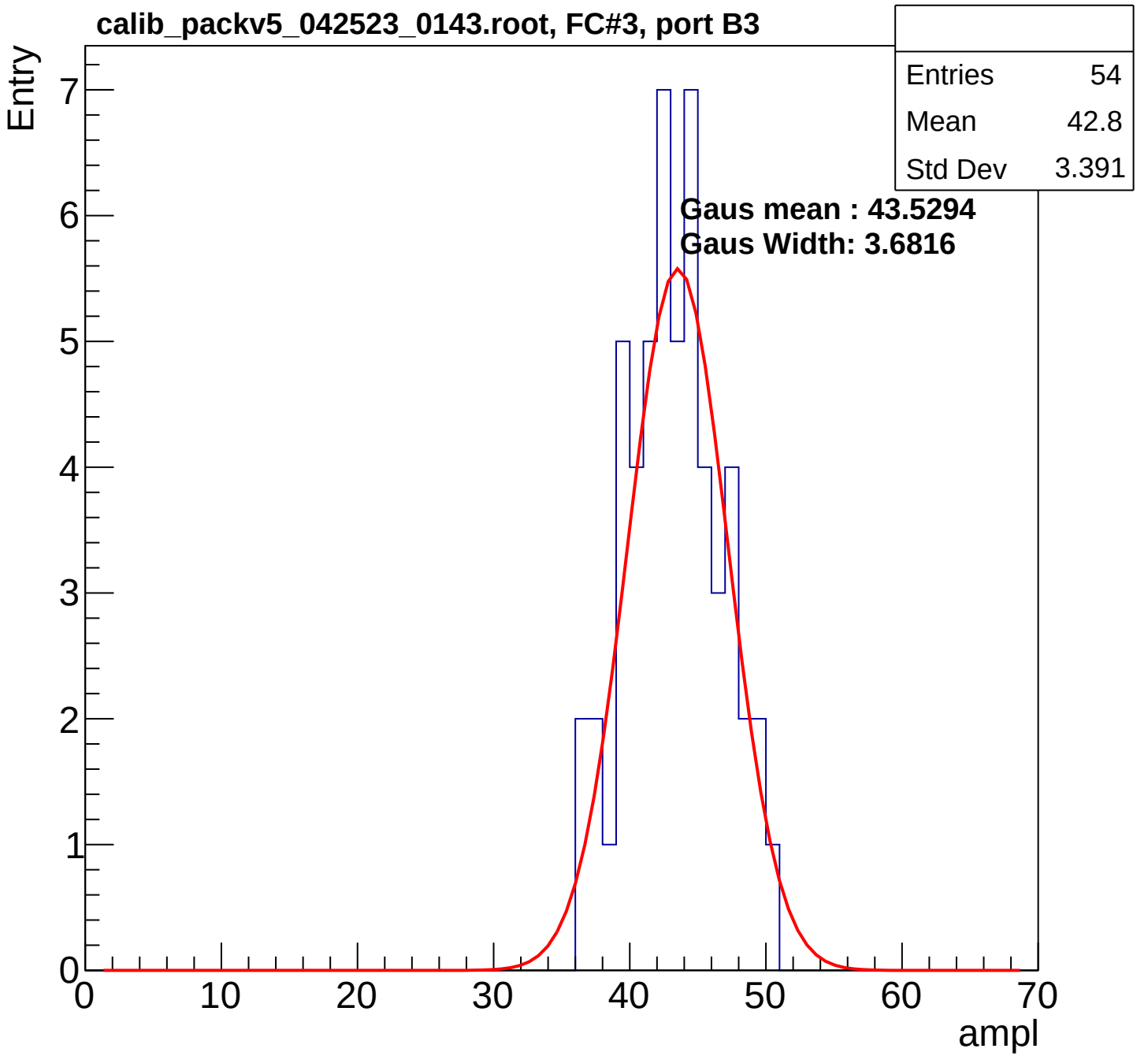
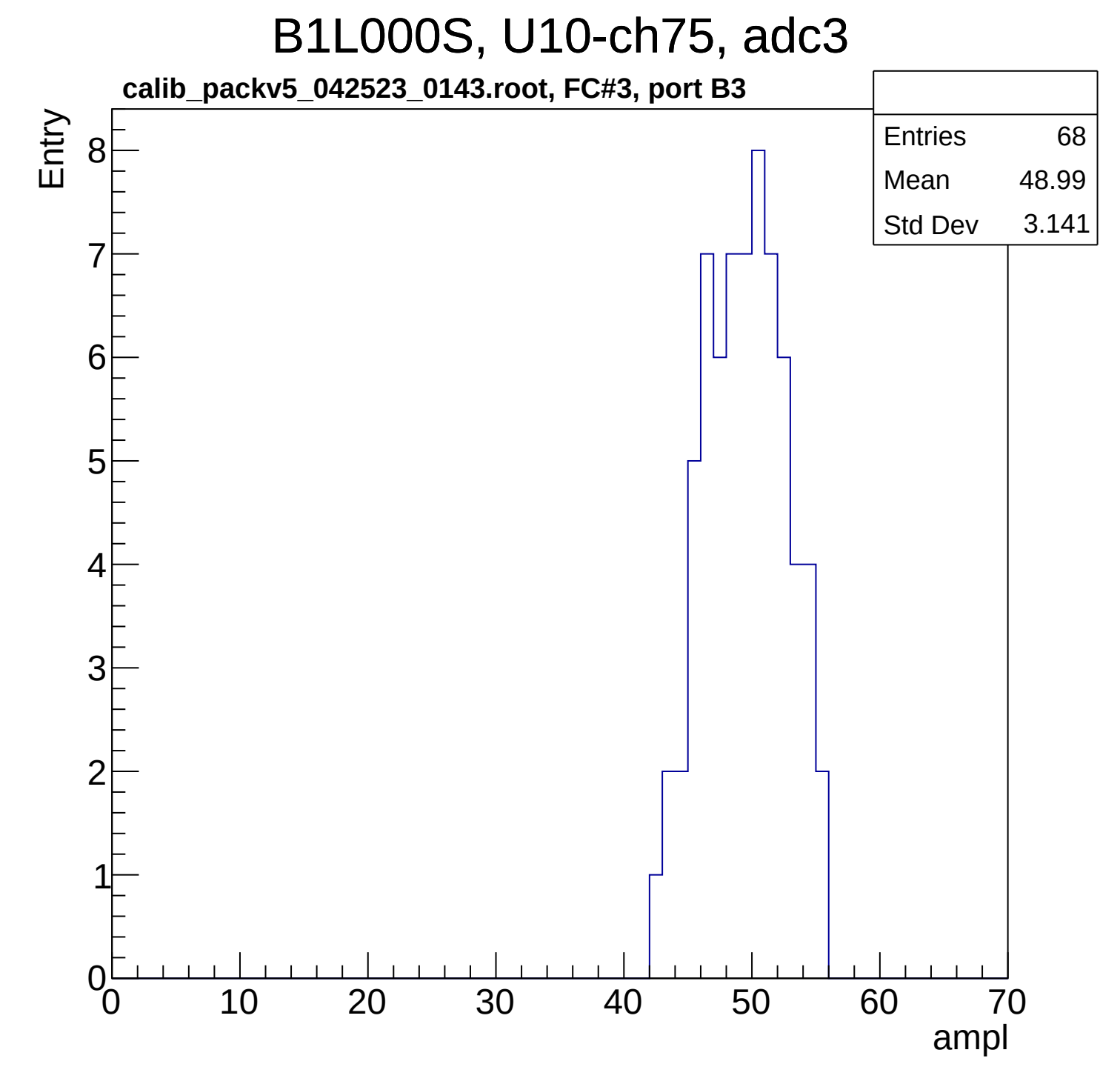

B1L000S, U10-ch75, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 42.8
Std Dev 3.391
Gaus mean : 43.5294
Gaus Width: 3.6816
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch75, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 48.99
Std Dev 3.141
Entry
ampl
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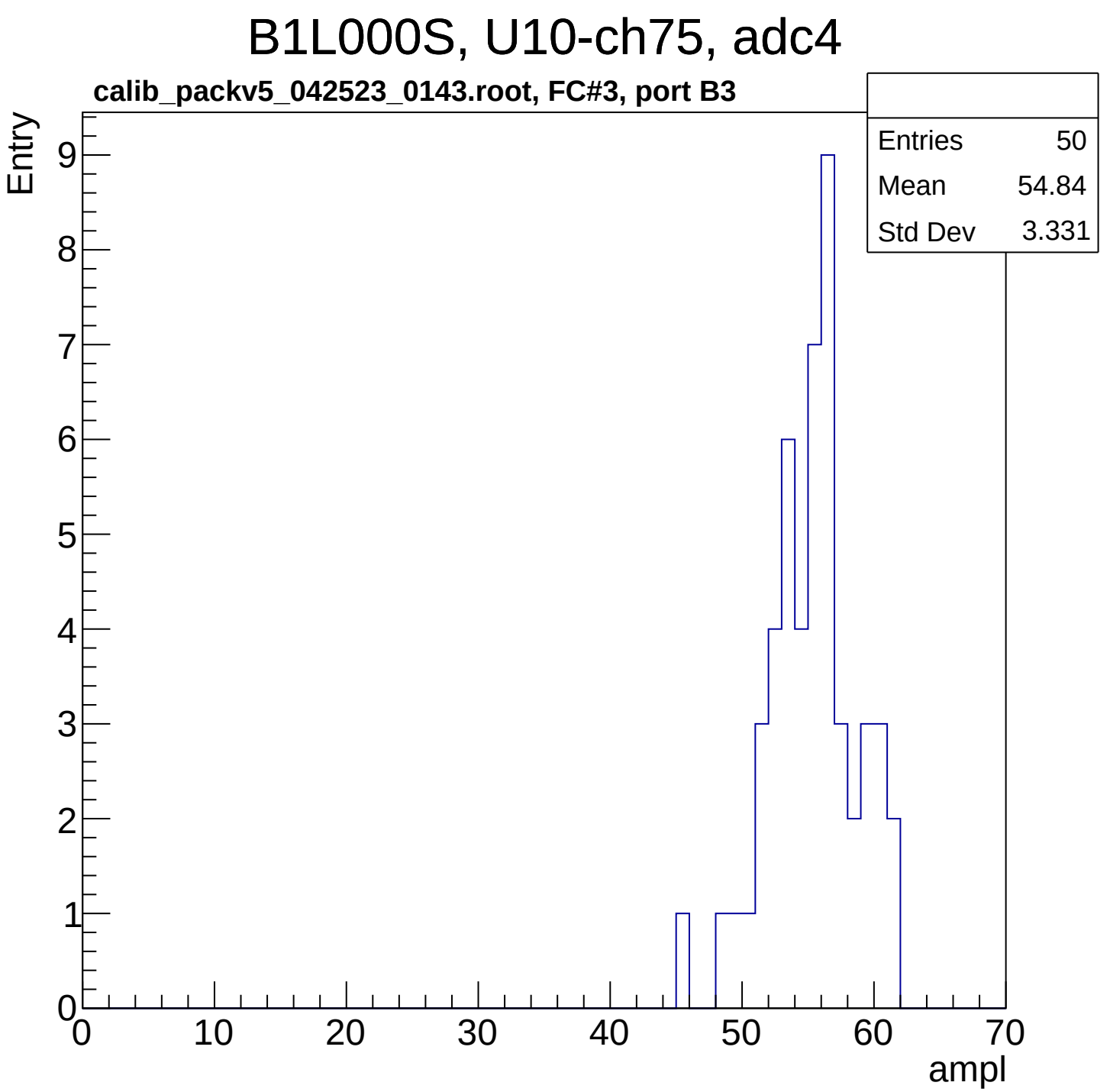
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch75, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 54.84
Std Dev 3.331
Entry
ampl
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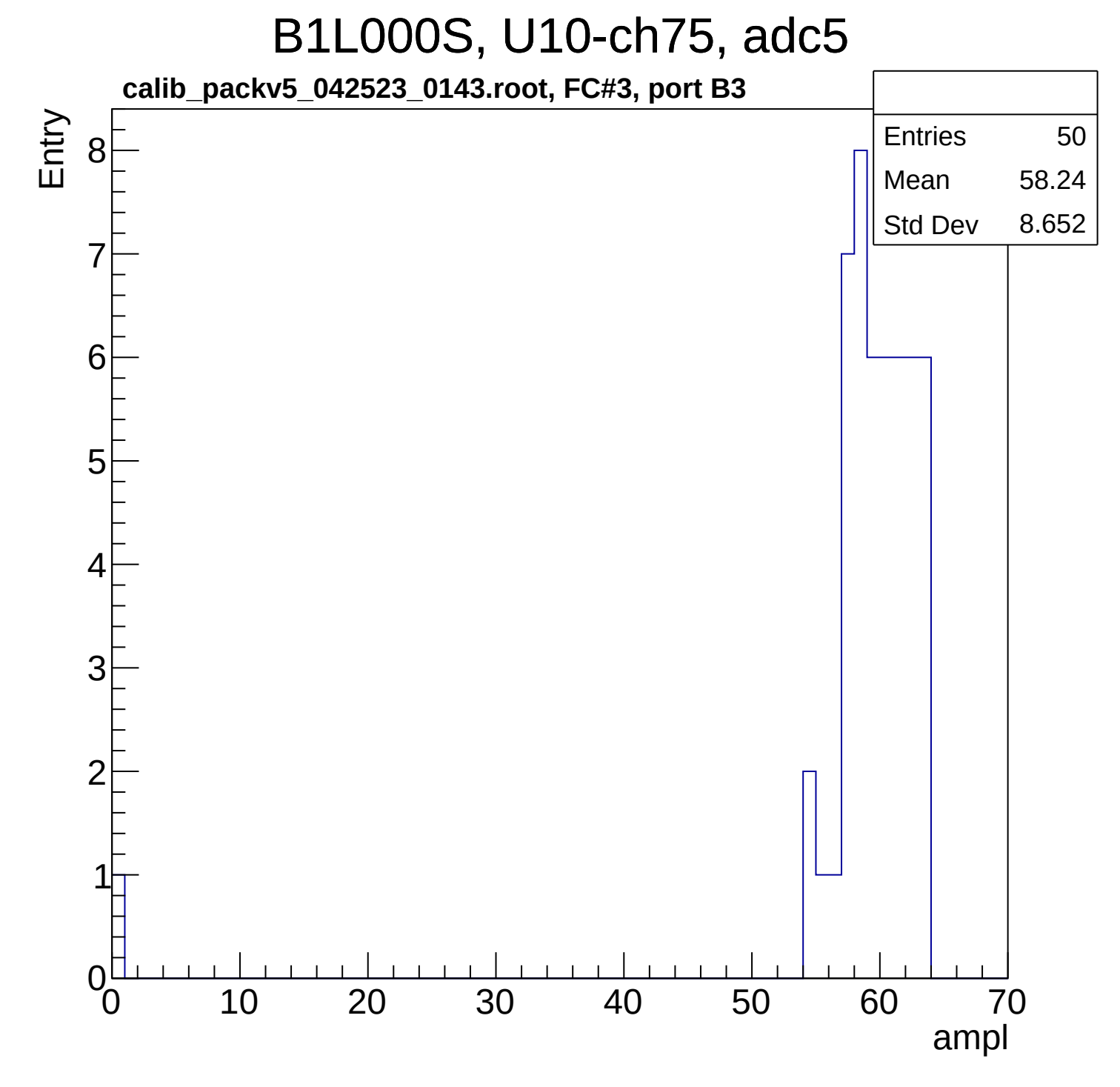
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch75, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.24
Std Dev 8.652
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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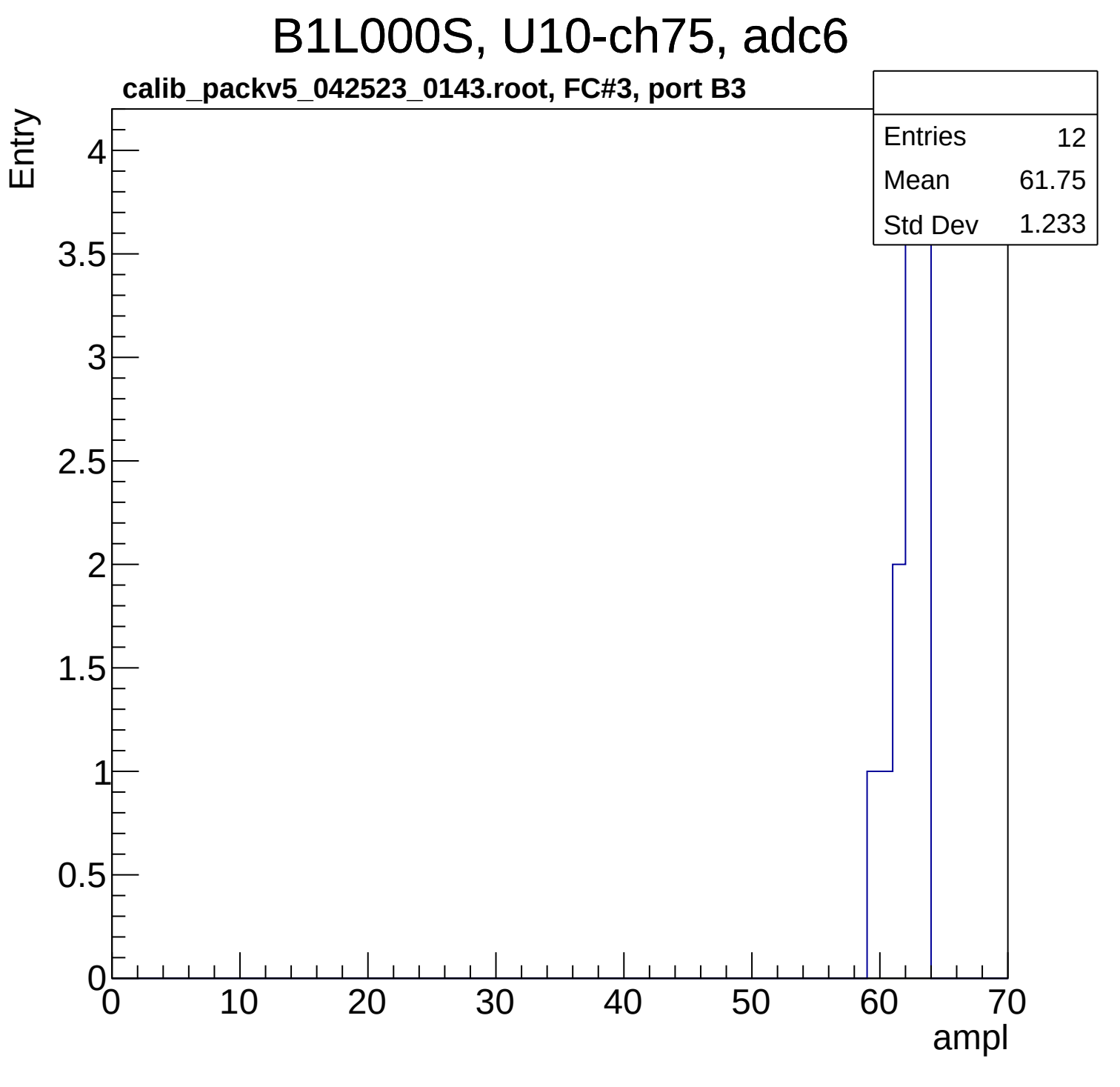
40
50
60
70


B1L000S, U10-ch75, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.75
Std Dev 1.233
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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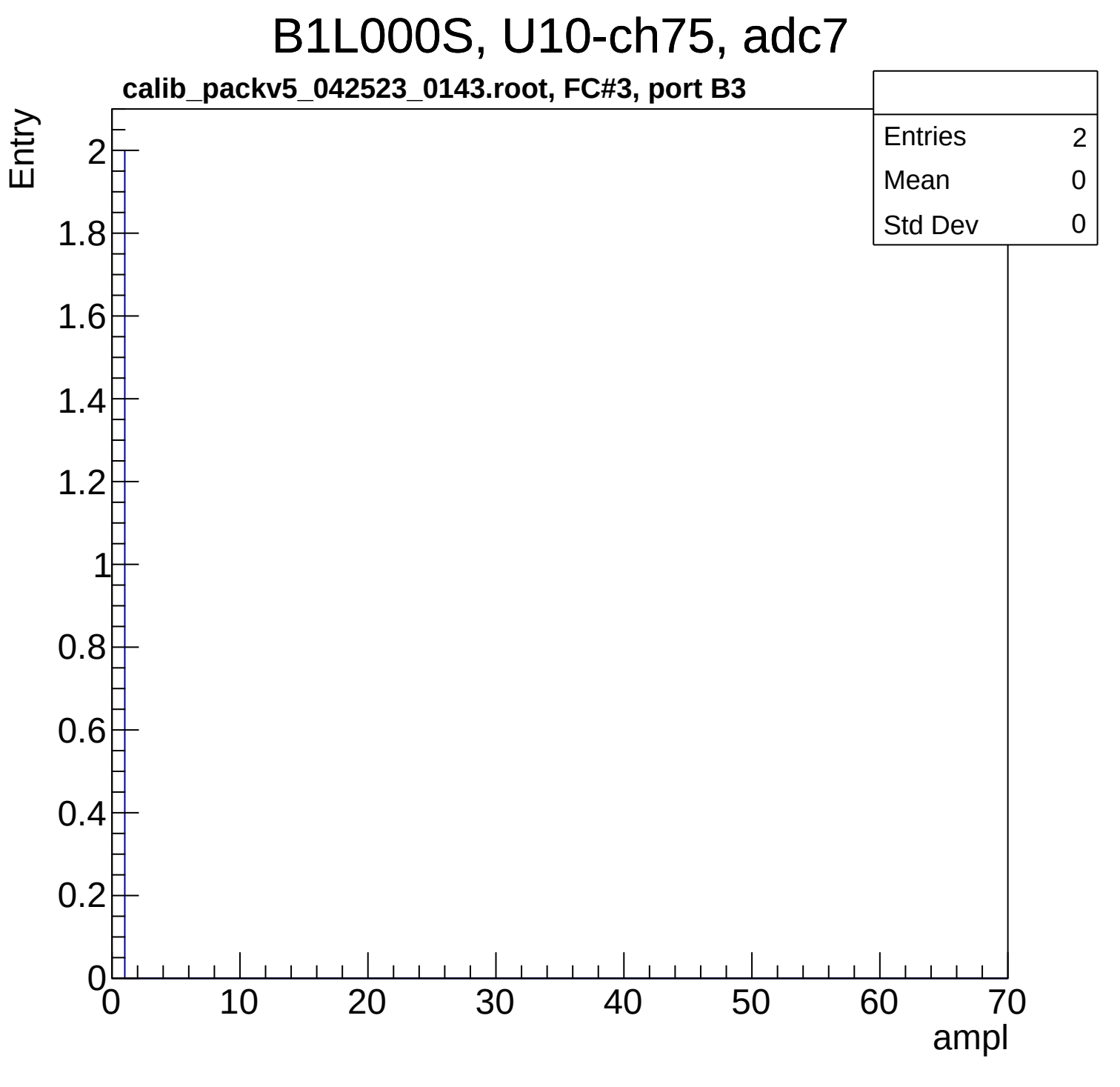
40
50
60
70


B1L000S, U10-ch75, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
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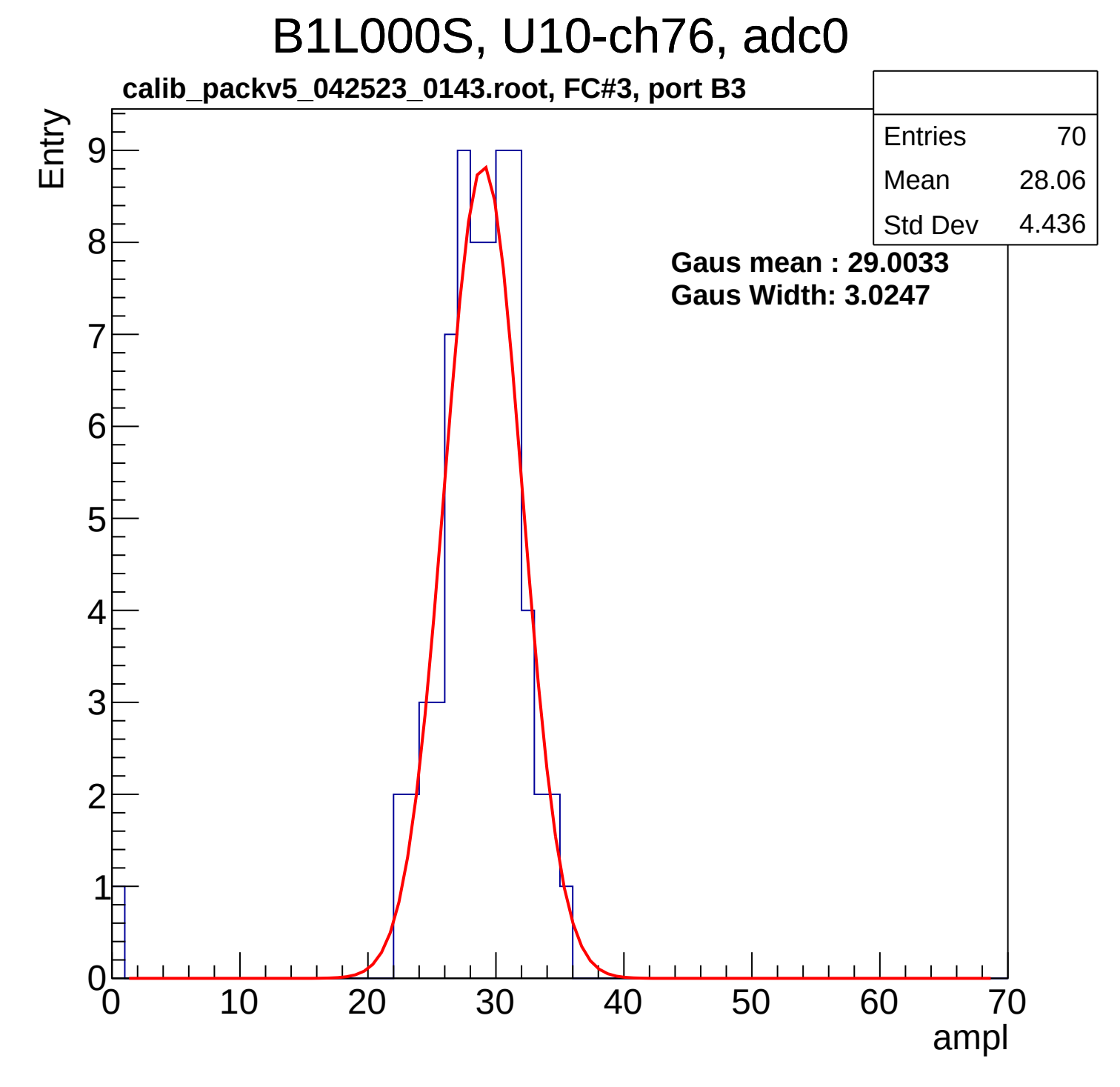
50
60
70
ampl


B1L000S, U10-ch76, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 28.06
Std Dev 4.436
Gaus mean : 29.0033
Gaus Width: 3.0247
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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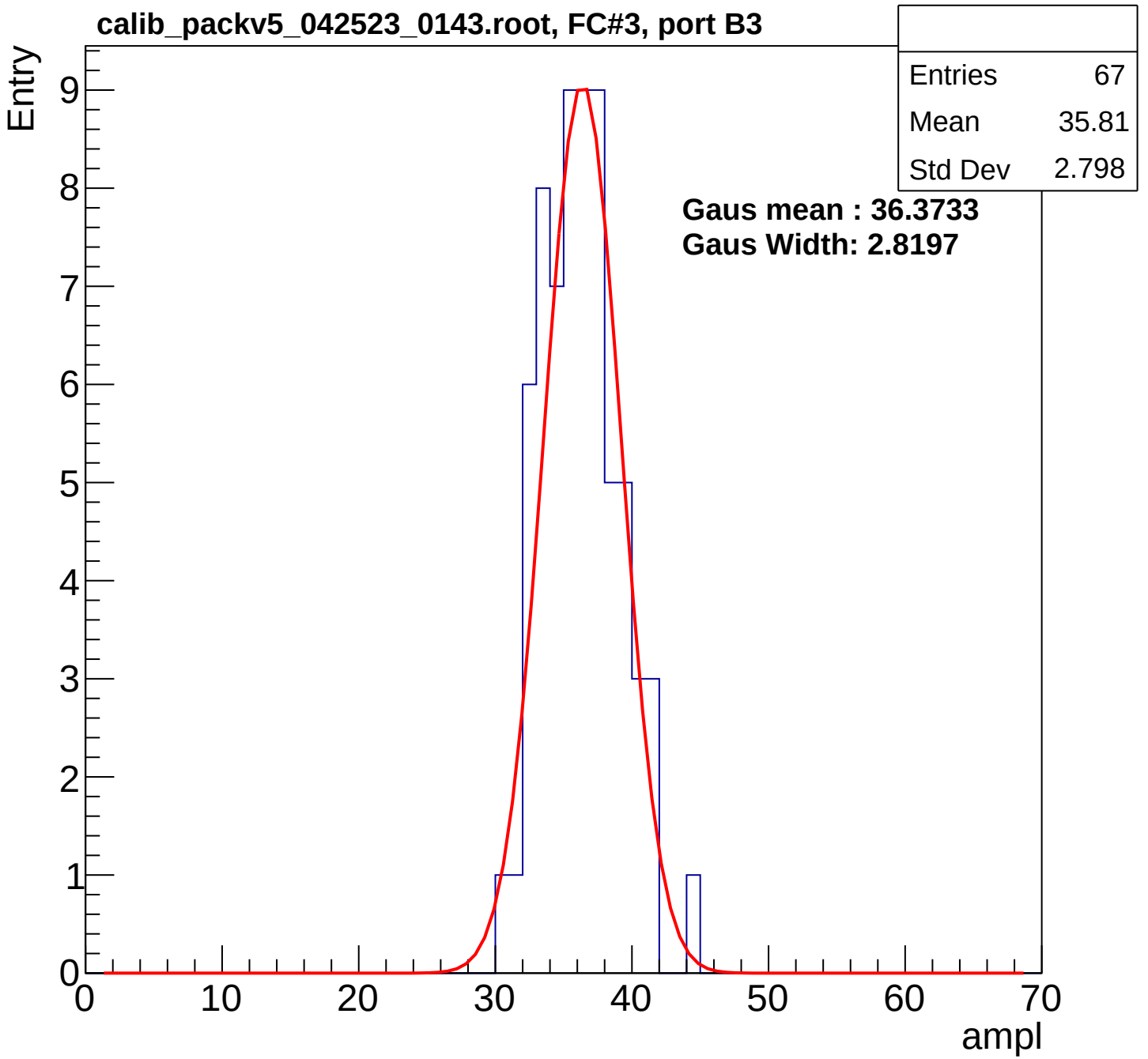
B1L000S, U10-ch76, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.81
Std Dev 2.798
Gaus mean : 36.3733
Gaus Width: 2.8197
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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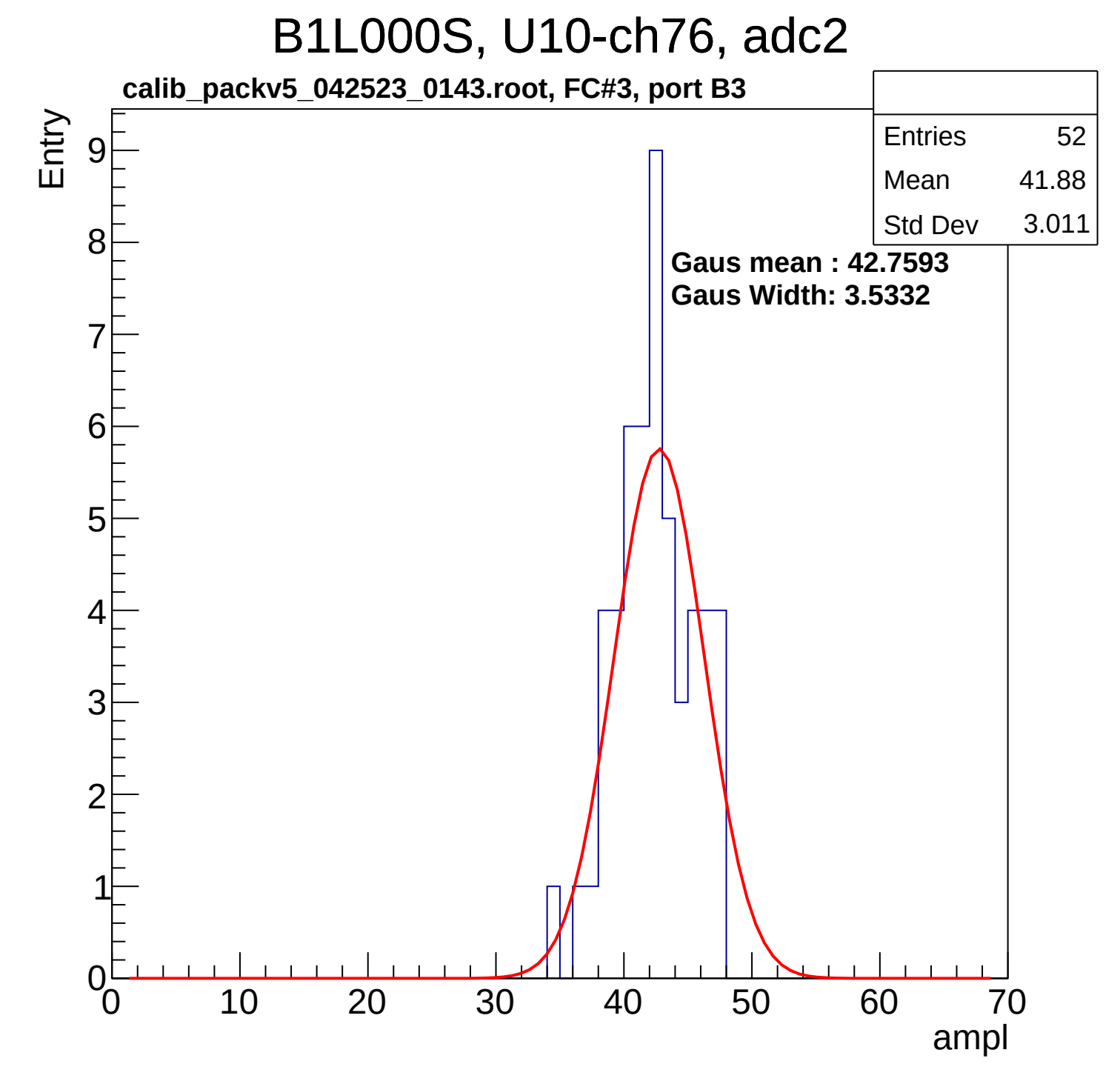
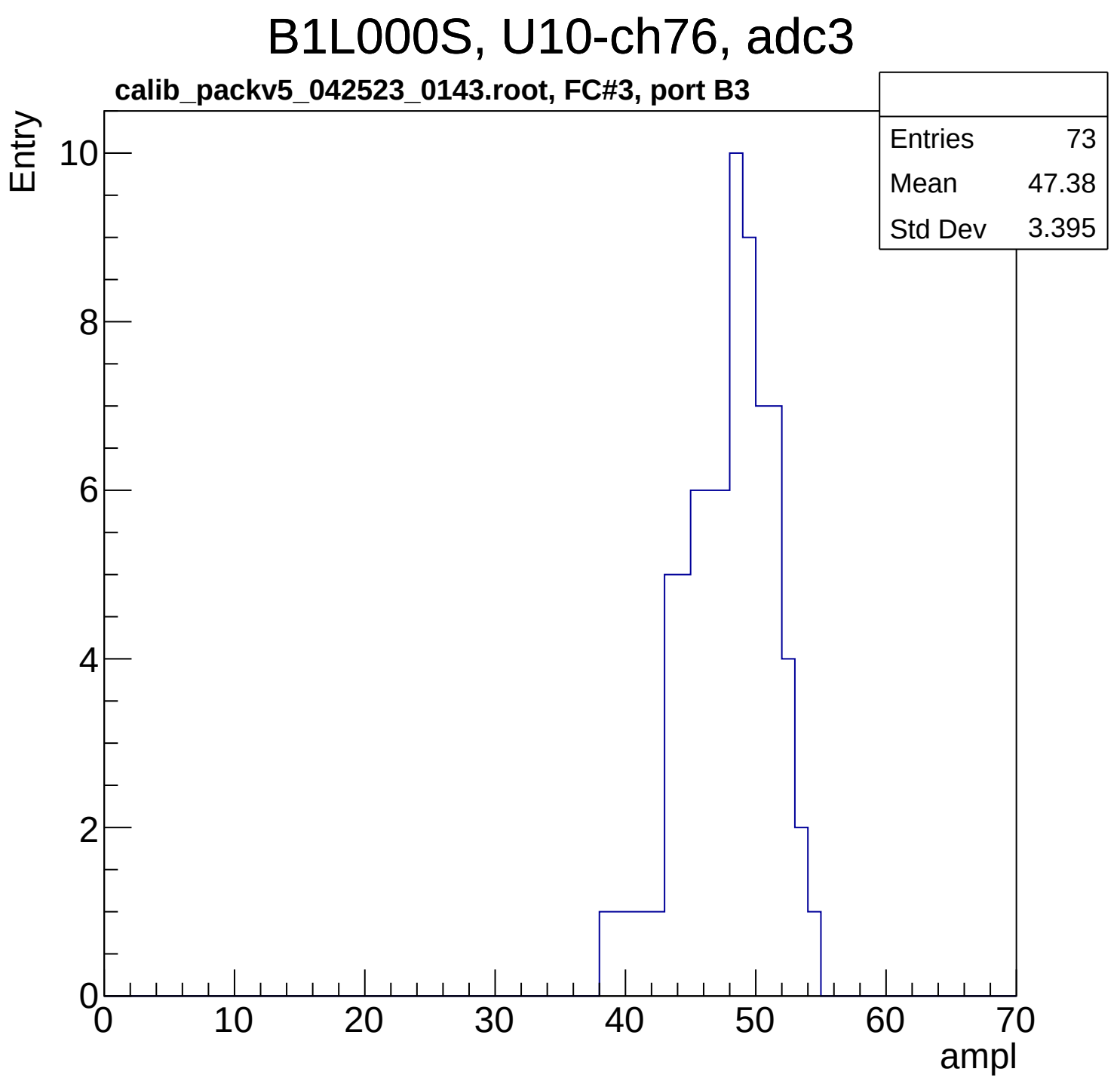

B1L000S, U10-ch76, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 41.88
Std Dev 3.011
Gaus mean : 42.7593
Gaus Width: 3.5332
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch76, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 47.38
Std Dev 3.395
Entry
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2
4
6
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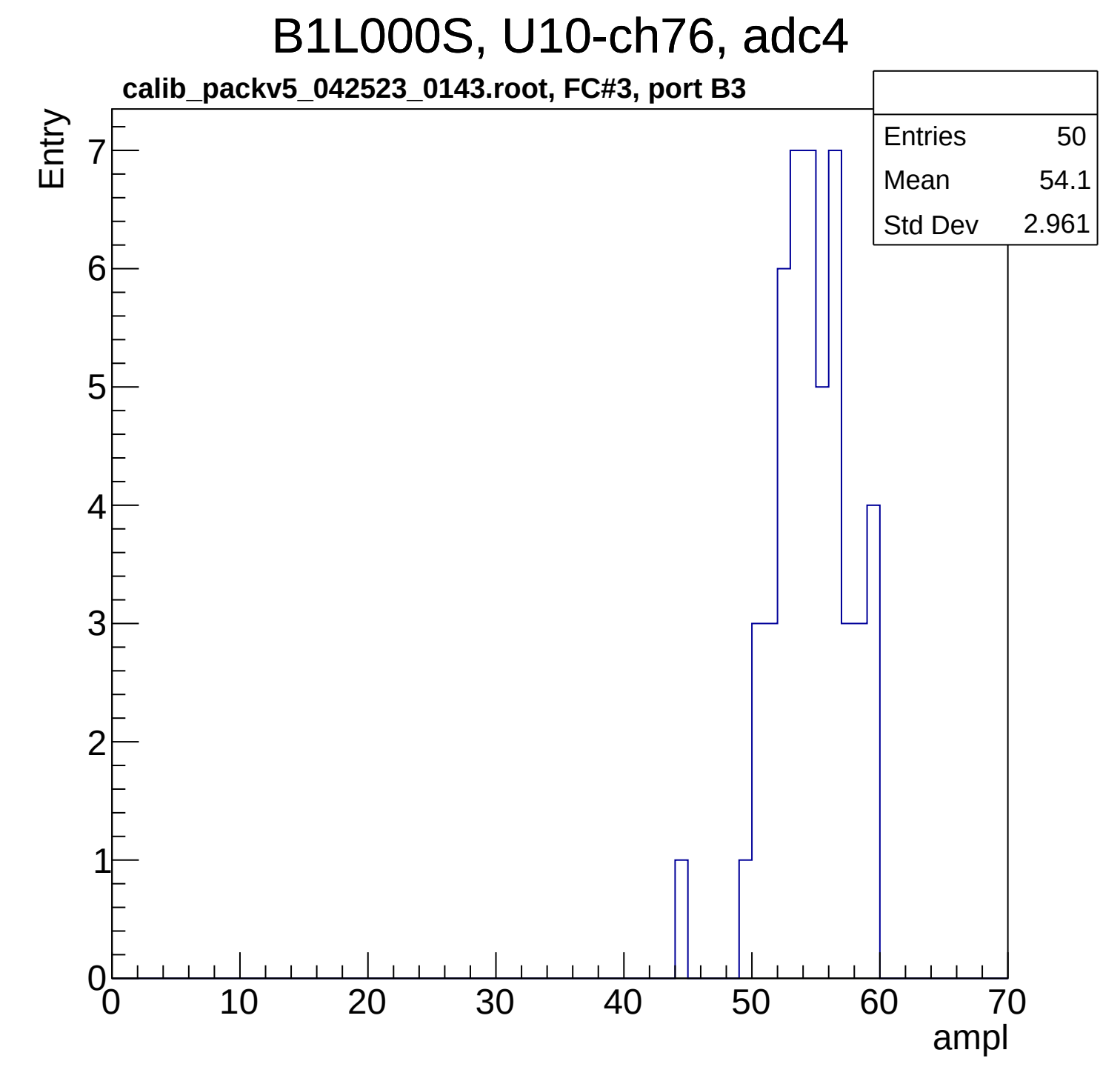
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch76, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 54.1
Std Dev 2.961
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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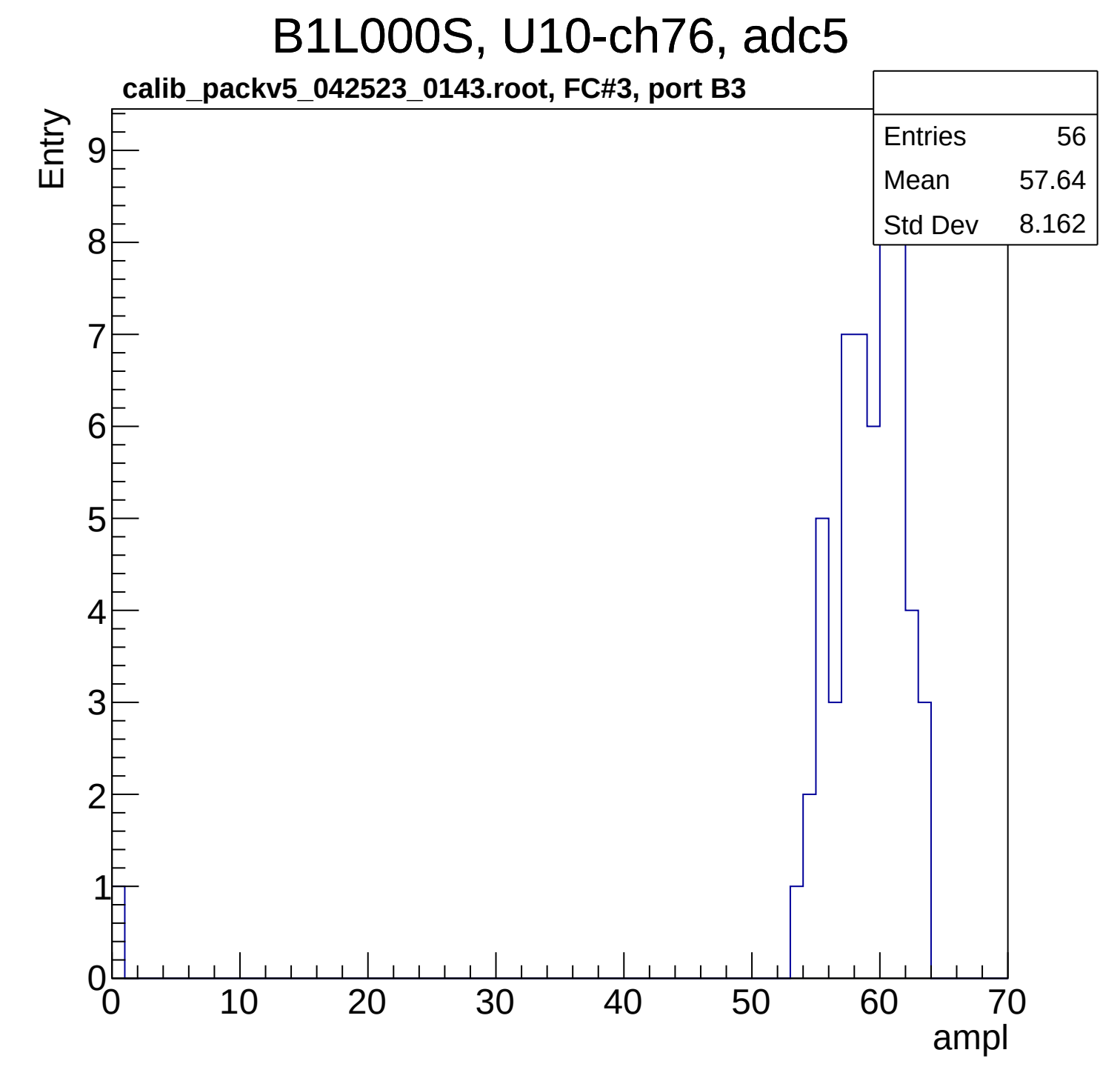
40
50
60
70


B1L000S, U10-ch76, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 57.64
Std Dev 8.162
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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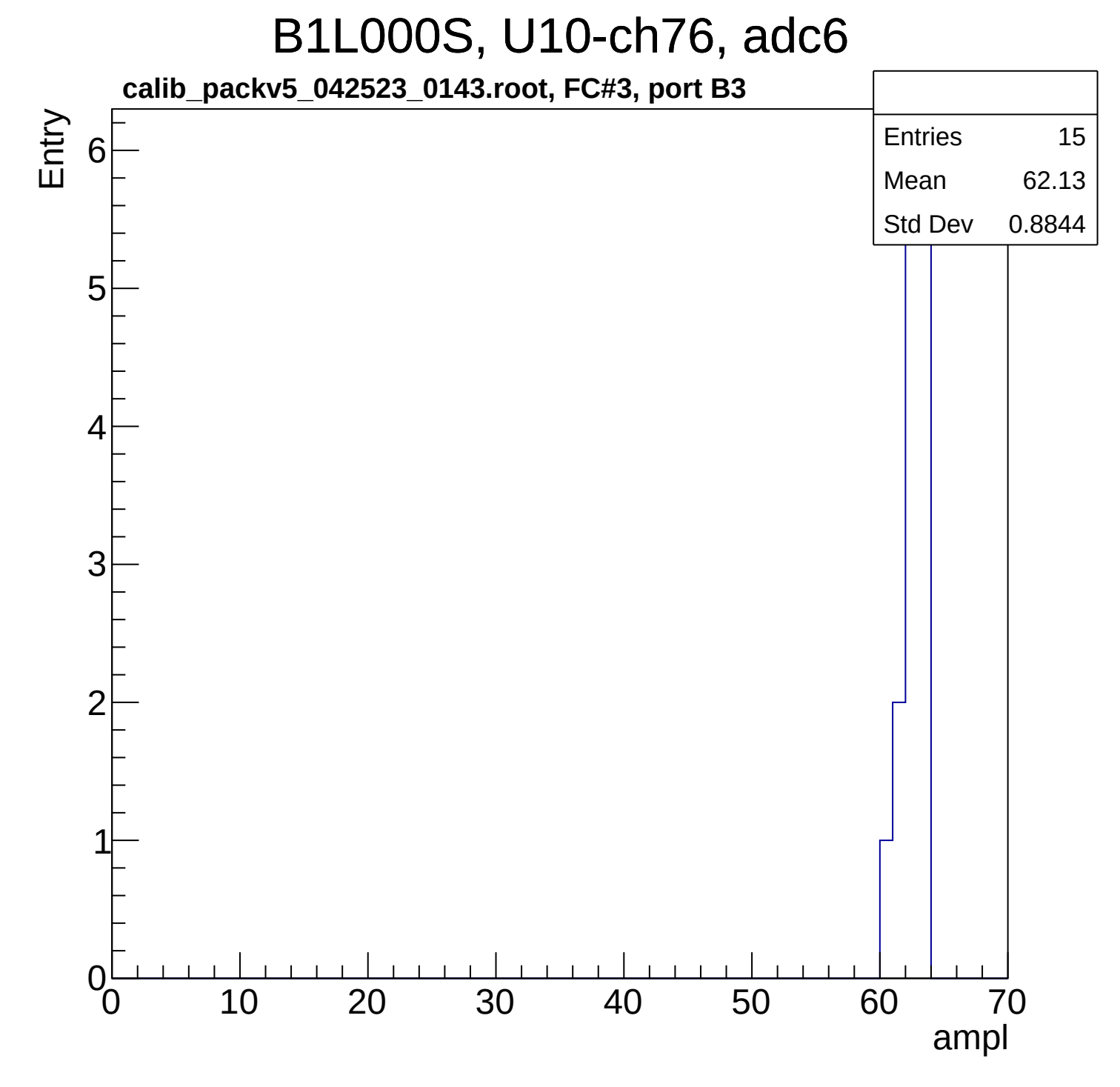
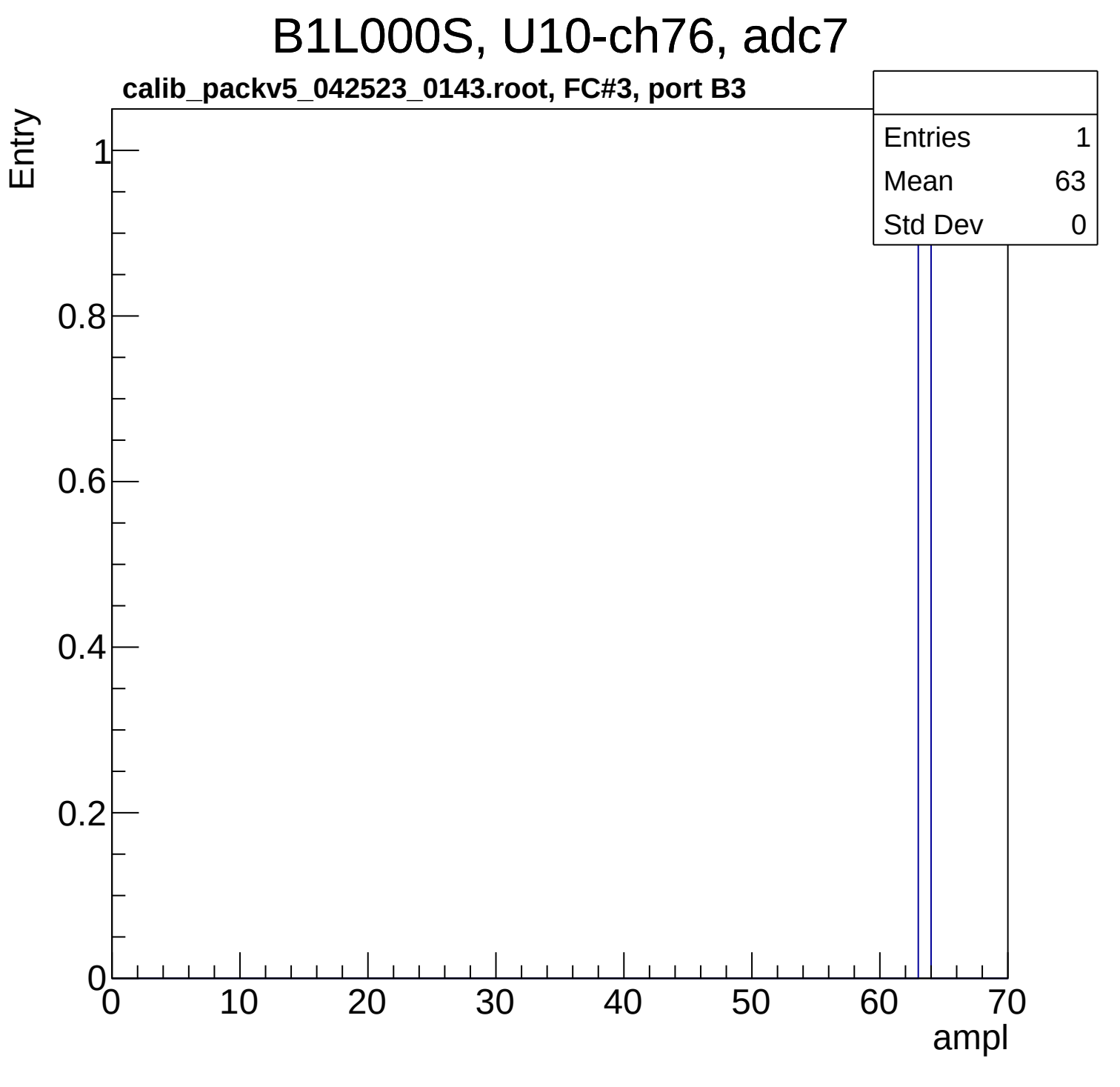

B1L000S, U10-ch76, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 62.13
Std Dev 0.8844
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch76, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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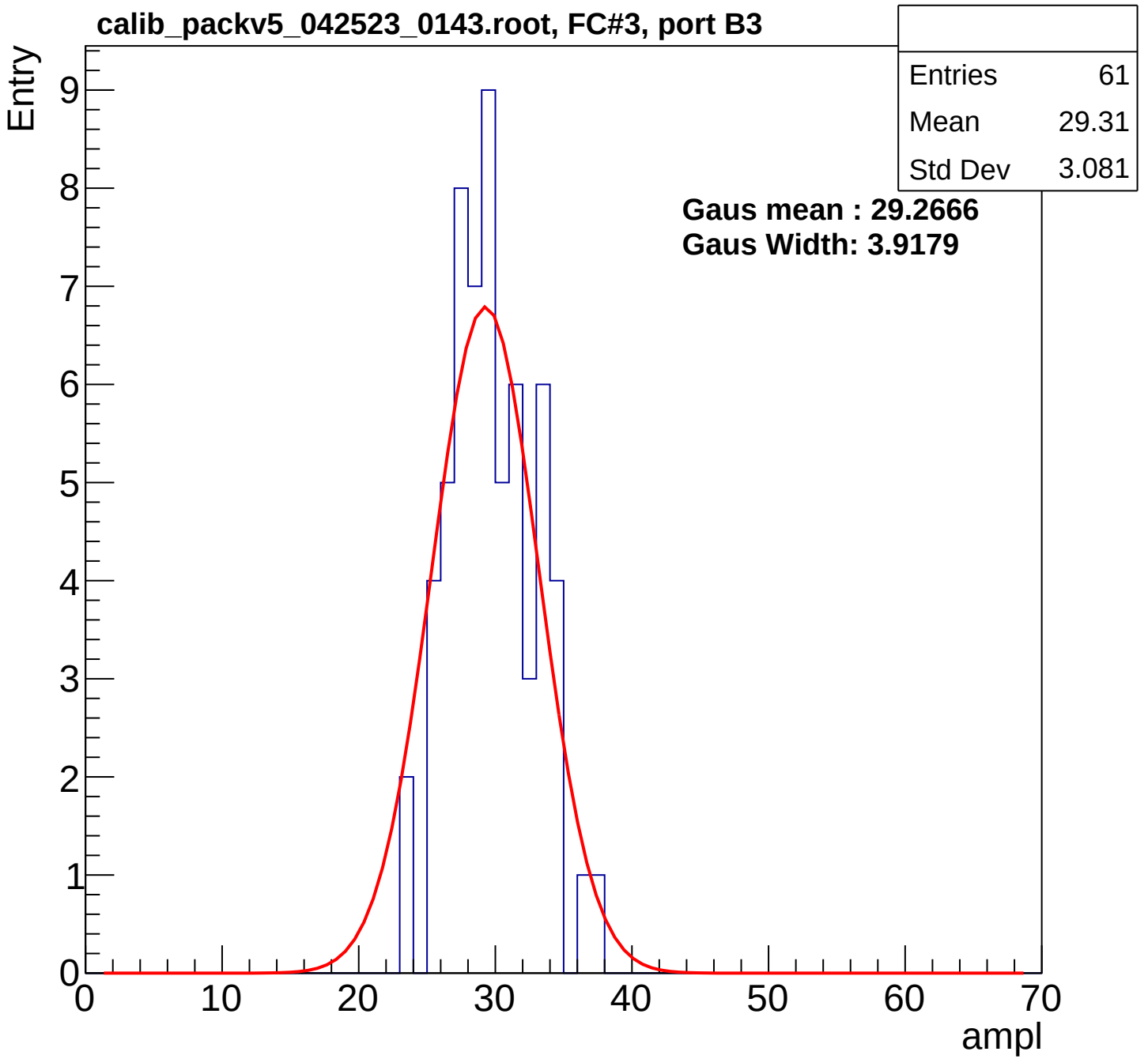
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch77, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 29.31
Std Dev 3.081
Gaus mean : 29.2666
Gaus Width: 3.9179
Entry
ampl
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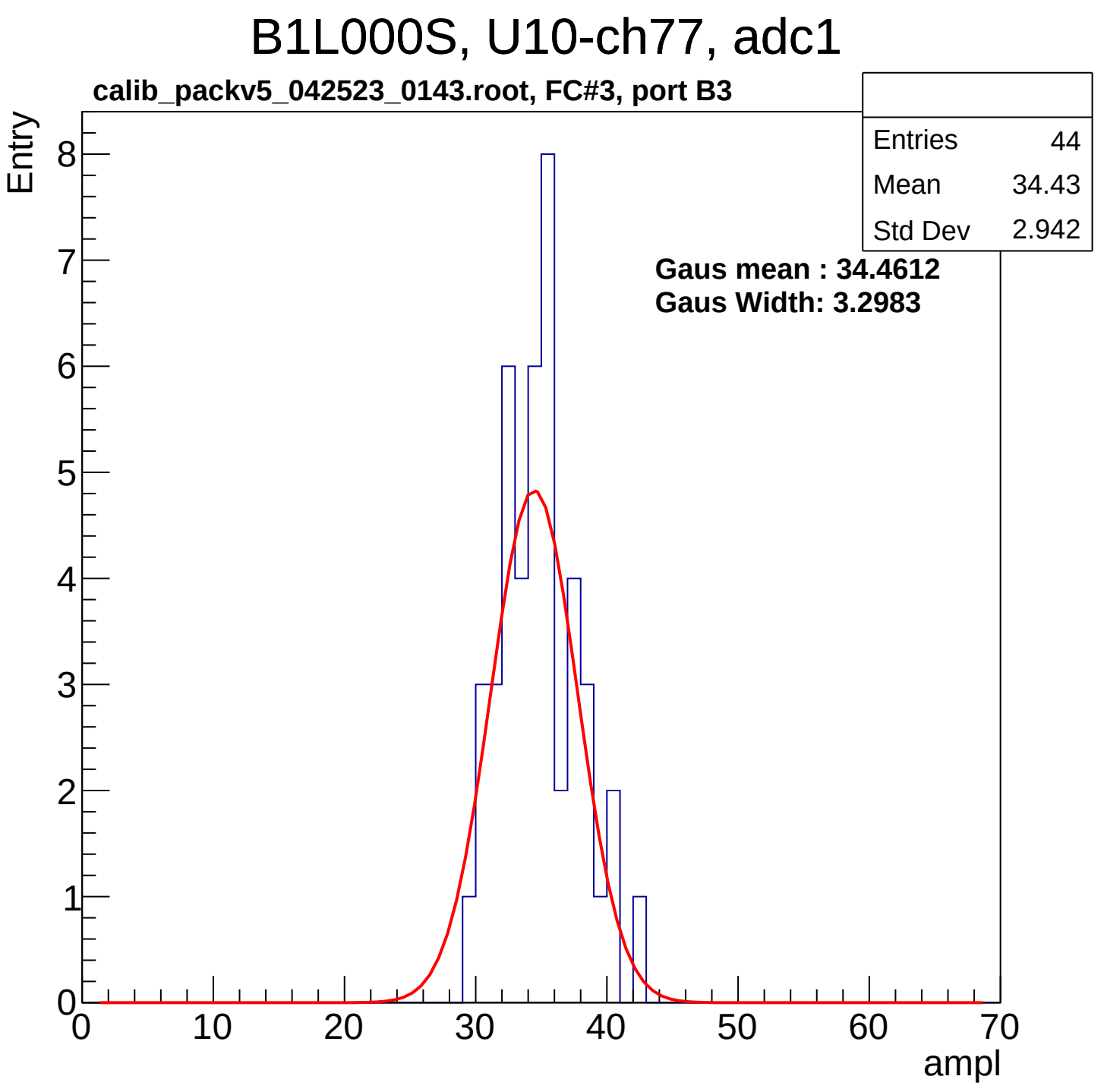
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch77, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 34.43
Std Dev 2.942
Gaus mean : 34.4612
Gaus Width: 3.2983
Entry
ampl
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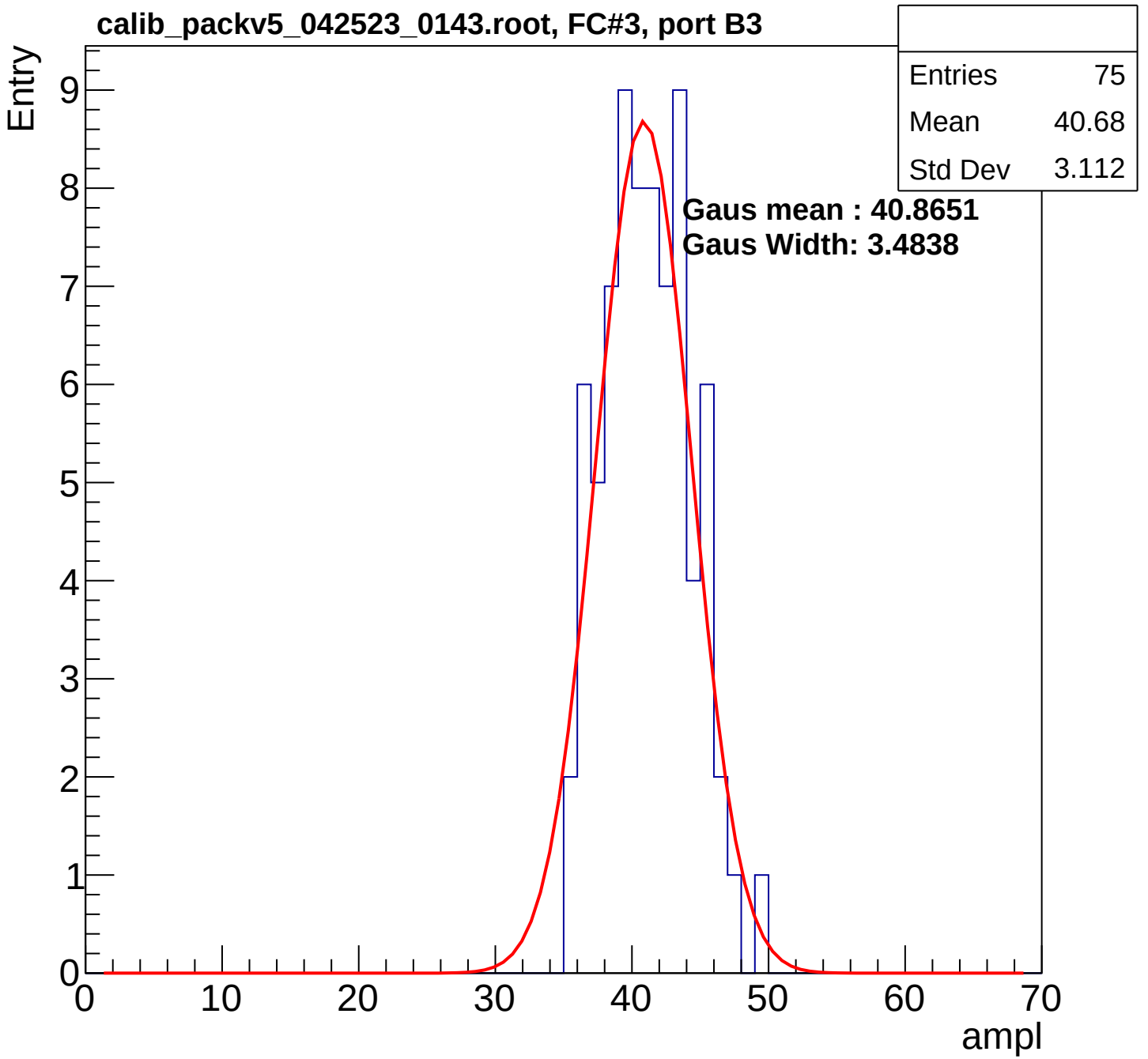
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch77, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 40.68
Std Dev 3.112
Gaus mean : 40.8651
Gaus Width: 3.4838
Entry
ampl
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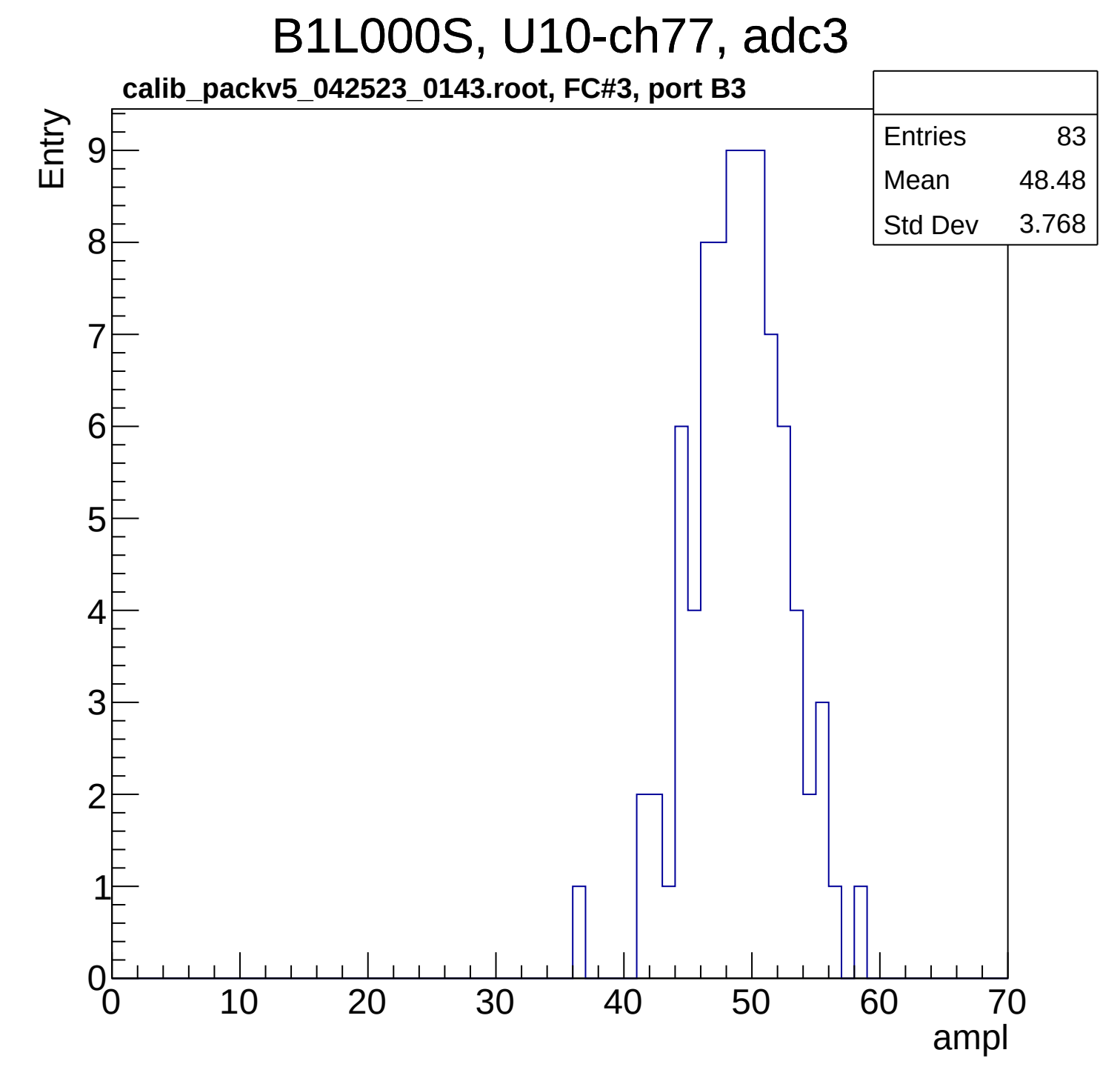
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch77, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 48.48
Std Dev 3.768
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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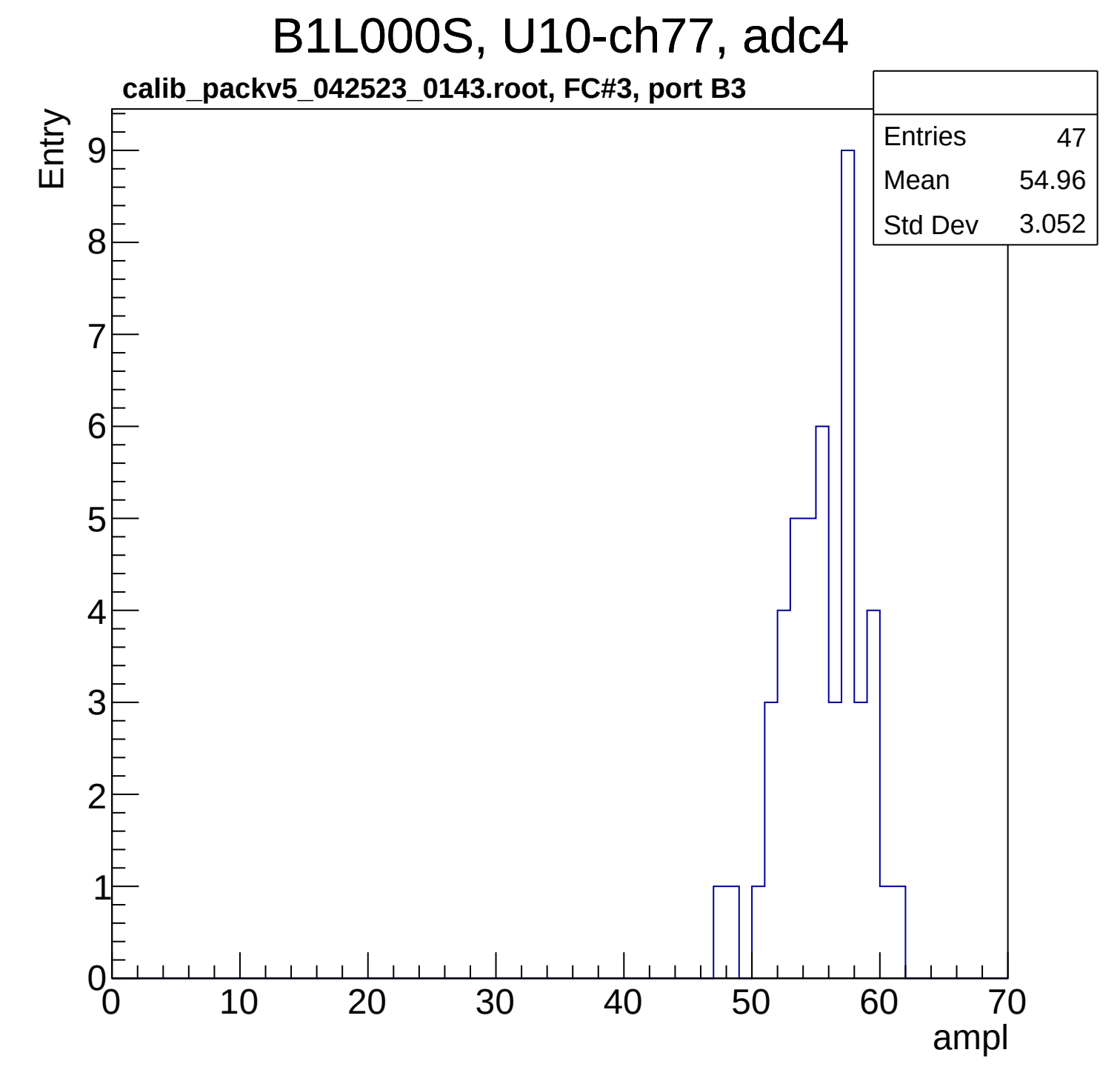
B1L000S, U10-ch77, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 54.96
Std Dev 3.052
Entry
ampl
0
1
2
3
4
5
6
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0
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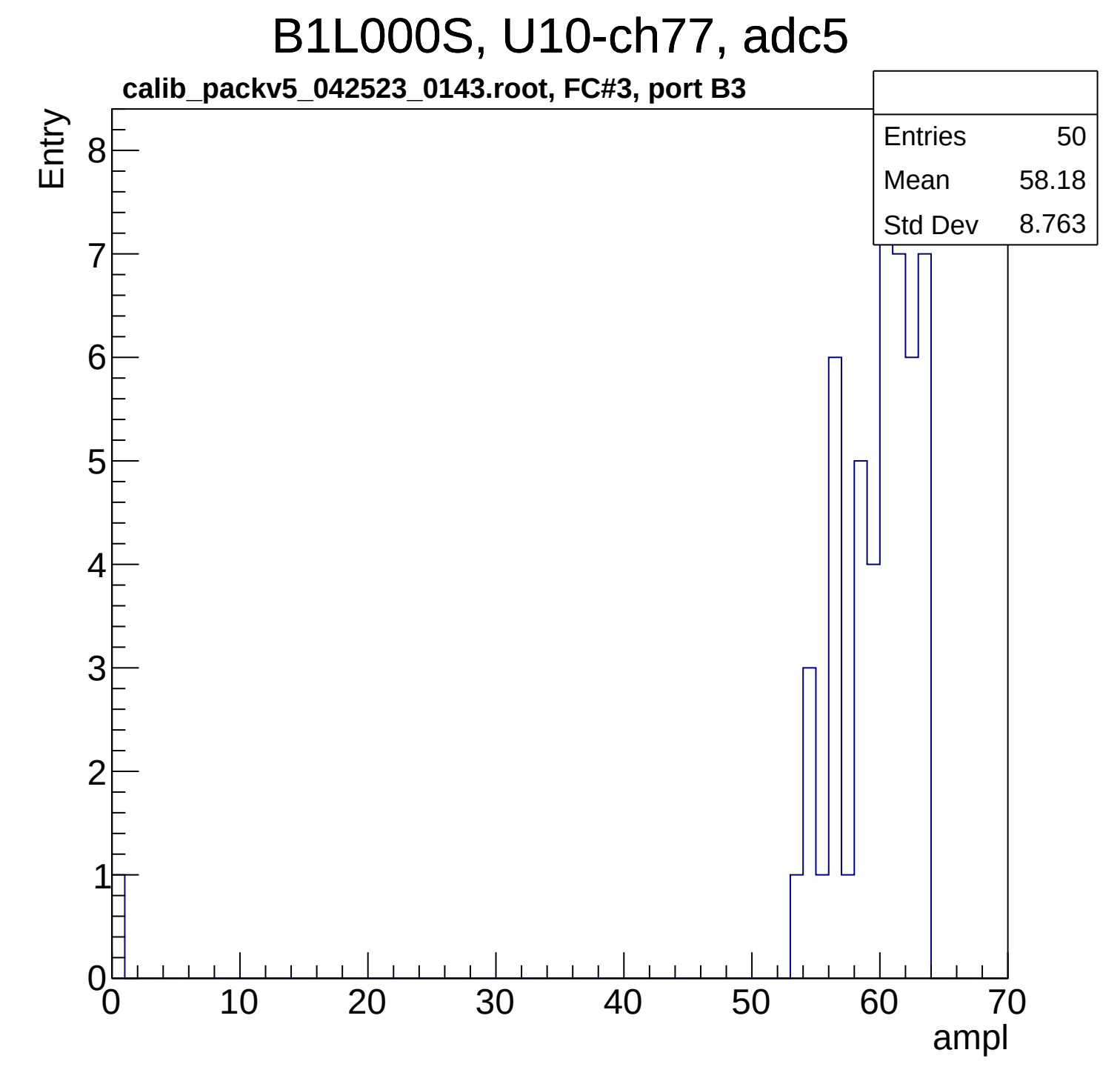
B1L000S, U10-ch77, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.18
Std Dev 8.763
Entry
ampl
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3
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0
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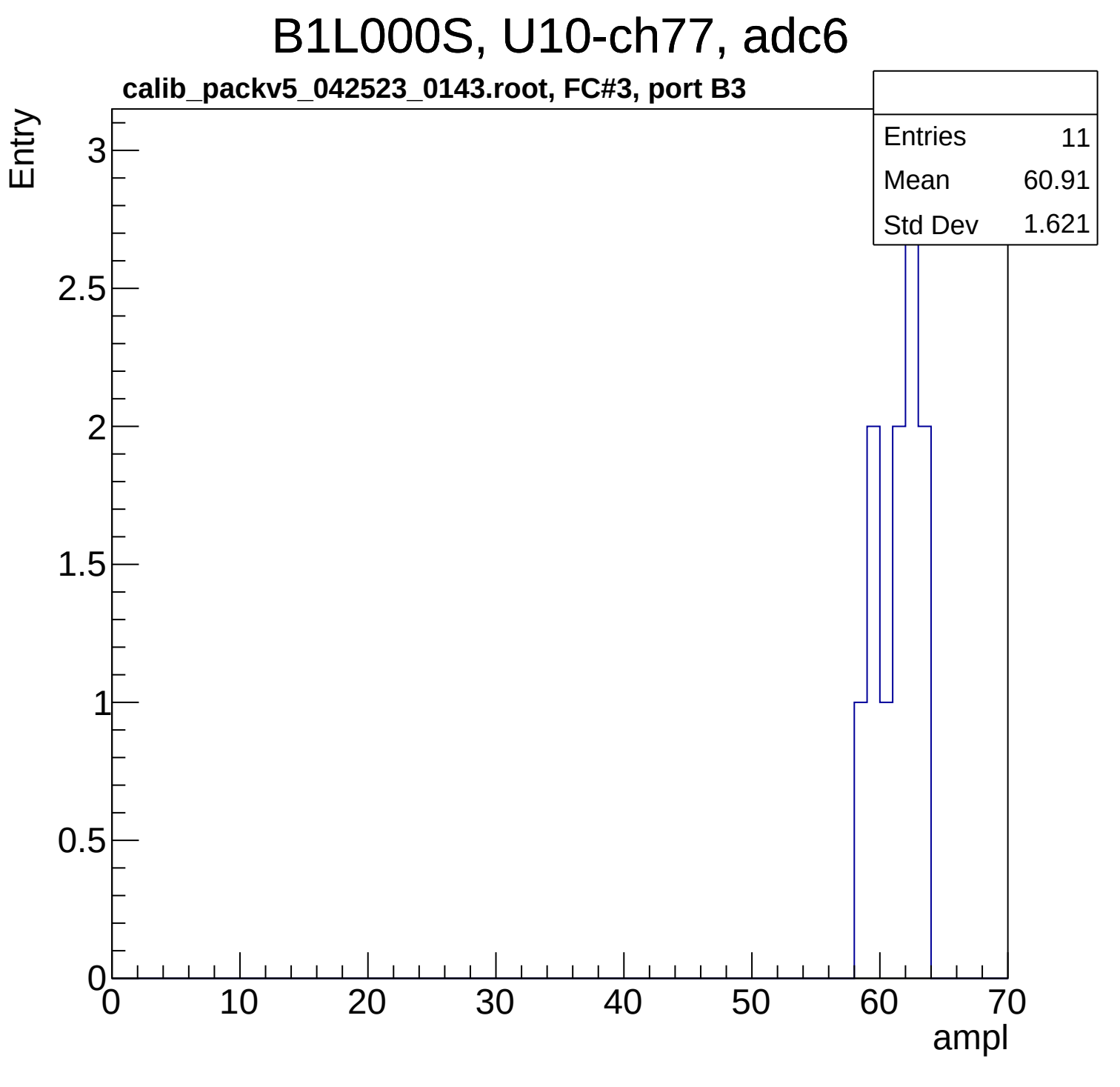
B1L000S, U10-ch77, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 60.91
Std Dev 1.621
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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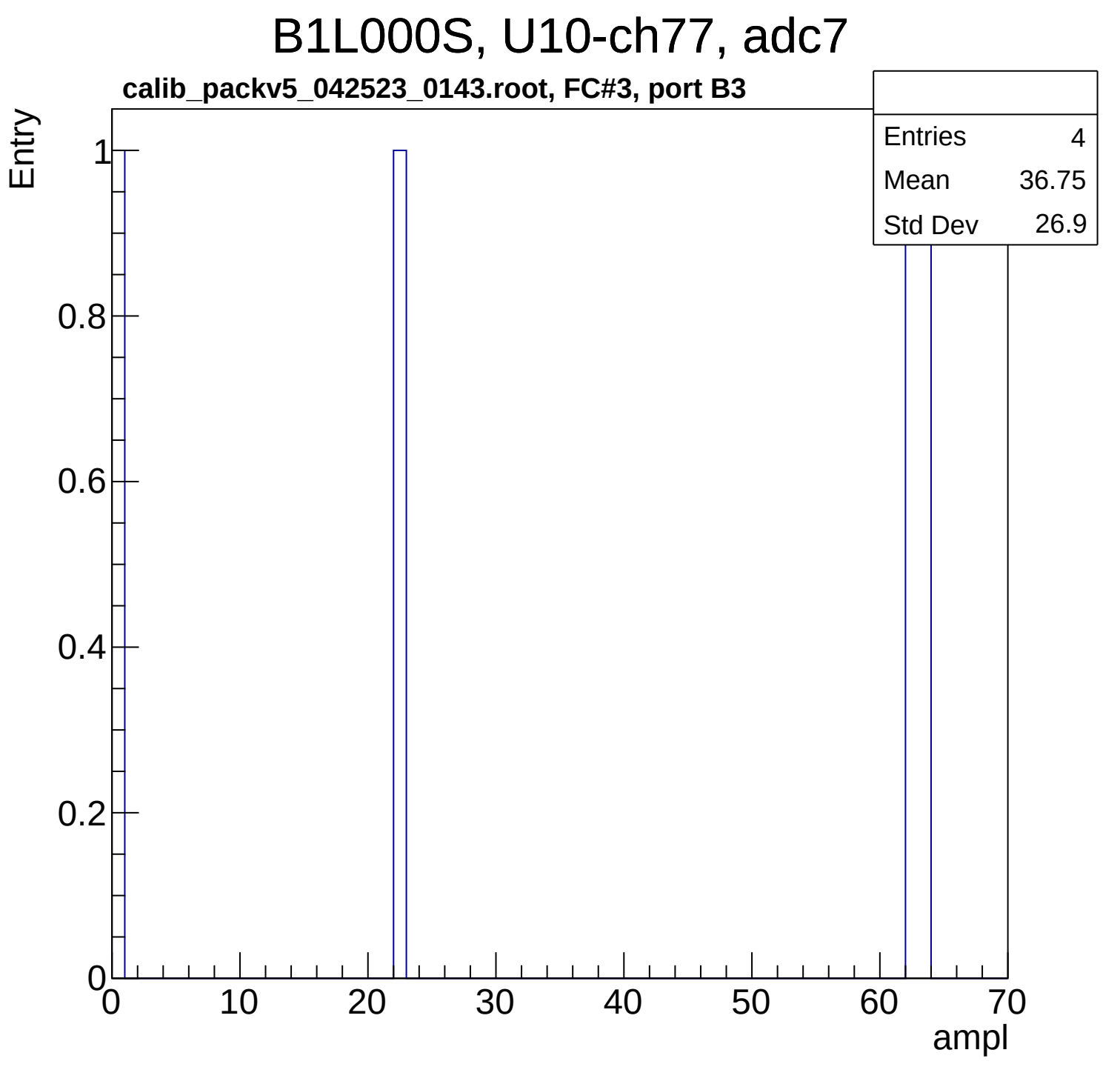
B1L000S, U10-ch77, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 36.75
Std Dev 26.9
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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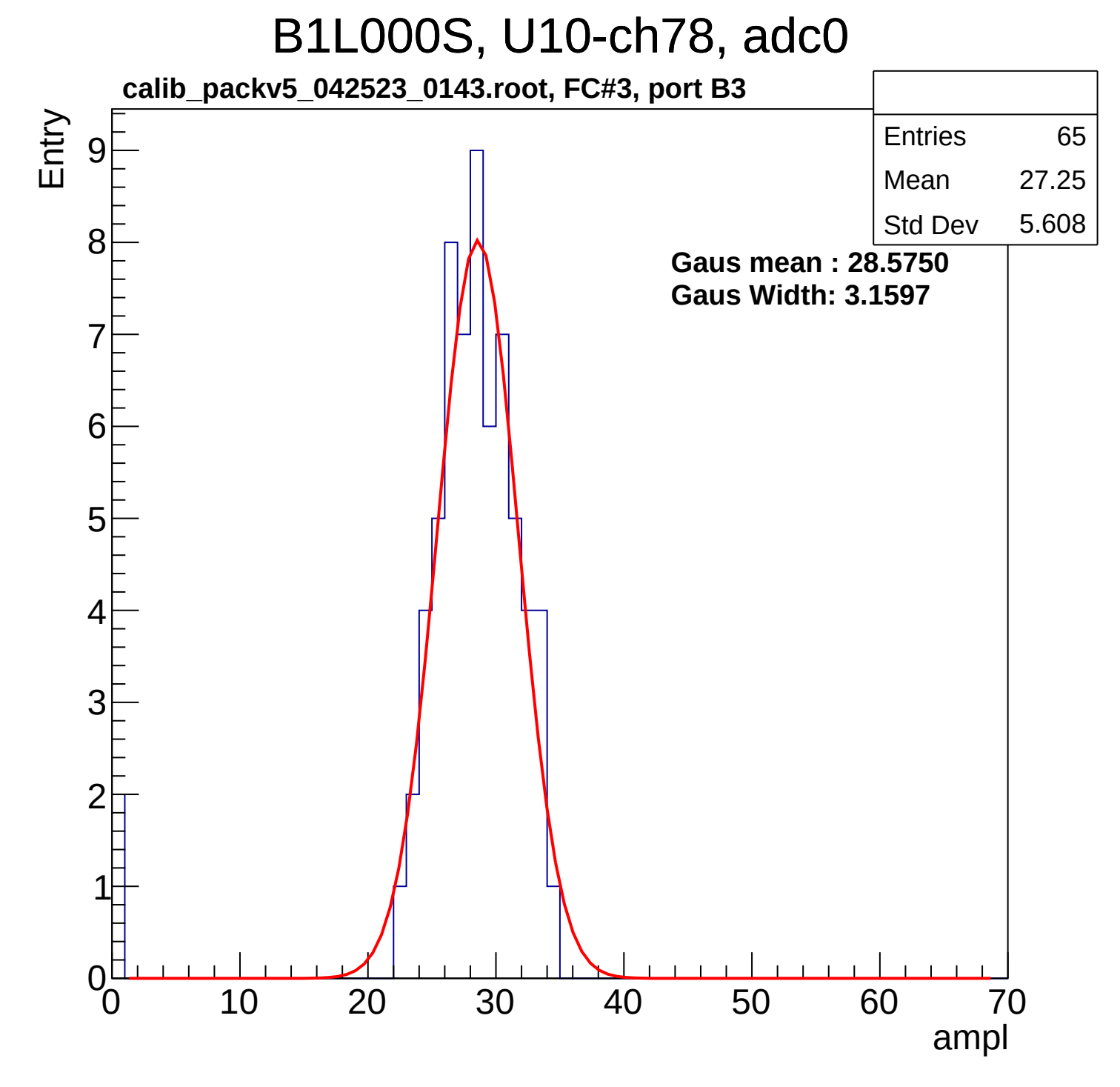
B1L000S, U10-ch78, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 27.25
Std Dev 5.608
Gaus mean : 28.5750
Gaus Width: 3.1597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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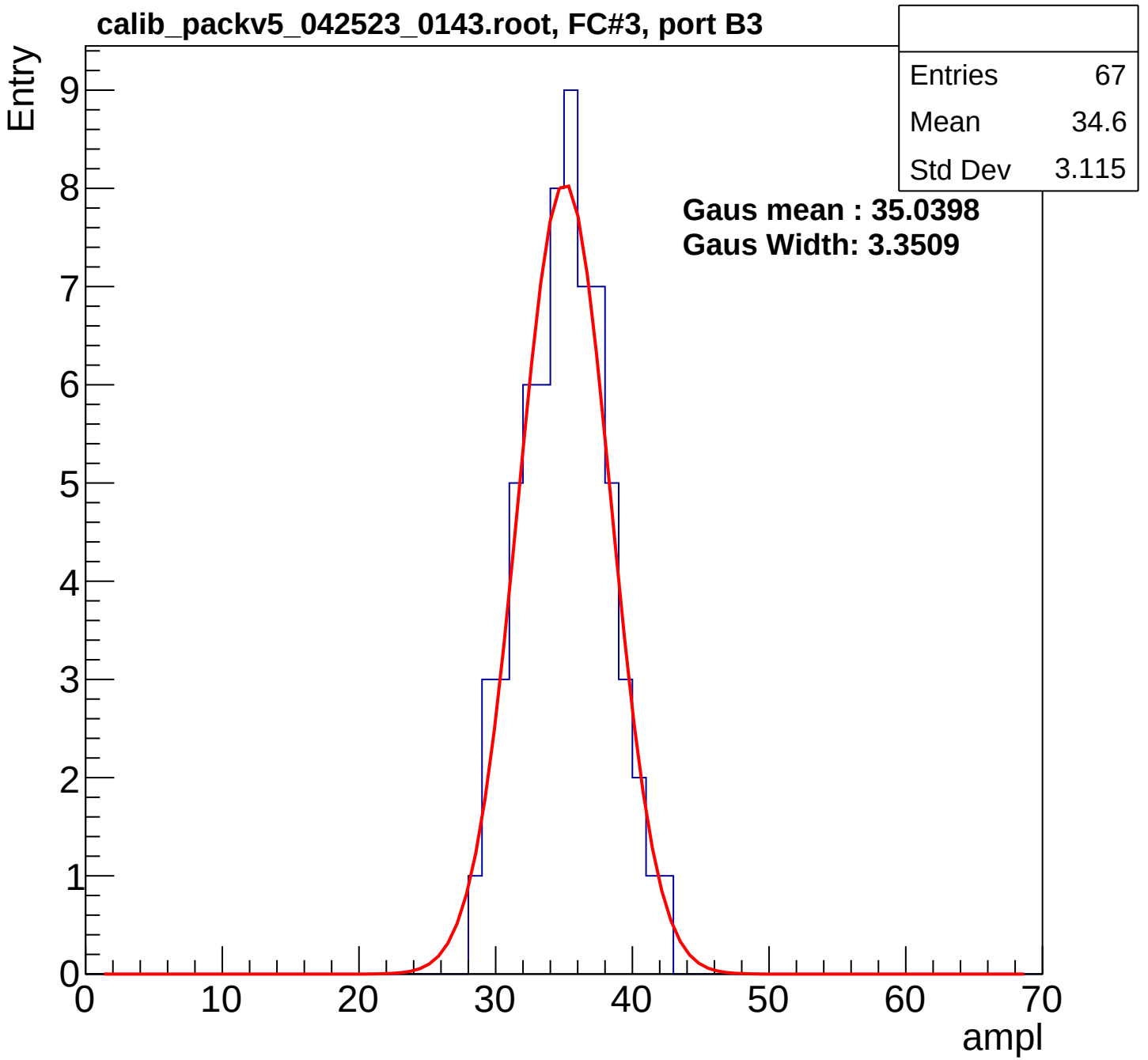
B1L000S, U10-ch78, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.6
Std Dev 3.115
Gaus mean : 35.0398
Gaus Width: 3.3509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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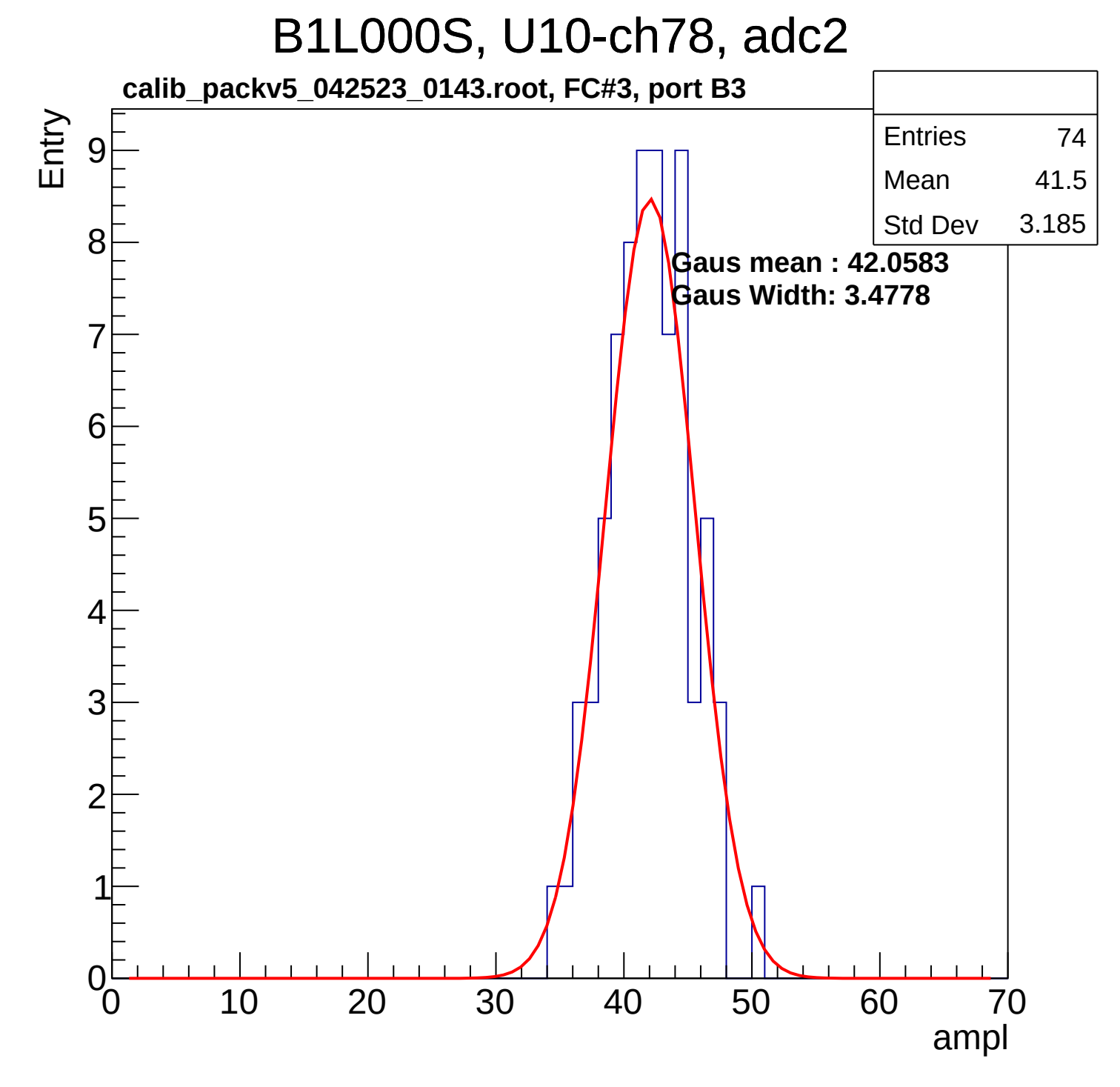
B1L000S, U10-ch78, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 41.5
Std Dev 3.185
Gaus mean : 42.0583
Gaus Width: 3.4778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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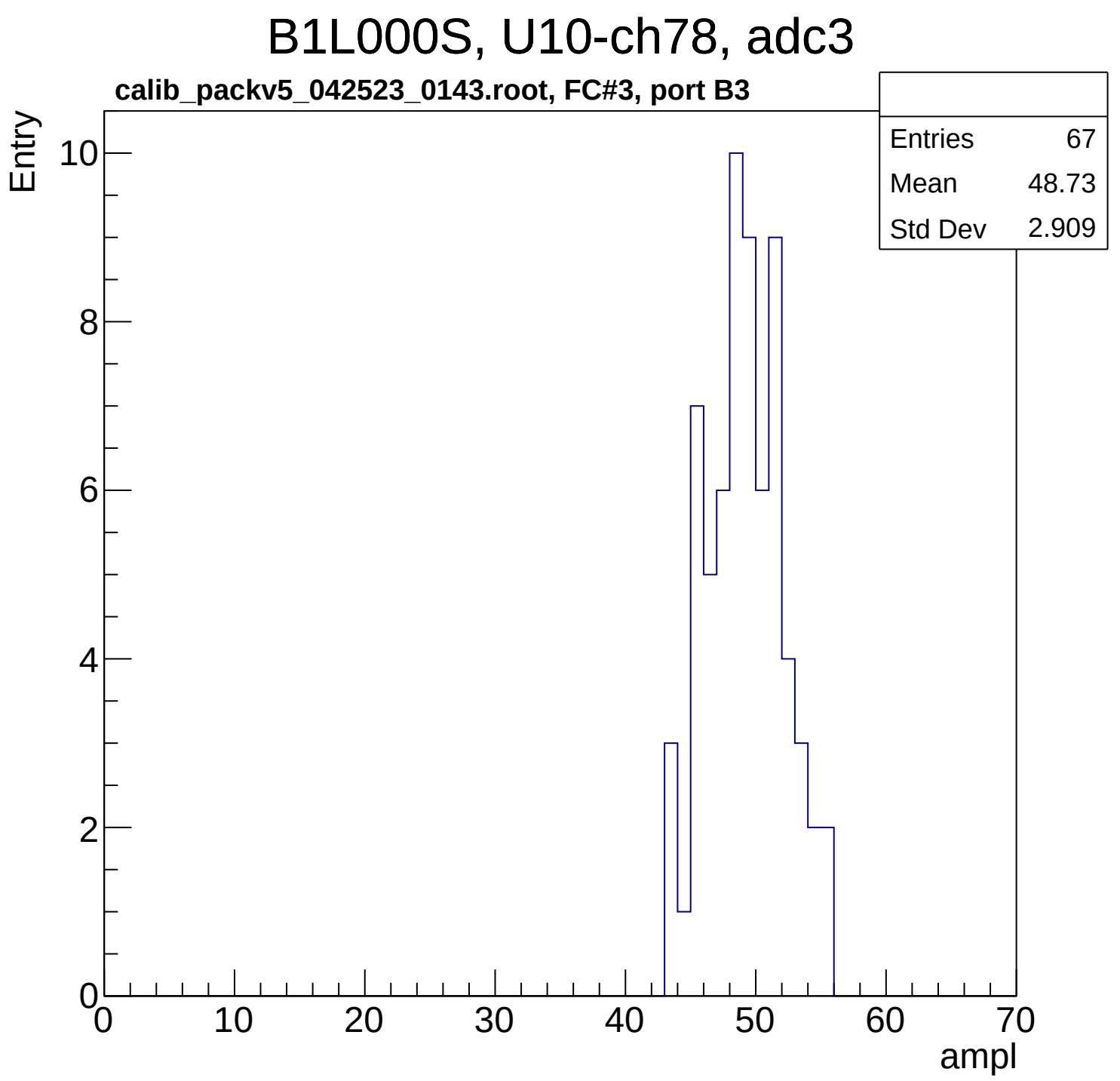
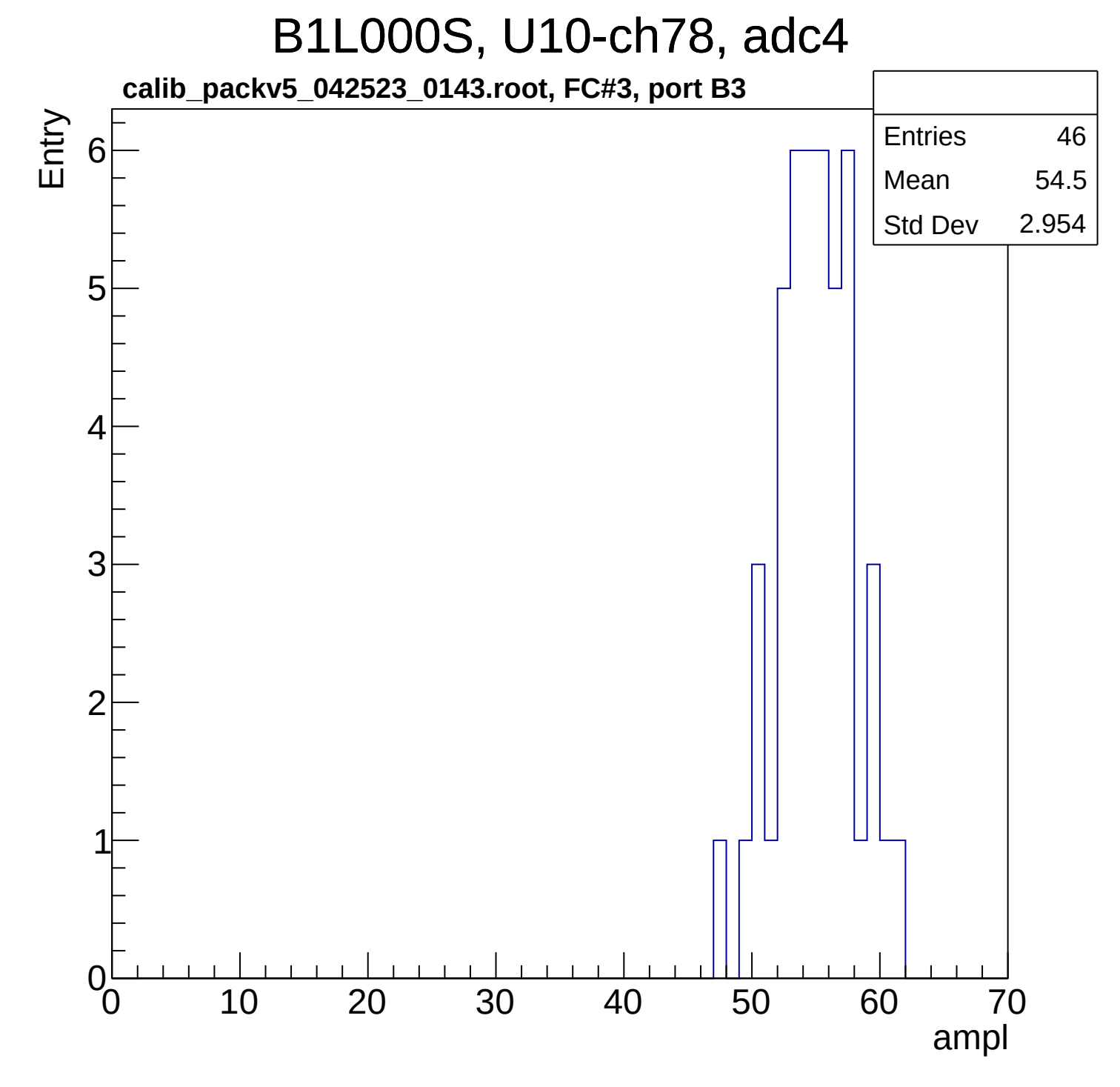
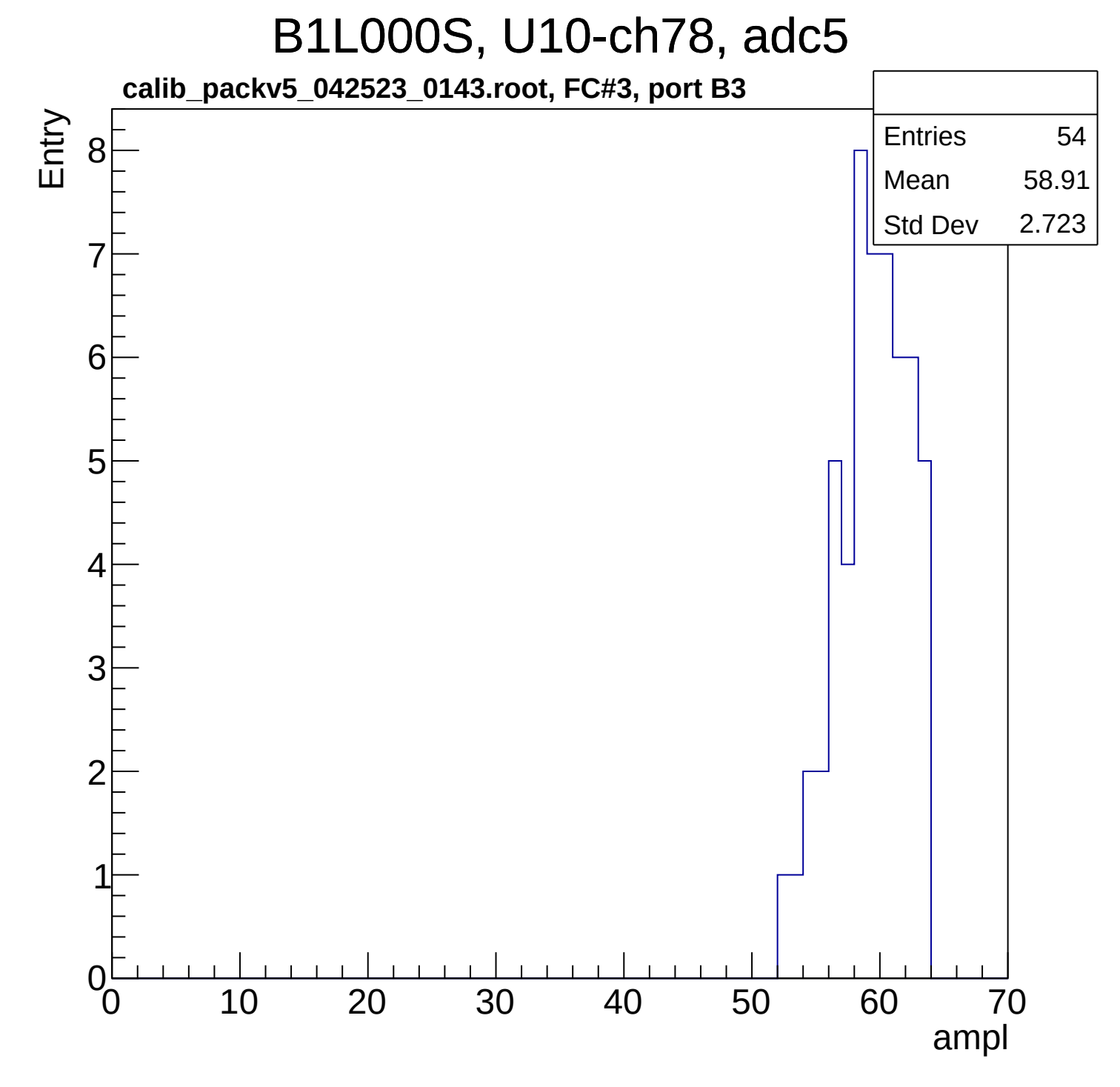

B1L000S, U10-ch78, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 48.73
Std Dev 2.909
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch78, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 54.5
Std Dev 2.954
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch78, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.91
Std Dev 2.723
Entry
ampl
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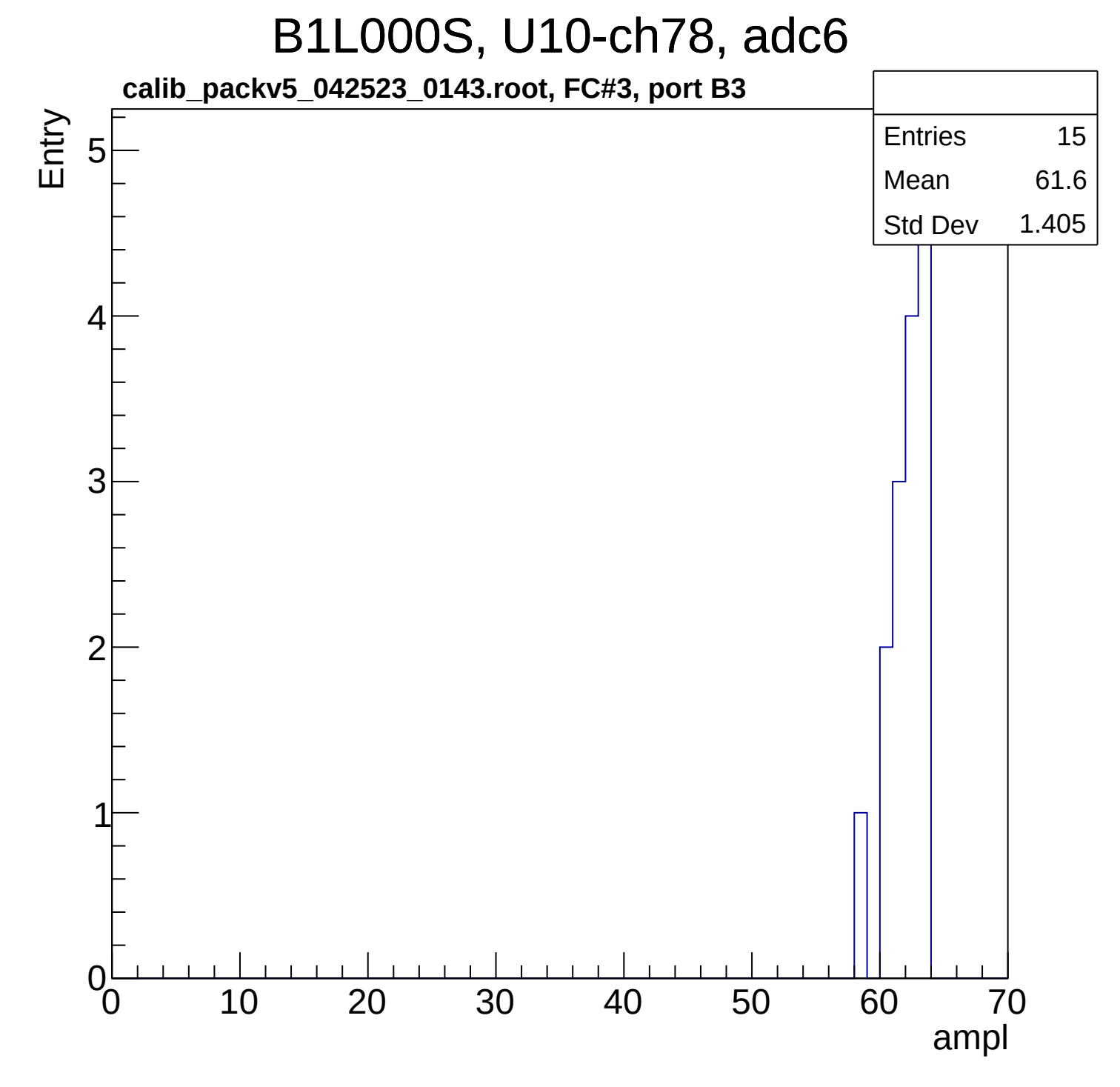
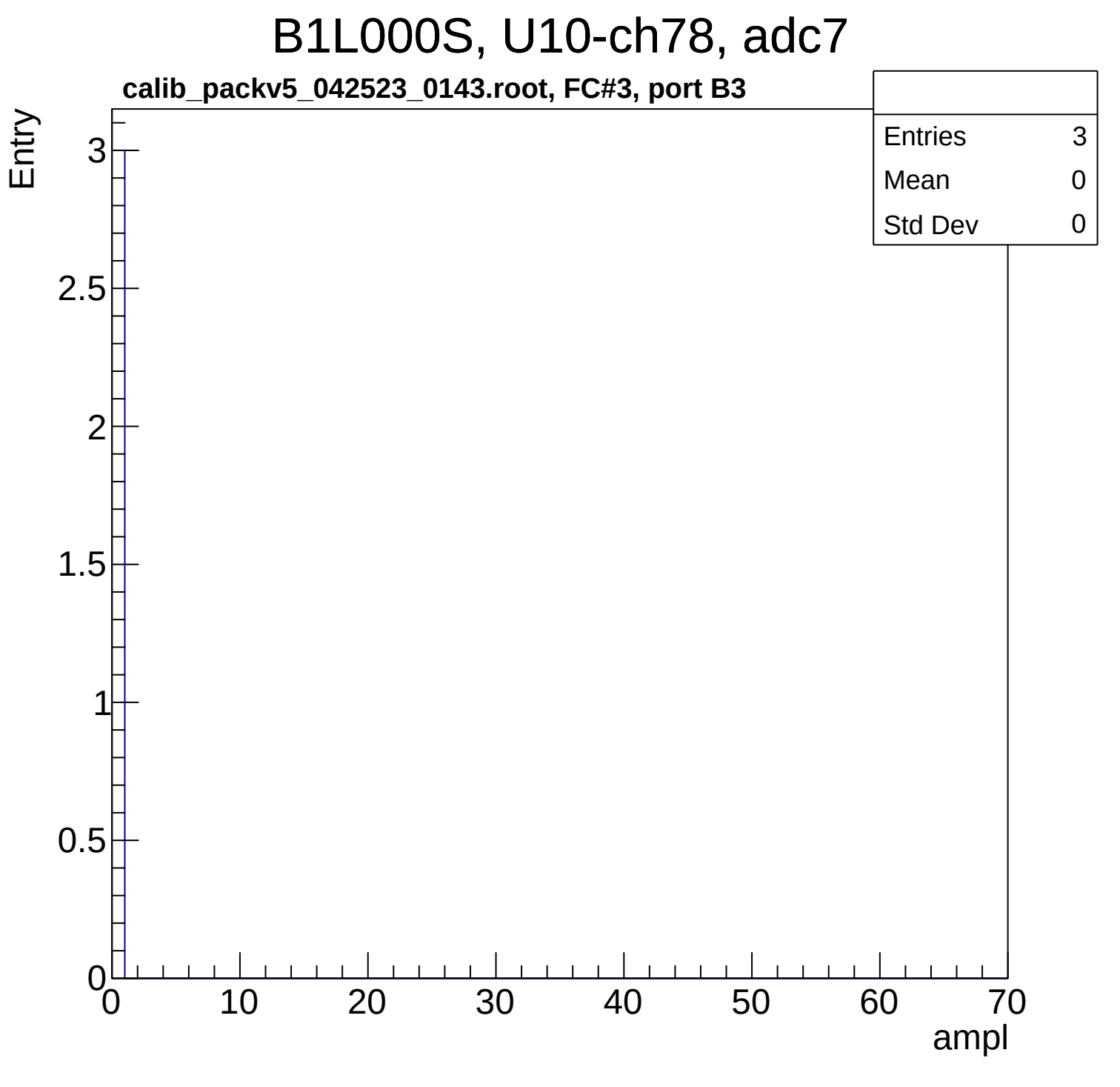
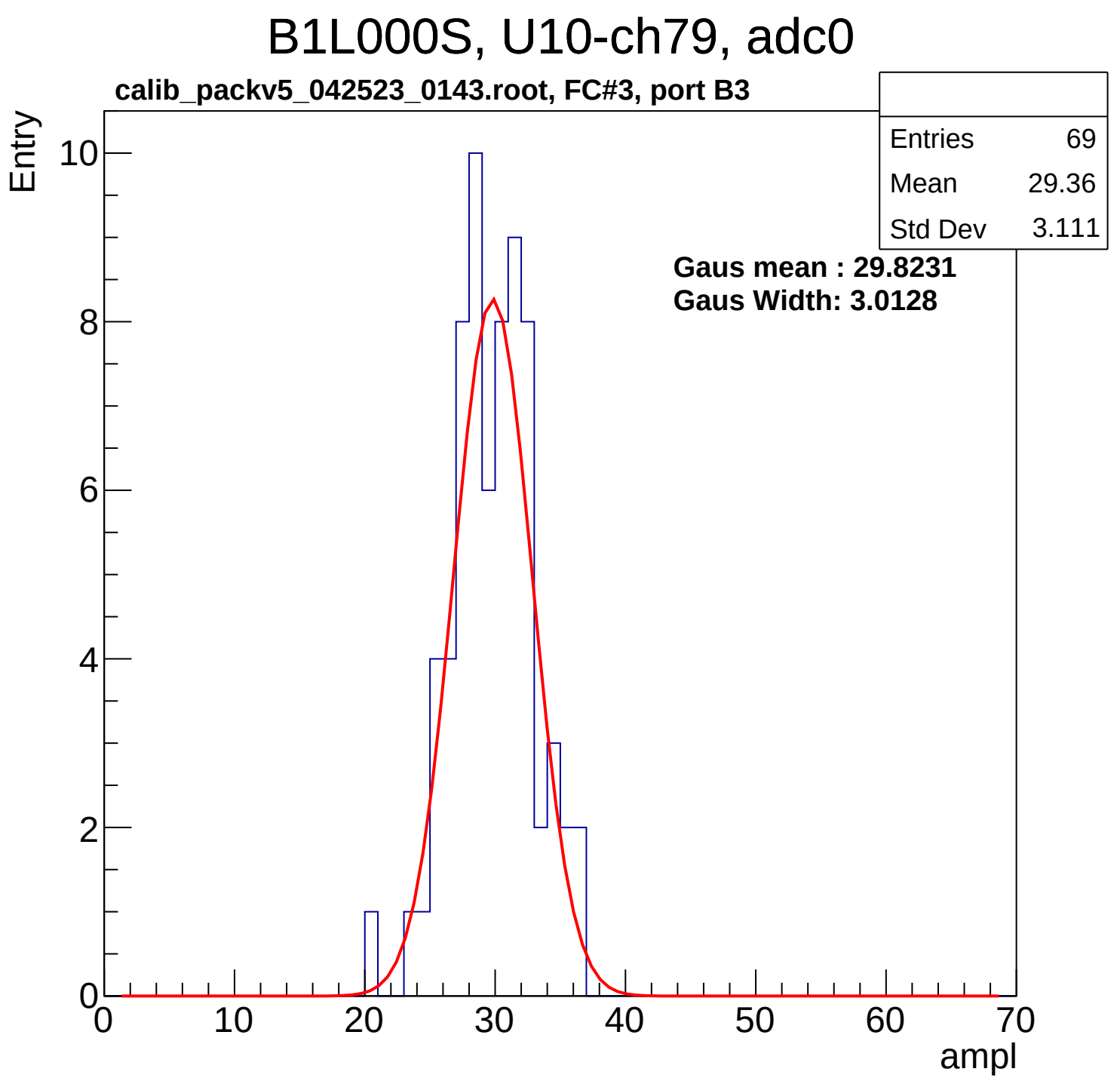
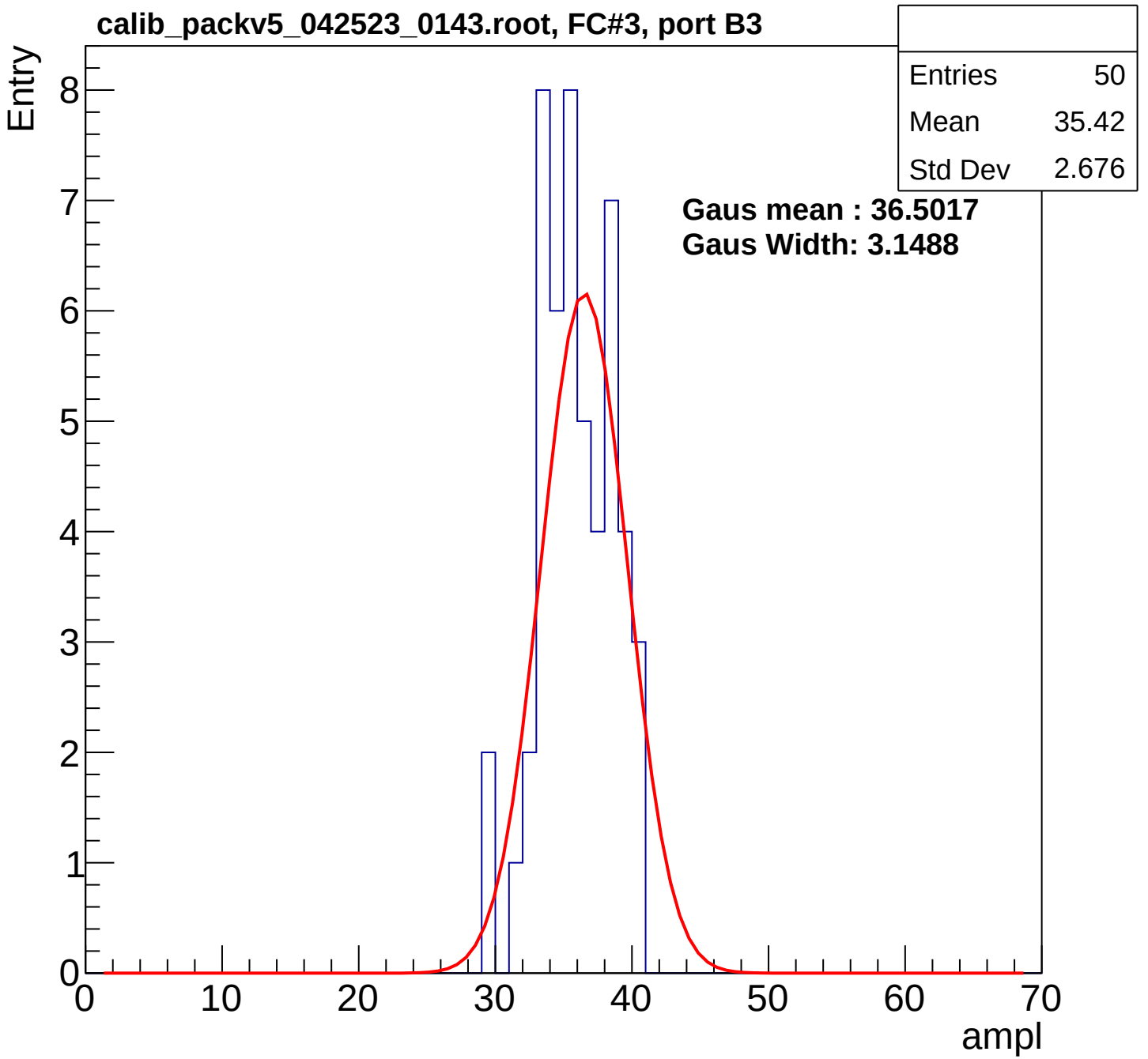
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch78, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.6
Std Dev 1.405
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch78, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch79, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 29.36
Std Dev 3.111
Gaus mean : 29.8231
Gaus Width: 3.0128
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch79, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 35.42
Std Dev 2.676
Gaus mean : 36.5017
Gaus Width: 3.1488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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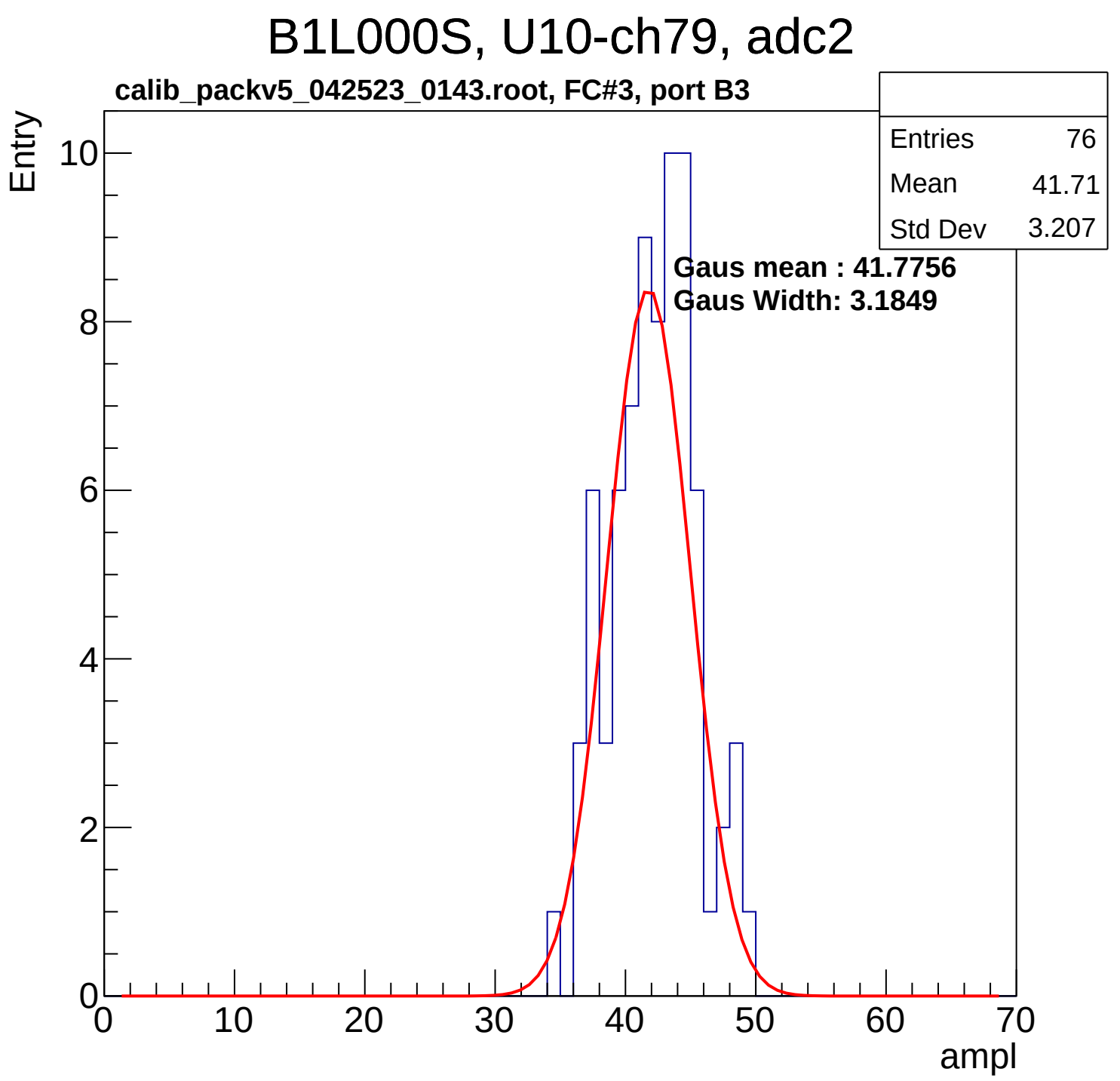
B1L000S, U10-ch79, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 41.71
Std Dev 3.207
Gaus mean : 41.7756
Gaus Width: 3.1849
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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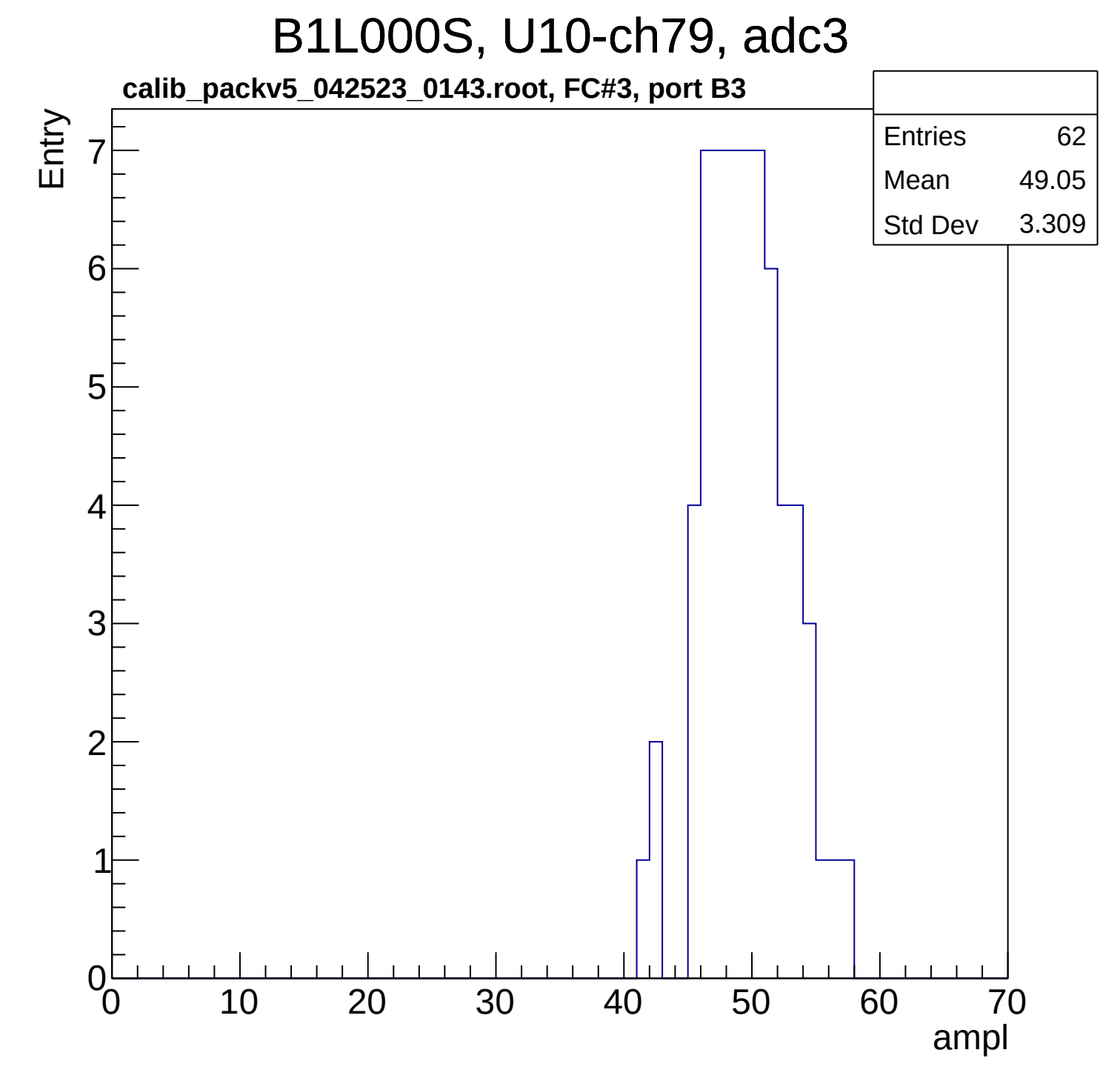
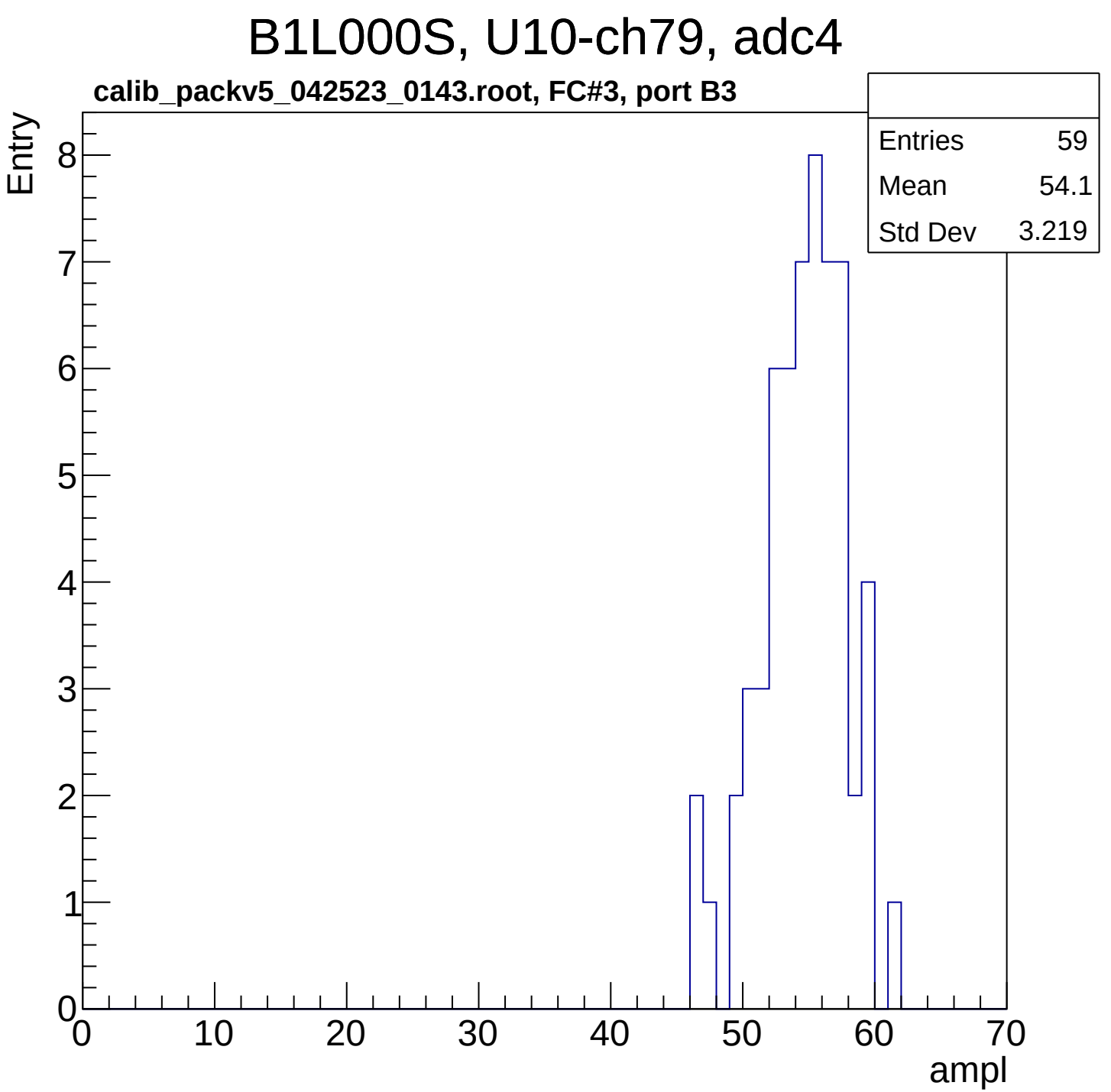
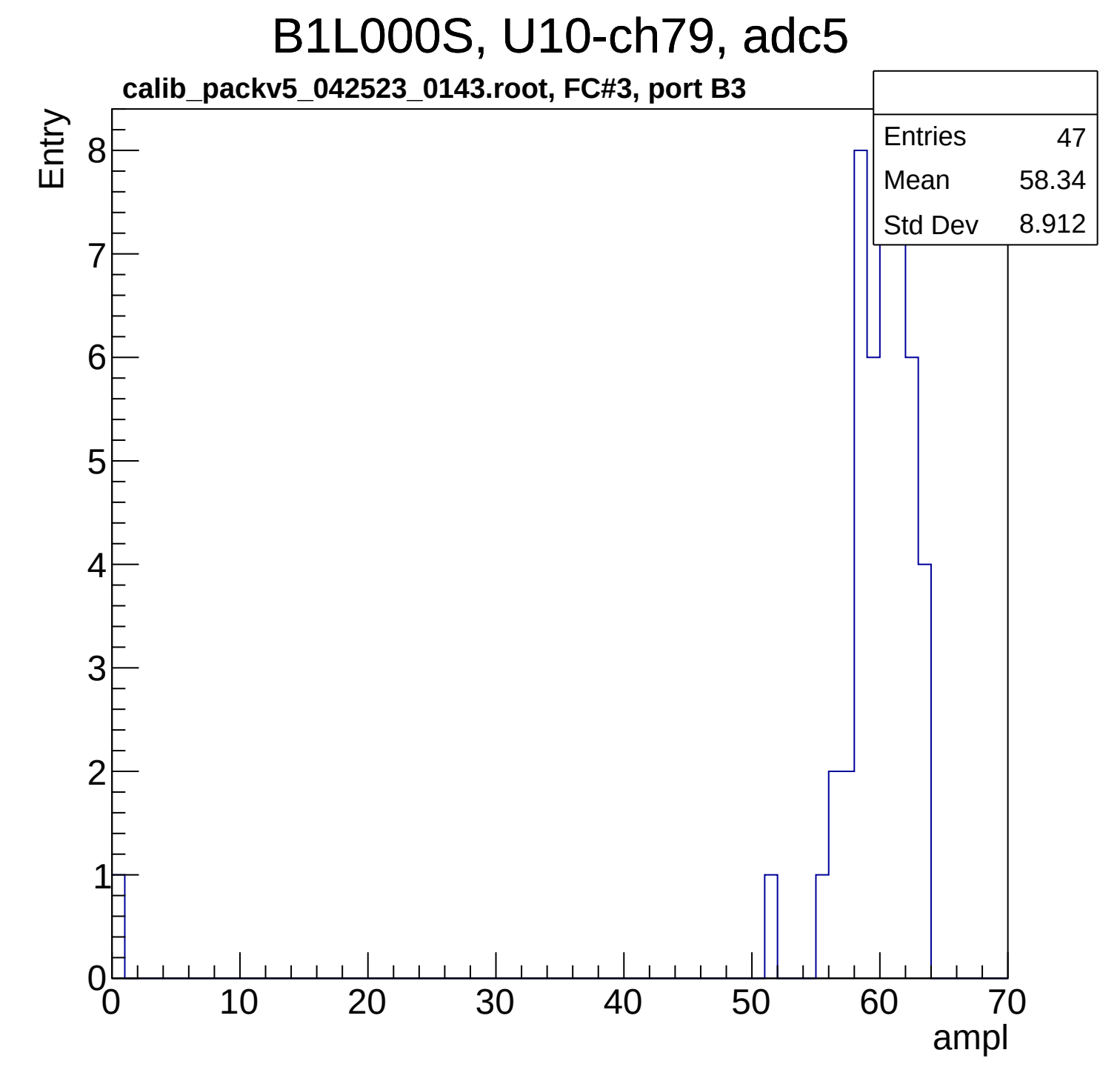

B1L000S, U10-ch79, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 49.05
Std Dev 3.309
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch79, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 54.1
Std Dev 3.219
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch79, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 58.34
Std Dev 8.912
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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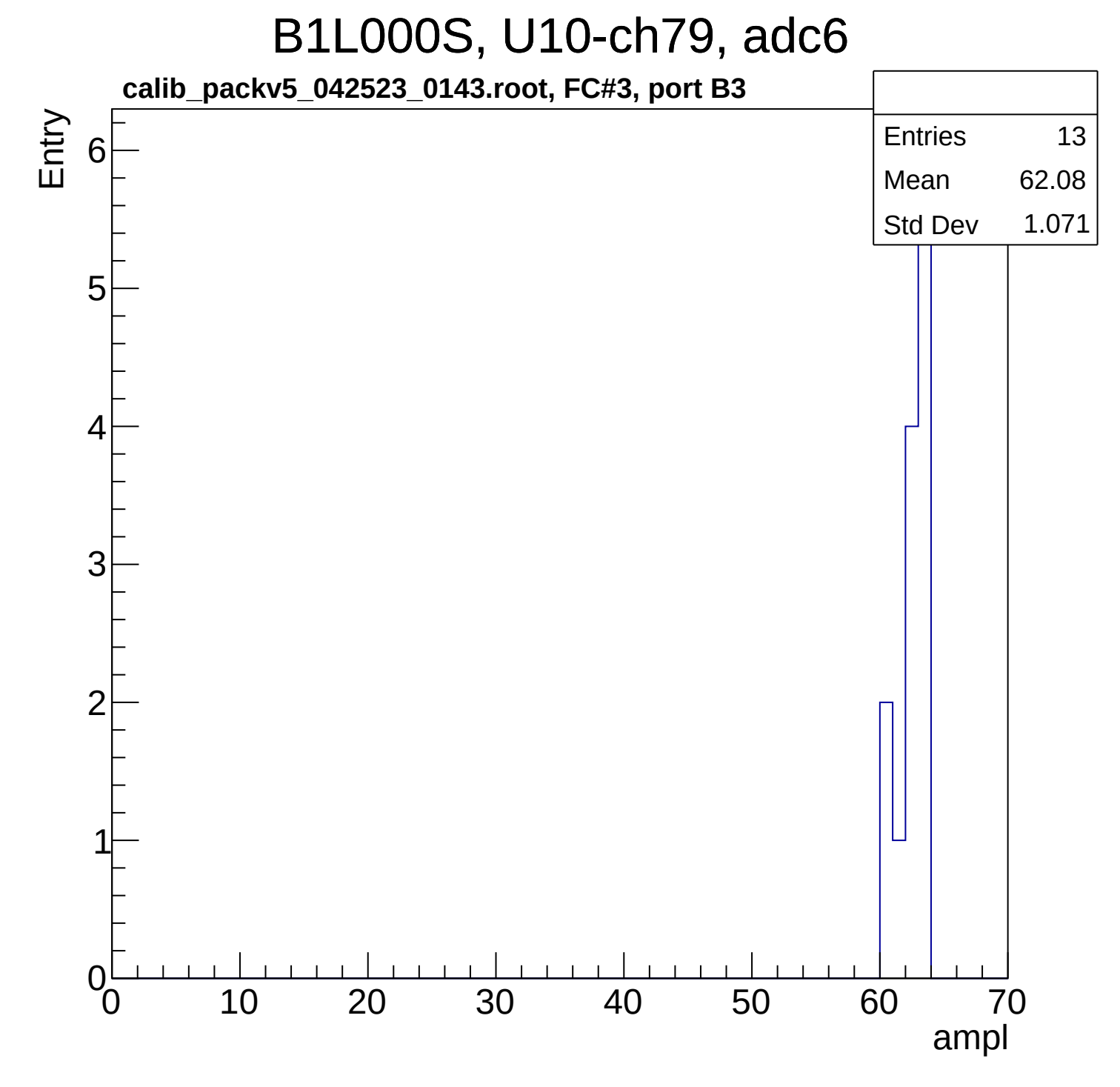
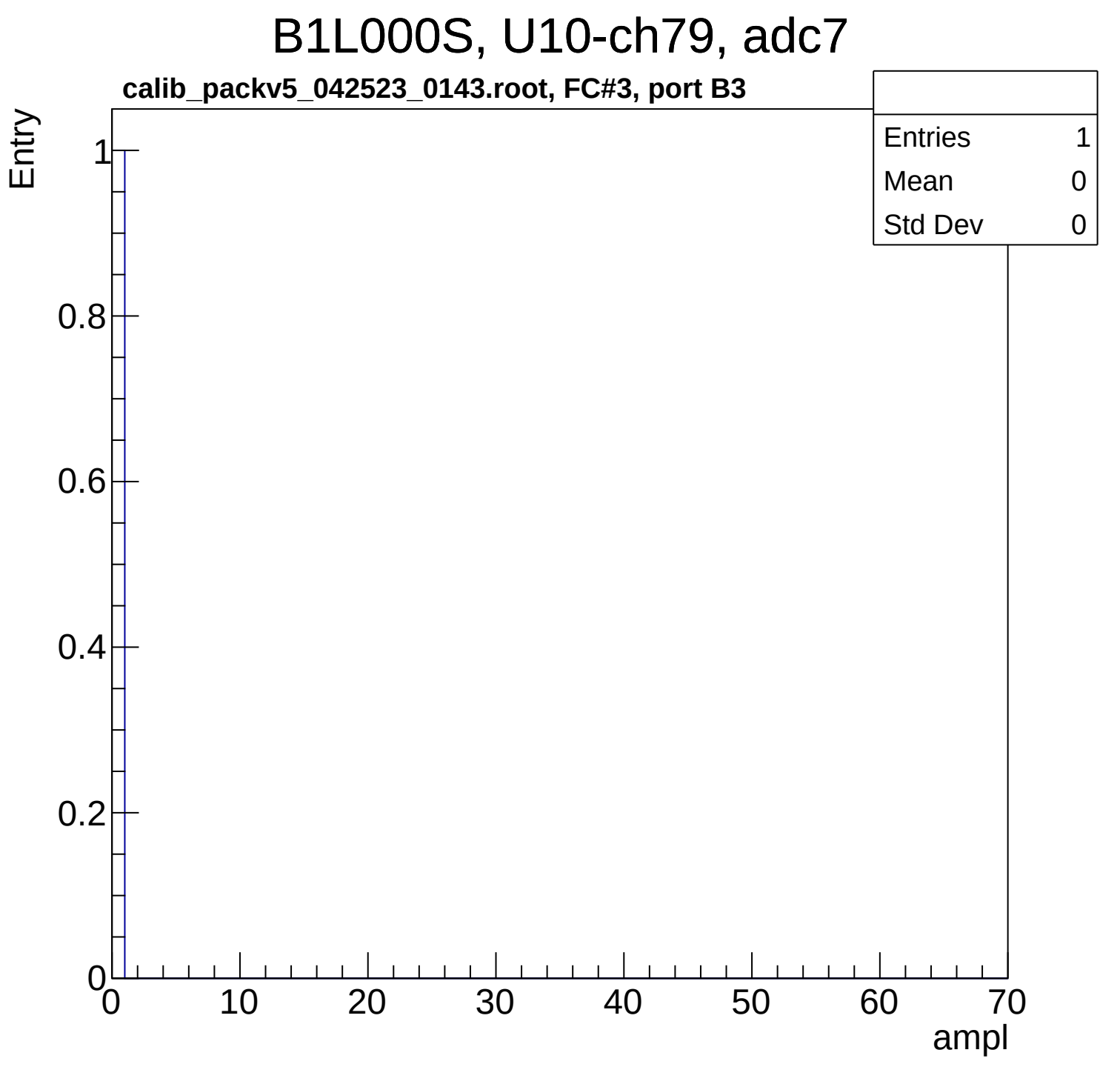
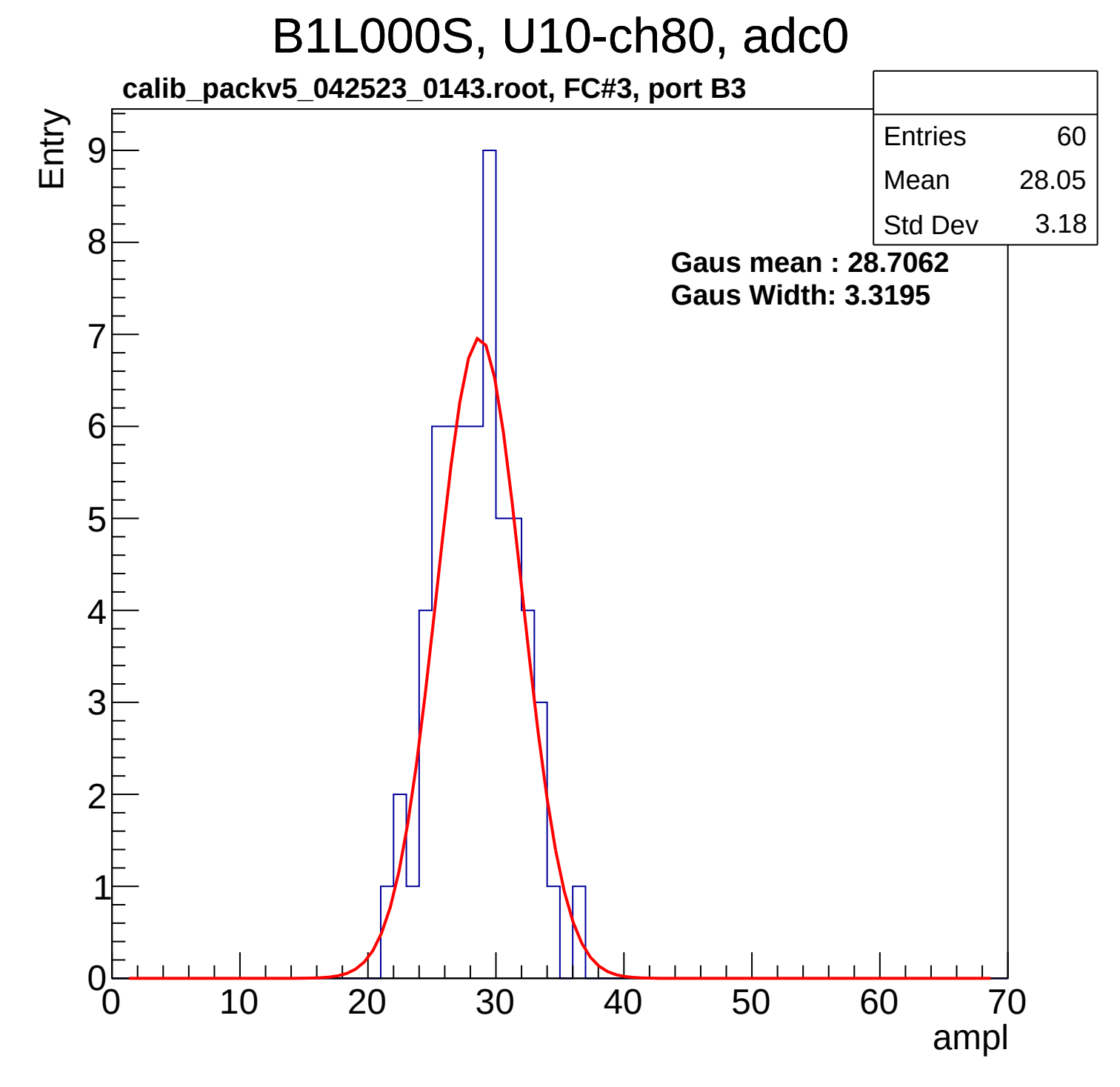

B1L000S, U10-ch79, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 62.08
Std Dev 1.071
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch79, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch80, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.05
Std Dev 3.18
Gaus mean : 28.7062
Gaus Width: 3.3195
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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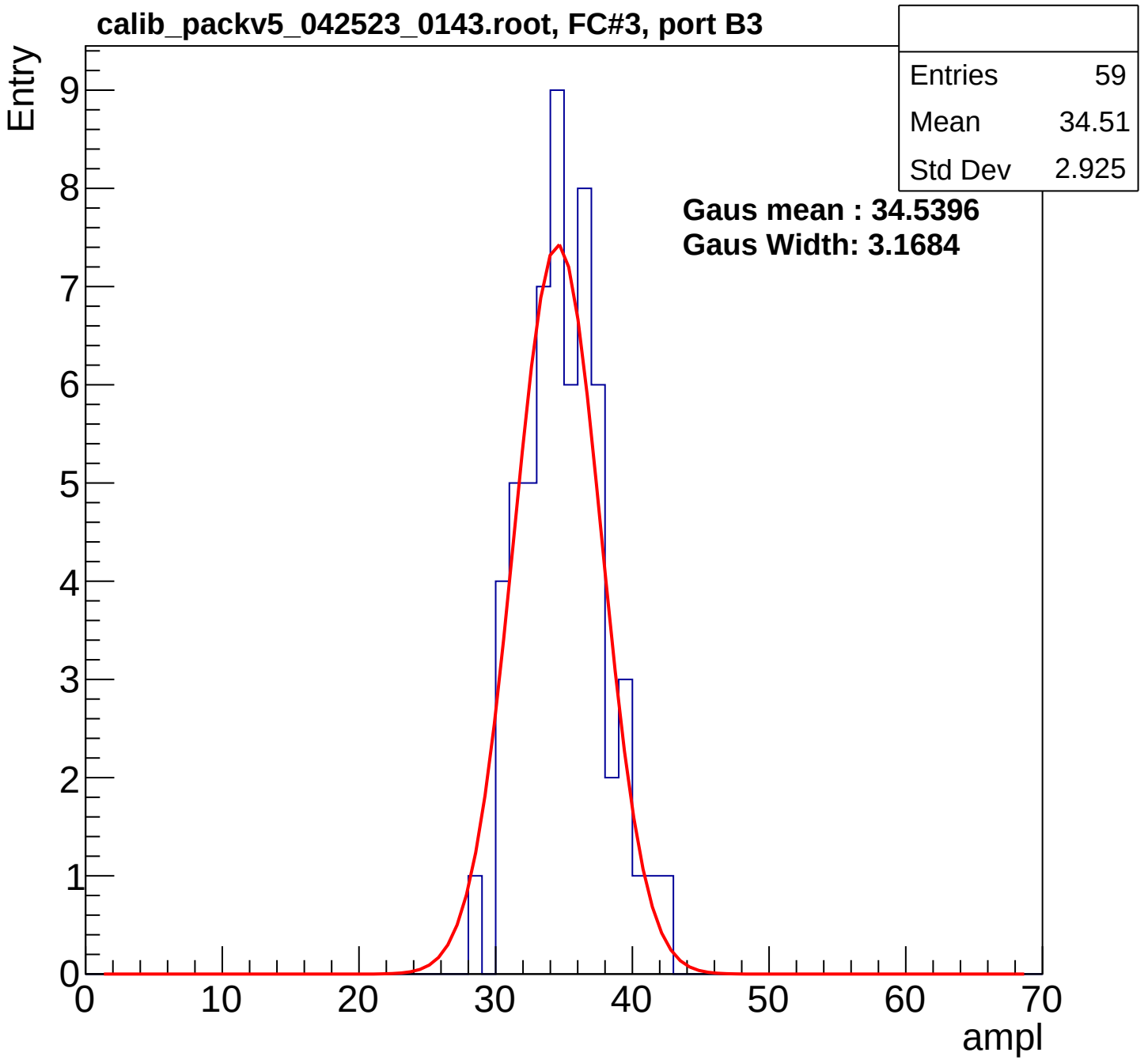
B1L000S, U10-ch80, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 34.51
Std Dev 2.925
Gaus mean : 34.5396
Gaus Width: 3.1684
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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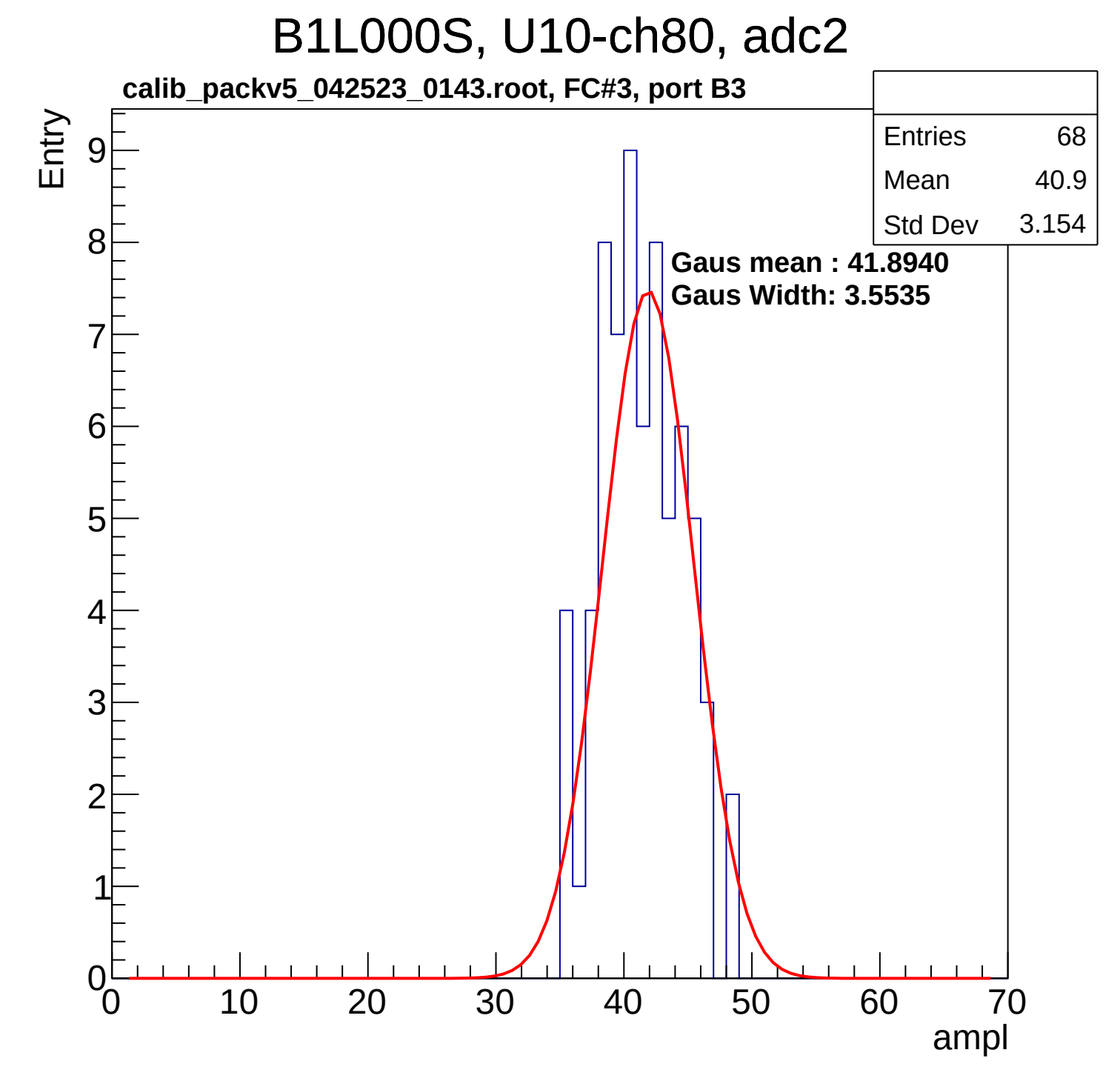
B1L000S, U10-ch80, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 40.9
Std Dev 3.154
Gaus mean : 41.8940
Gaus Width: 3.5535
Entry
ampl
0
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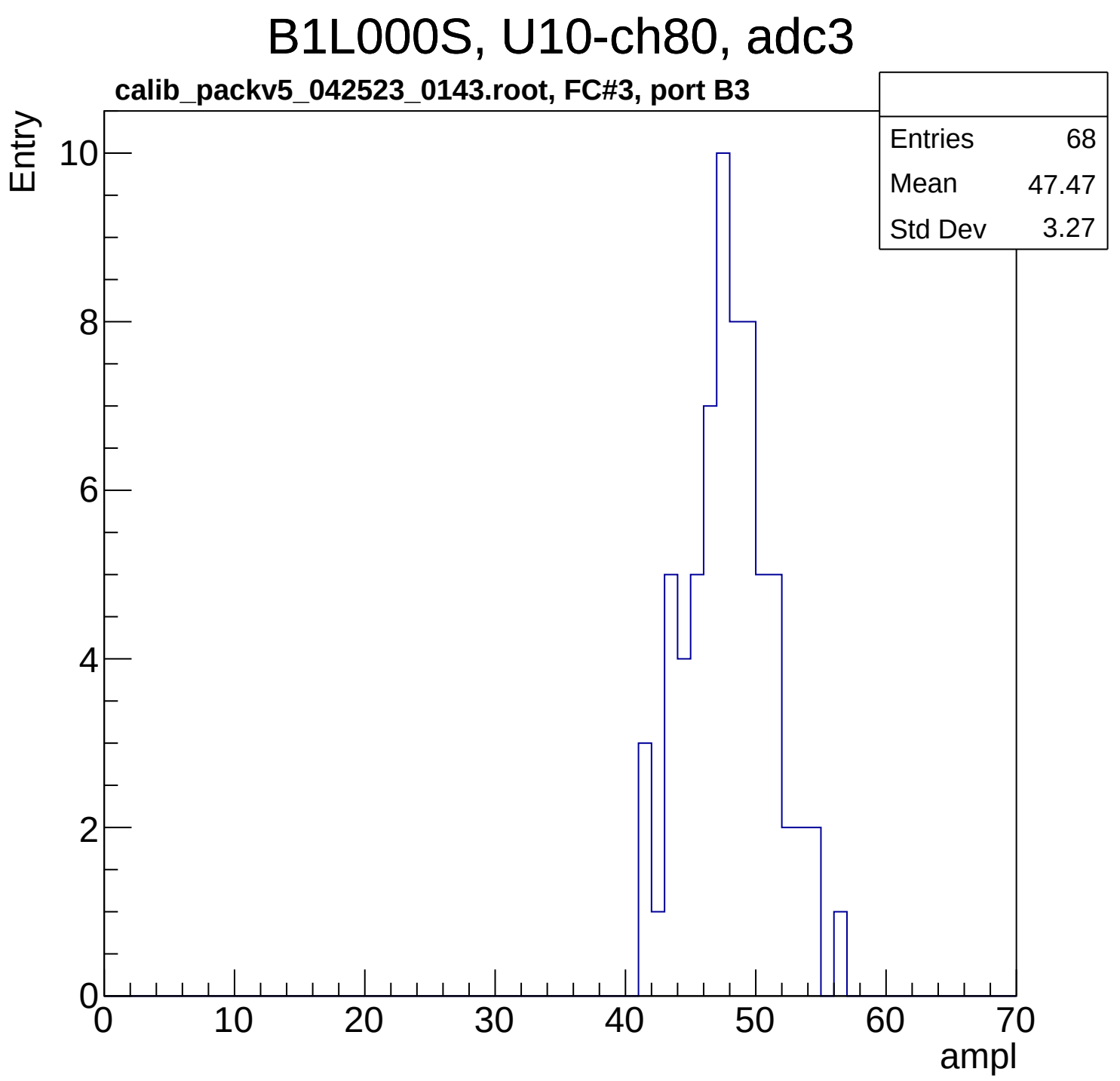
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch80, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 47.47
Std Dev 3.27
Entry
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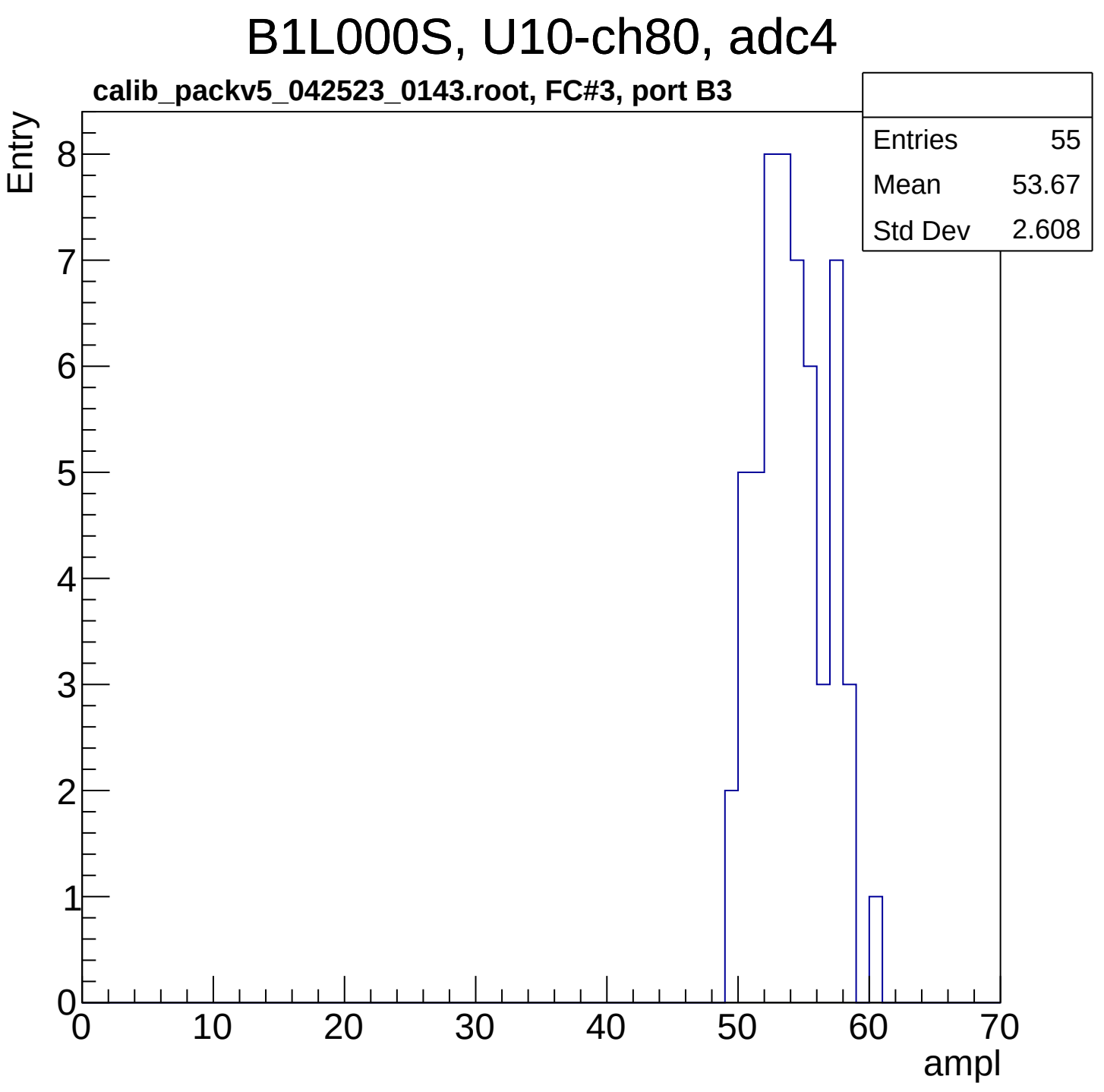
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch80, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.67
Std Dev 2.608
Entry
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2
3
4
5
6
7
8
0
10
20
30
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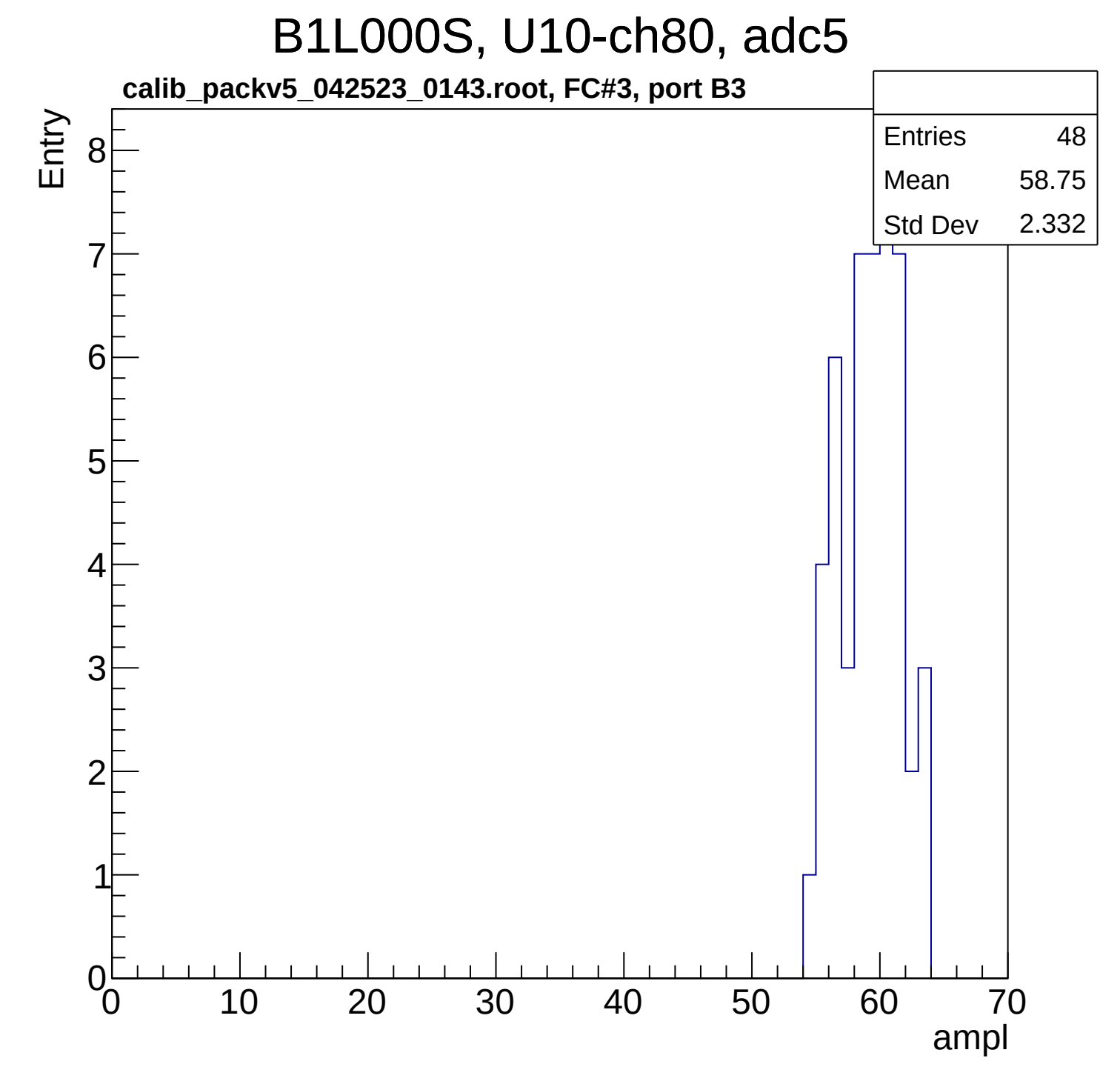
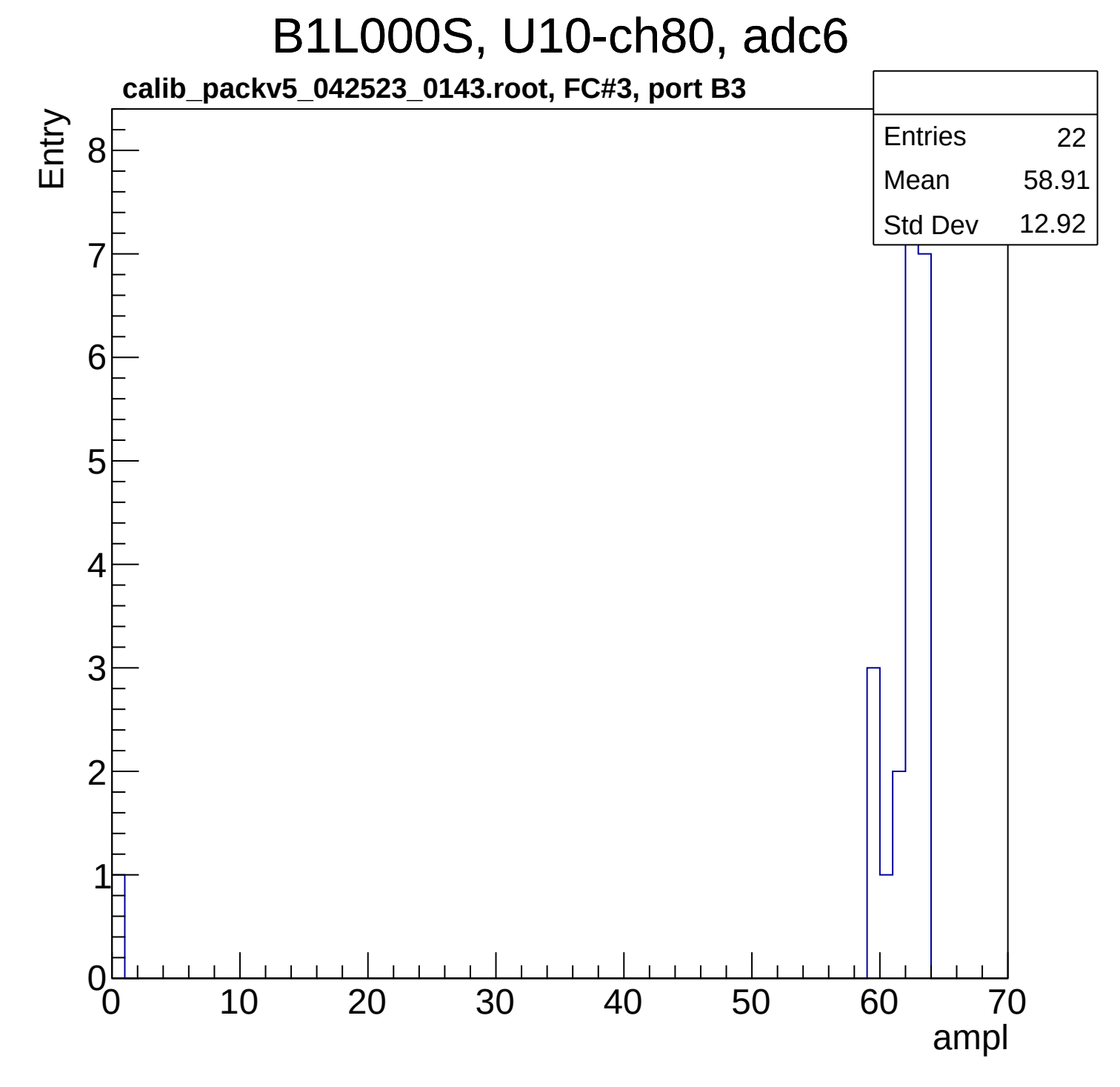
50
60
70
ampl


B1L000S, U10-ch80, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.75
Std Dev 2.332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch80, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.91
Std Dev 12.92
Entry
ampl
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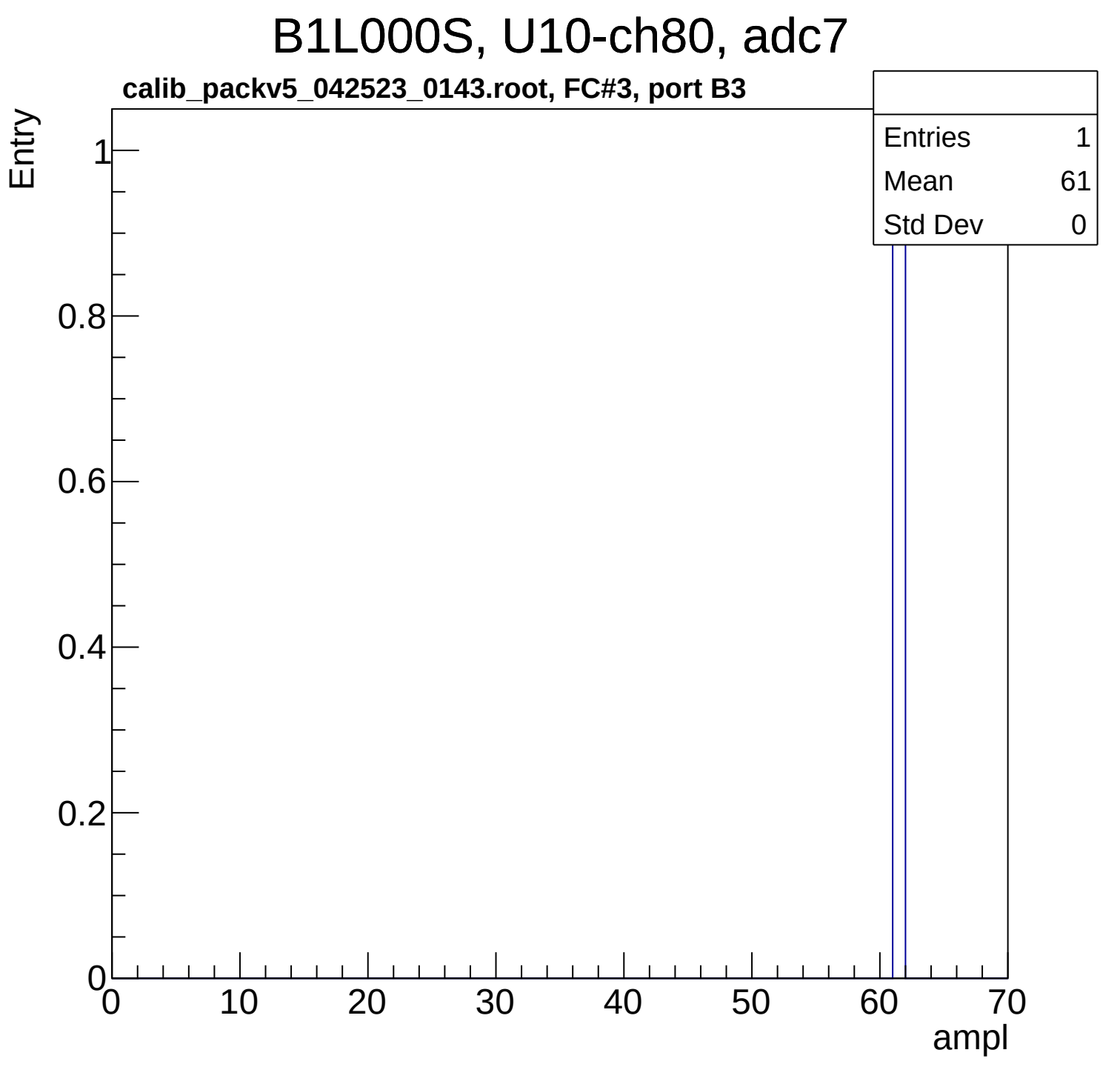
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch80, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 61
Std Dev 0
Entry
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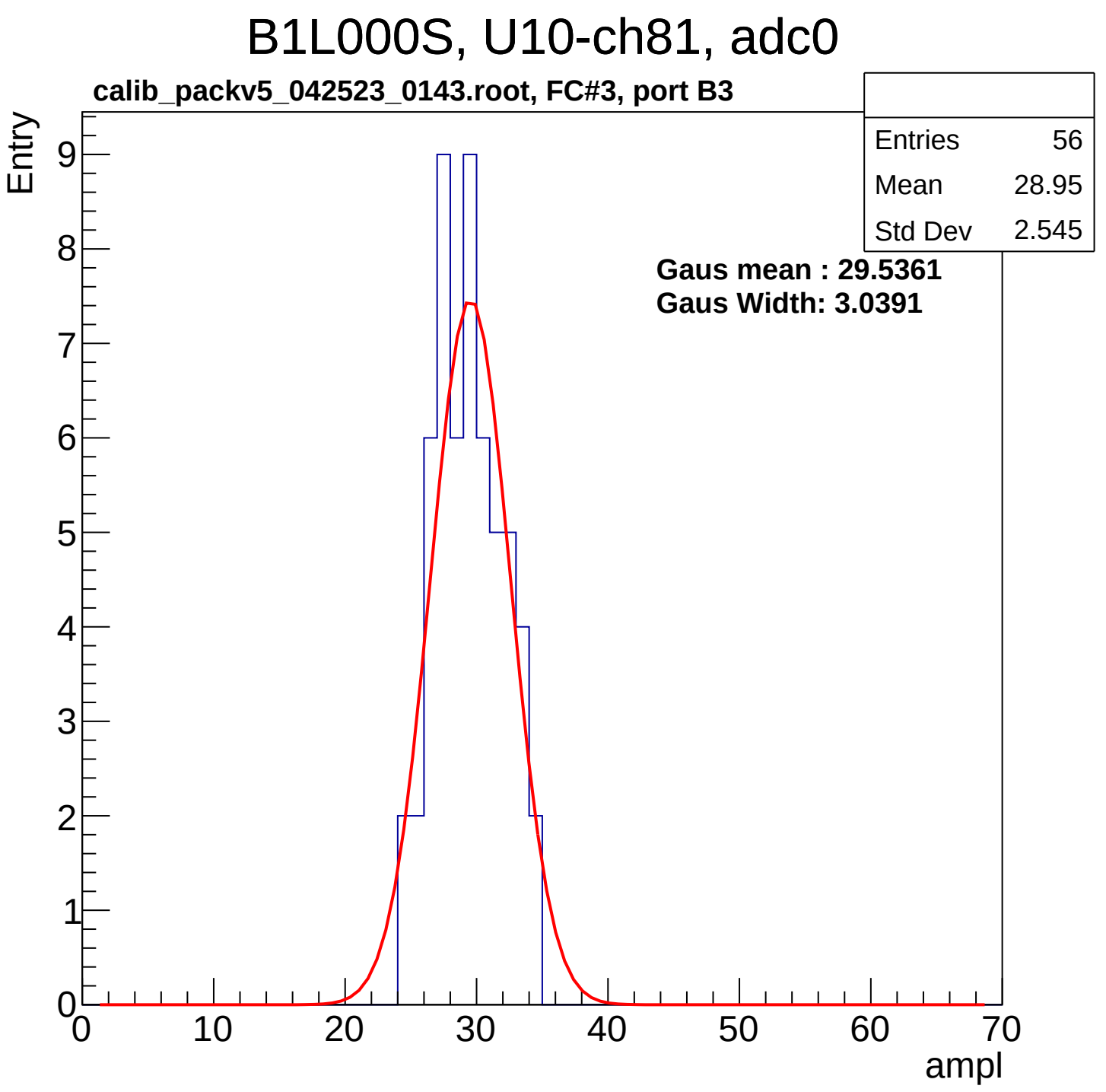
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch81, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 28.95
Std Dev 2.545
Gaus mean : 29.5361
Gaus Width: 3.0391
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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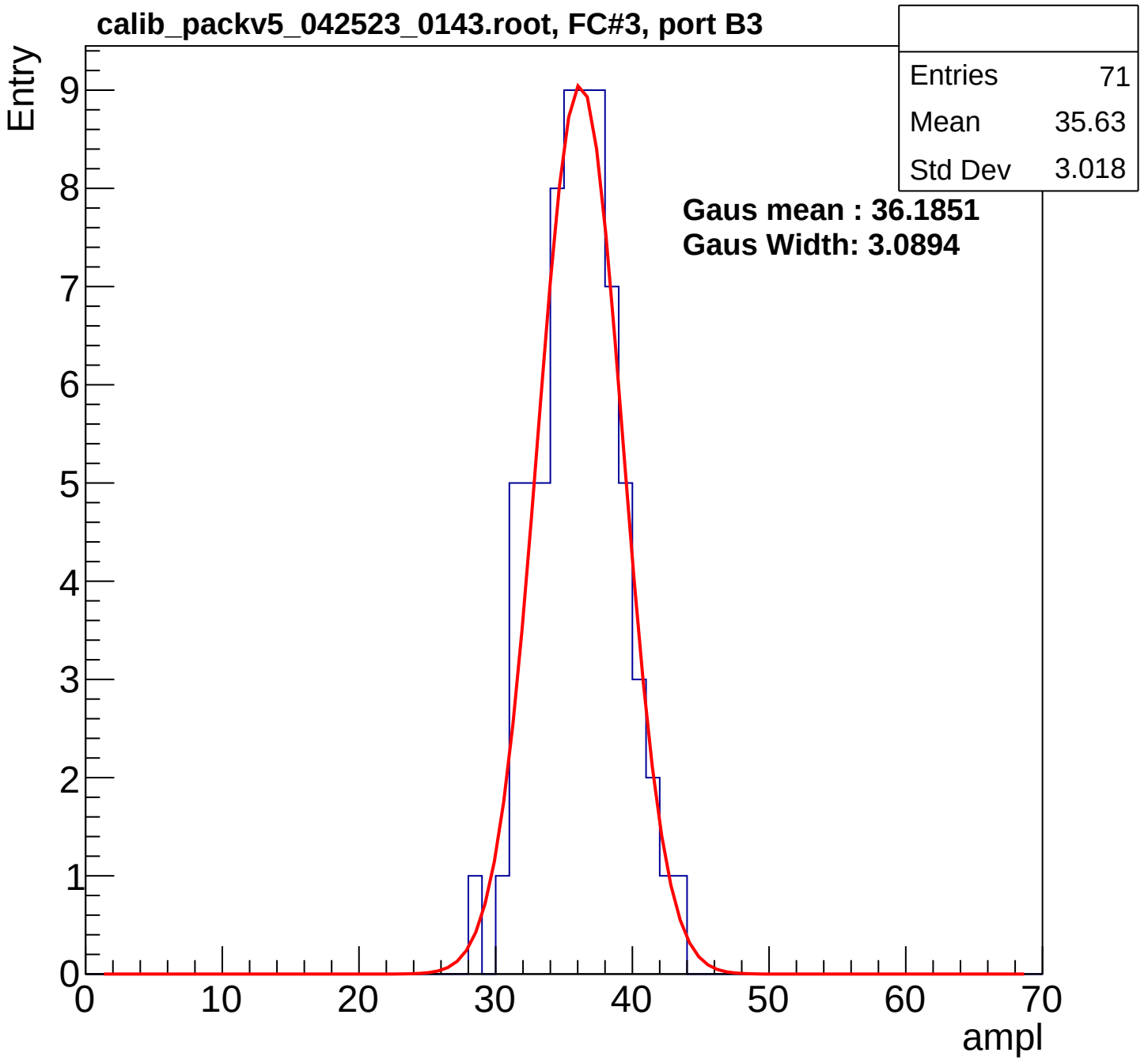
B1L000S, U10-ch81, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.63
Std Dev 3.018
Gaus mean : 36.1851
Gaus Width: 3.0894
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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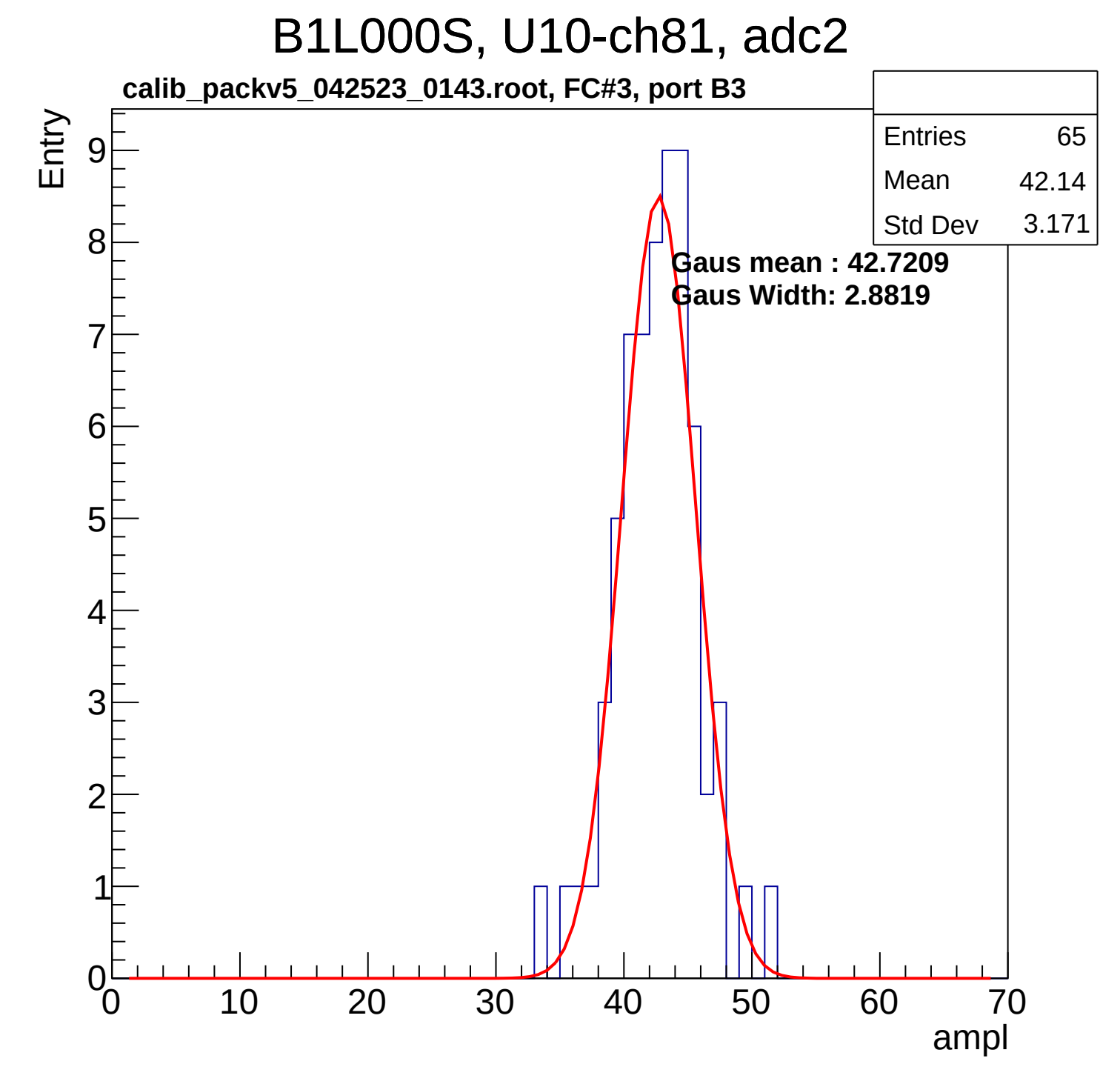
B1L000S, U10-ch81, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 42.14
Std Dev 3.171
Gaus mean : 42.7209
Gaus Width: 2.8819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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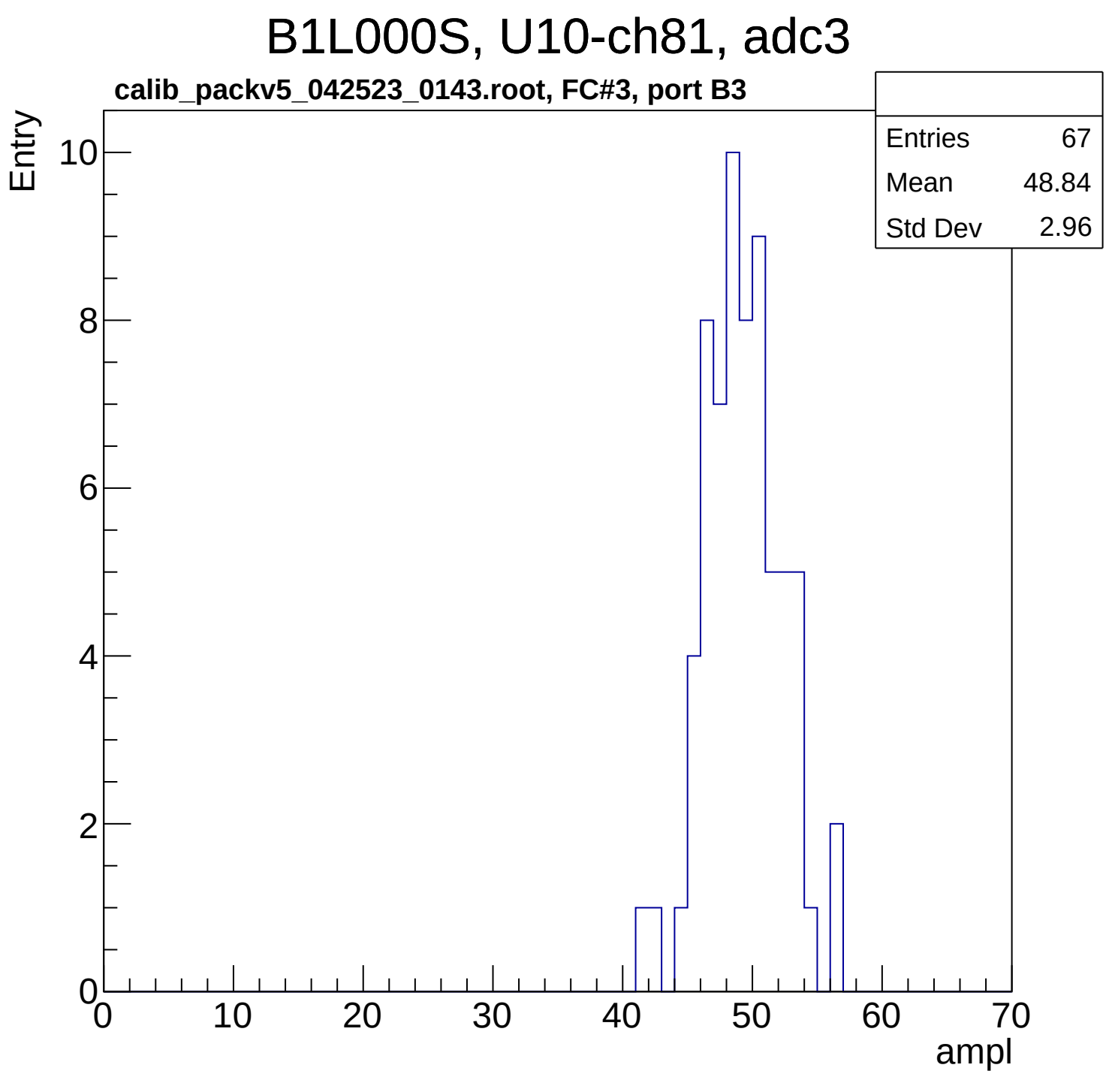
B1L000S, U10-ch81, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 48.84
Std Dev 2.96
Entry
ampl
0
2
4
6
8
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0
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40
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60
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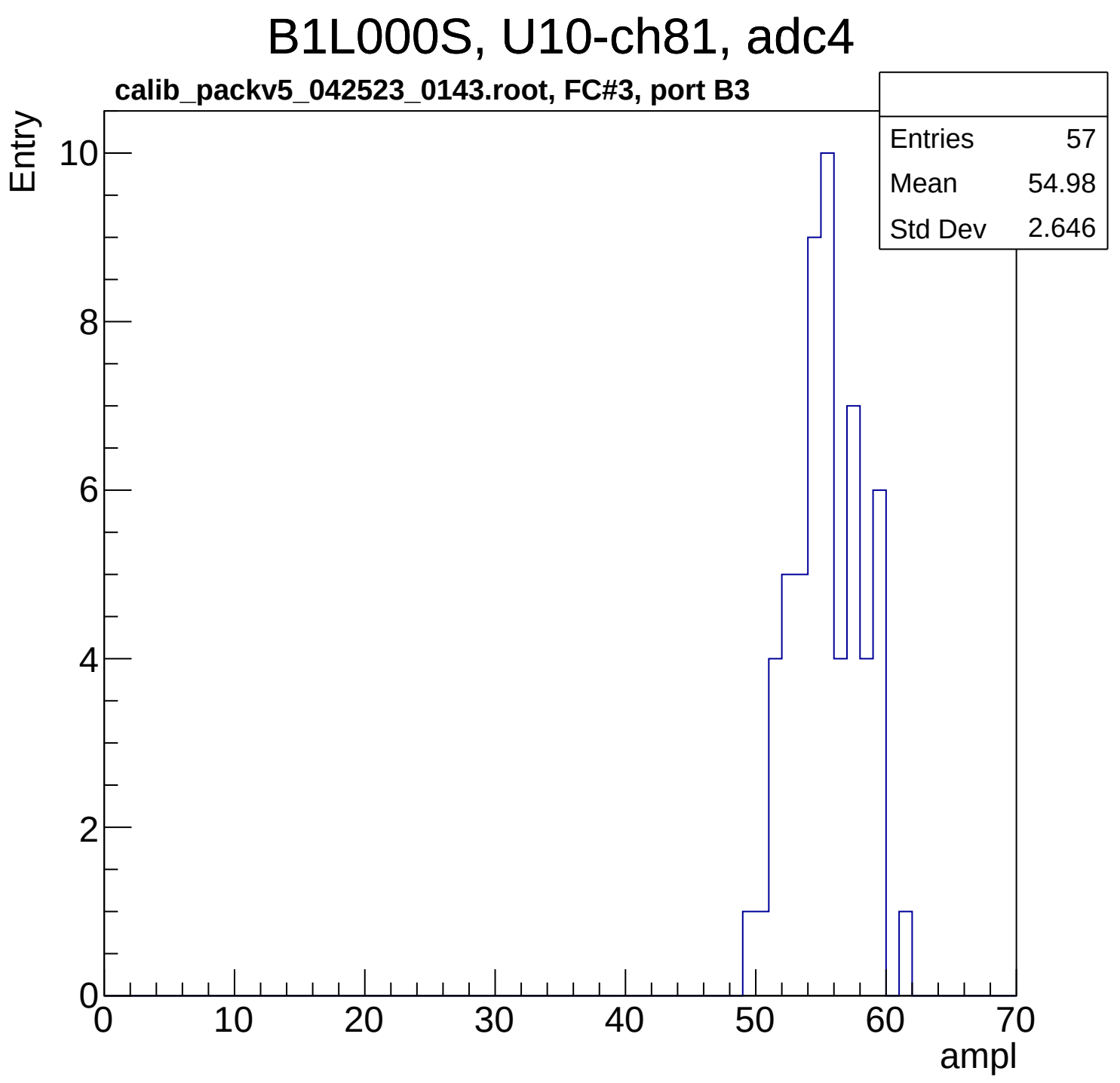
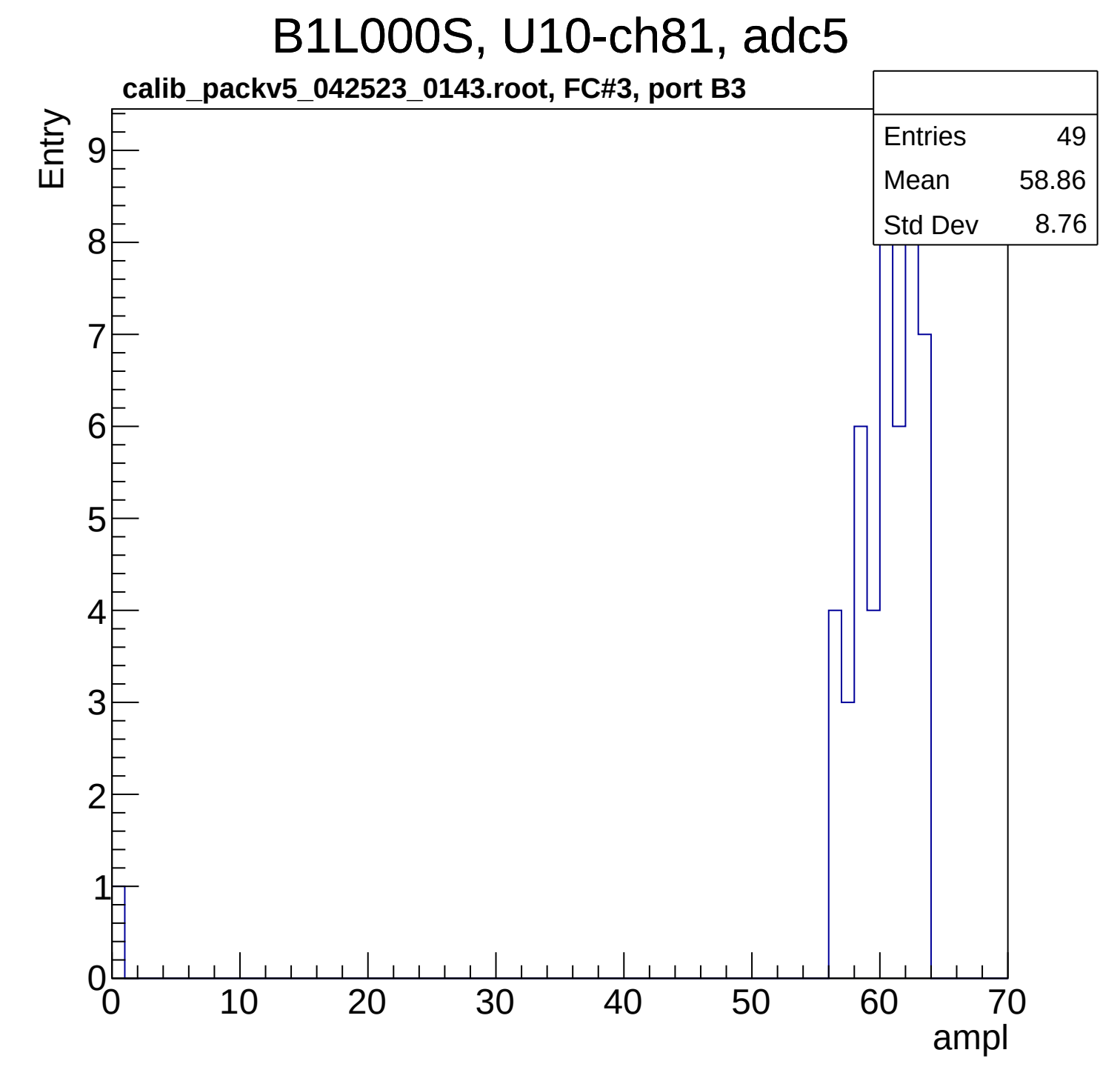

B1L000S, U10-ch81, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 54.98
Std Dev 2.646
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch81, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.86
Std Dev 8.76
Entry
ampl
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2
3
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6
7
8
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0
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60
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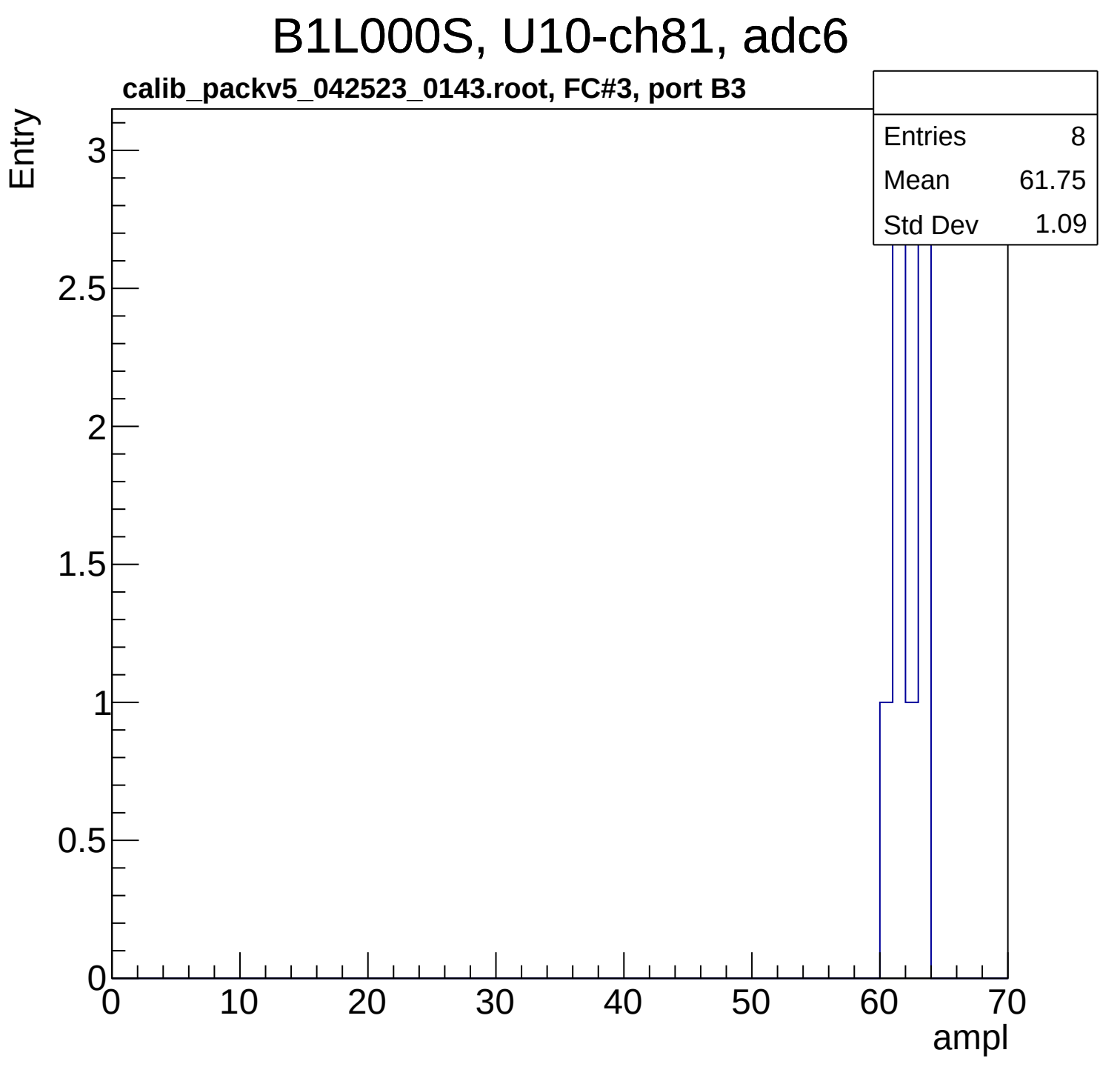
B1L000S, U10-ch81, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.75
Std Dev 1.09
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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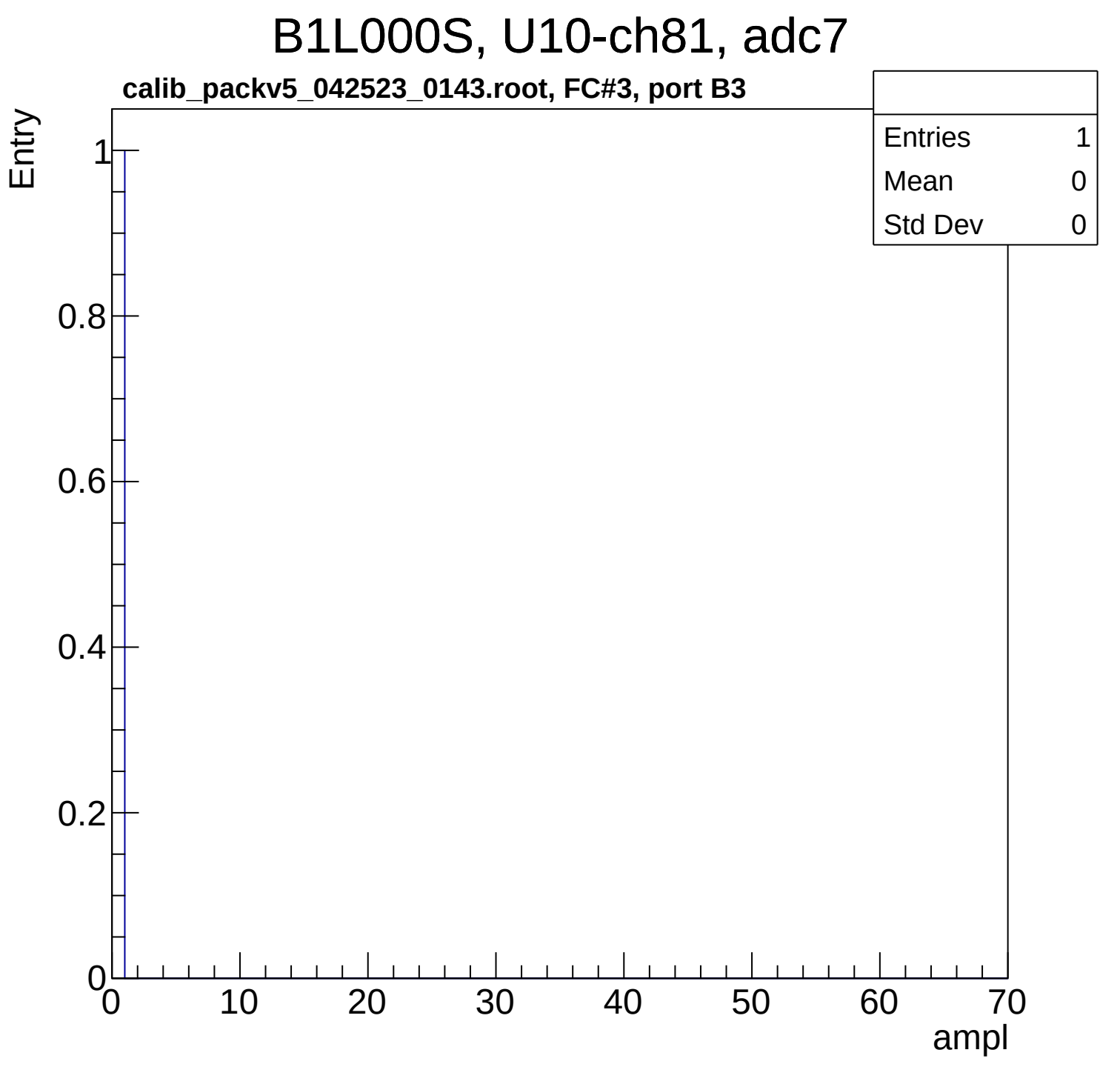
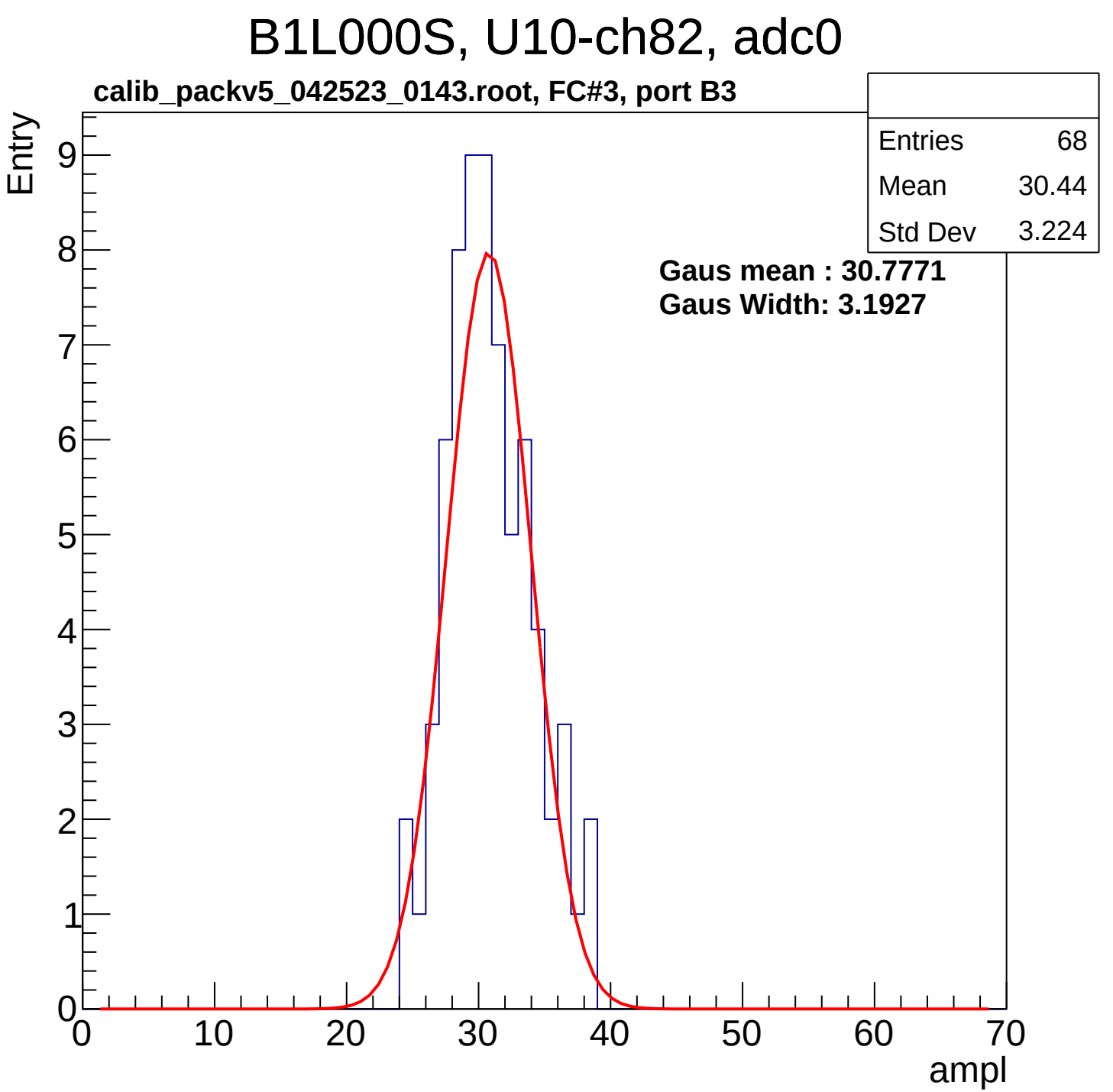
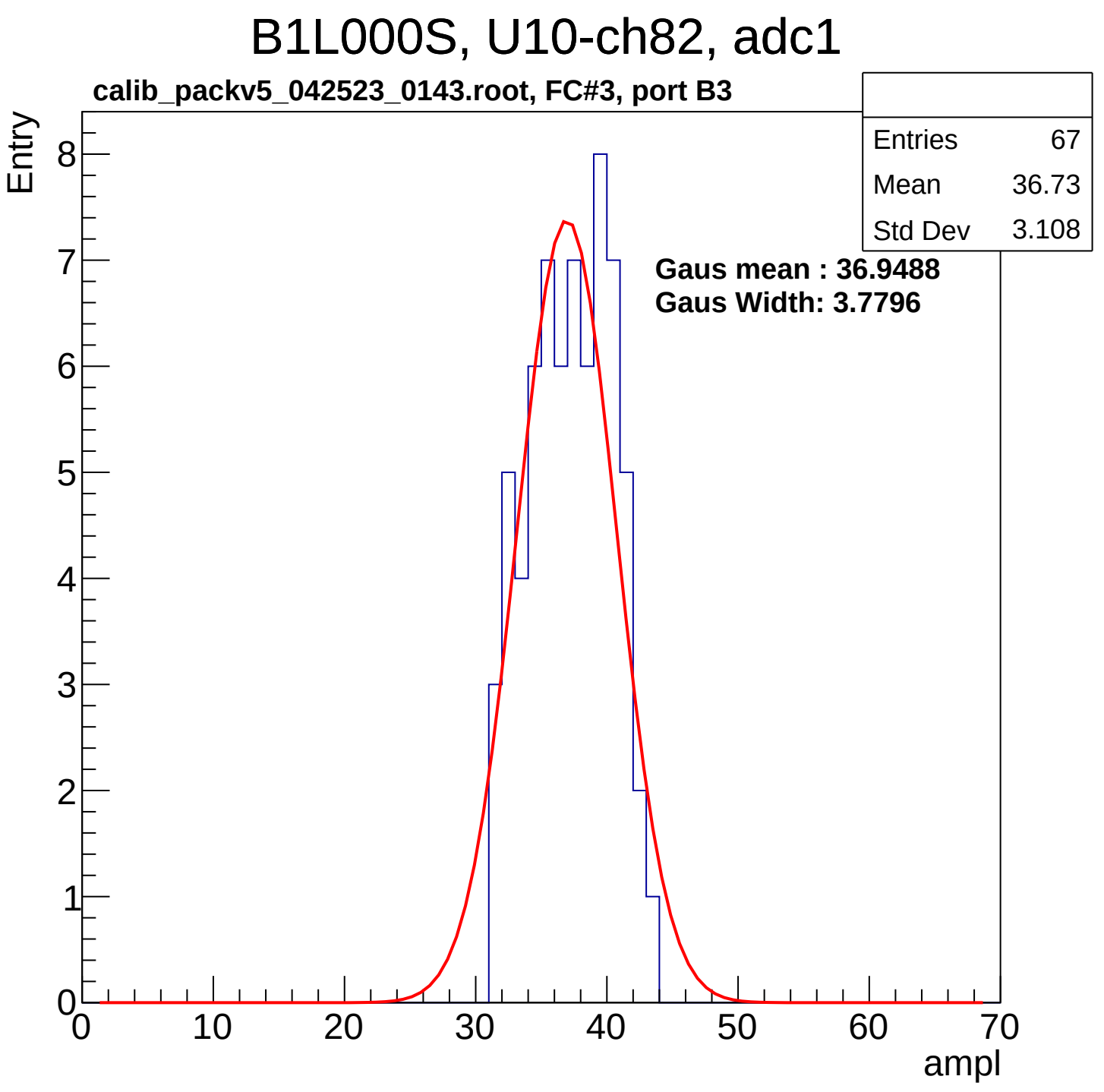

B1L000S, U10-ch81, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch82, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 30.44
Std Dev 3.224
Gaus mean : 30.7771
Gaus Width: 3.1927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch82, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 36.73
Std Dev 3.108
Gaus mean : 36.9488
Gaus Width: 3.7796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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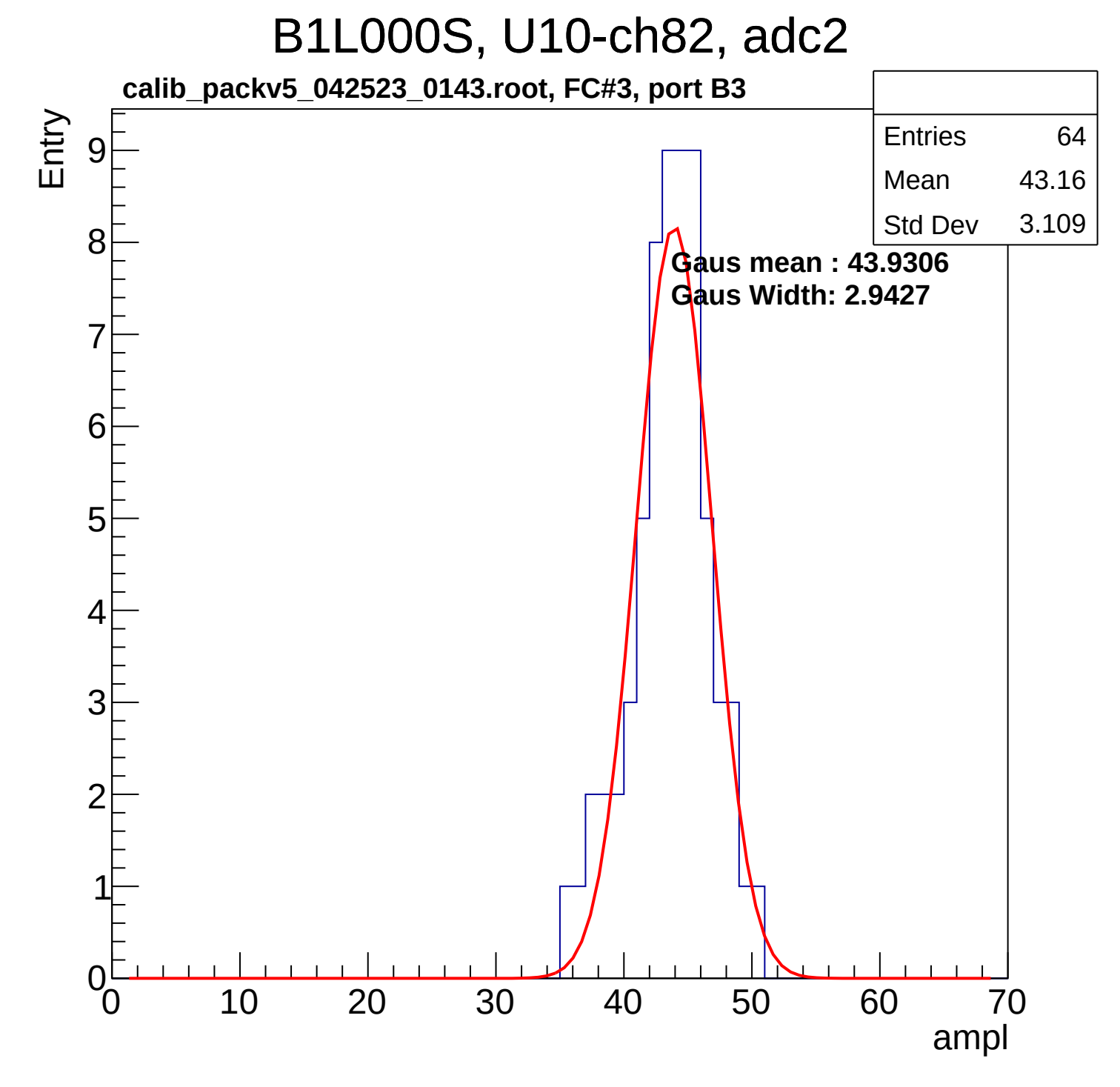
B1L000S, U10-ch82, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 43.16
Std Dev 3.109
Gaus mean : 43.9306
Gaus Width: 2.9427
Entry
ampl
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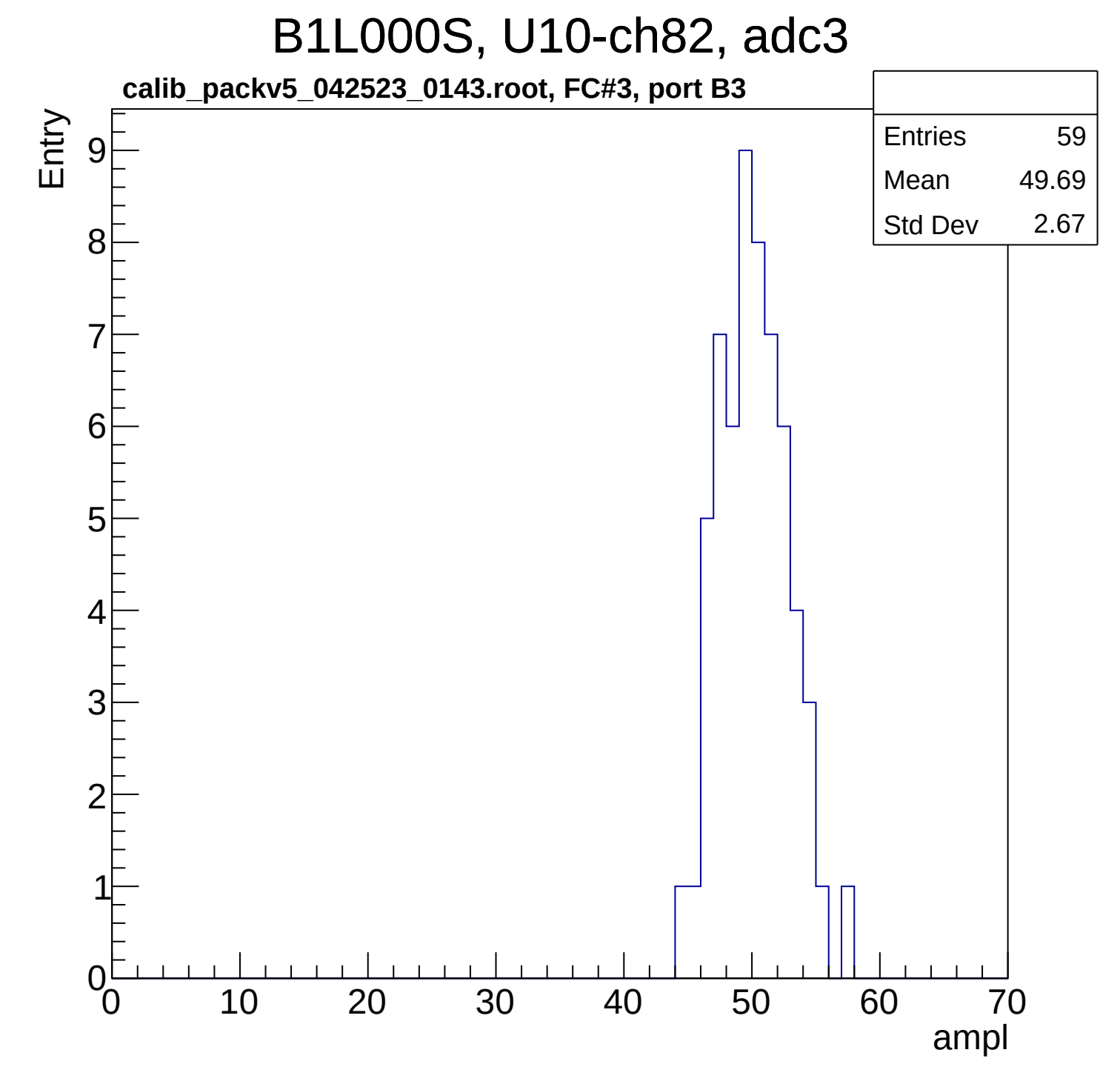
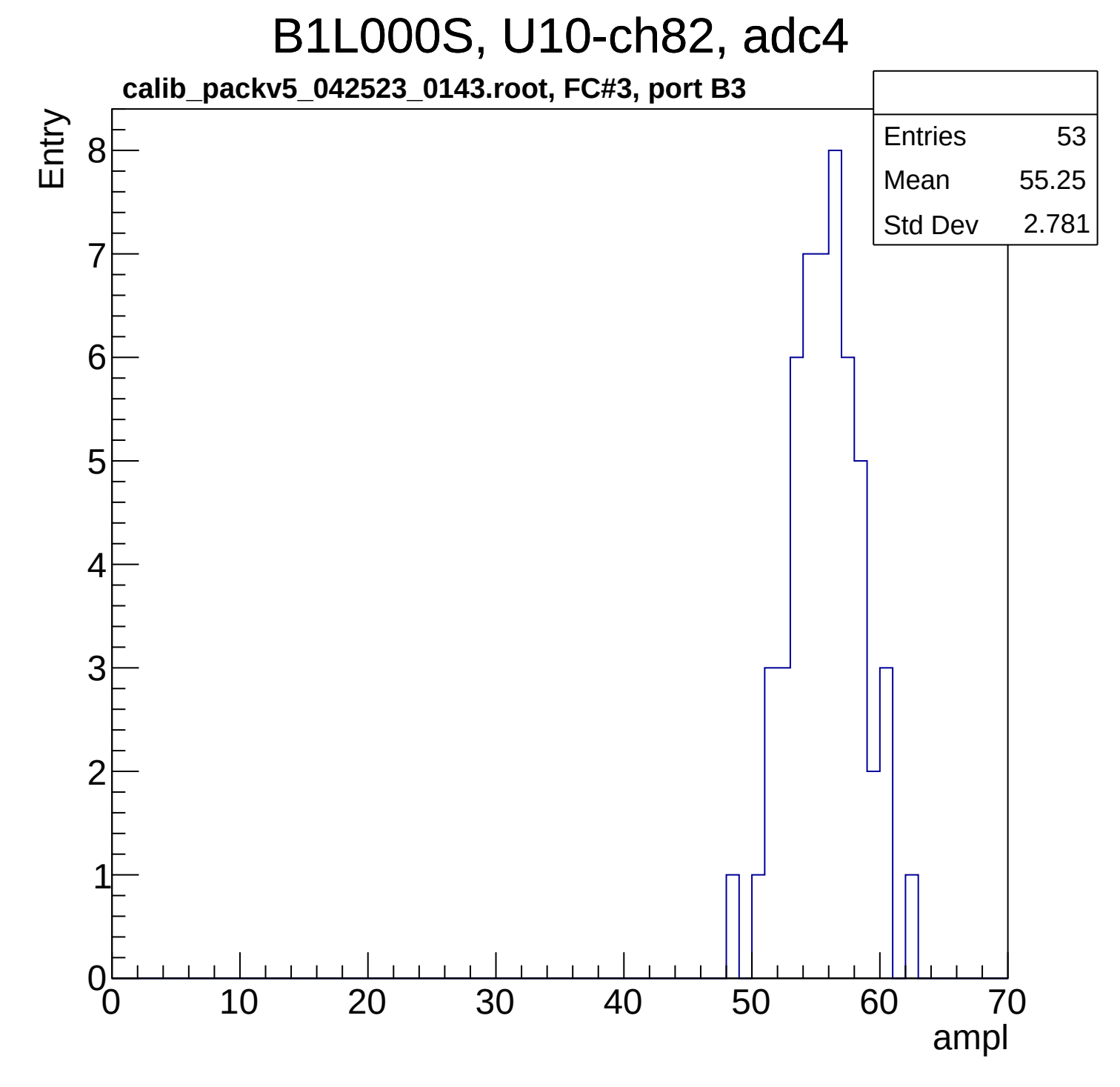
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U10-ch82, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 49.69
Std Dev 2.67
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch82, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 55.25
Std Dev 2.781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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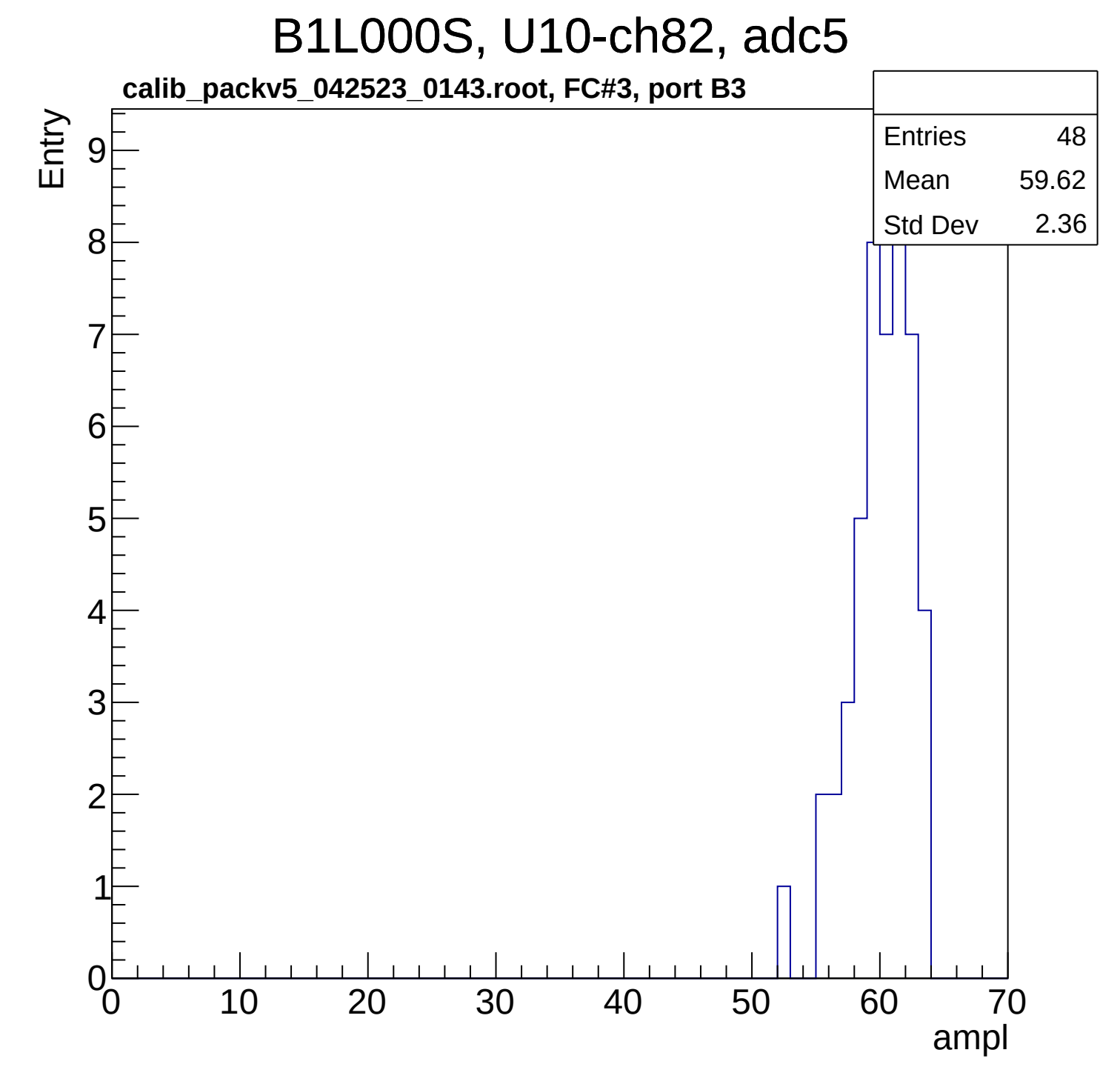
B1L000S, U10-ch82, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 59.62
Std Dev 2.36
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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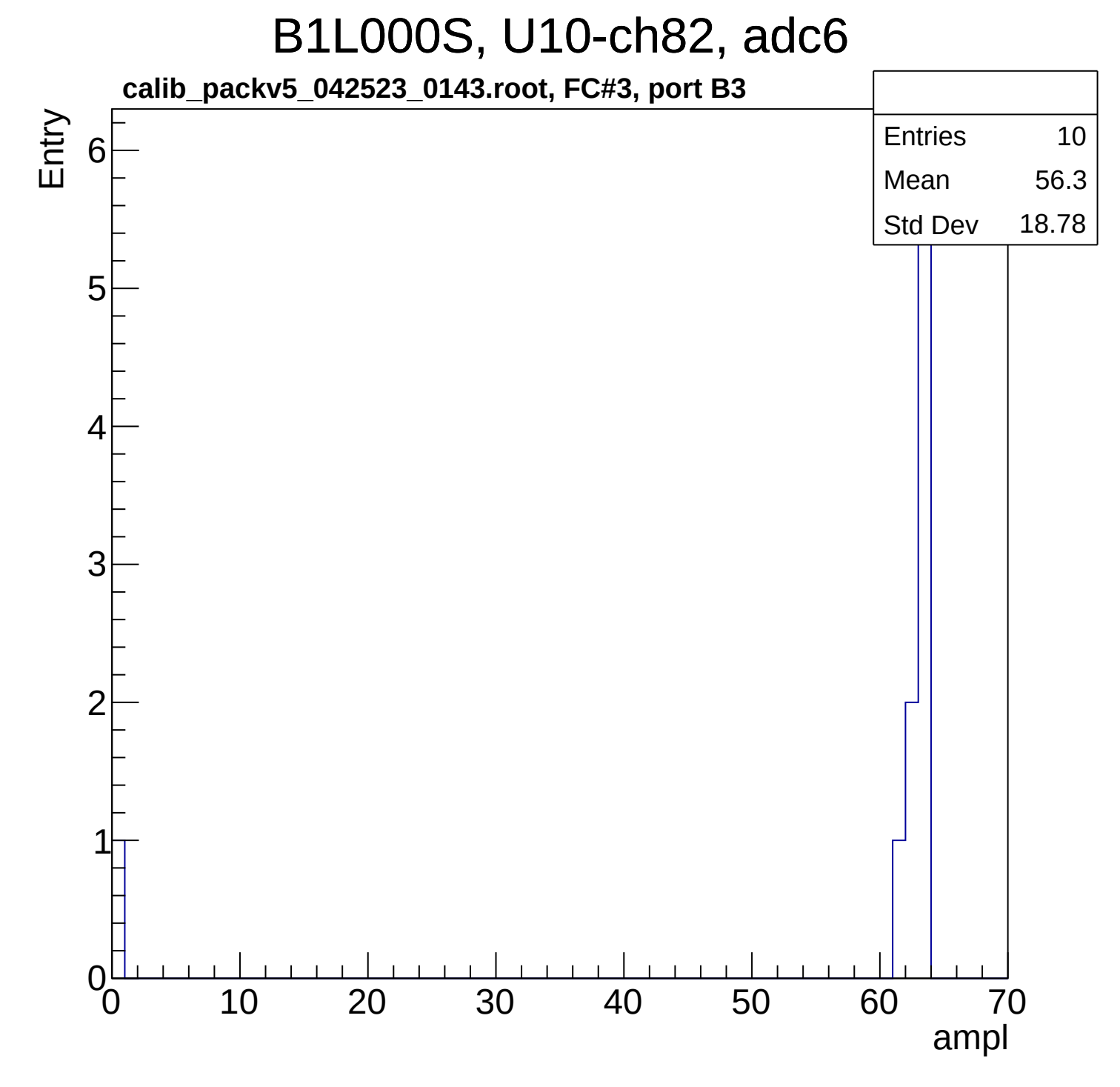
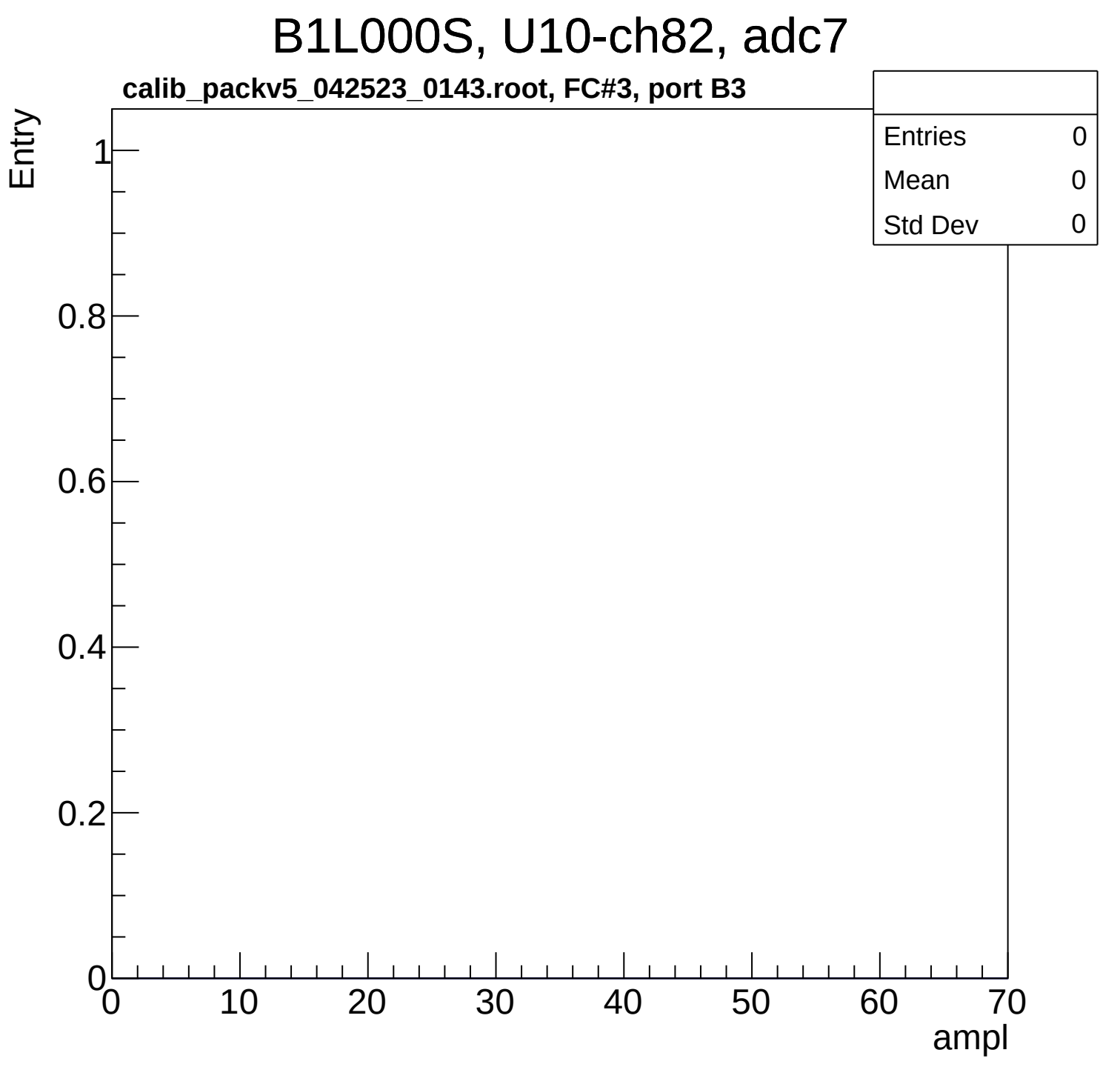
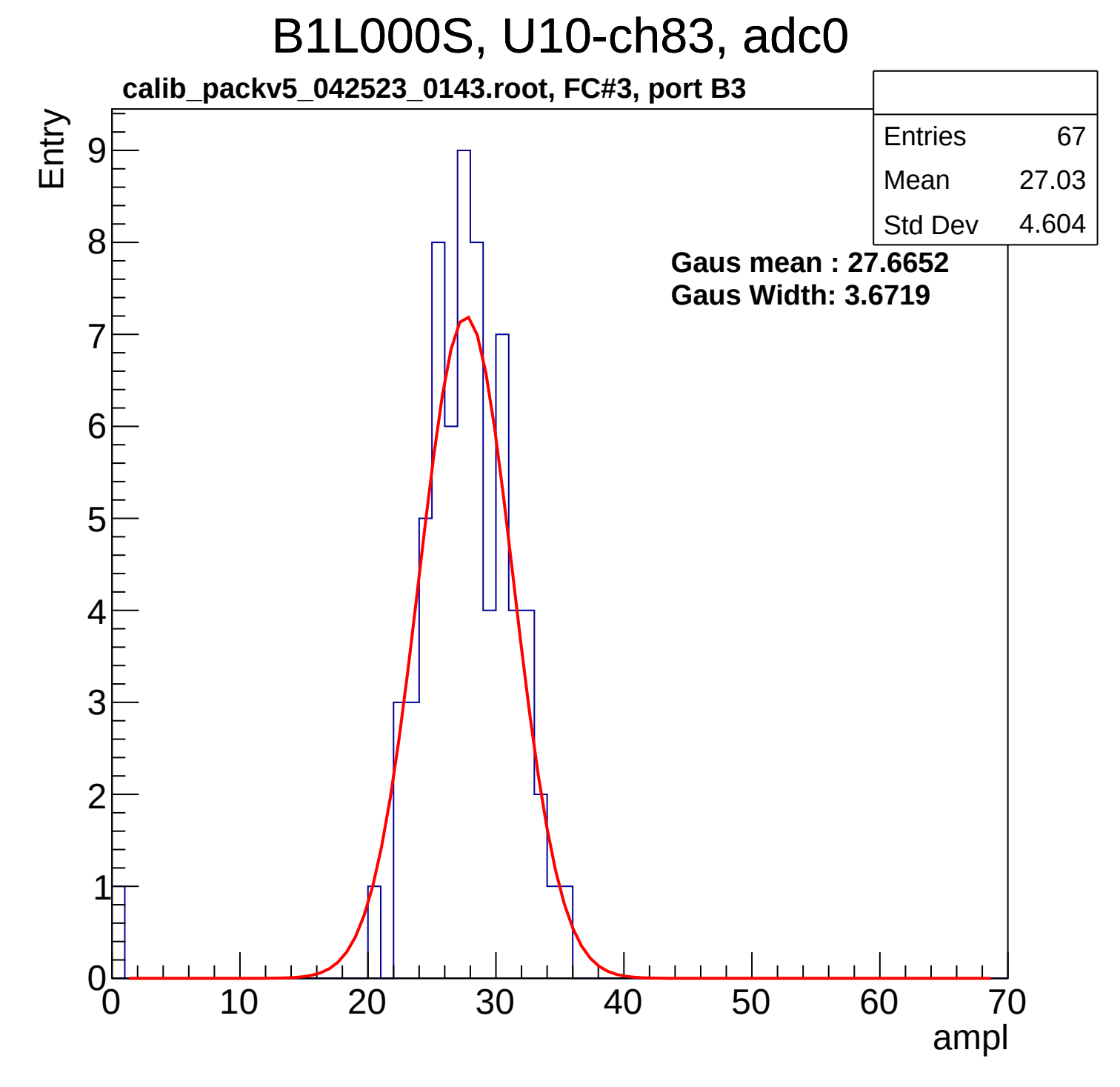

B1L000S, U10-ch82, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 56.3
Std Dev 18.78
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch82, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch83, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.03
Std Dev 4.604
Gaus mean : 27.6652
Gaus Width: 3.6719
Entry
ampl
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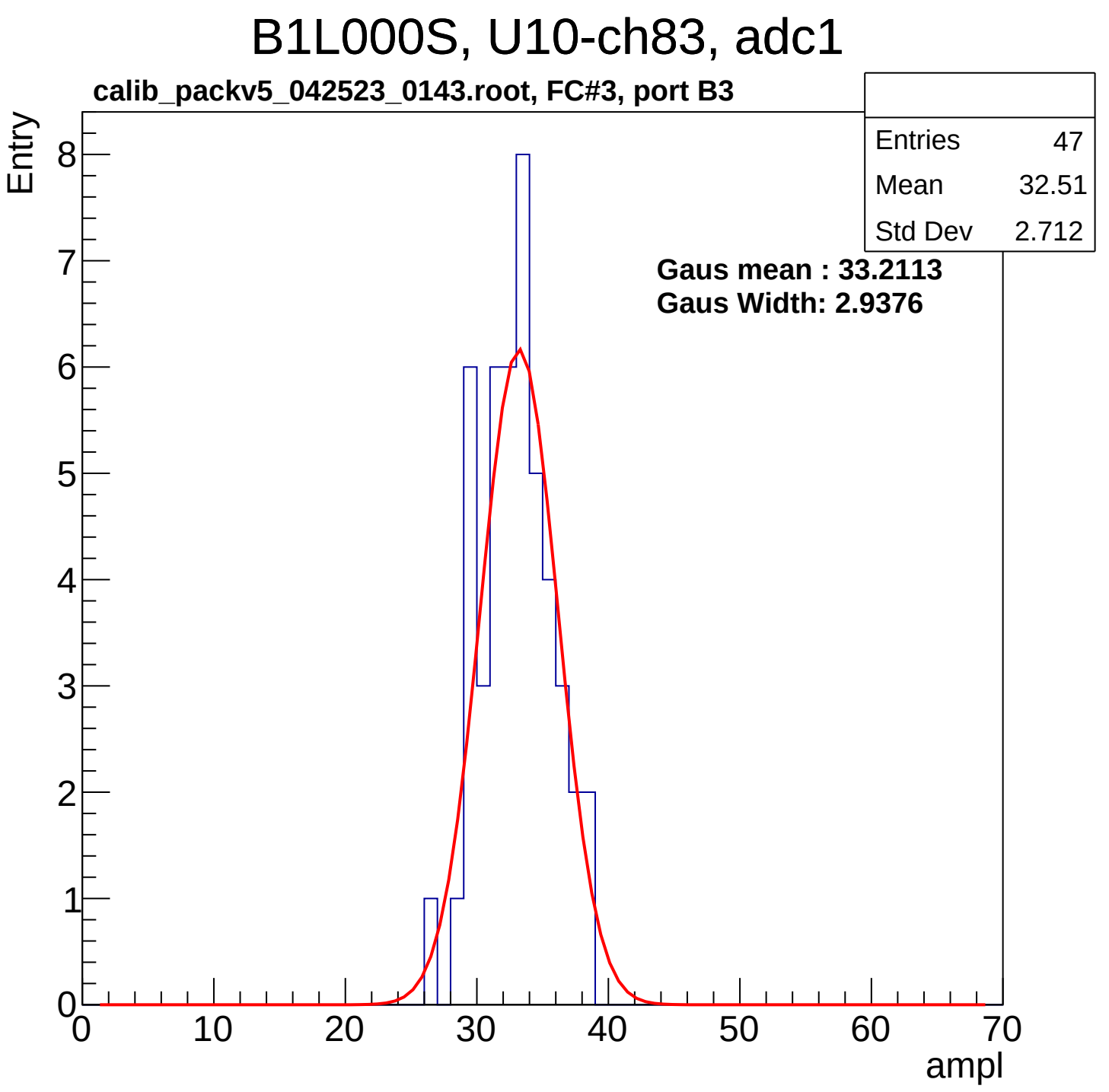
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch83, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 32.51
Std Dev 2.712
Gaus mean : 33.2113
Gaus Width: 2.9376
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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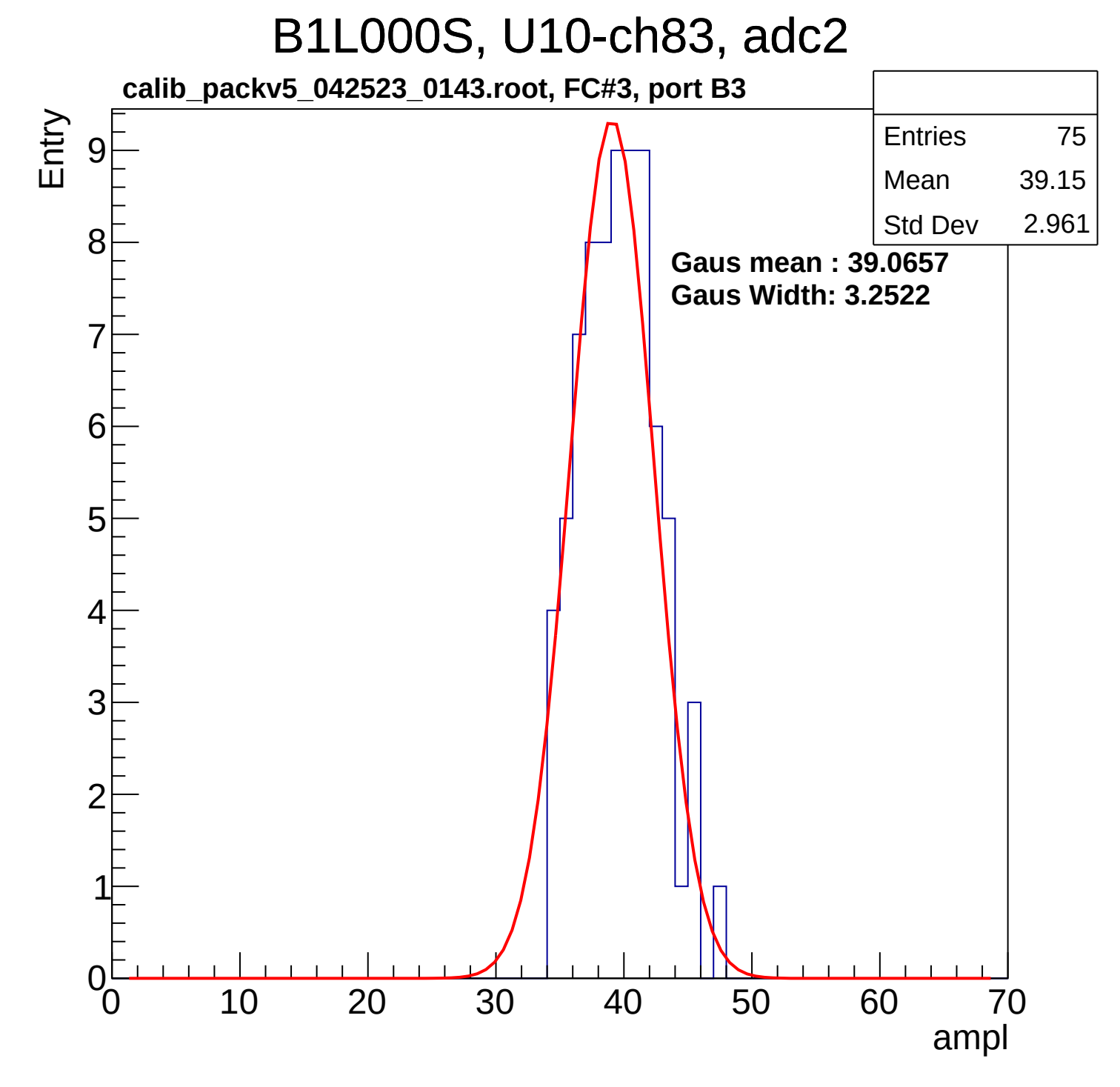
B1L000S, U10-ch83, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 39.15
Std Dev 2.961
Gaus mean : 39.0657
Gaus Width: 3.2522
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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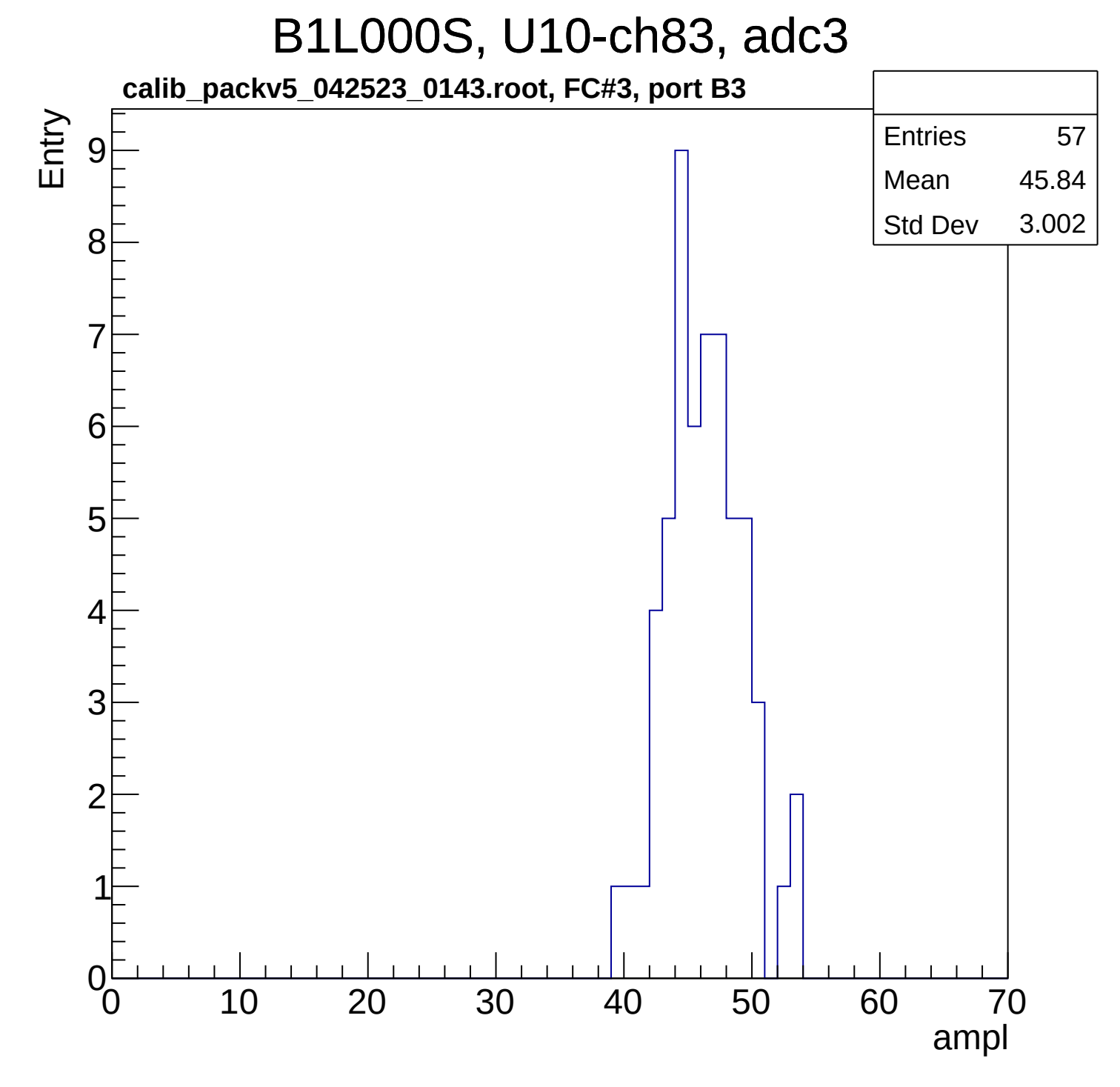
B1L000S, U10-ch83, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 45.84
Std Dev 3.002
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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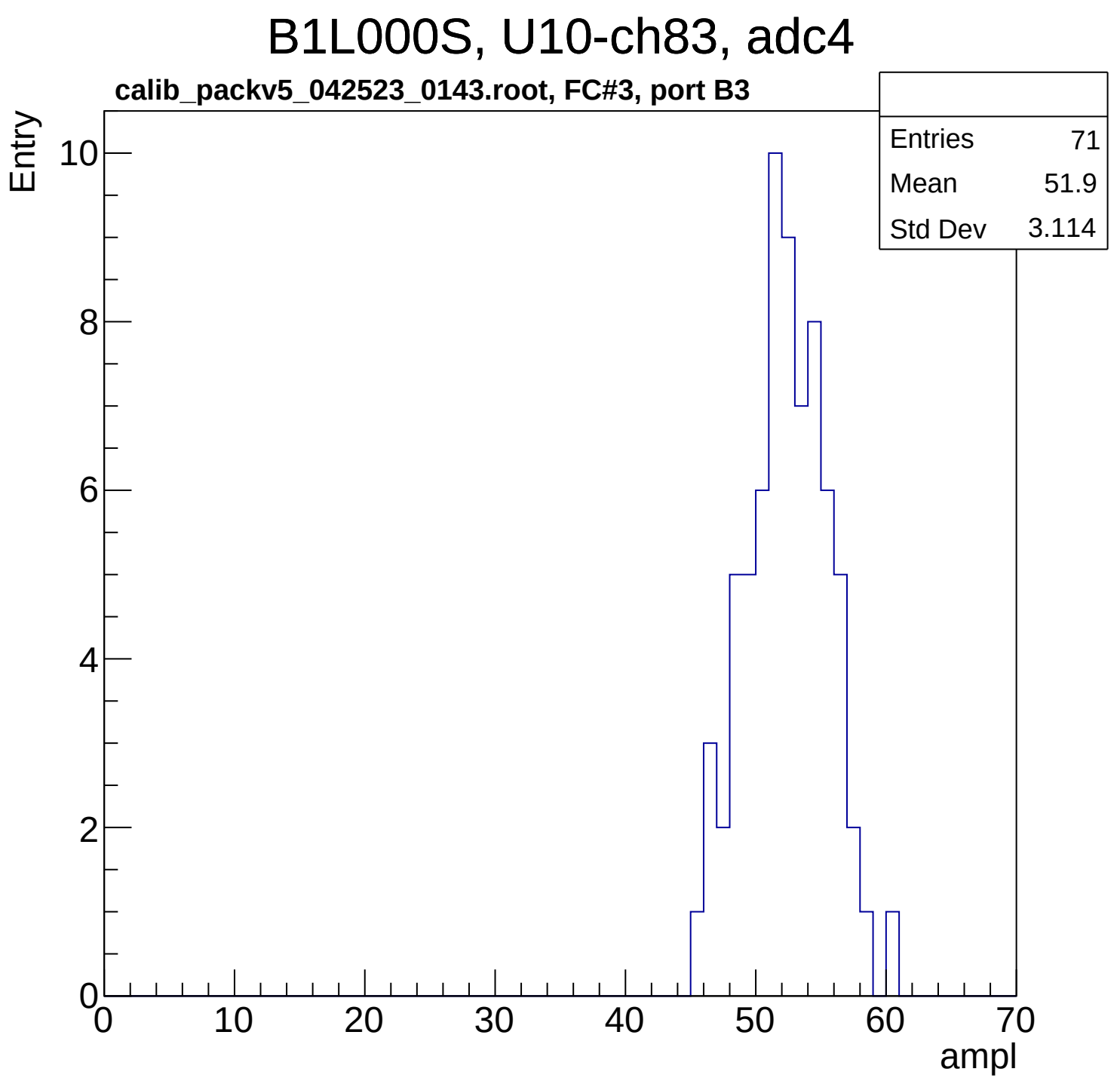
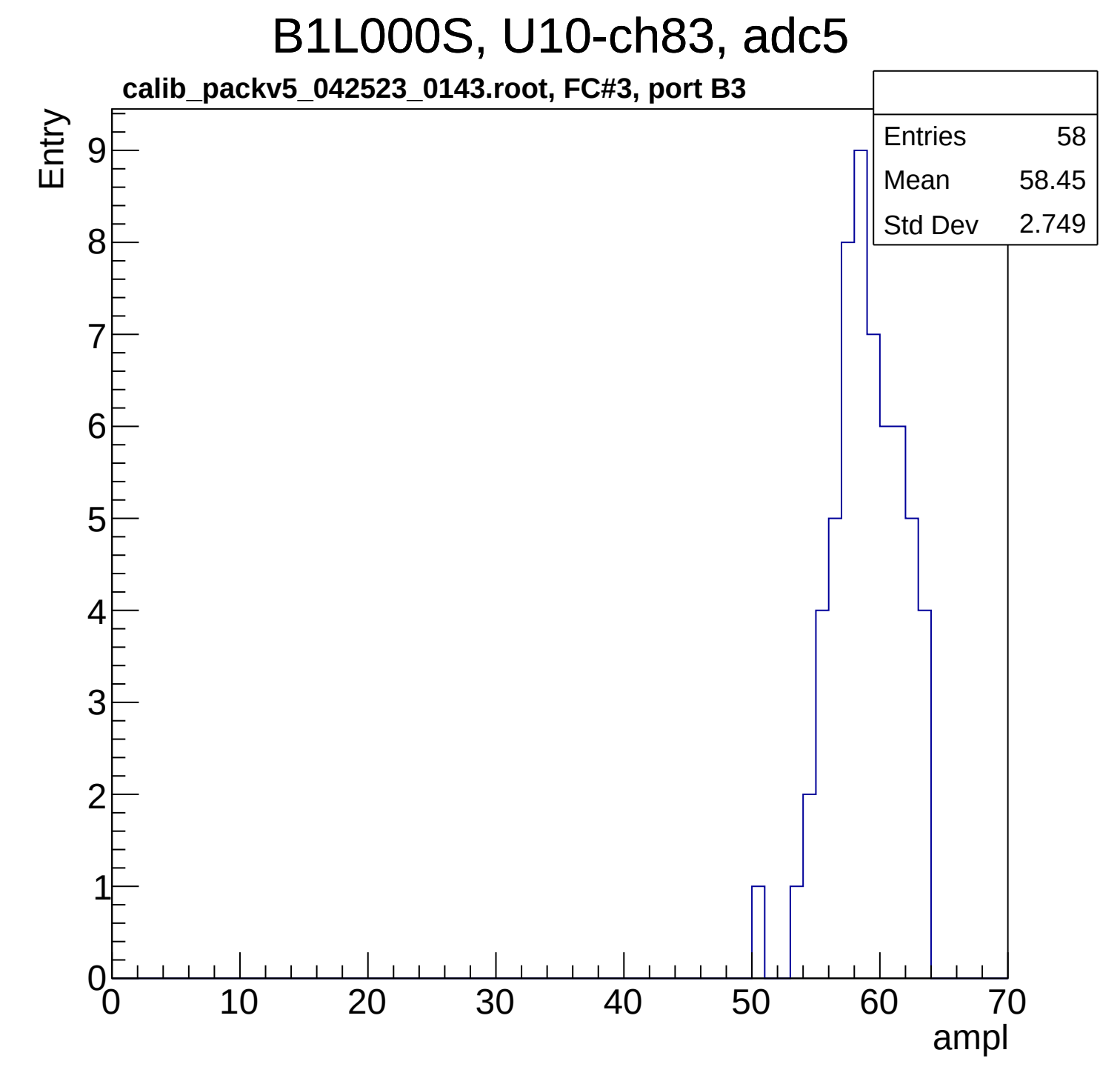

B1L000S, U10-ch83, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 51.9
Std Dev 3.114
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch83, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.45
Std Dev 2.749
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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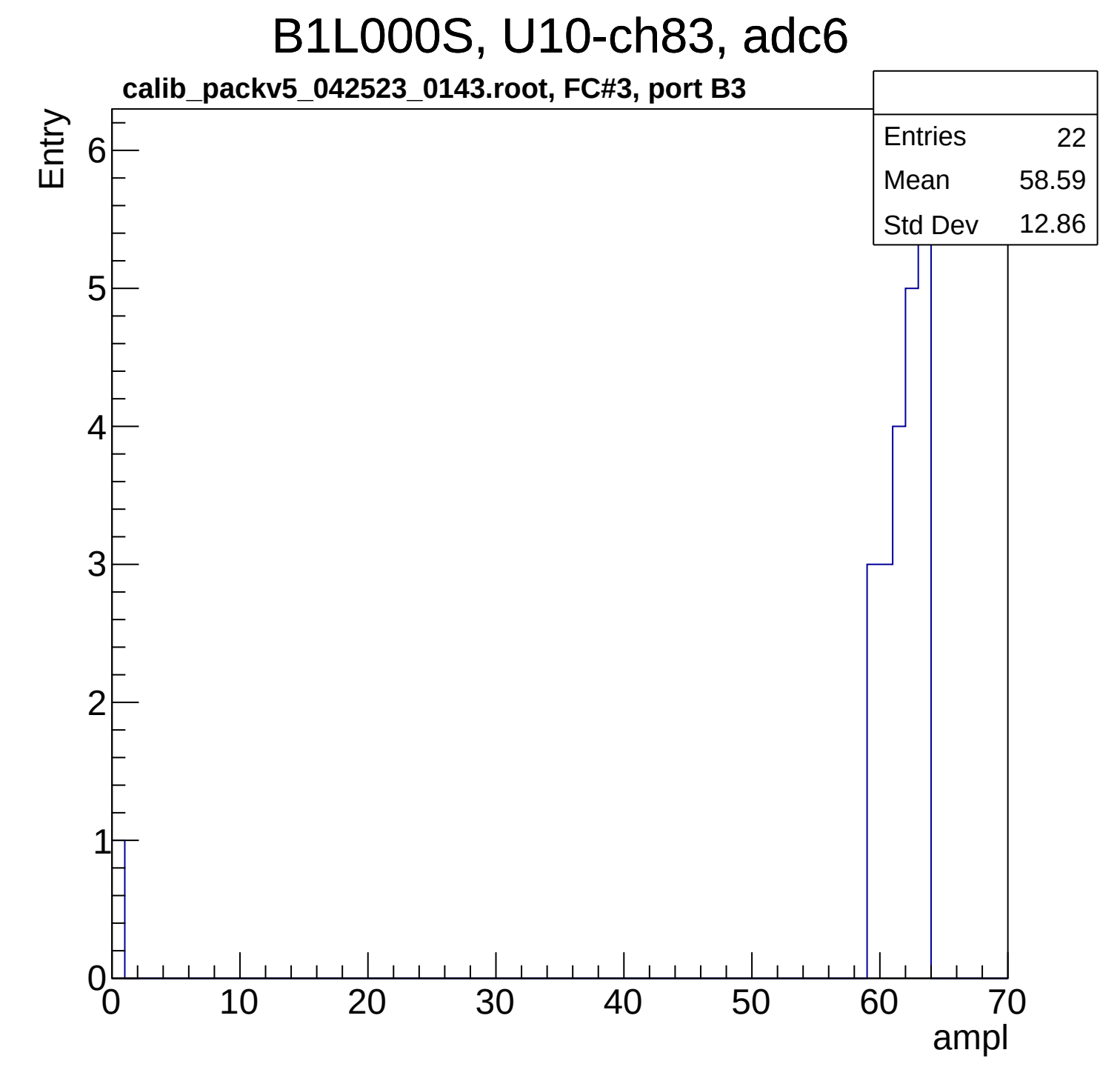
B1L000S, U10-ch83, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.59
Std Dev 12.86
Entry
0
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2
3
4
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6
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10
20
30
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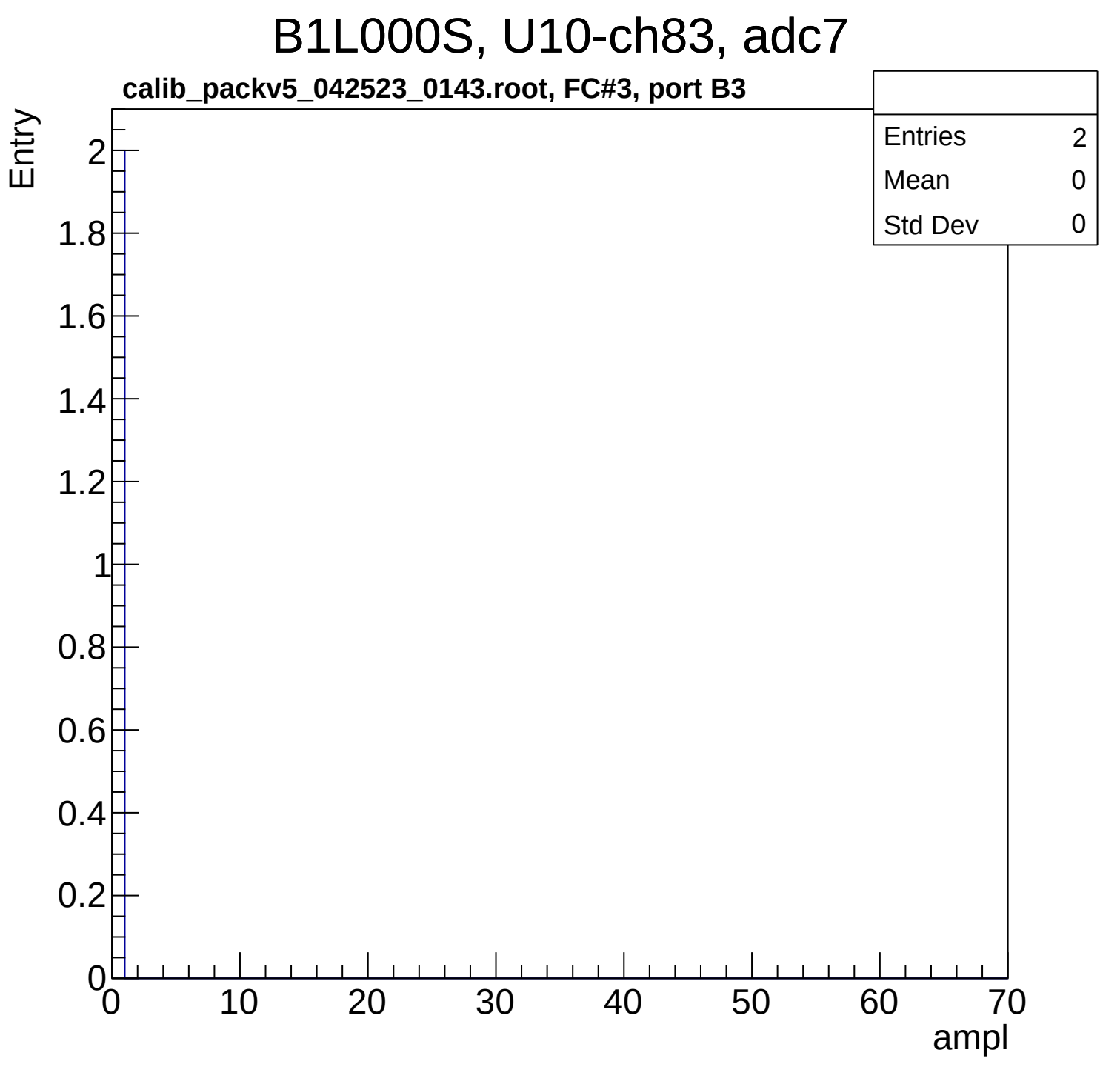
50
60
70
ampl


B1L000S, U10-ch83, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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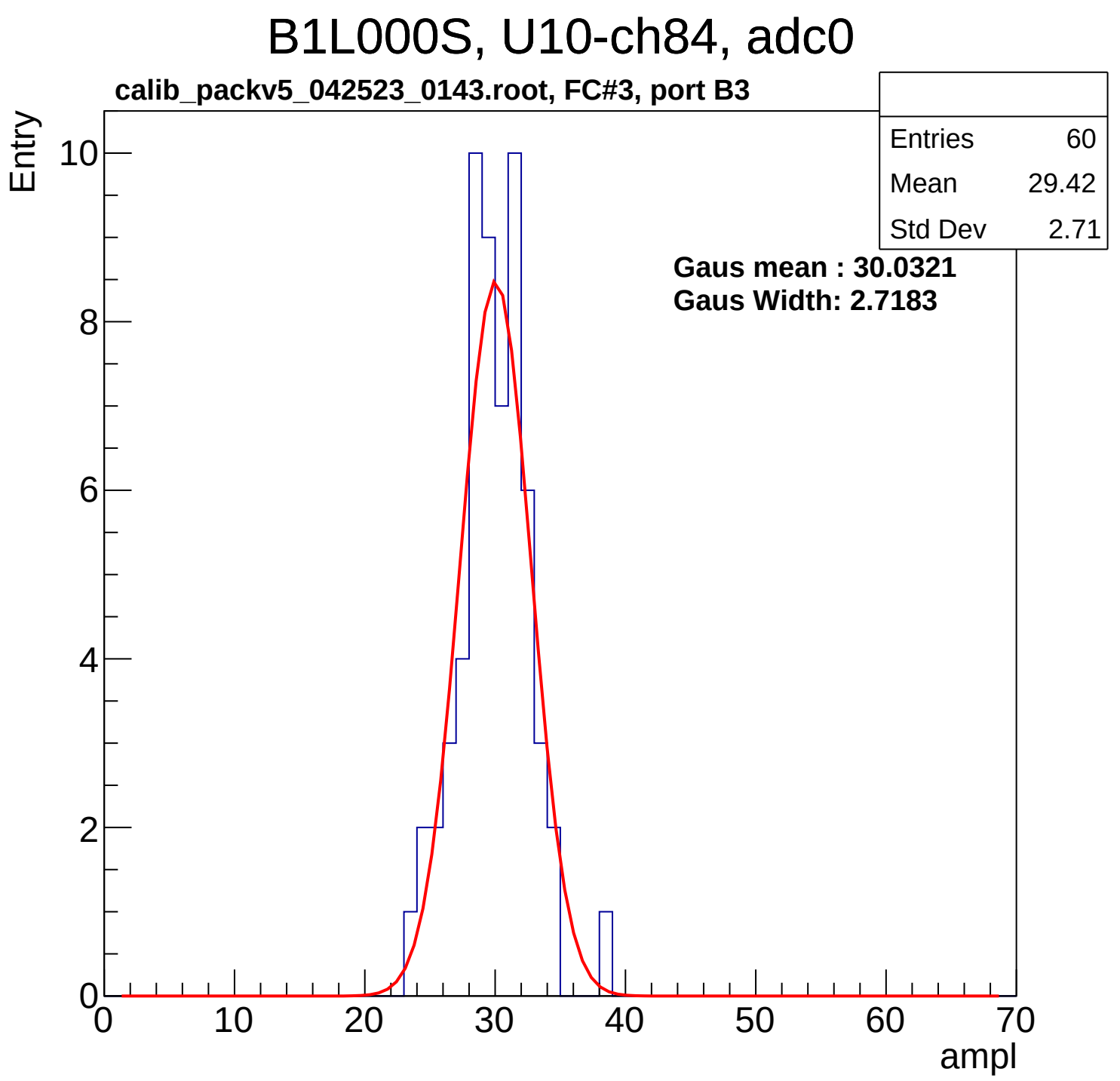
40
50
60
70


B1L000S, U10-ch84, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 29.42
Std Dev 2.71
Gaus mean : 30.0321
Gaus Width: 2.7183
Entry
ampl
0
2
4
6
8
10
0
10
20
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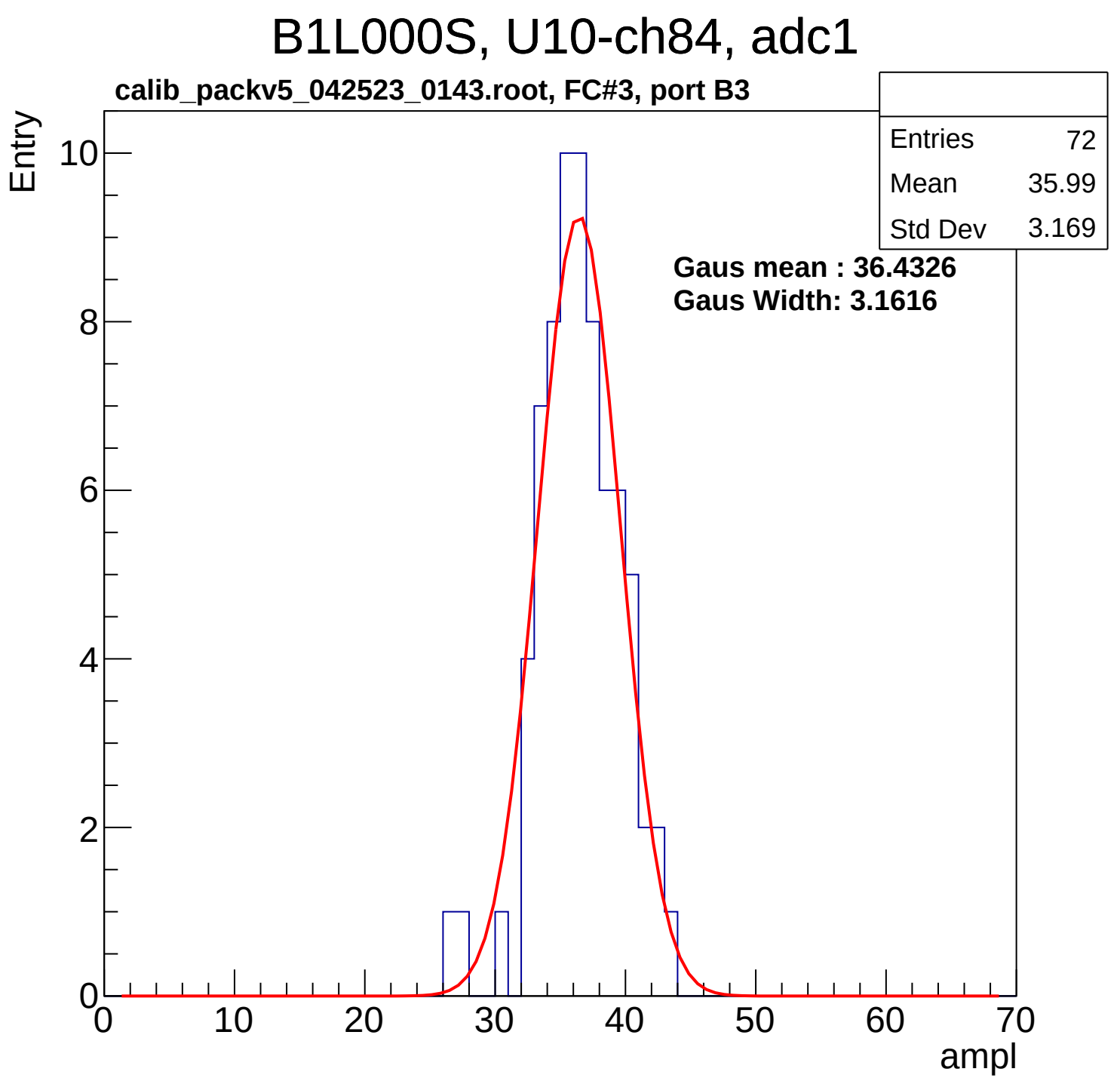
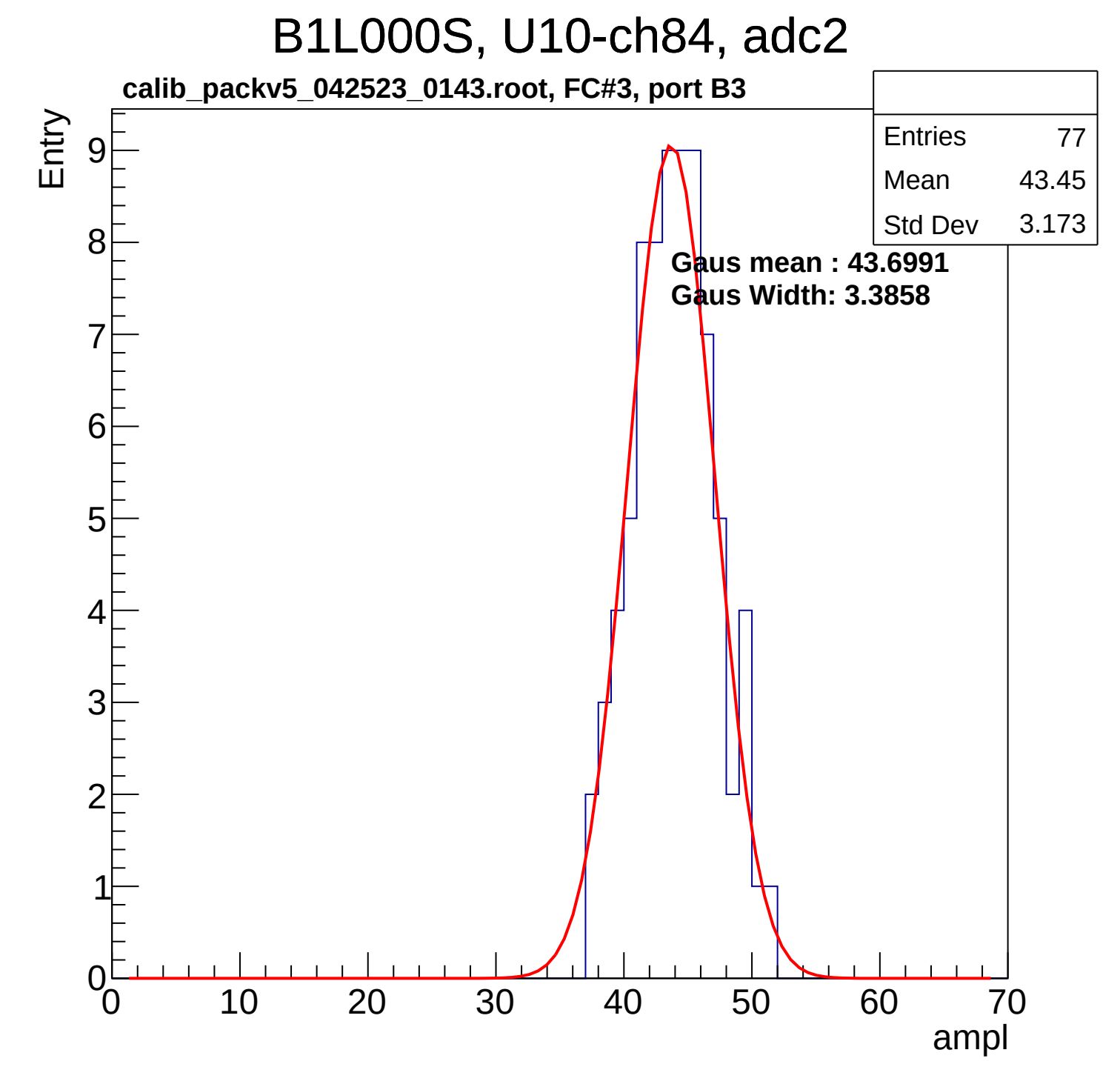
40
50
60
70


B1L000S, U10-ch84, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.99
Std Dev 3.169
Gaus mean : 36.4326
Gaus Width: 3.1616
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch84, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 43.45
Std Dev 3.173
Gaus mean : 43.6991
Gaus Width: 3.3858
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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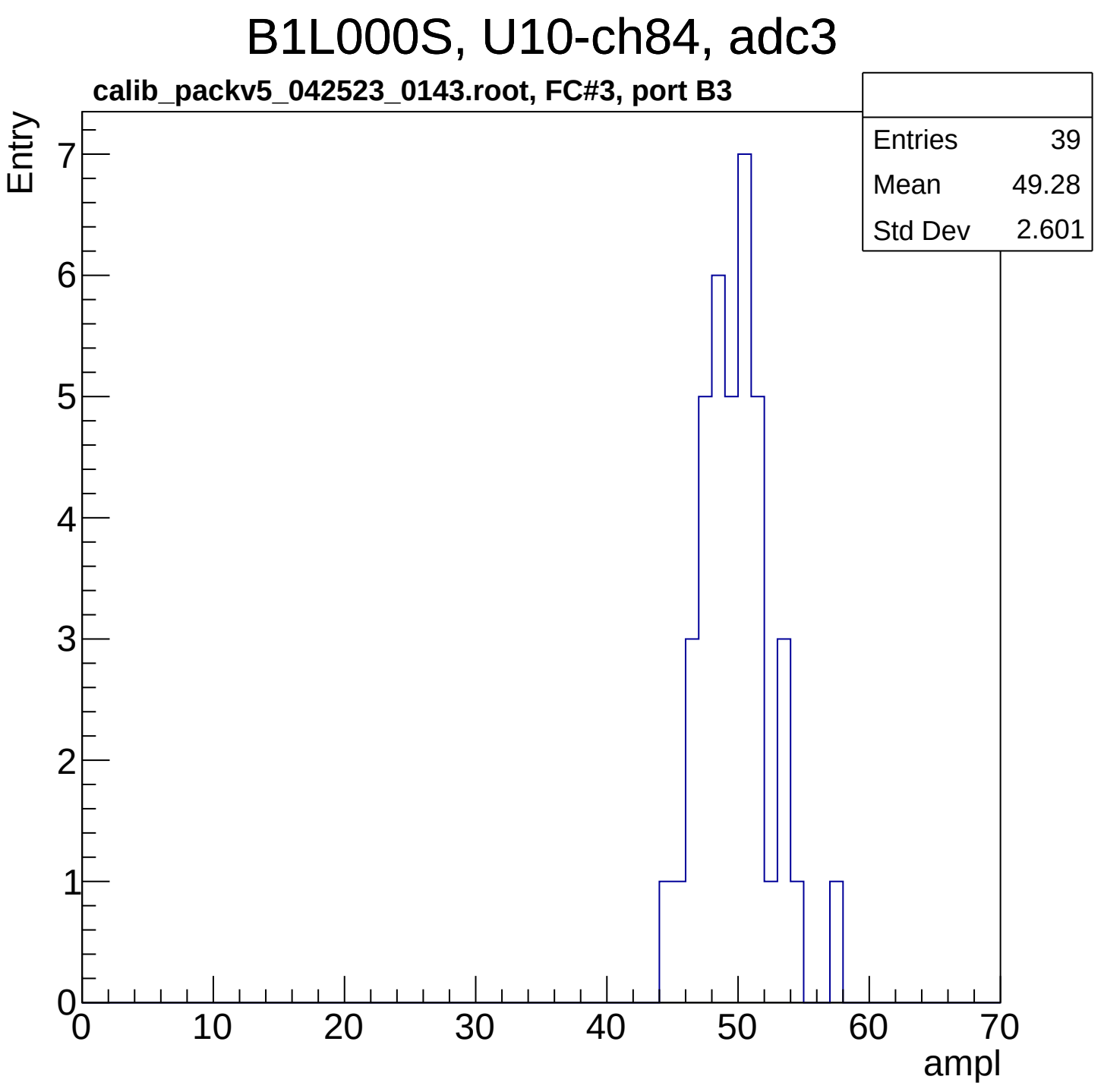
B1L000S, U10-ch84, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 49.28
Std Dev 2.601
Entry
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3
4
5
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7
0
10
20
30
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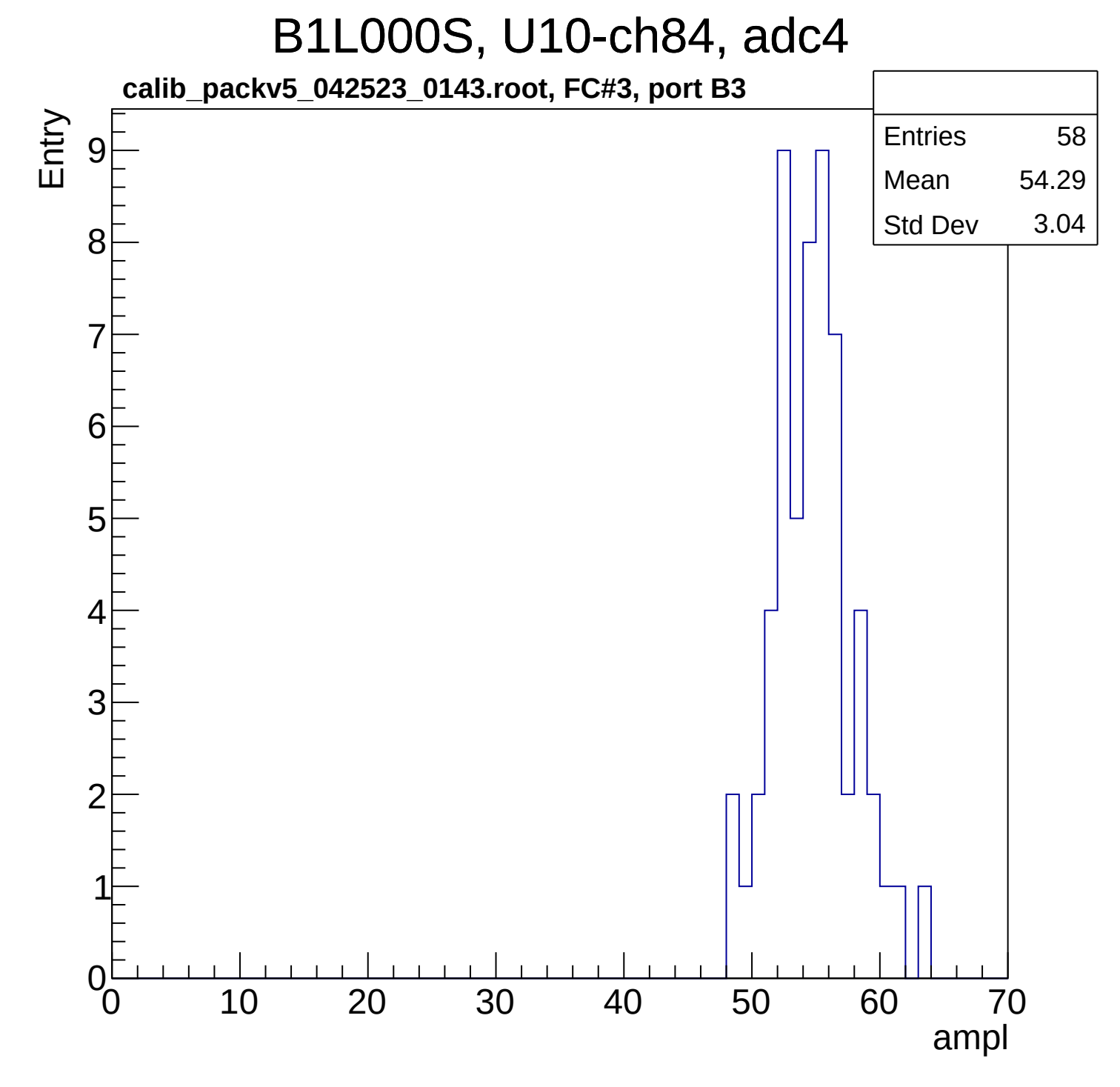
50
60
70
ampl


B1L000S, U10-ch84, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.29
Std Dev 3.04
Entry
ampl
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2
3
4
5
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7
8
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0
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60
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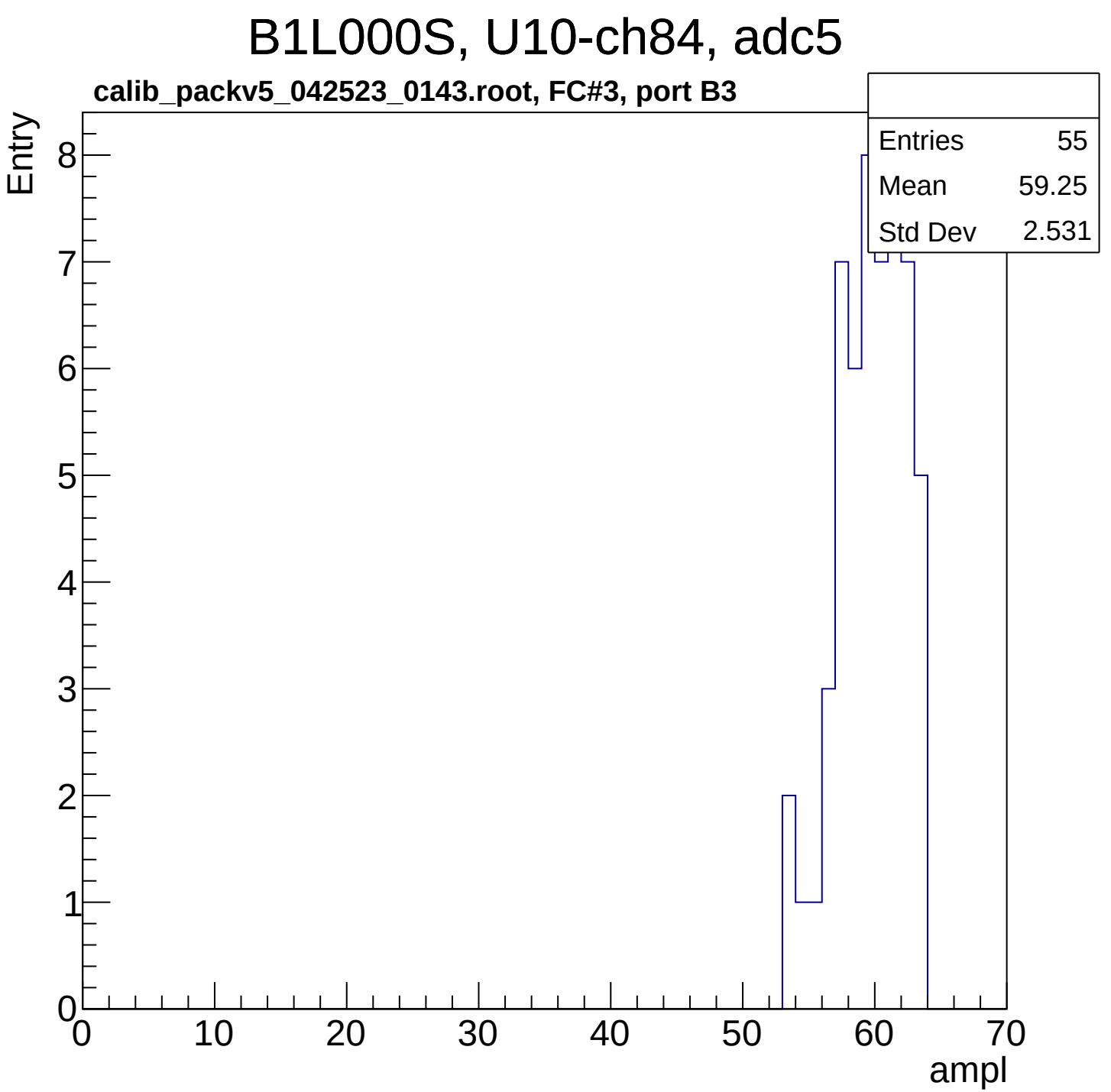
B1L000S, U10-ch84, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 59.25
Std Dev 2.531
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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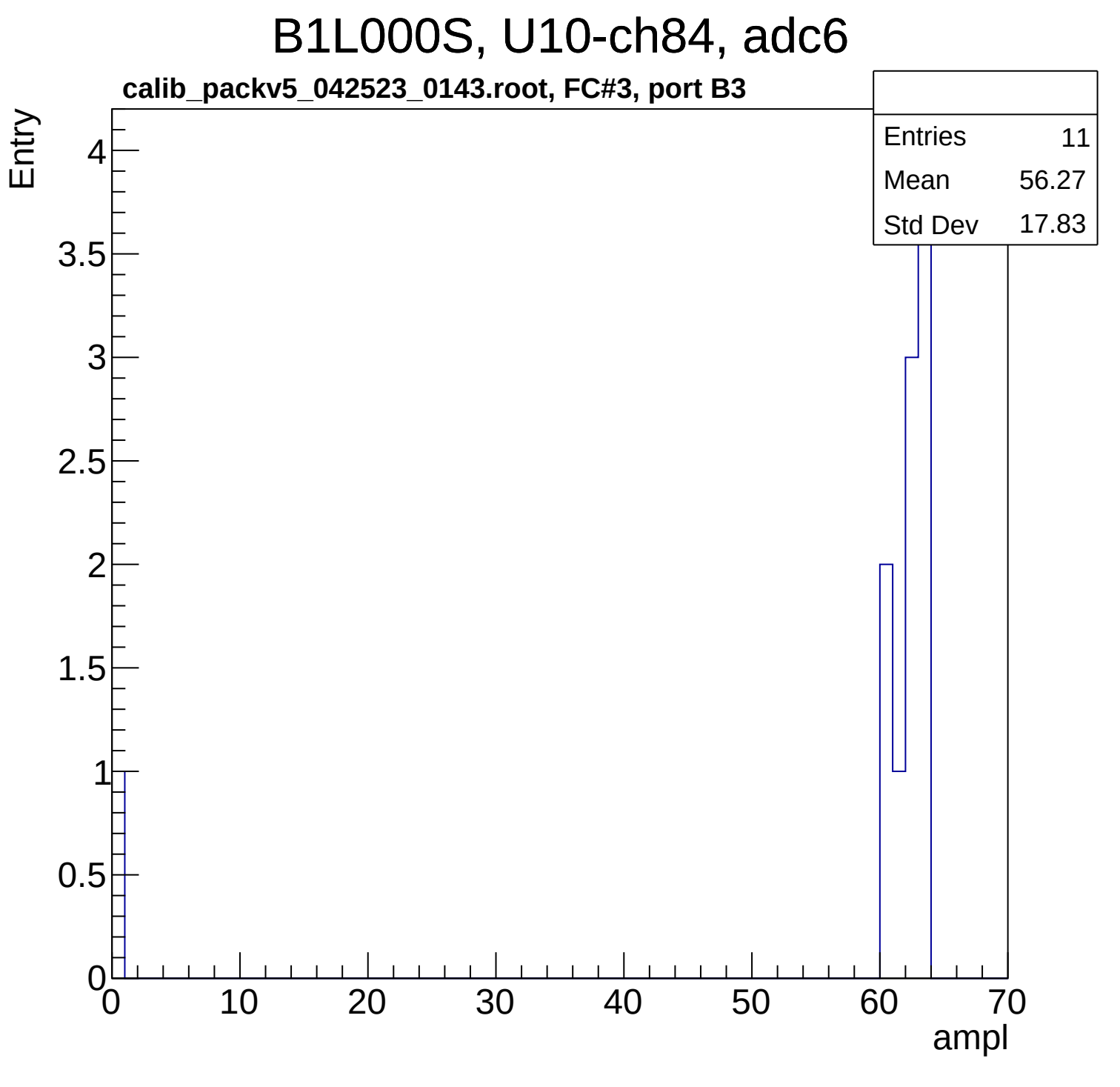
B1L000S, U10-ch84, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.27
Std Dev 17.83
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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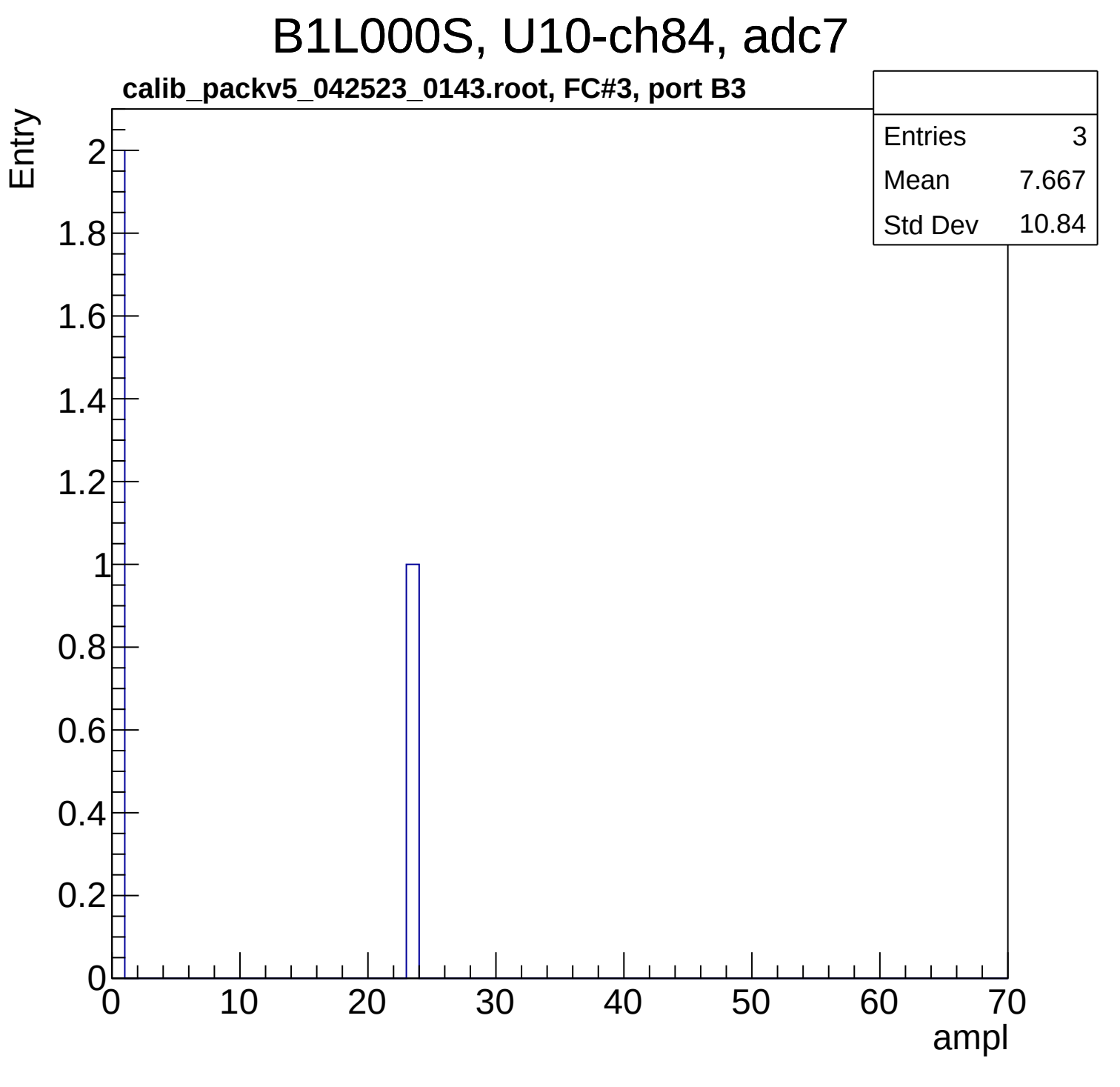
B1L000S, U10-ch84, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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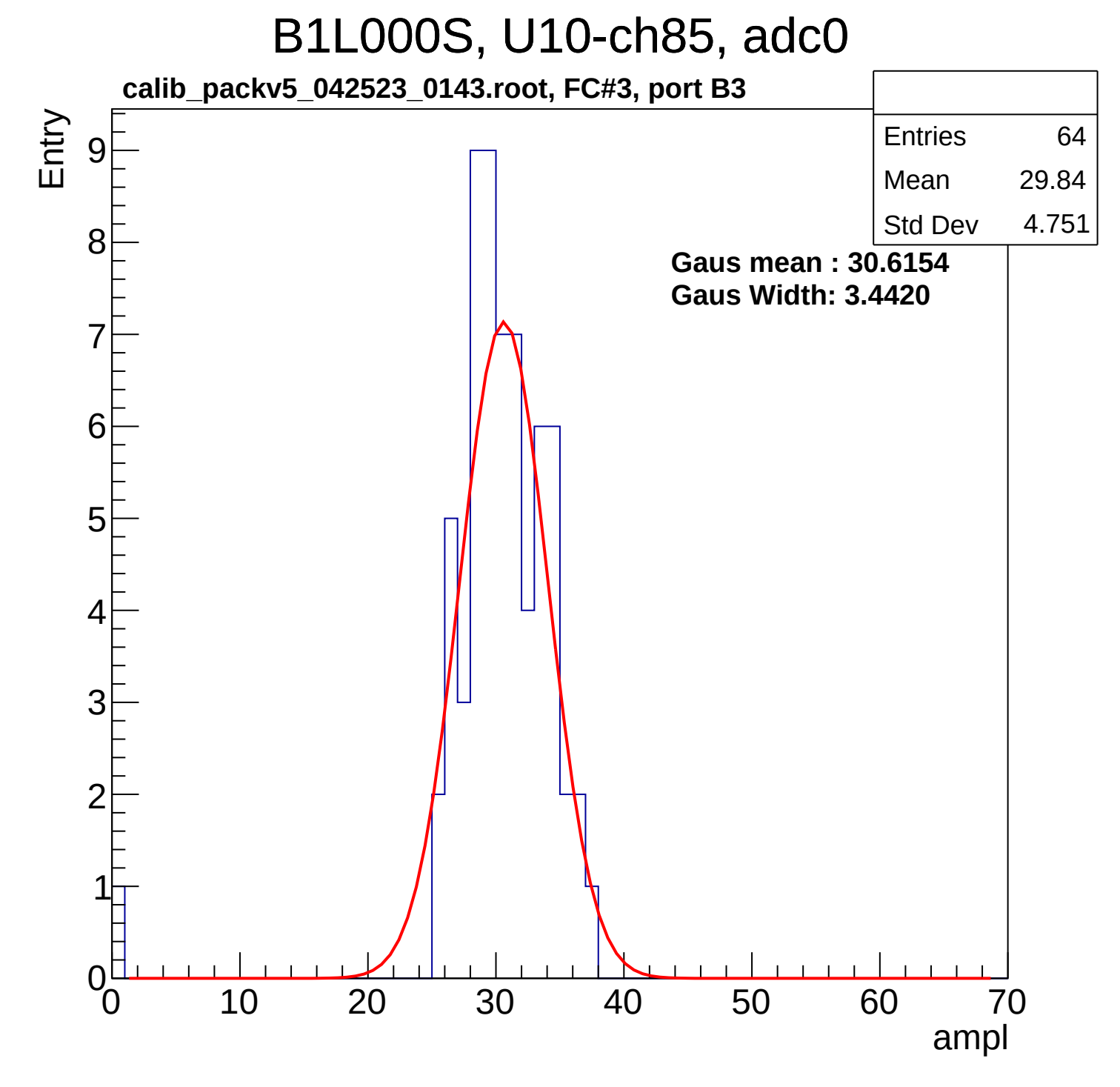
B1L000S, U10-ch85, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 29.84
Std Dev 4.751
Gaus mean : 30.6154
Gaus Width: 3.4420
Entry
ampl
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2
3
4
5
6
7
8
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0
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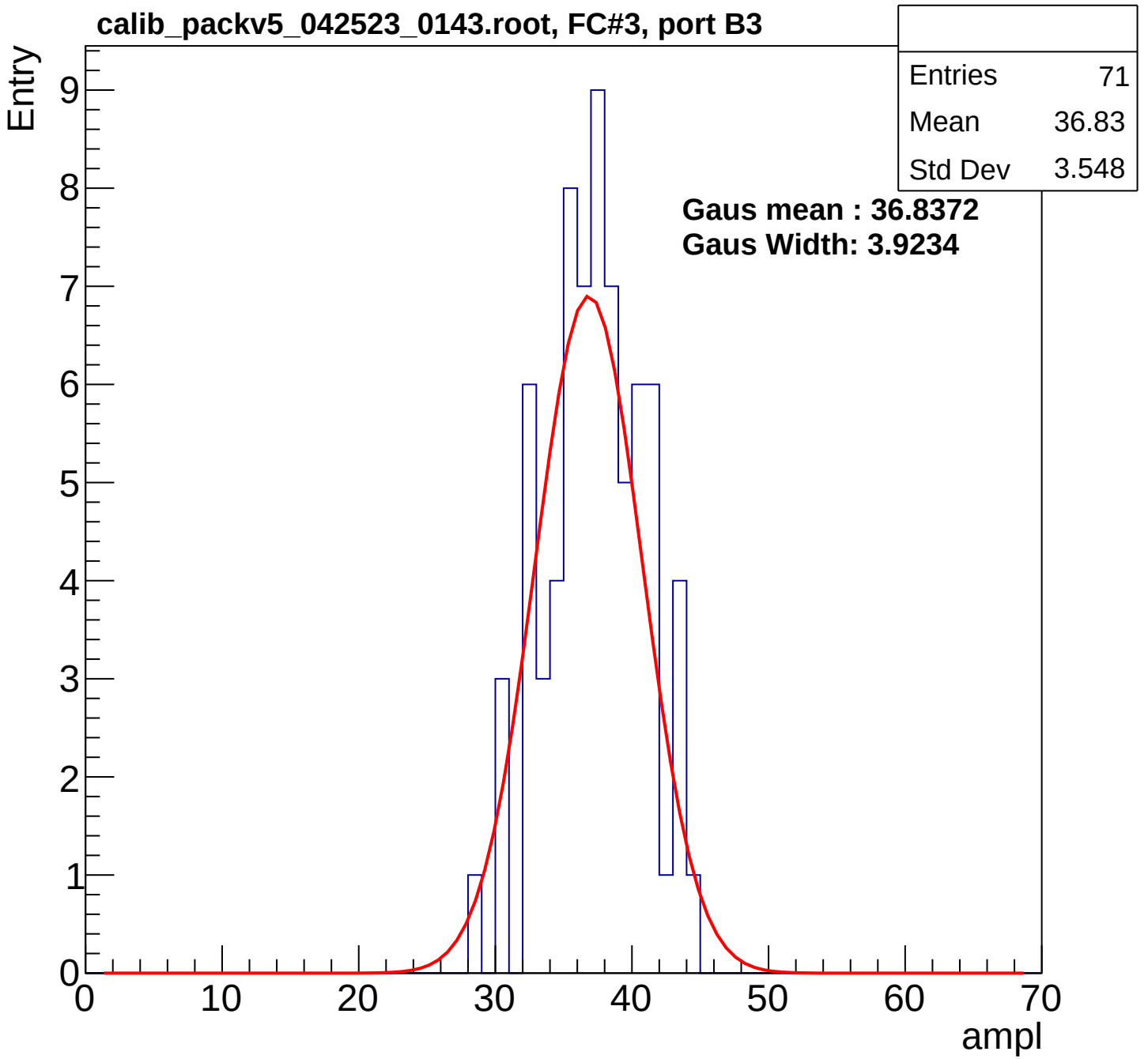
B1L000S, U10-ch85, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 36.83
Std Dev 3.548
Gaus mean : 36.8372
Gaus Width: 3.9234
Entry
ampl
0
1
2
3
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5
6
7
8
9
0
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60
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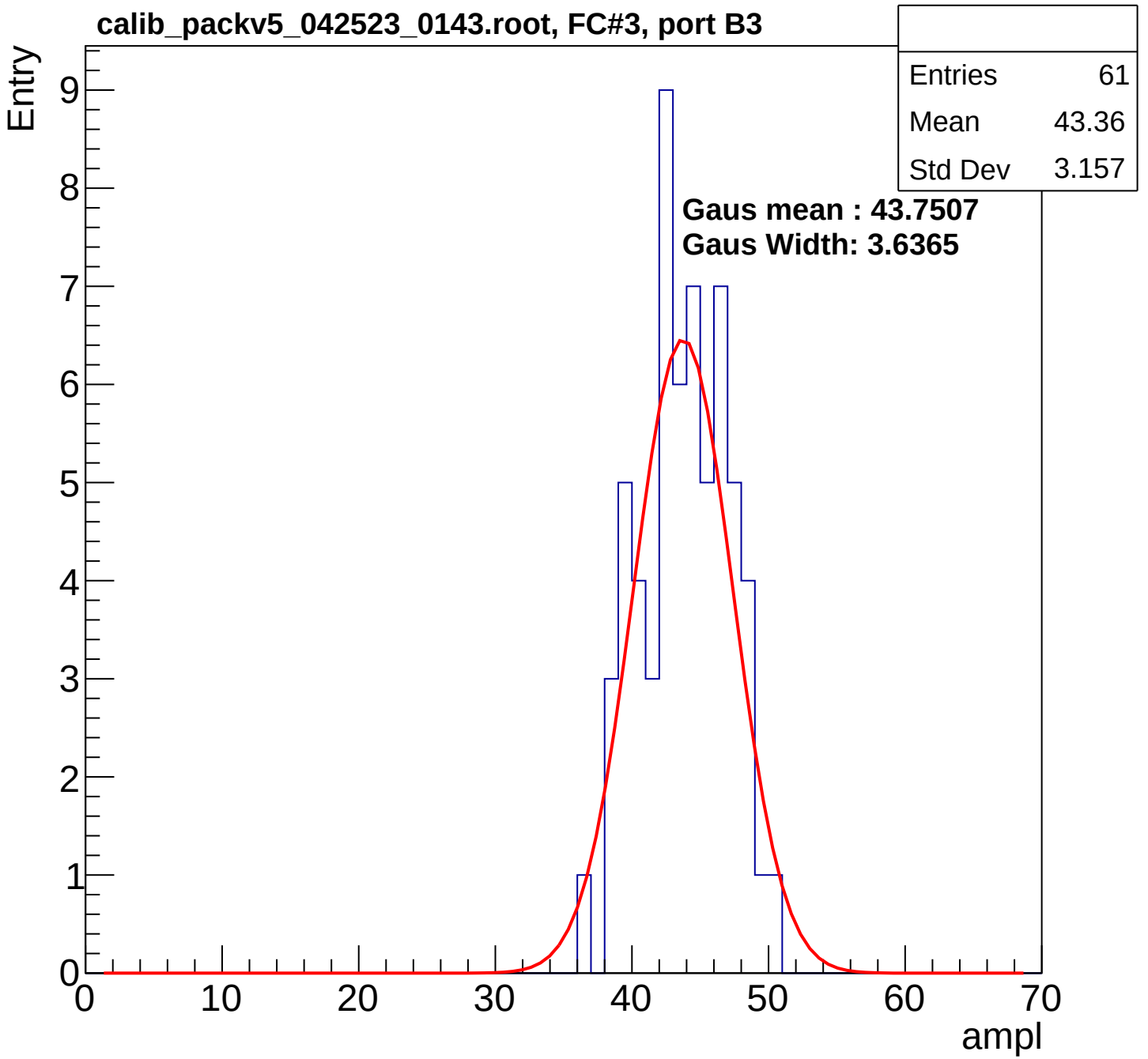
B1L000S, U10-ch85, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 43.36
Std Dev 3.157
Gaus mean : 43.7507
Gaus Width: 3.6365
Entry
ampl
0
10
20
30
40
50
60
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1
2
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4
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6
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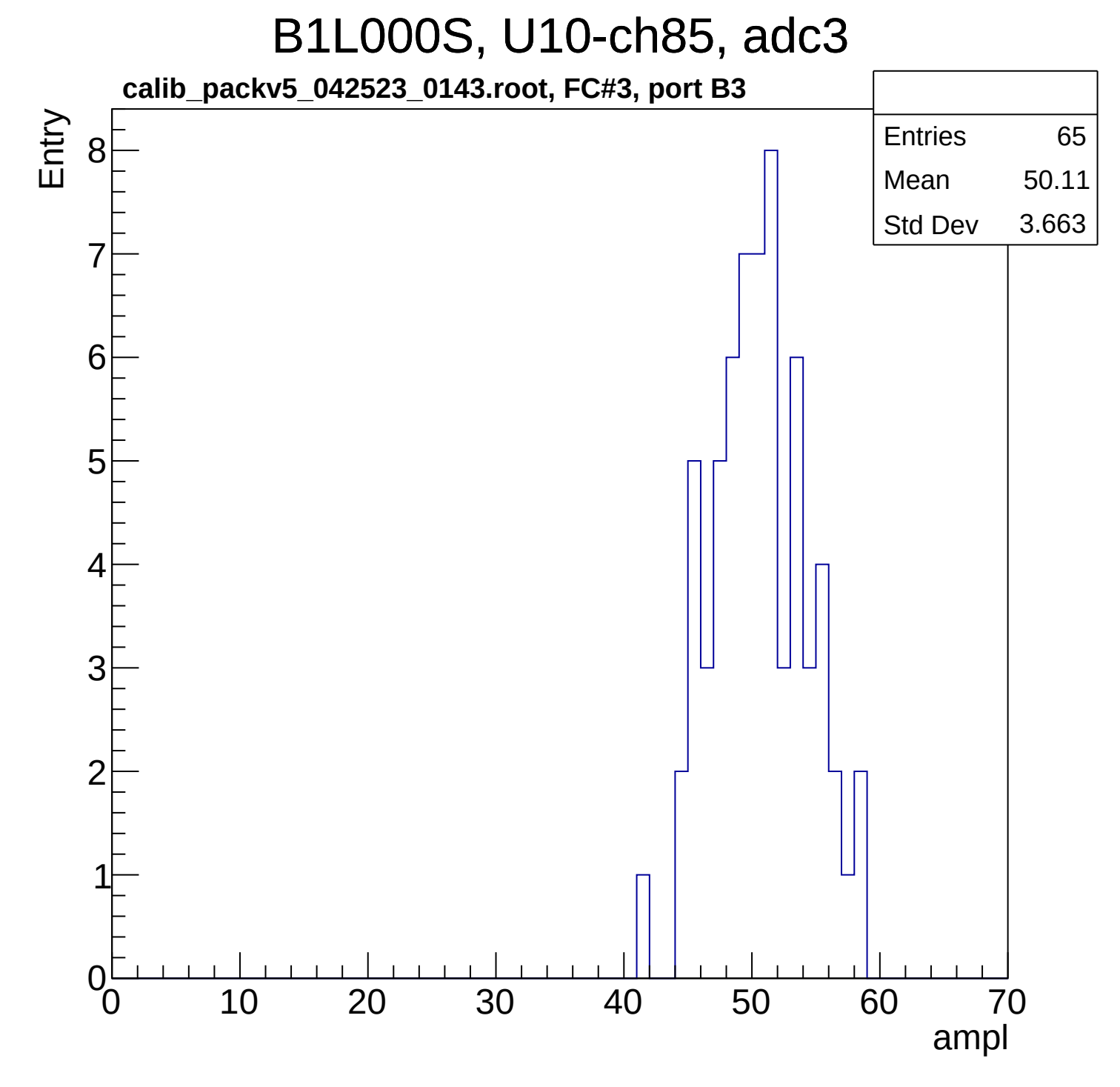
8
9


B1L000S, U10-ch85, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 50.11
Std Dev 3.663
Entry
ampl
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1
2
3
4
5
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7
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0
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40
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60
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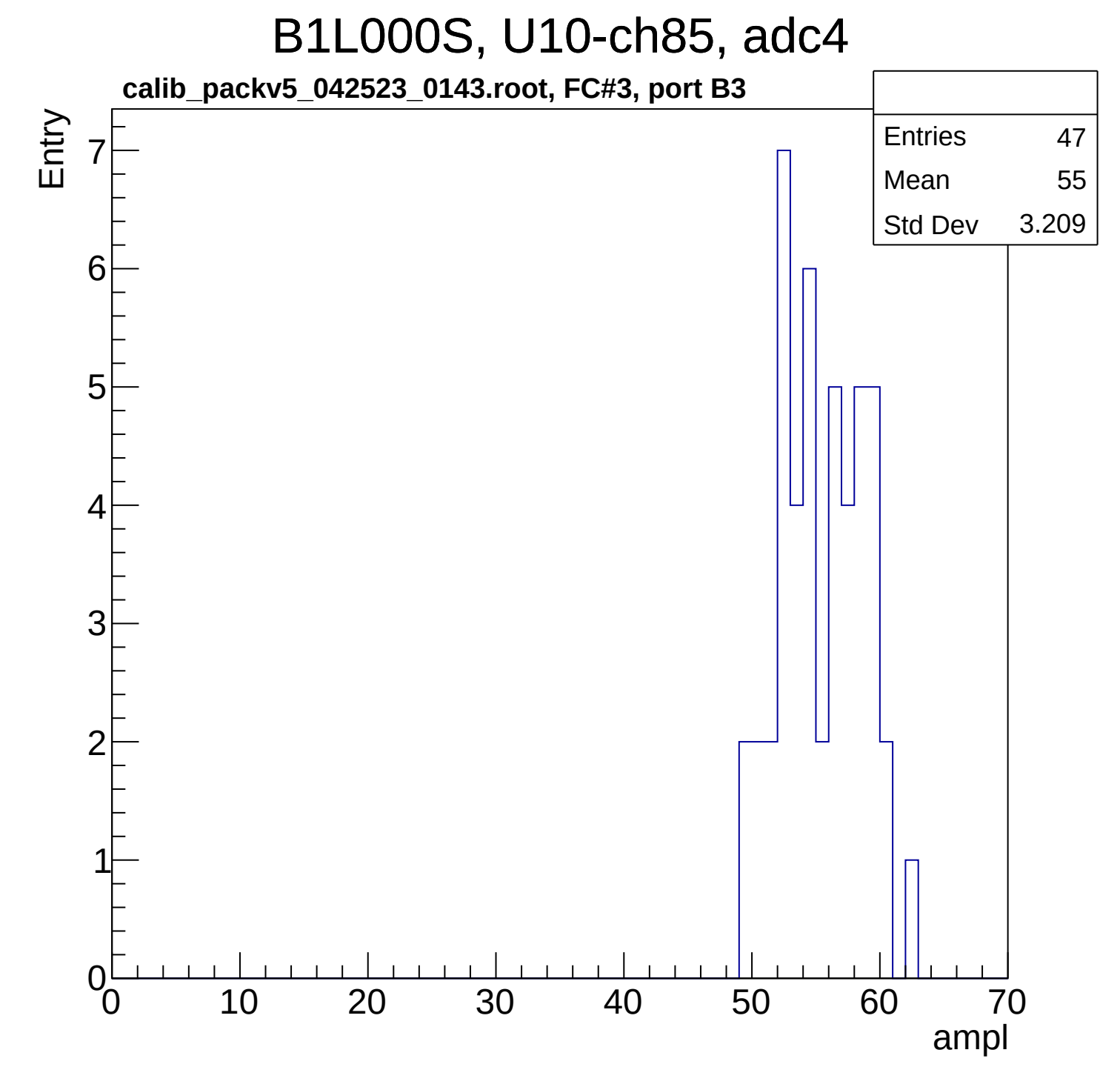
B1L000S, U10-ch85, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 55
Std Dev 3.209
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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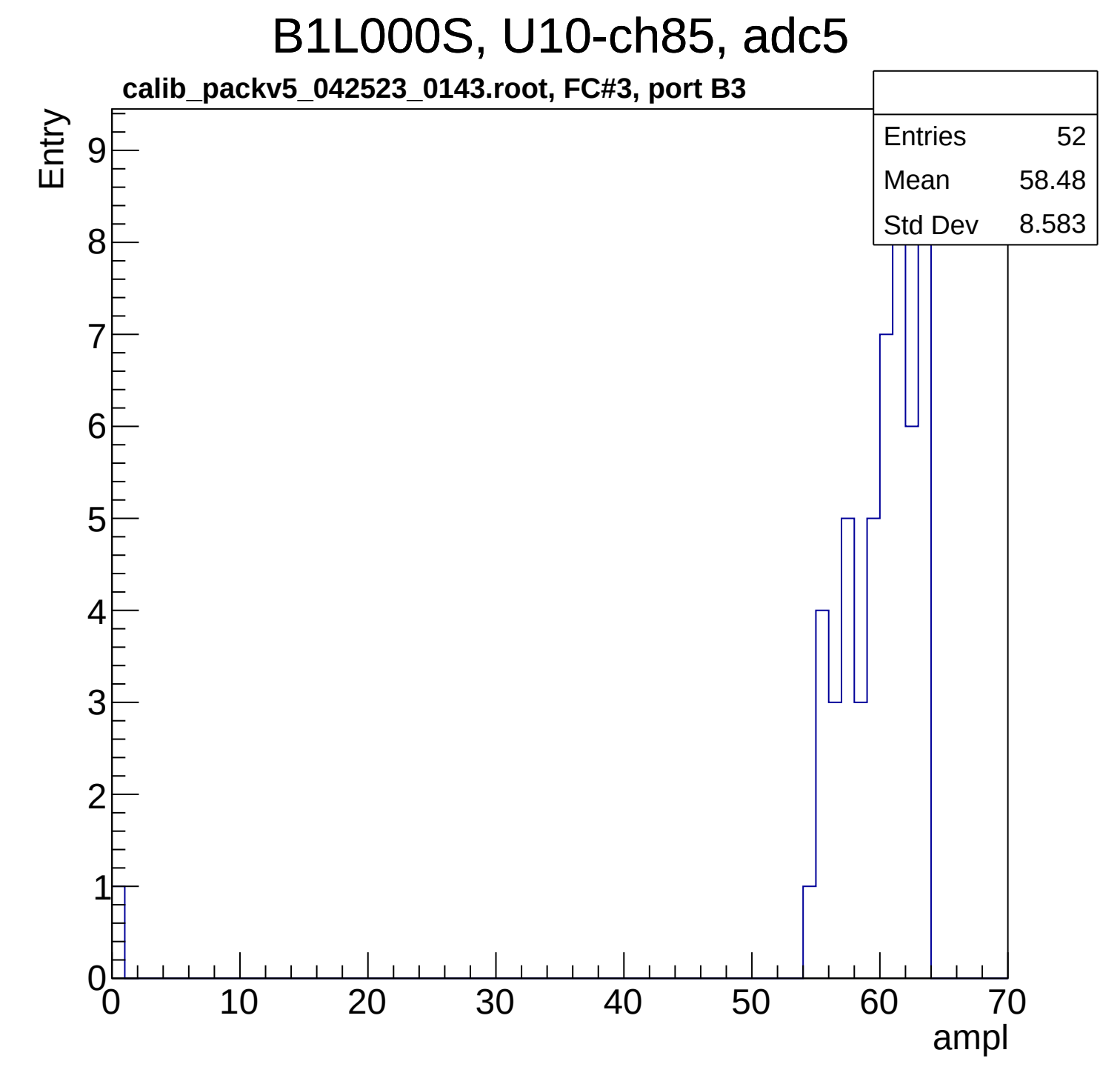
B1L000S, U10-ch85, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.48
Std Dev 8.583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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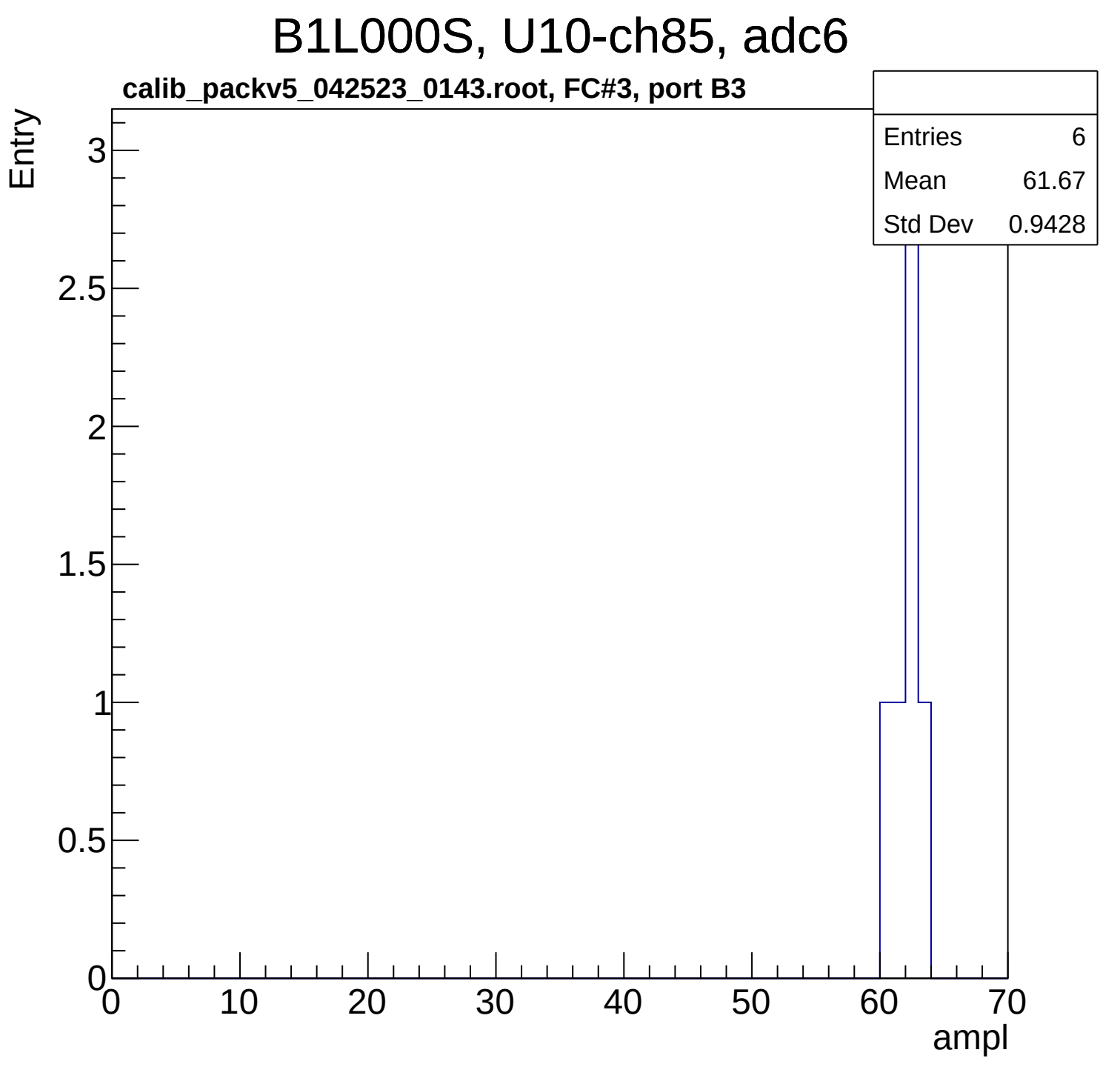
B1L000S, U10-ch85, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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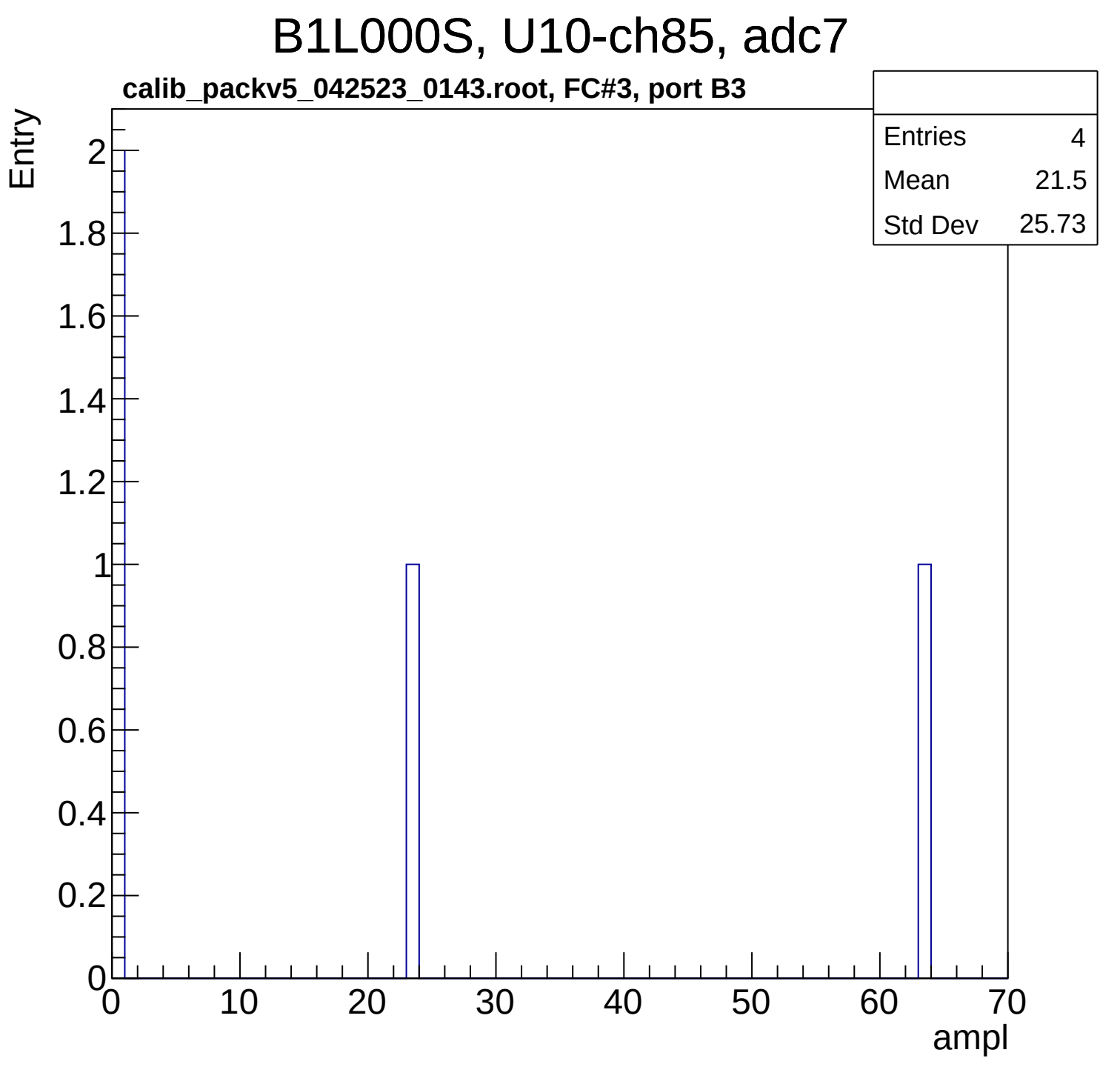
B1L000S, U10-ch85, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 21.5
Std Dev 25.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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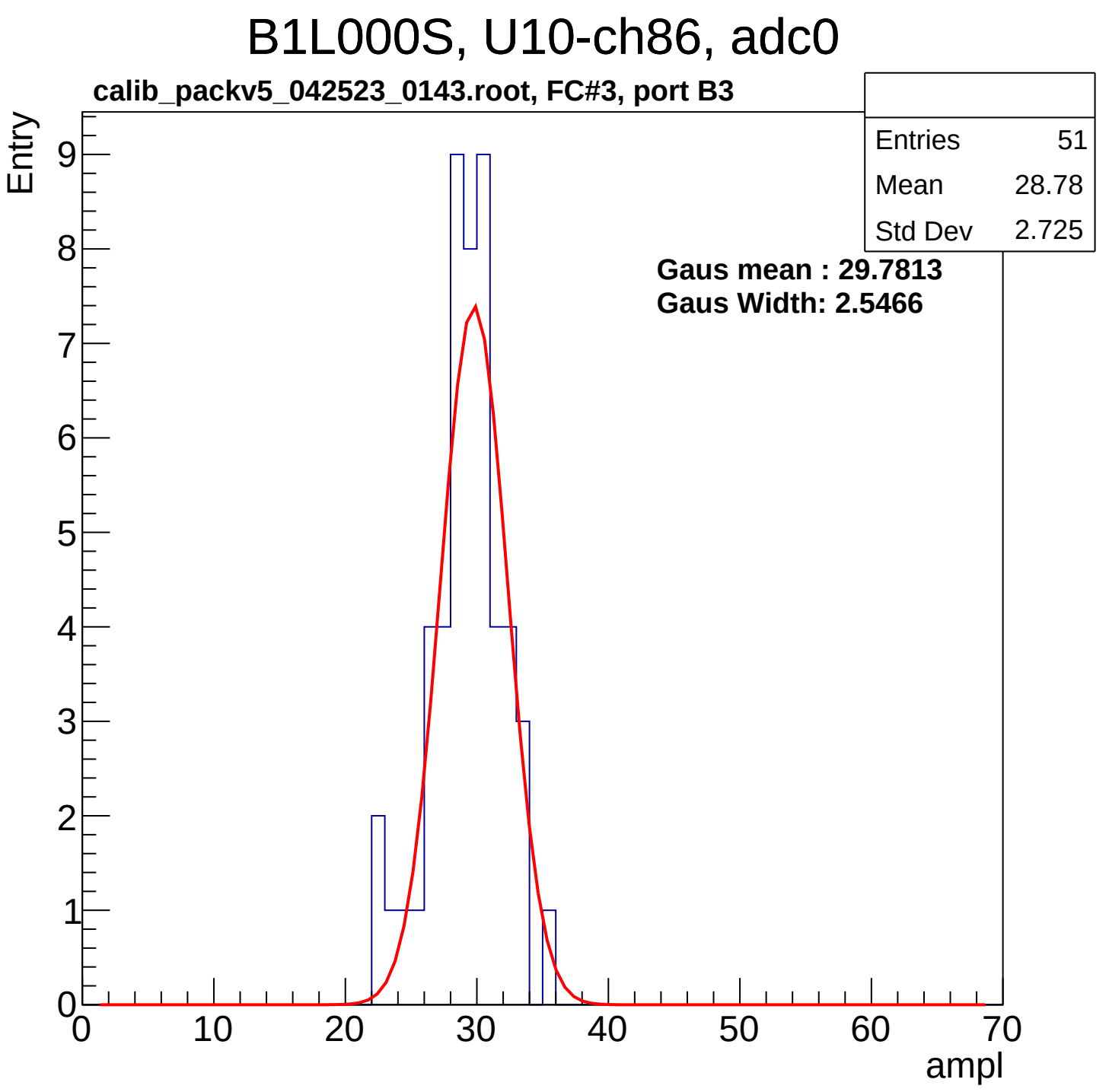
B1L000S, U10-ch86, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 28.78
Std Dev 2.725
Gaus mean : 29.7813
Gaus Width: 2.5466
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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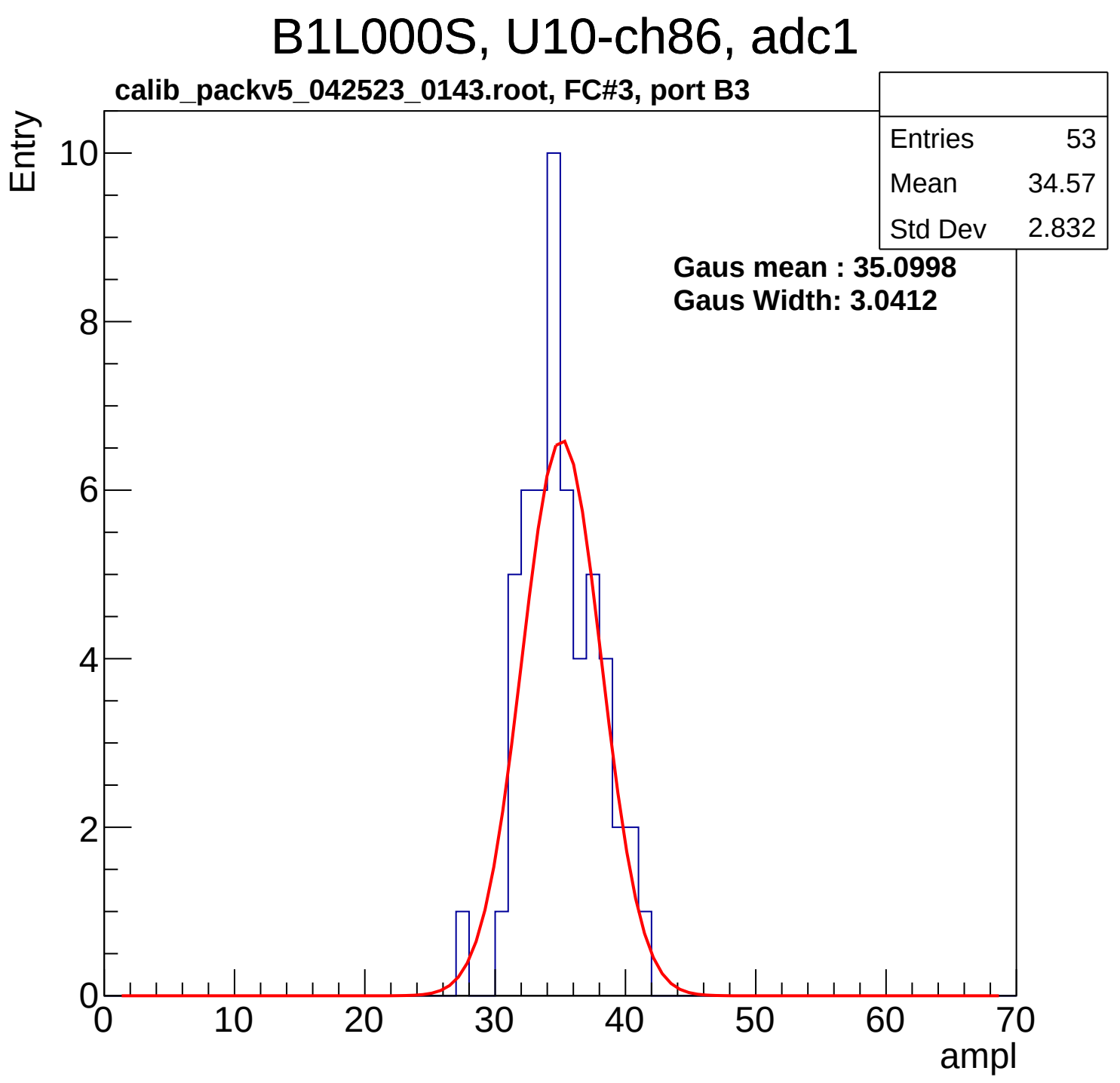
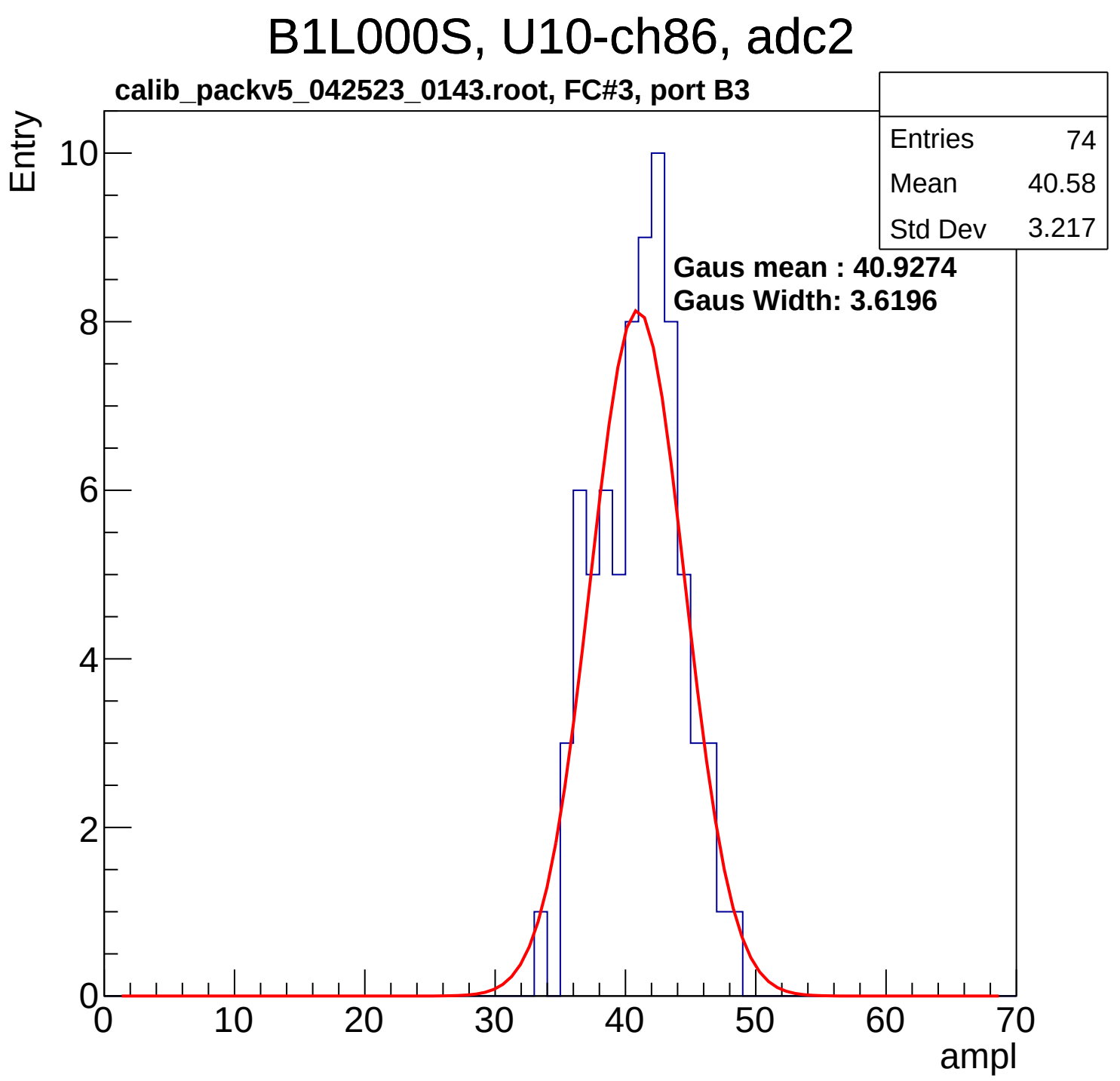
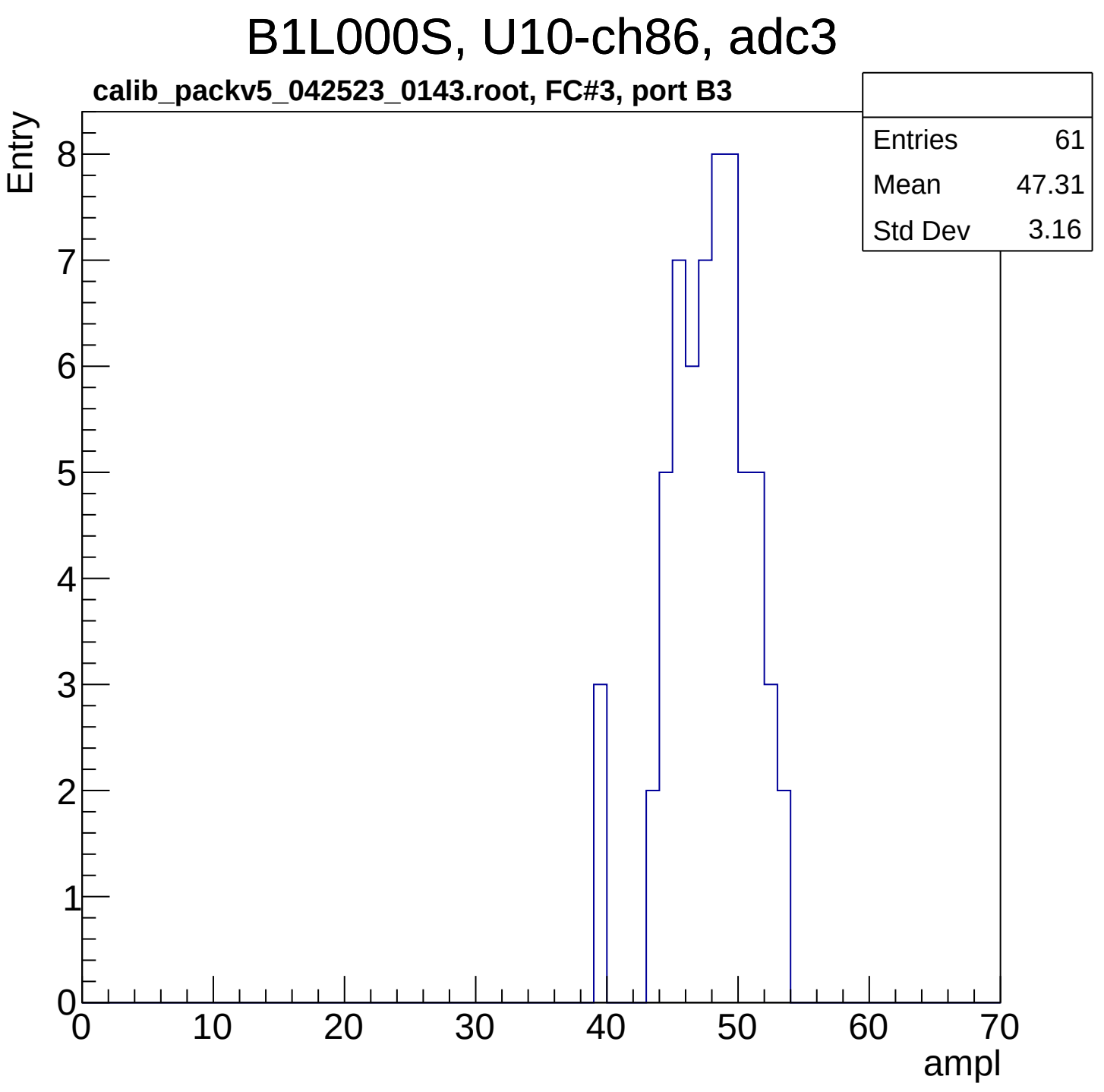

B1L000S, U10-ch86, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 34.57
Std Dev 2.832
Gaus mean : 35.0998
Gaus Width: 3.0412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch86, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 40.58
Std Dev 3.217
Gaus mean : 40.9274
Gaus Width: 3.6196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch86, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 47.31
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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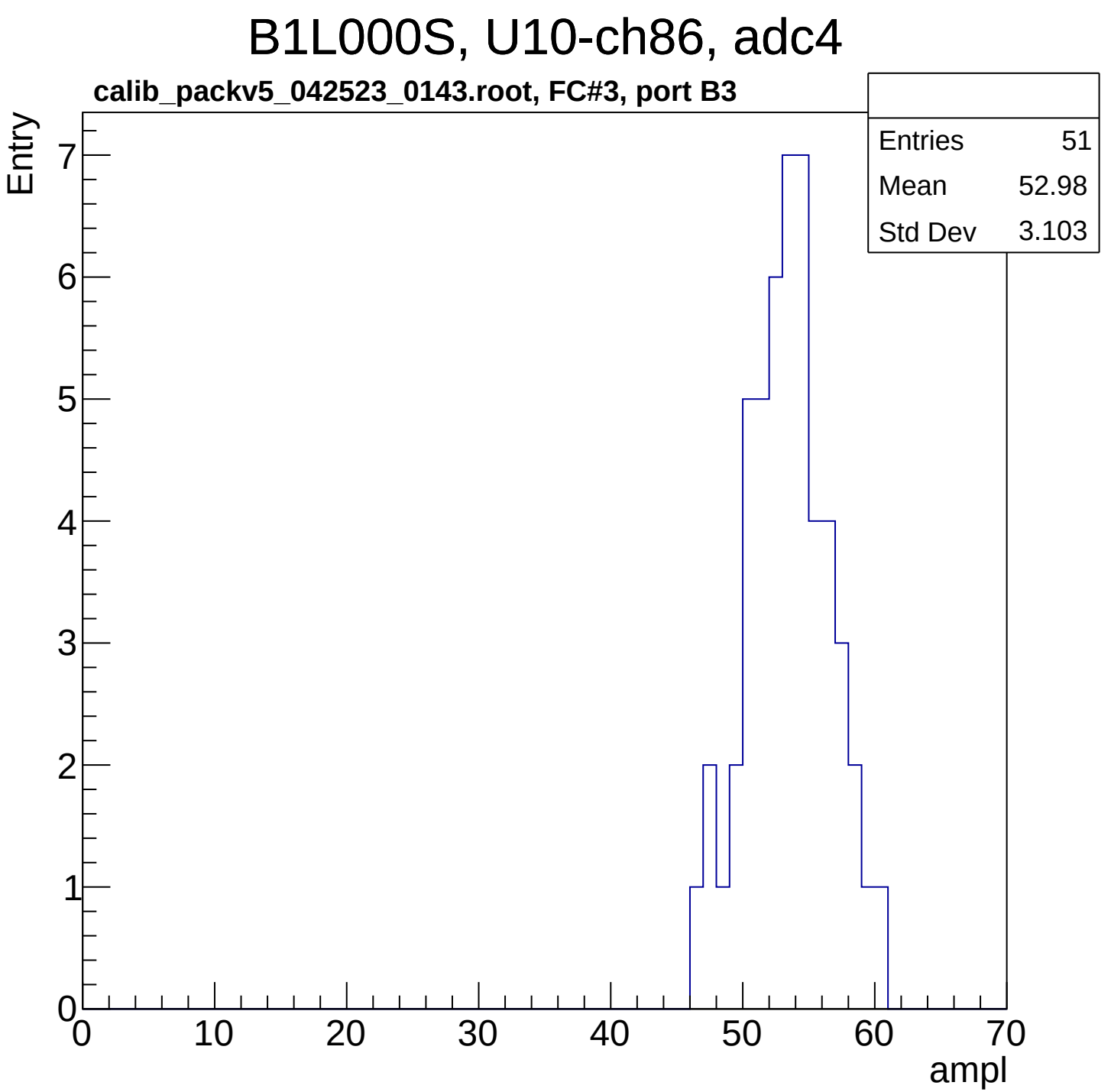
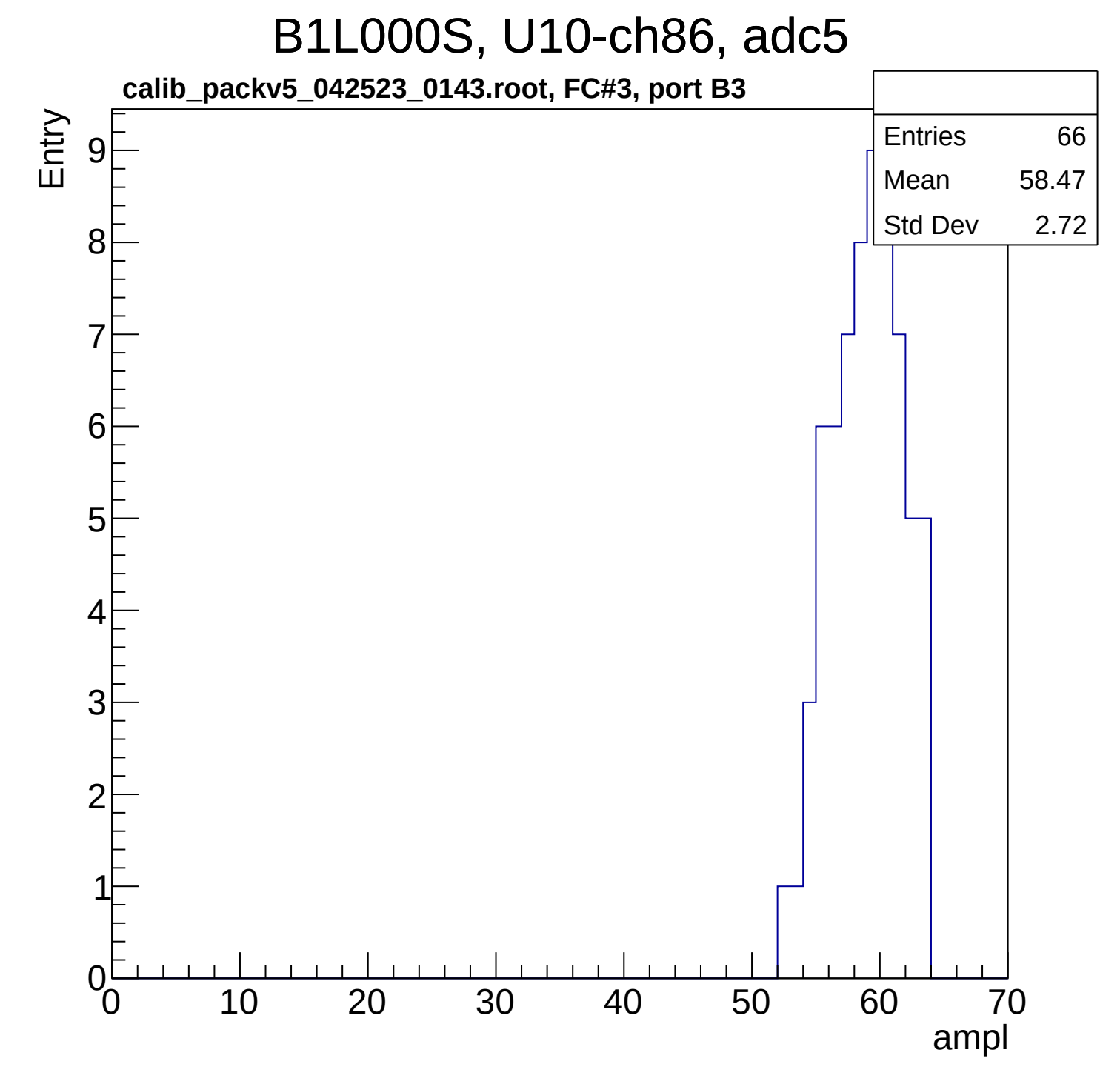

B1L000S, U10-ch86, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 52.98
Std Dev 3.103
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch86, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 58.47
Std Dev 2.72
Entry
ampl
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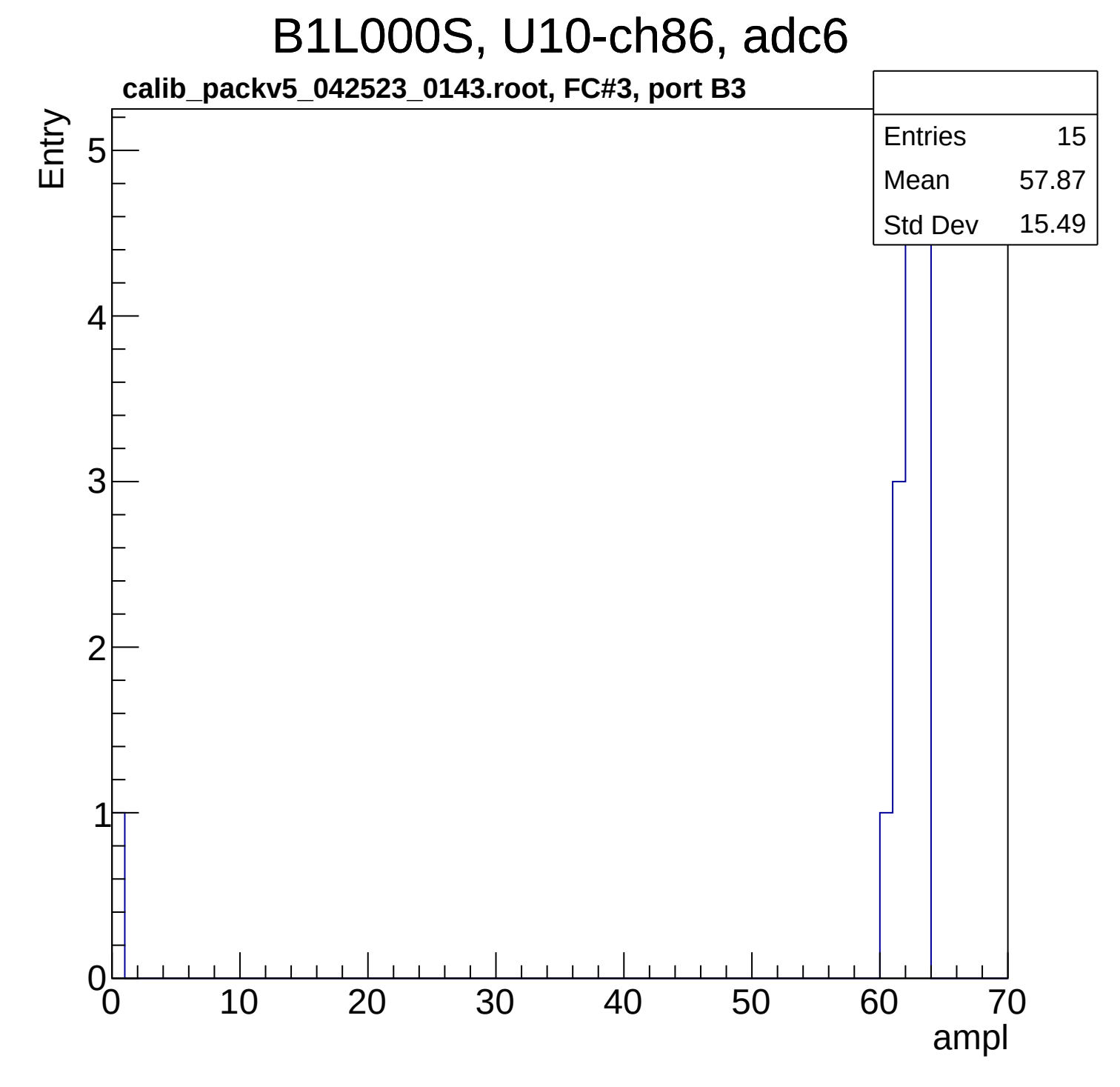
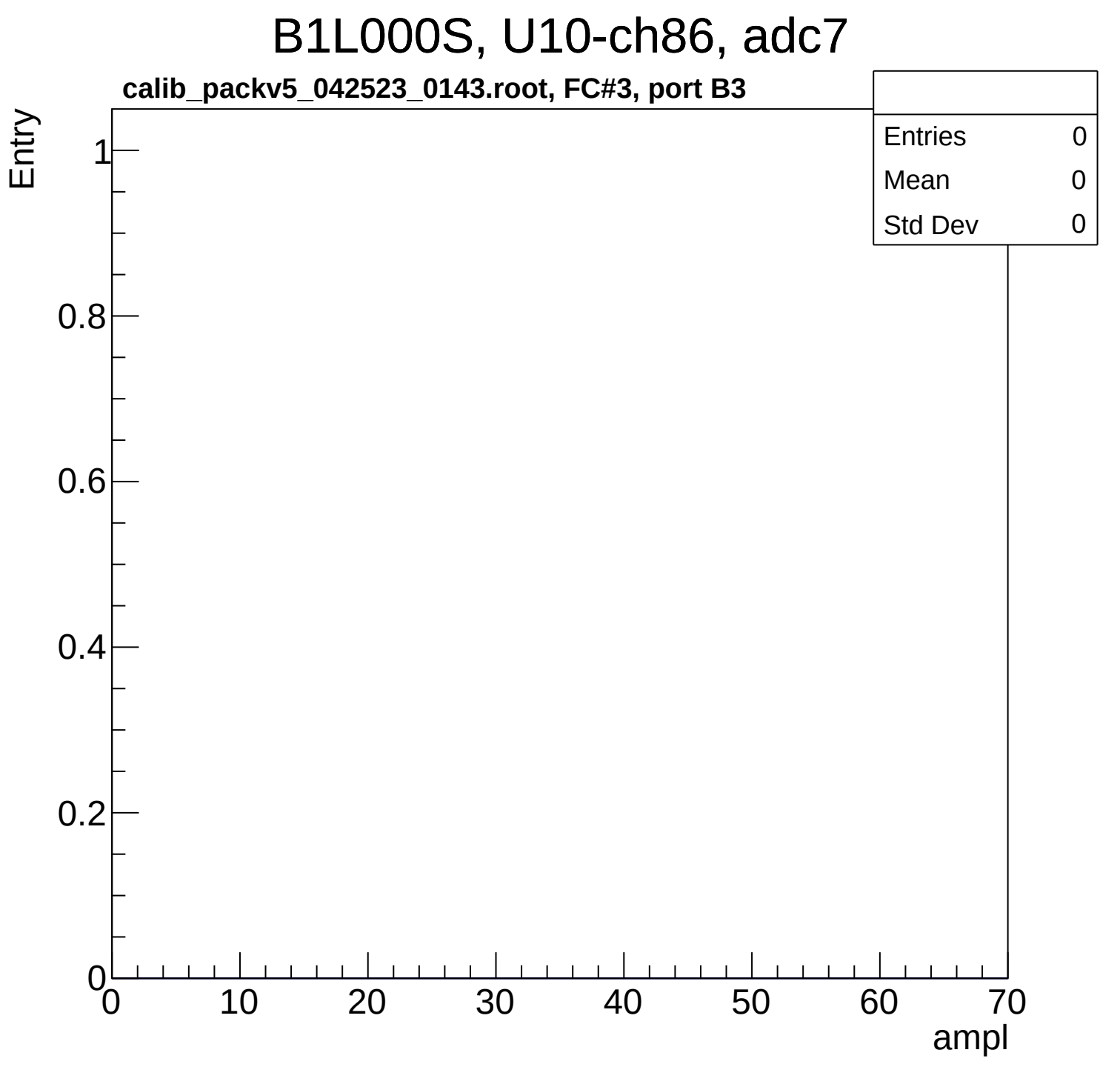
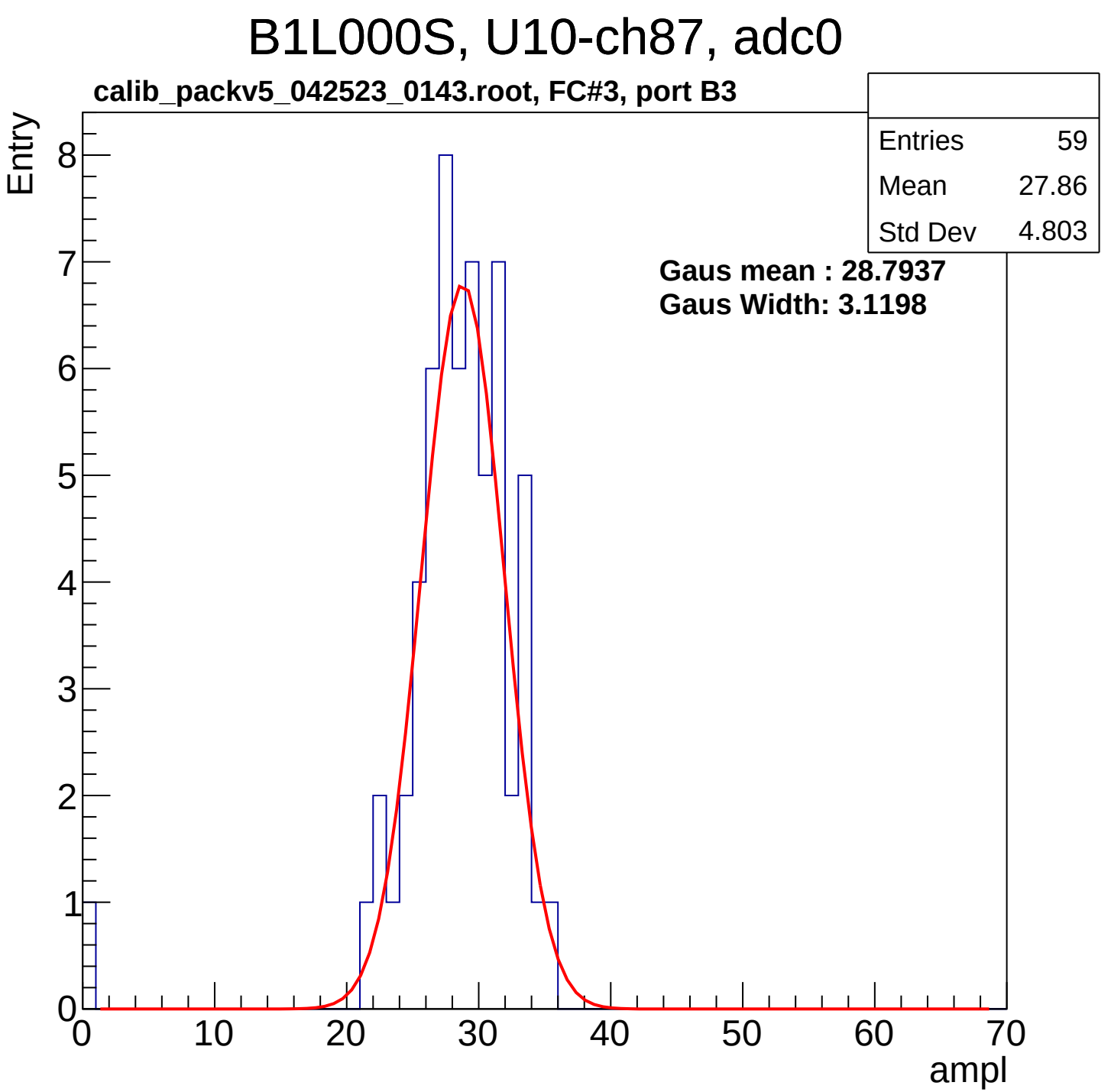
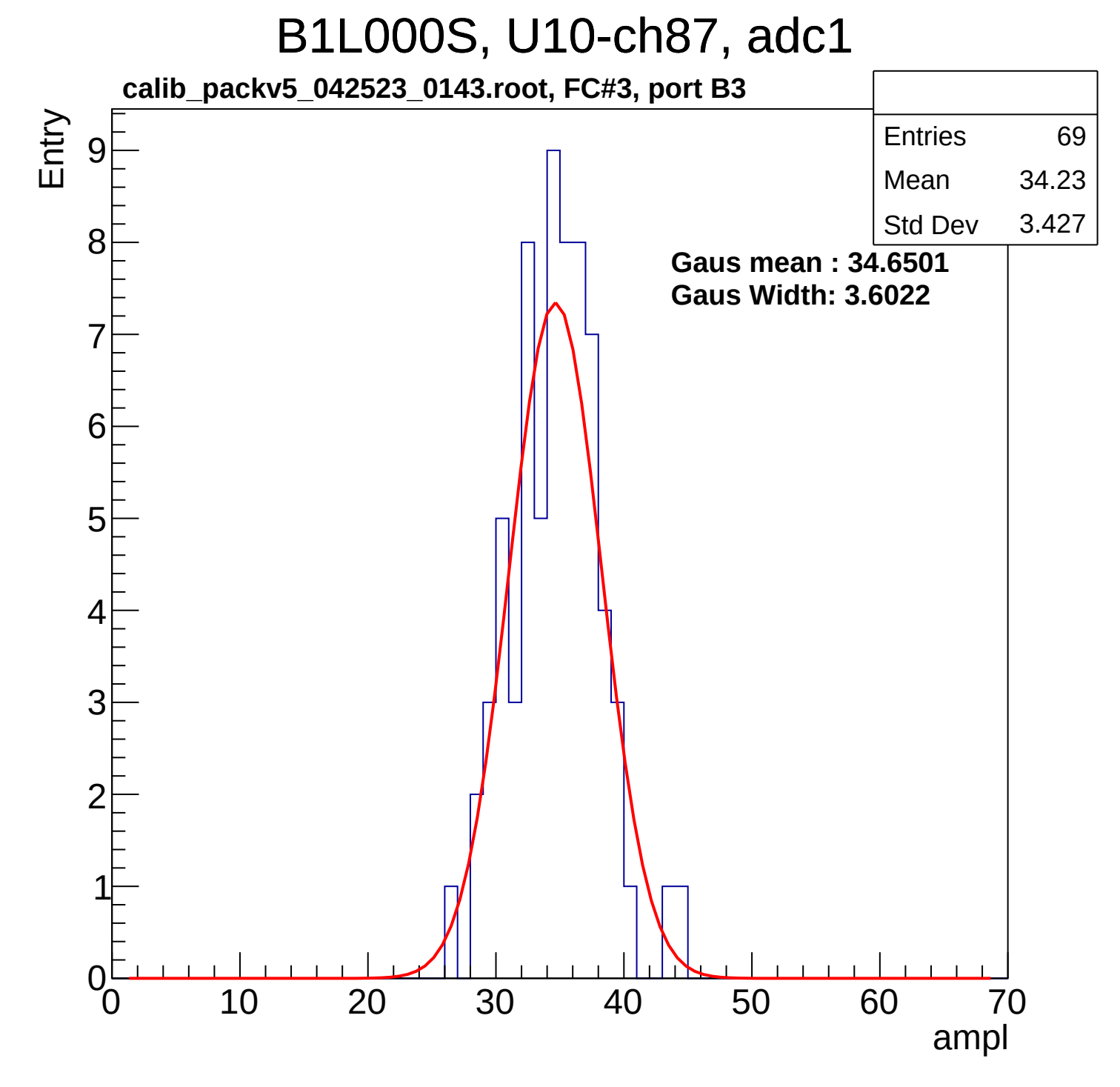
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch86, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.87
Std Dev 15.49
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch86, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch87, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 27.86
Std Dev 4.803
Gaus mean : 28.7937
Gaus Width: 3.1198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch87, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.23
Std Dev 3.427
Gaus mean : 34.6501
Gaus Width: 3.6022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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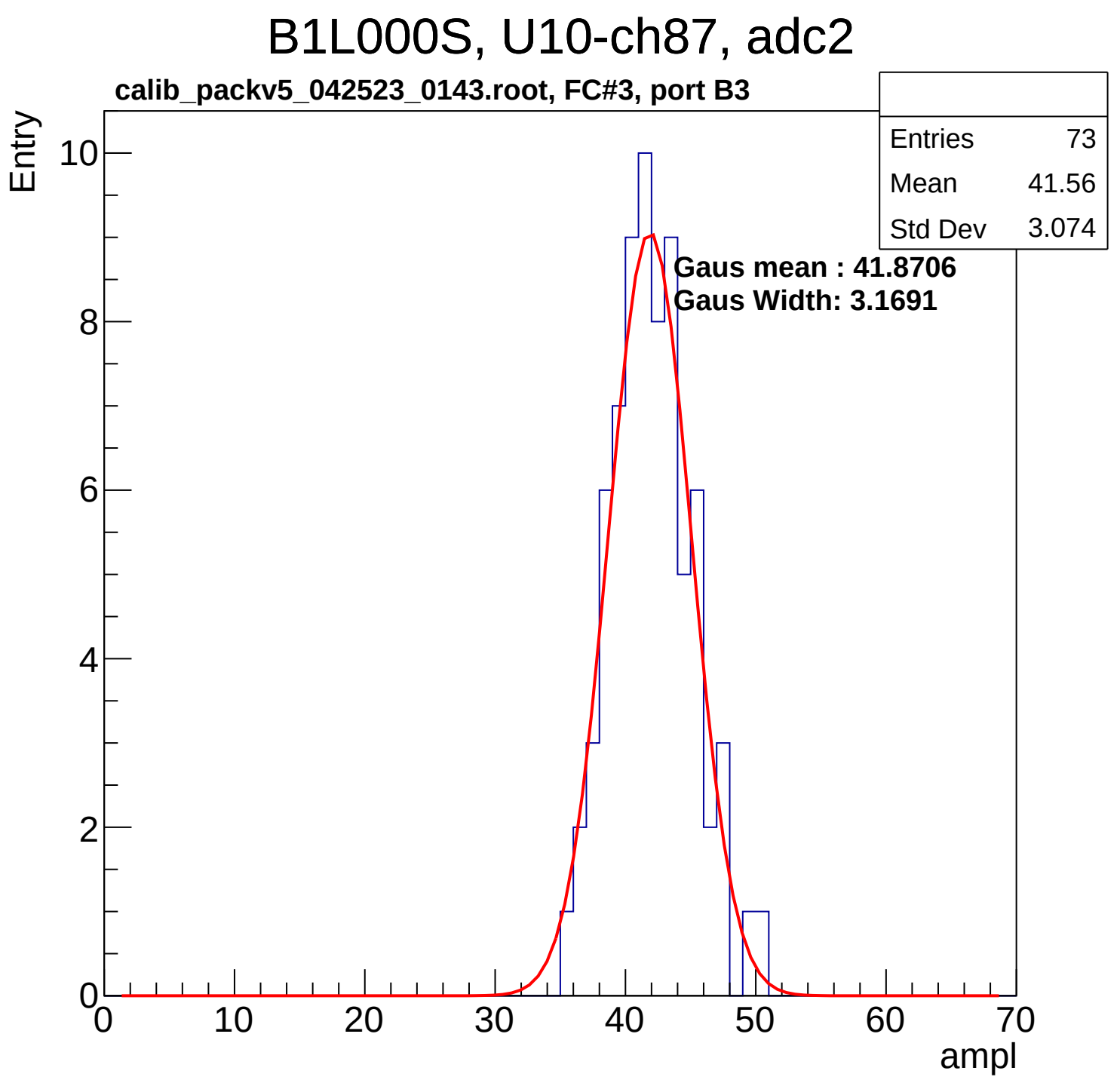
B1L000S, U10-ch87, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 41.56
Std Dev 3.074
Gaus mean : 41.8706
Gaus Width: 3.1691
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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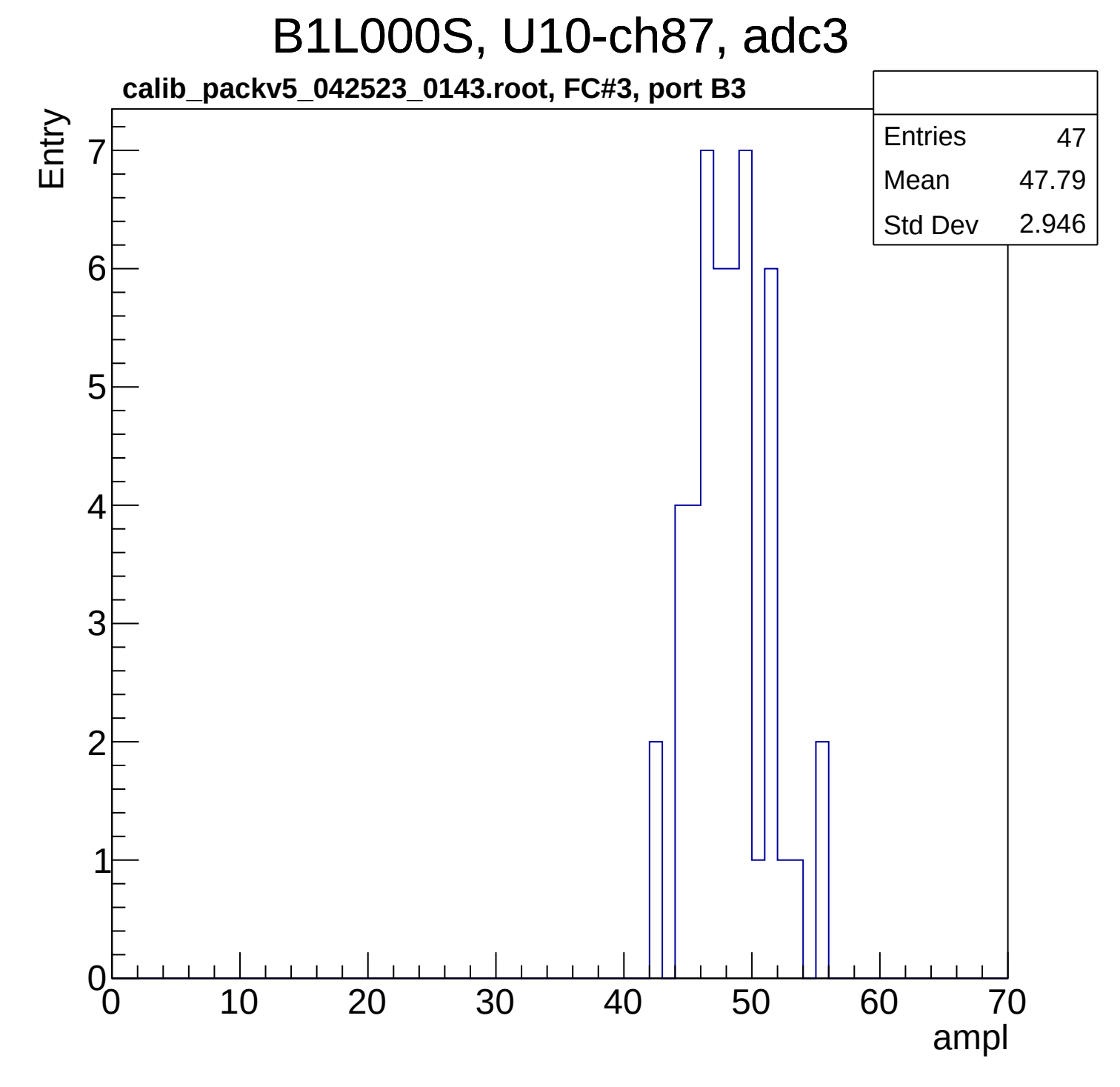
B1L000S, U10-ch87, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 47.79
Std Dev 2.946
Entry
ampl
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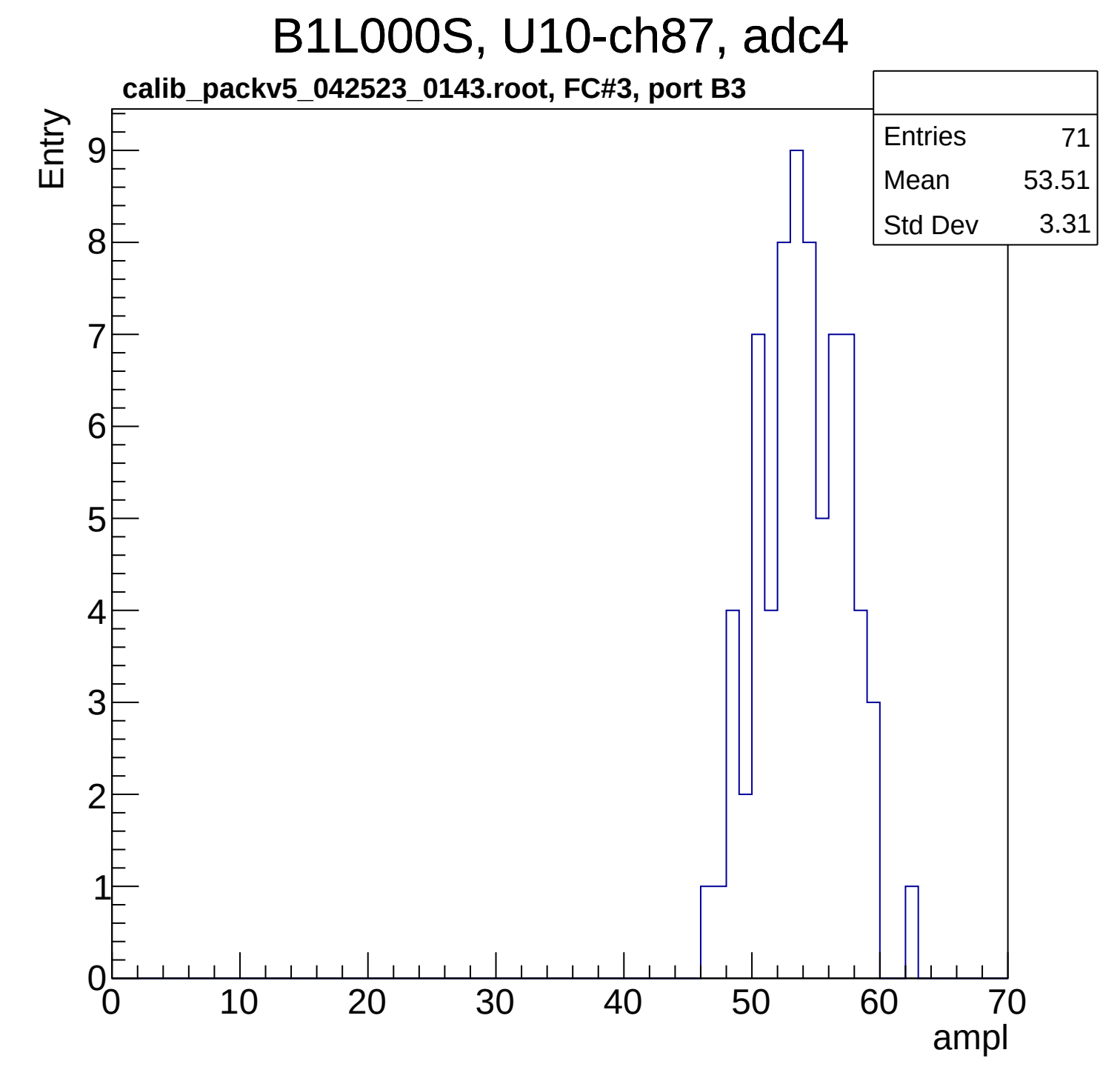
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch87, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 53.51
Std Dev 3.31
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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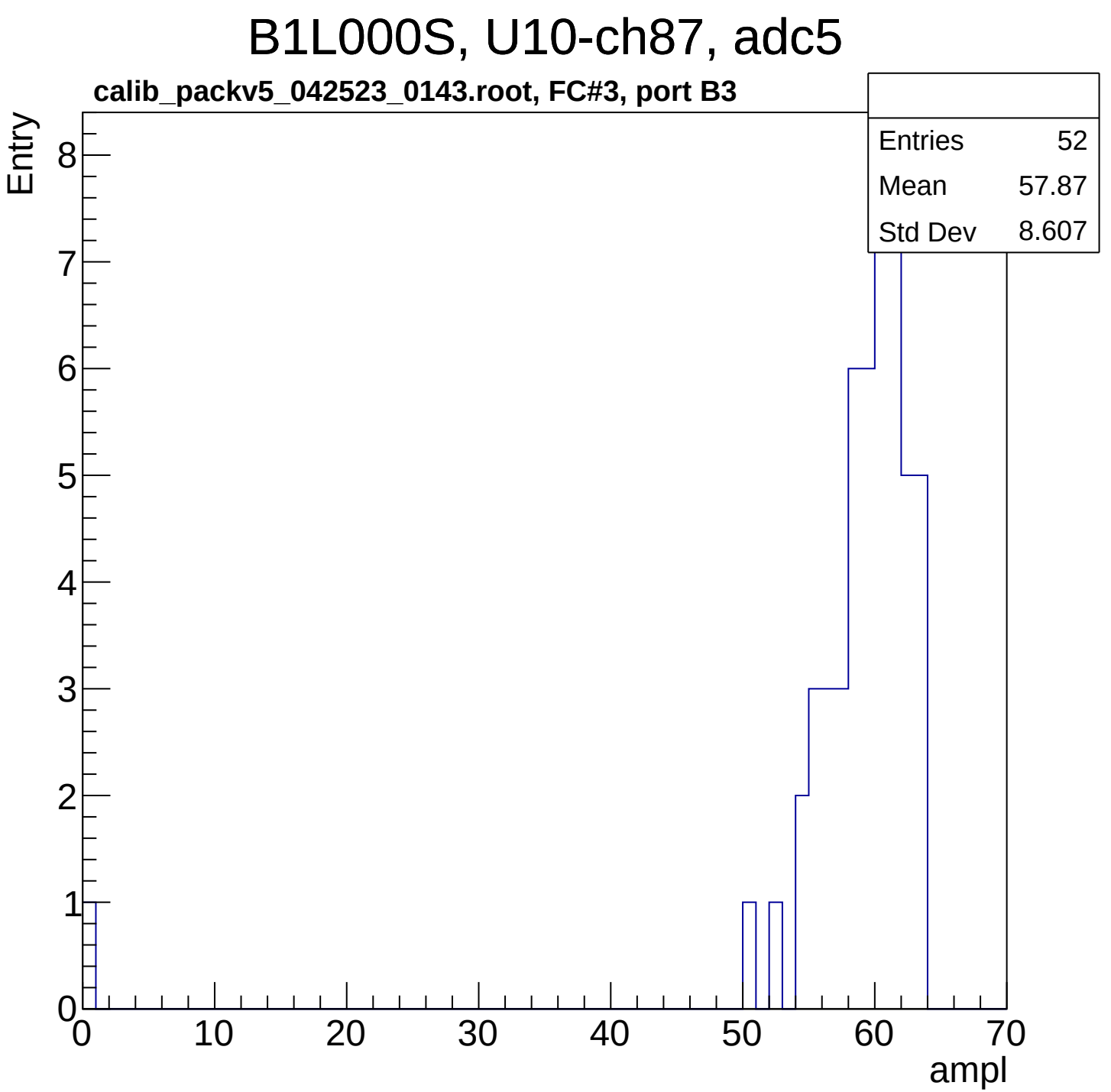
B1L000S, U10-ch87, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 57.87
Std Dev 8.607
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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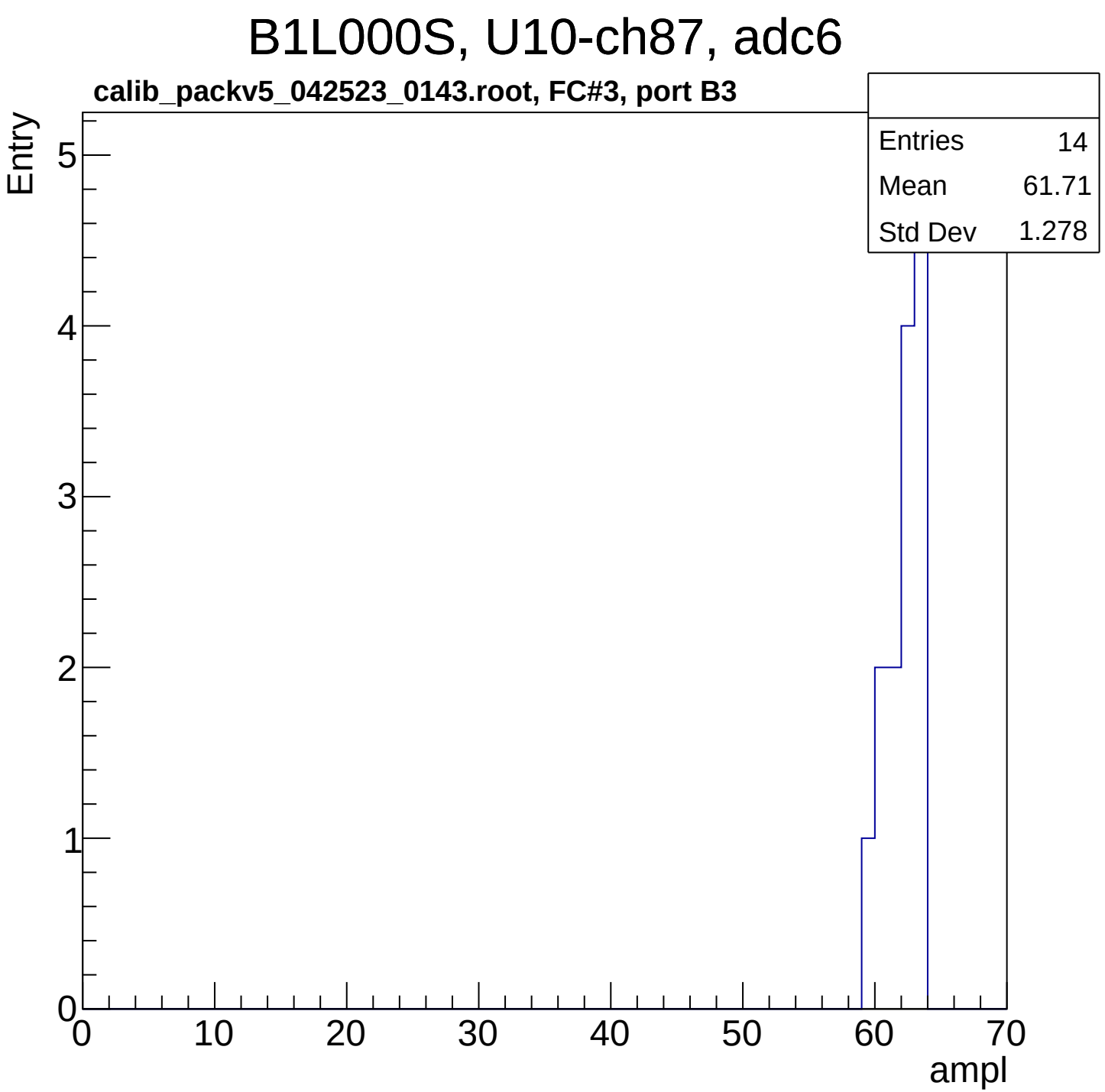
B1L000S, U10-ch87, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 61.71
Std Dev 1.278
Entry
ampl
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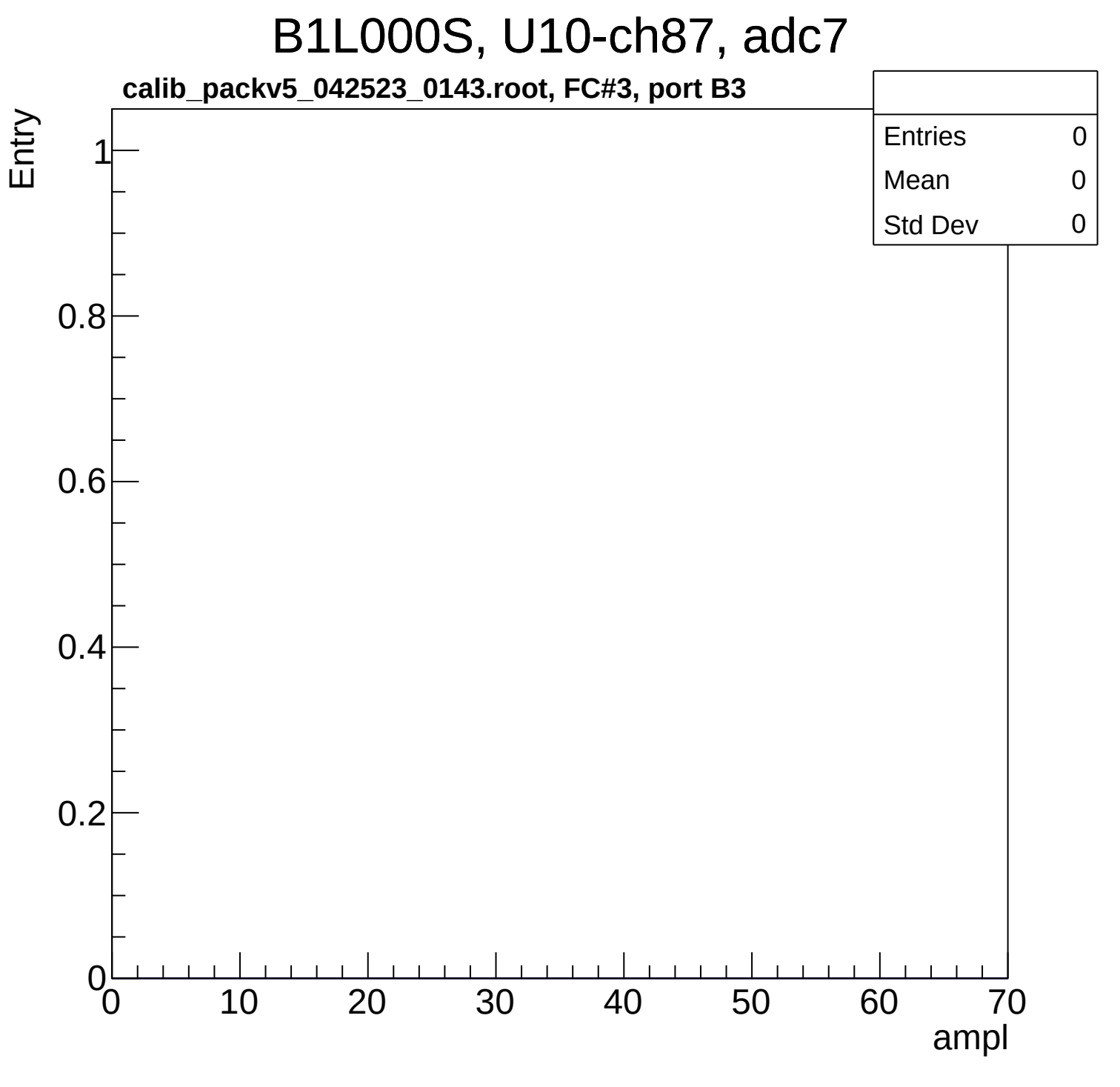
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L000S, U10-ch87, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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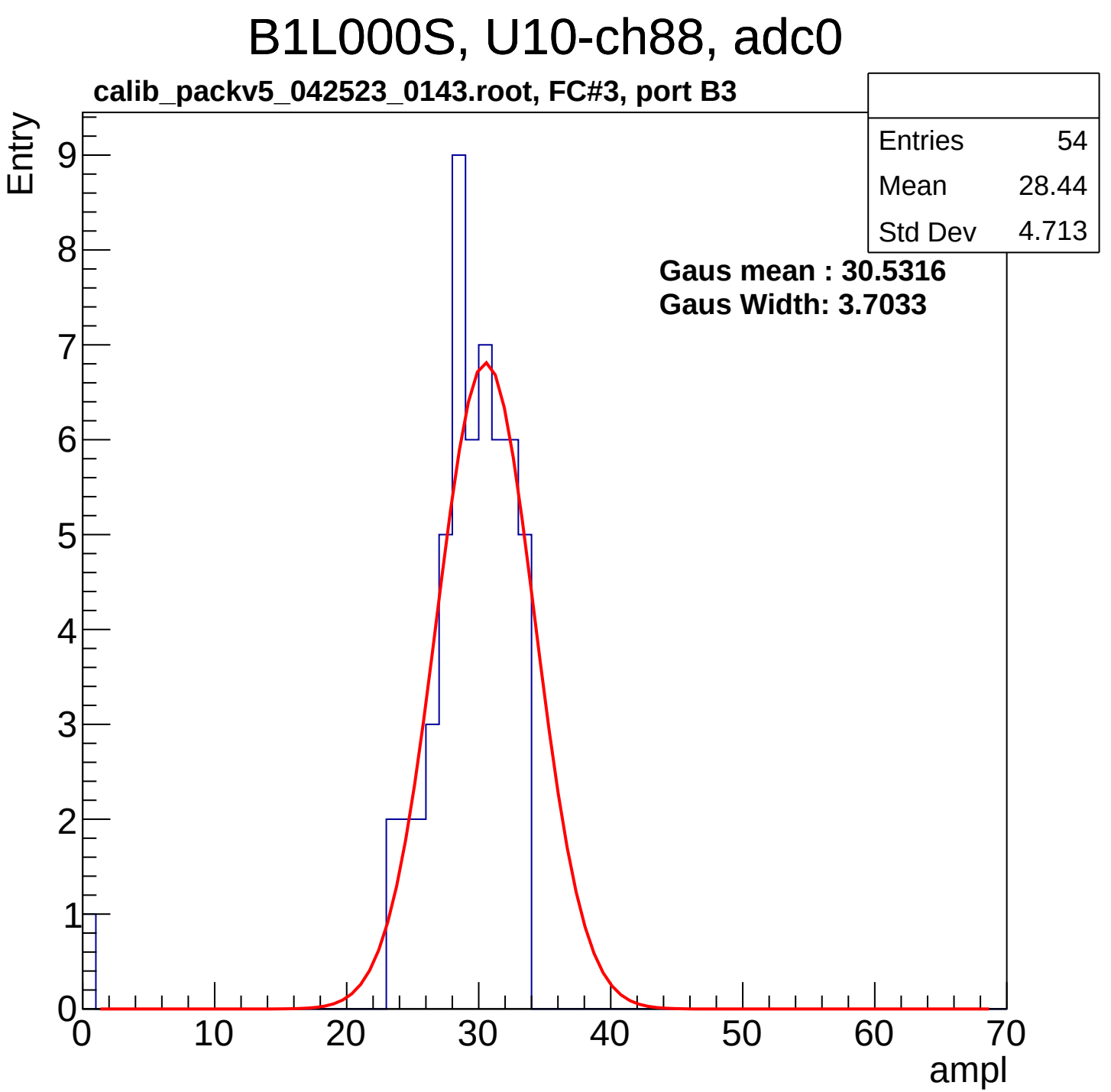
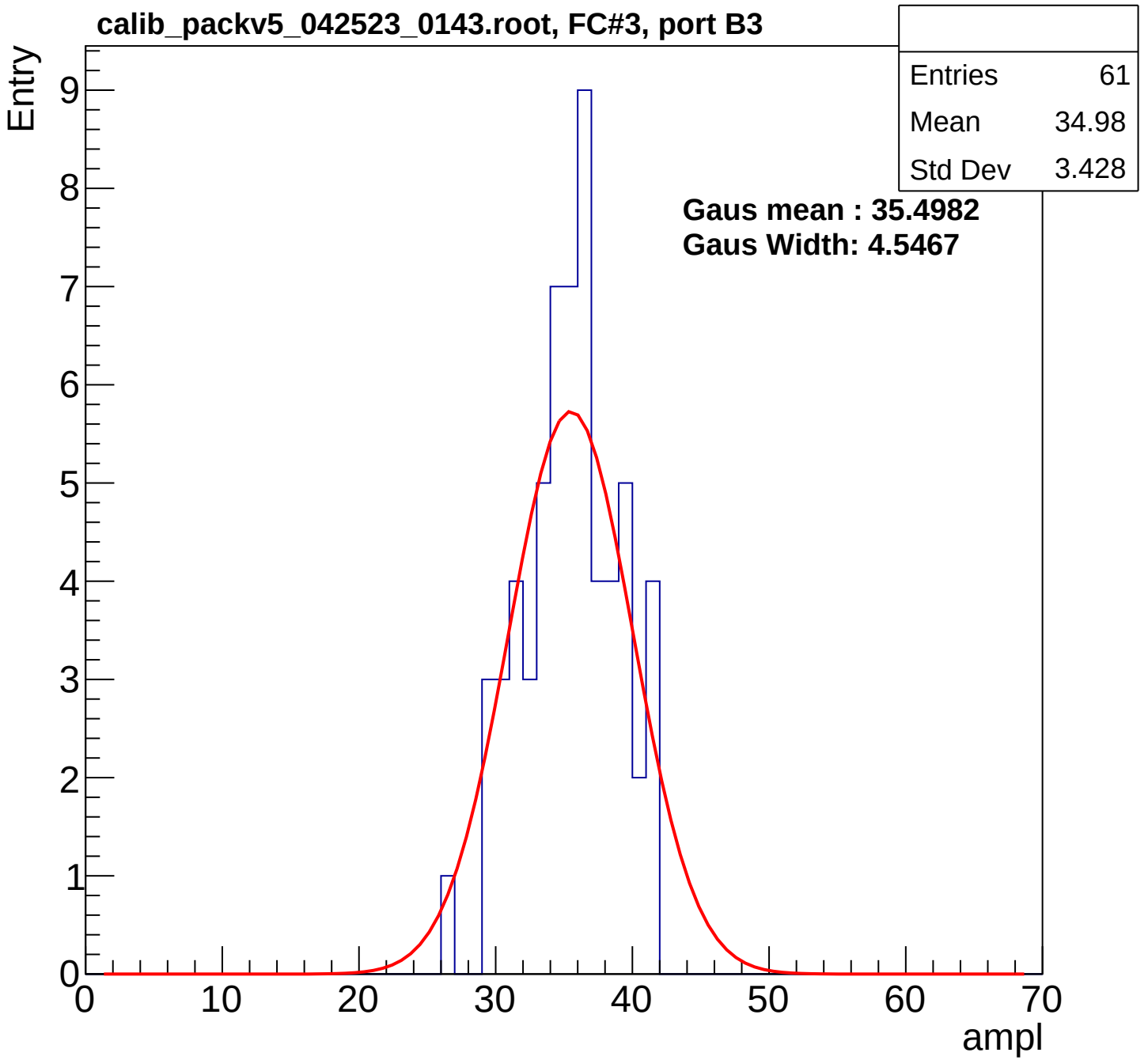
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch88, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 28.44
Std Dev 4.713
Gaus mean : 30.5316
Gaus Width: 3.7033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 34.98
Std Dev 3.428
Gaus mean : 35.4982
Gaus Width: 4.5467
Entry
ampl
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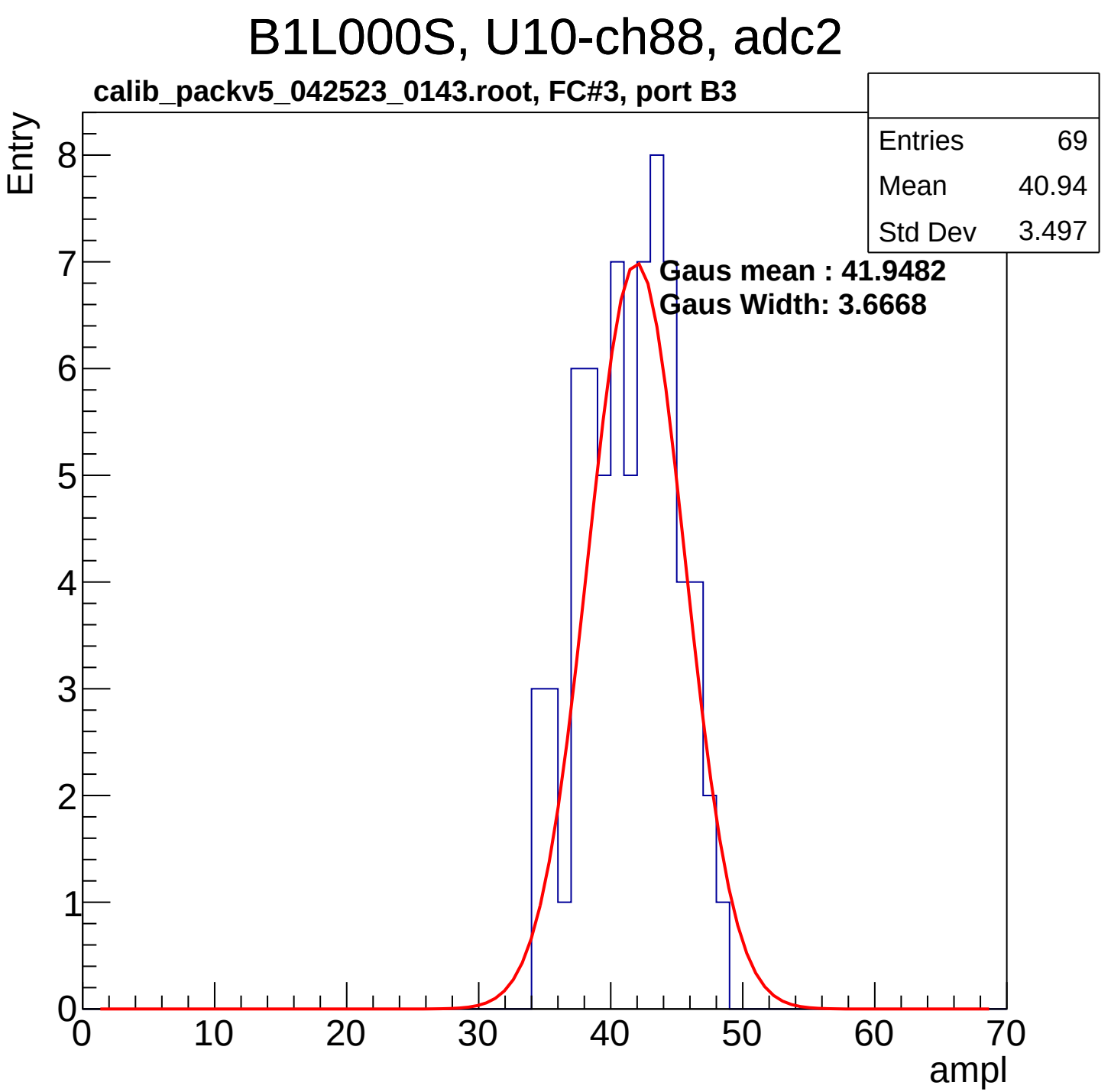
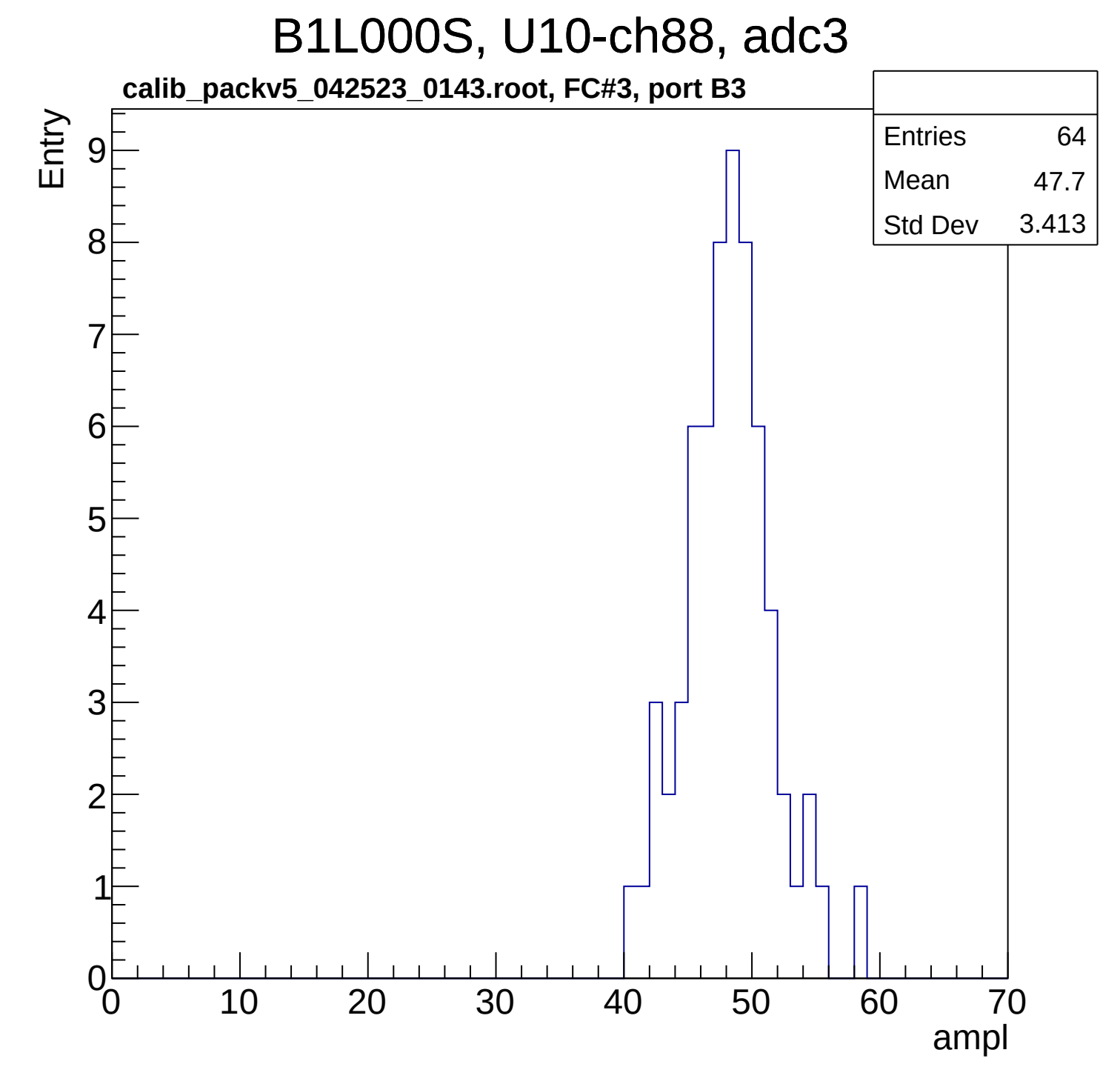
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 40.94
Std Dev 3.497
Gaus mean : 41.9482
Gaus Width: 3.6668
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.7
Std Dev 3.413
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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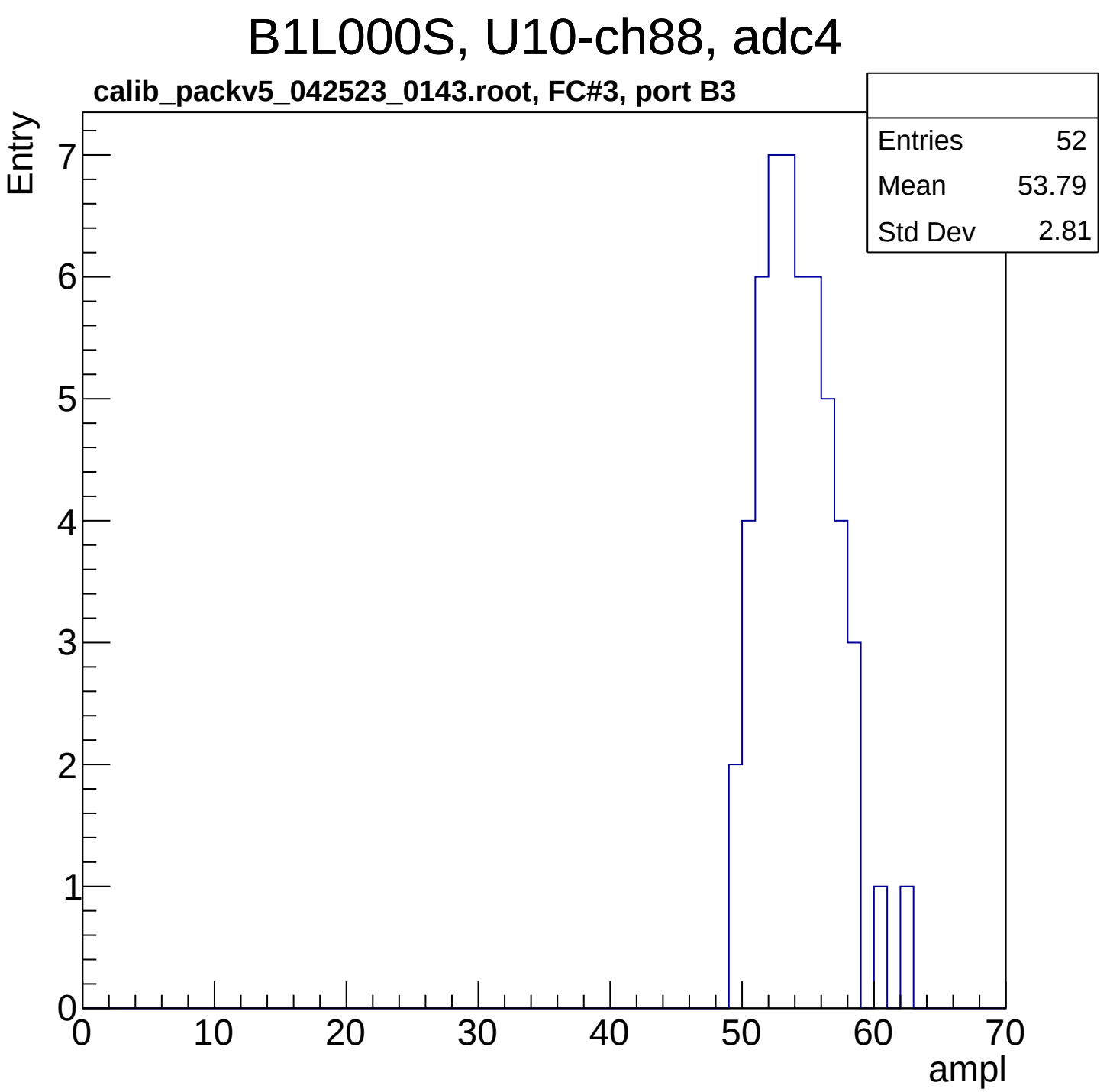
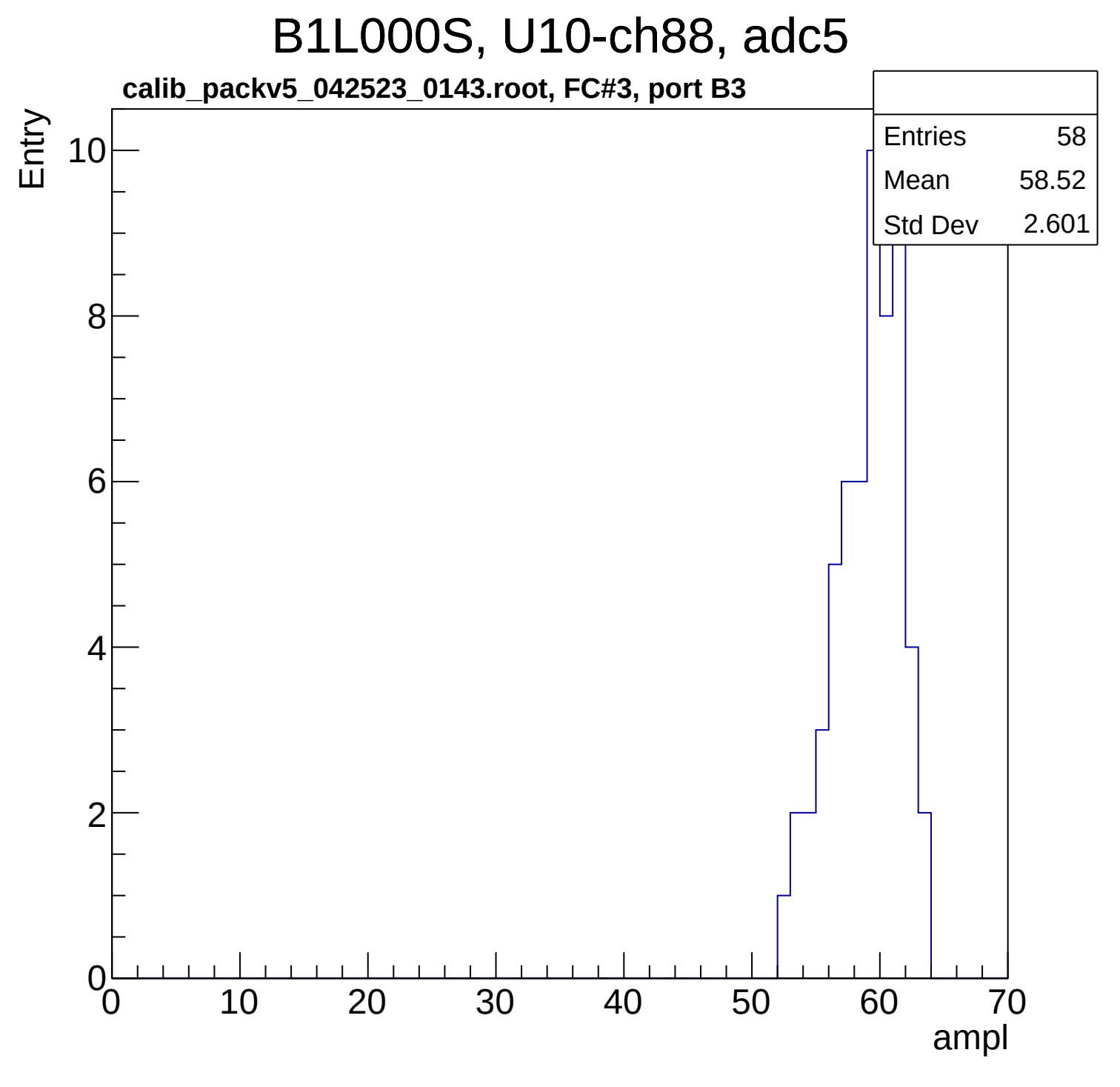
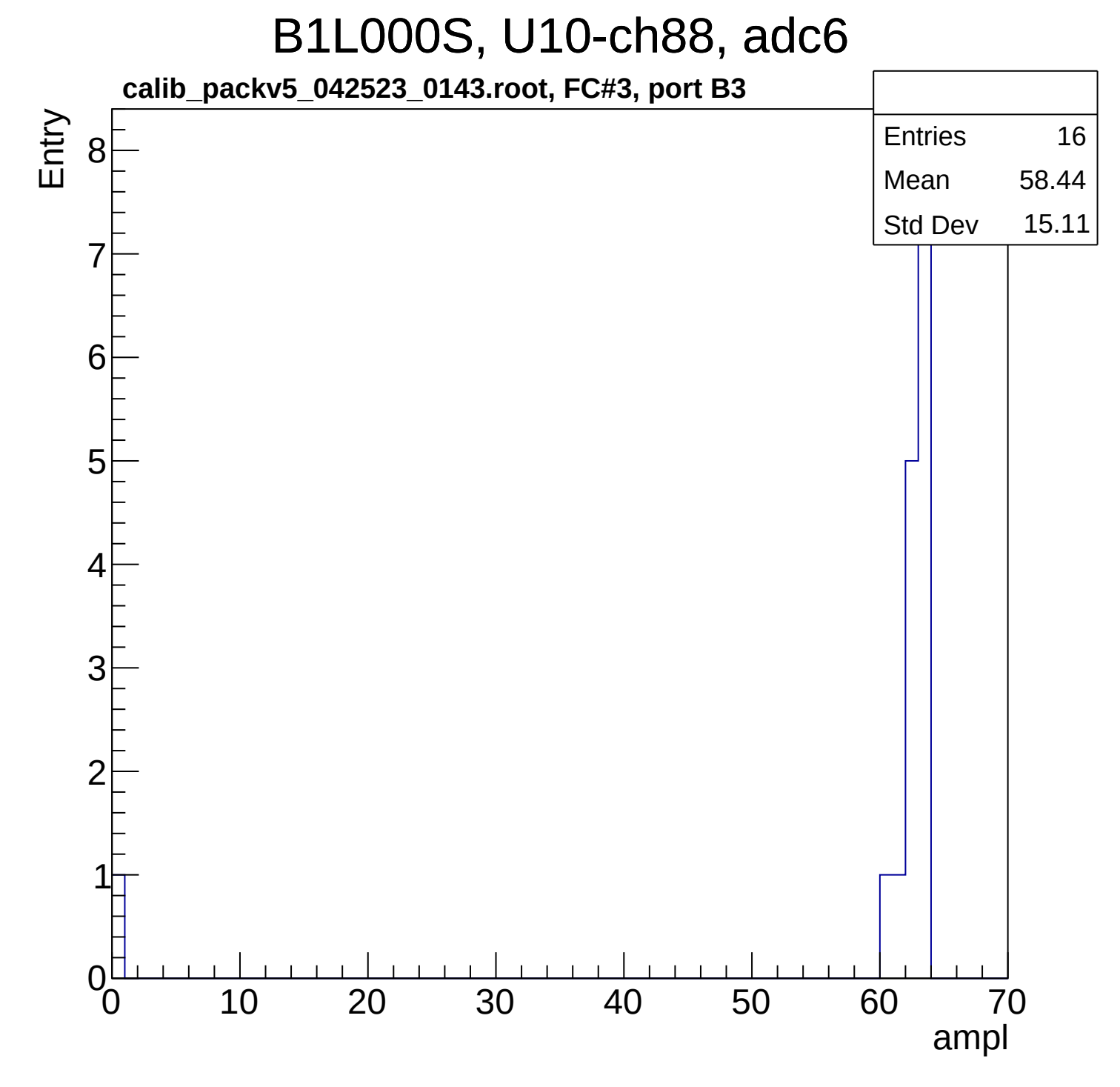

B1L000S, U10-ch88, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 53.79
Std Dev 2.81
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.52
Std Dev 2.601
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 58.44
Std Dev 15.11
Entry
ampl
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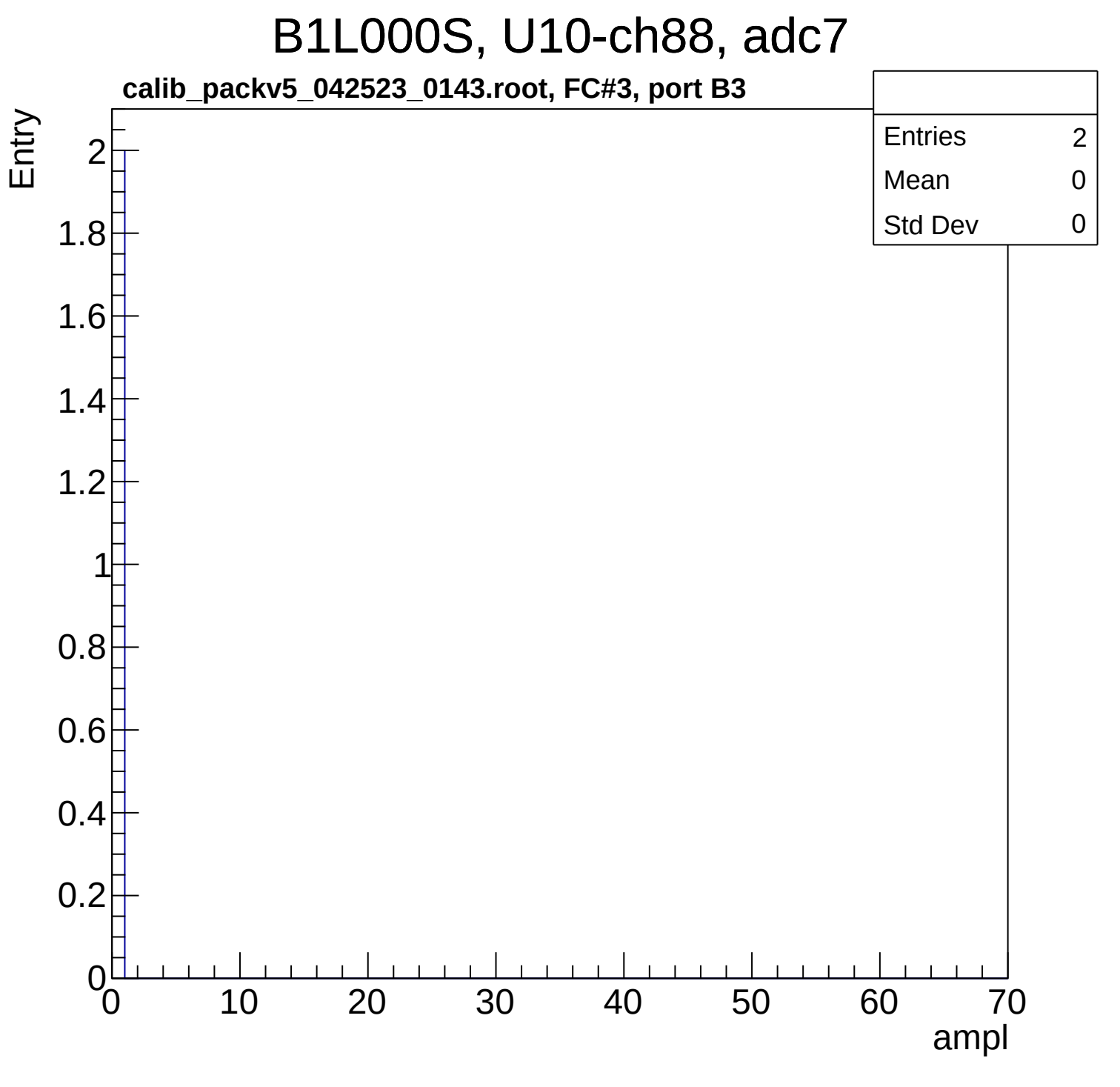
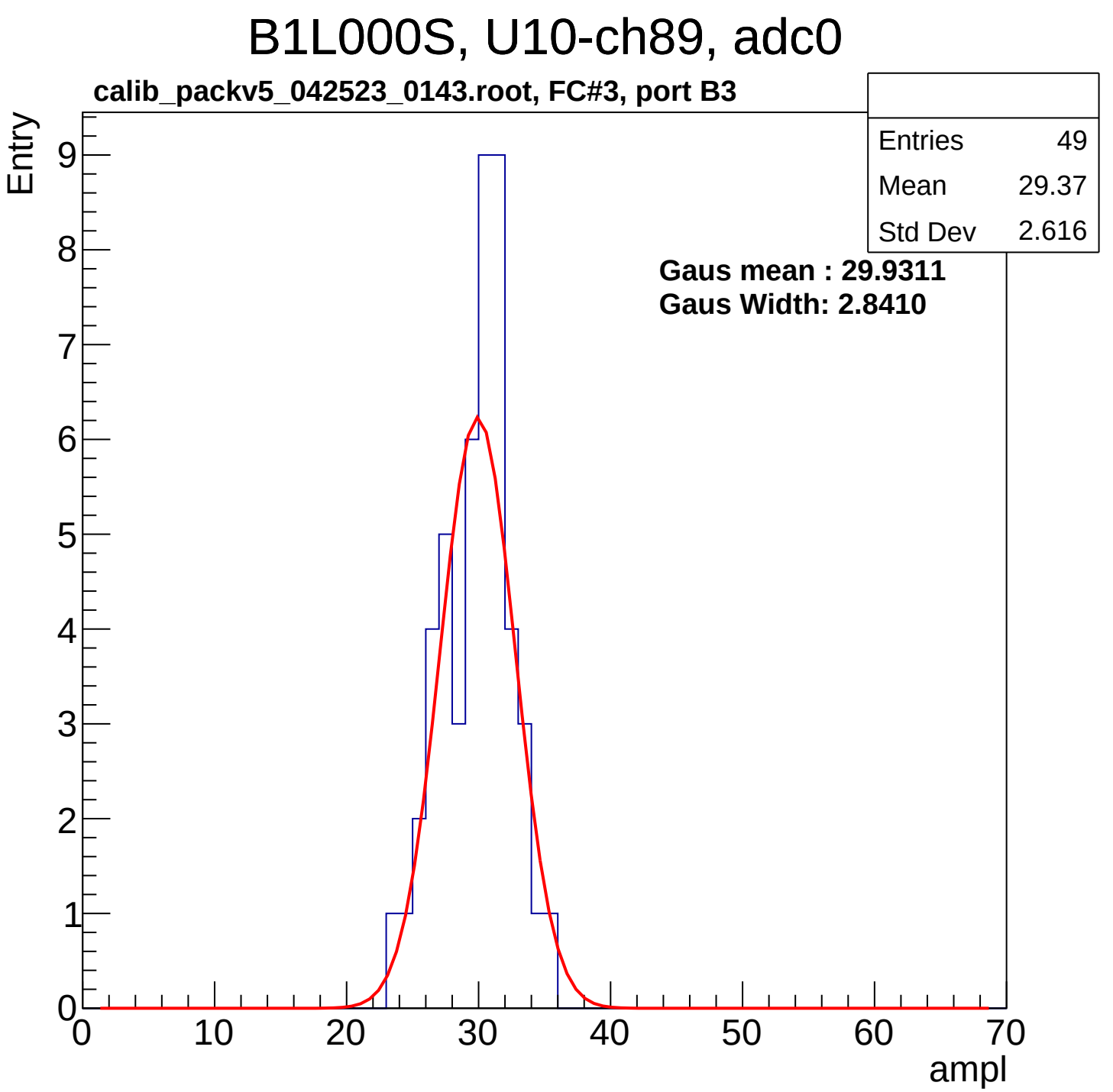
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch88, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch89, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 29.37
Std Dev 2.616
Gaus mean : 29.9311
Gaus Width: 2.8410
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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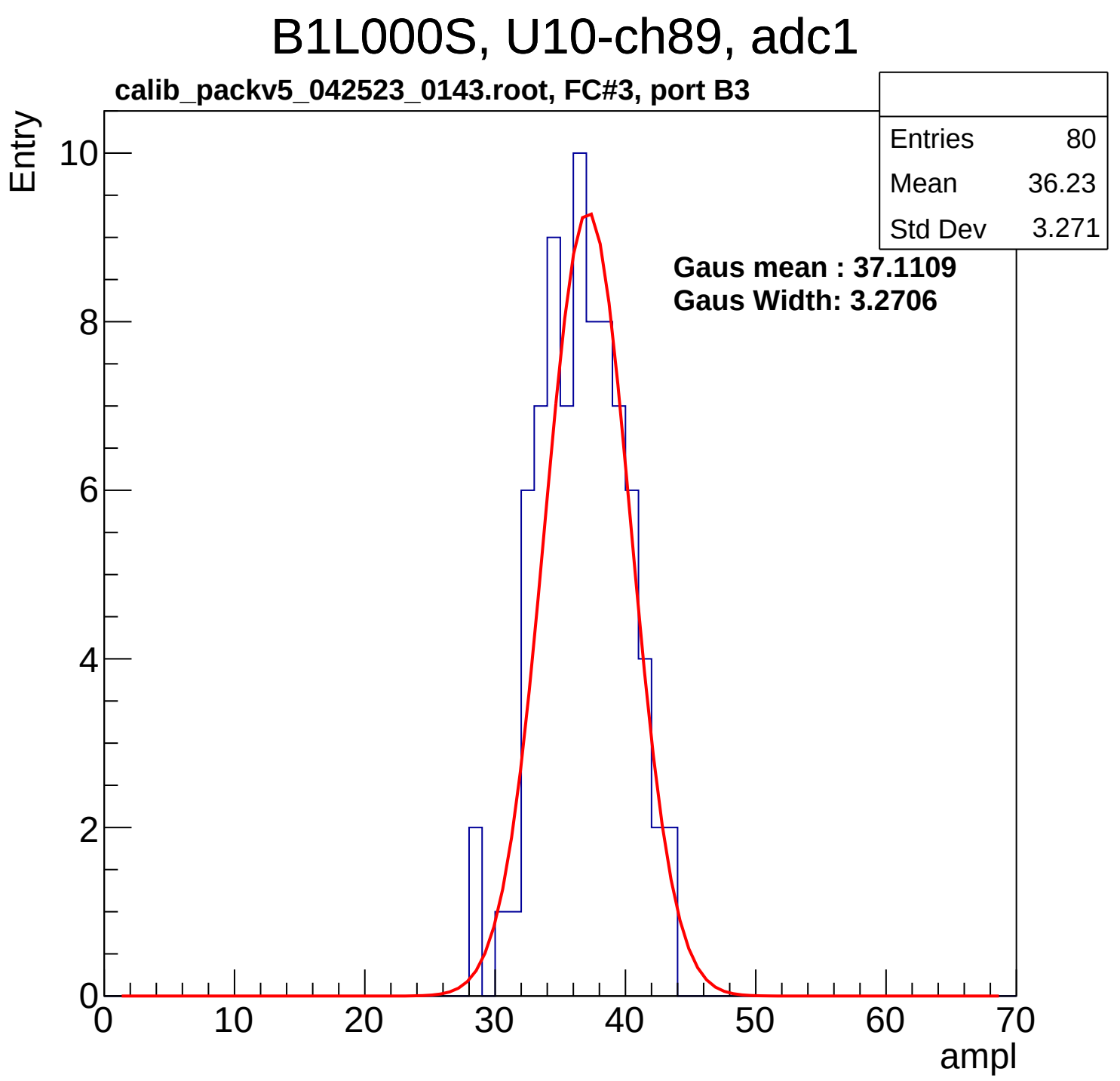
B1L000S, U10-ch89, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 36.23
Std Dev 3.271
Gaus mean : 37.1109
Gaus Width: 3.2706
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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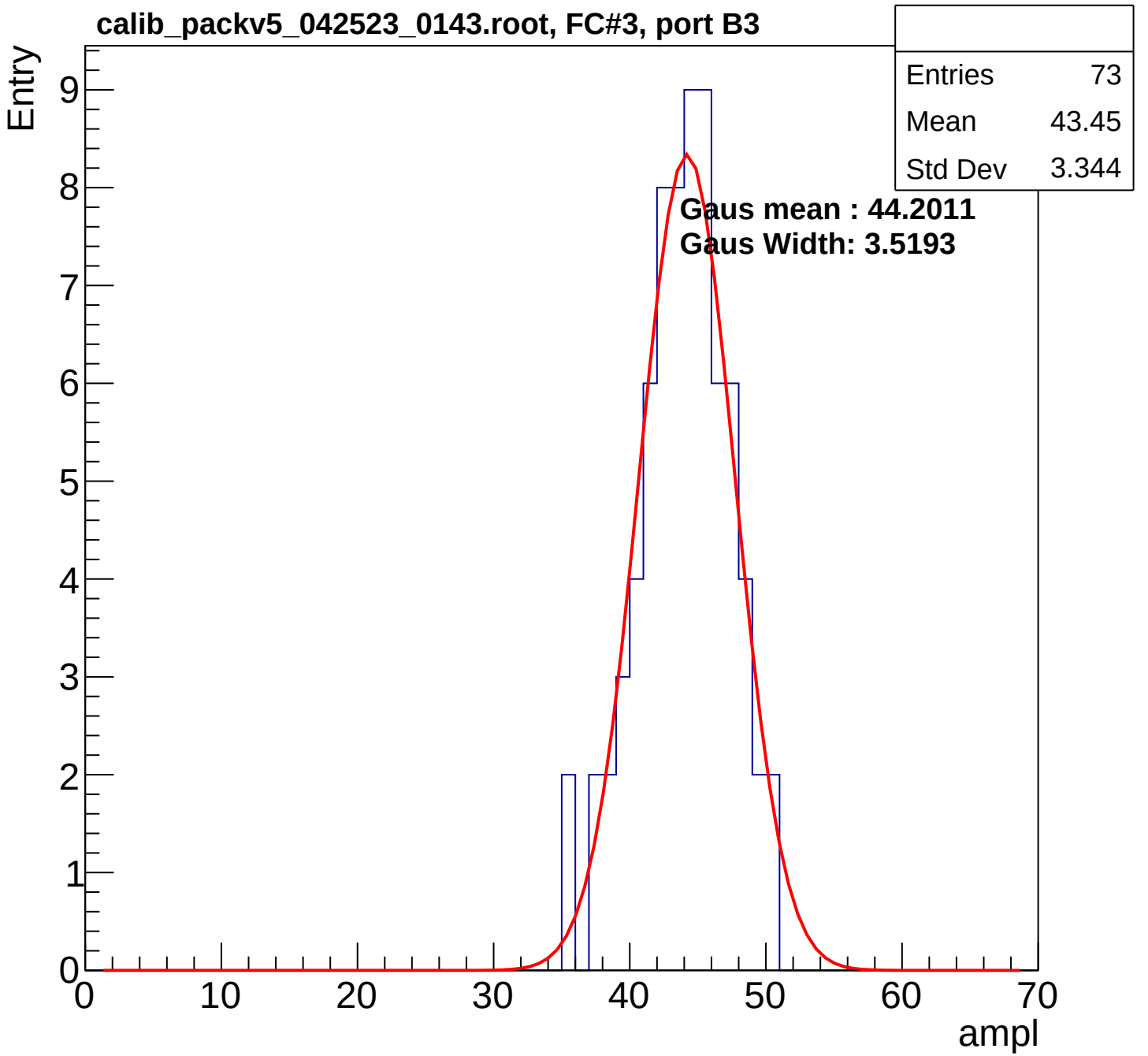
B1L000S, U10-ch89, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 43.45
Std Dev 3.344
Gaus mean : 44.2011
Gaus Width: 3.5193
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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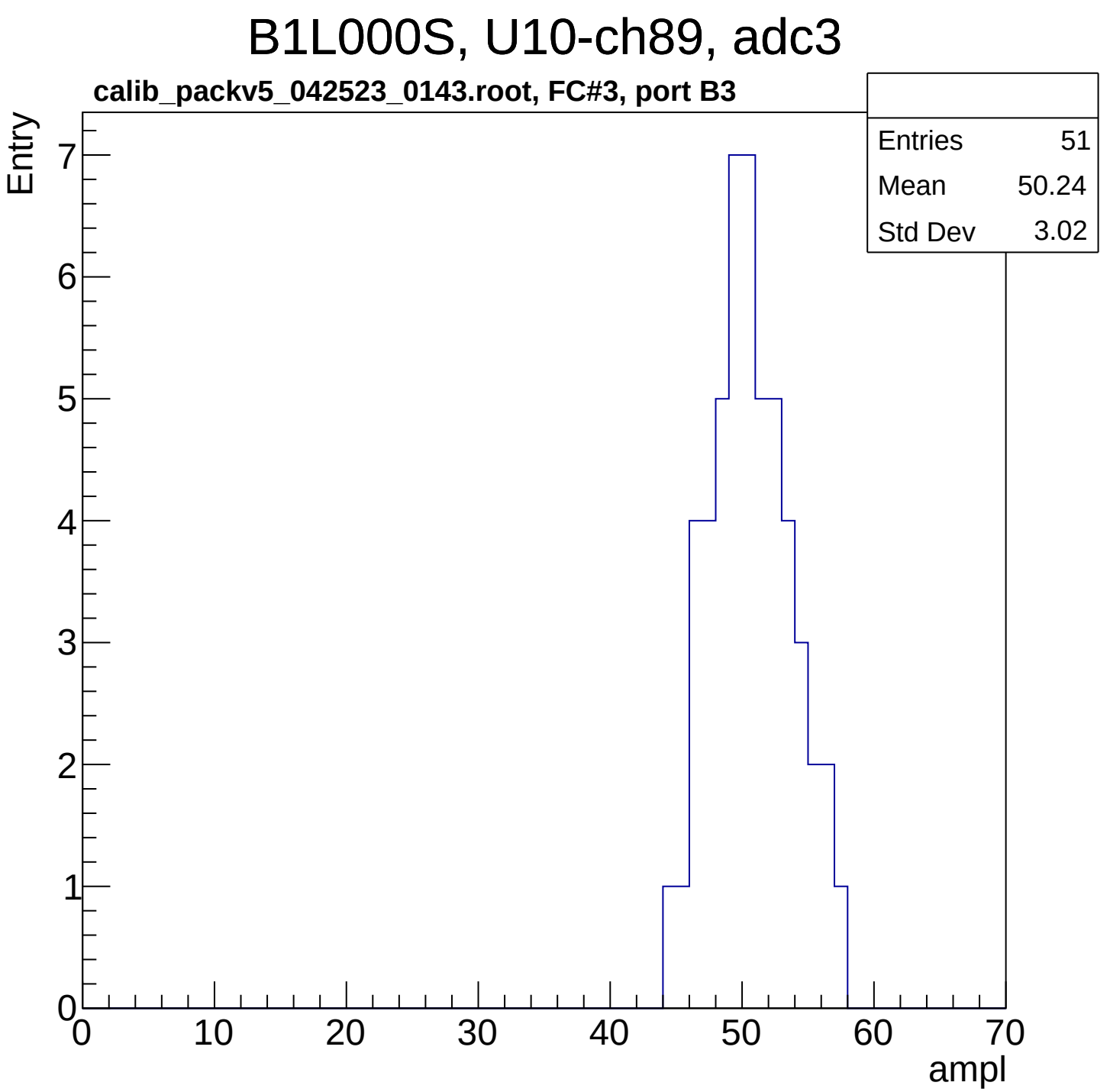
B1L000S, U10-ch89, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 50.24
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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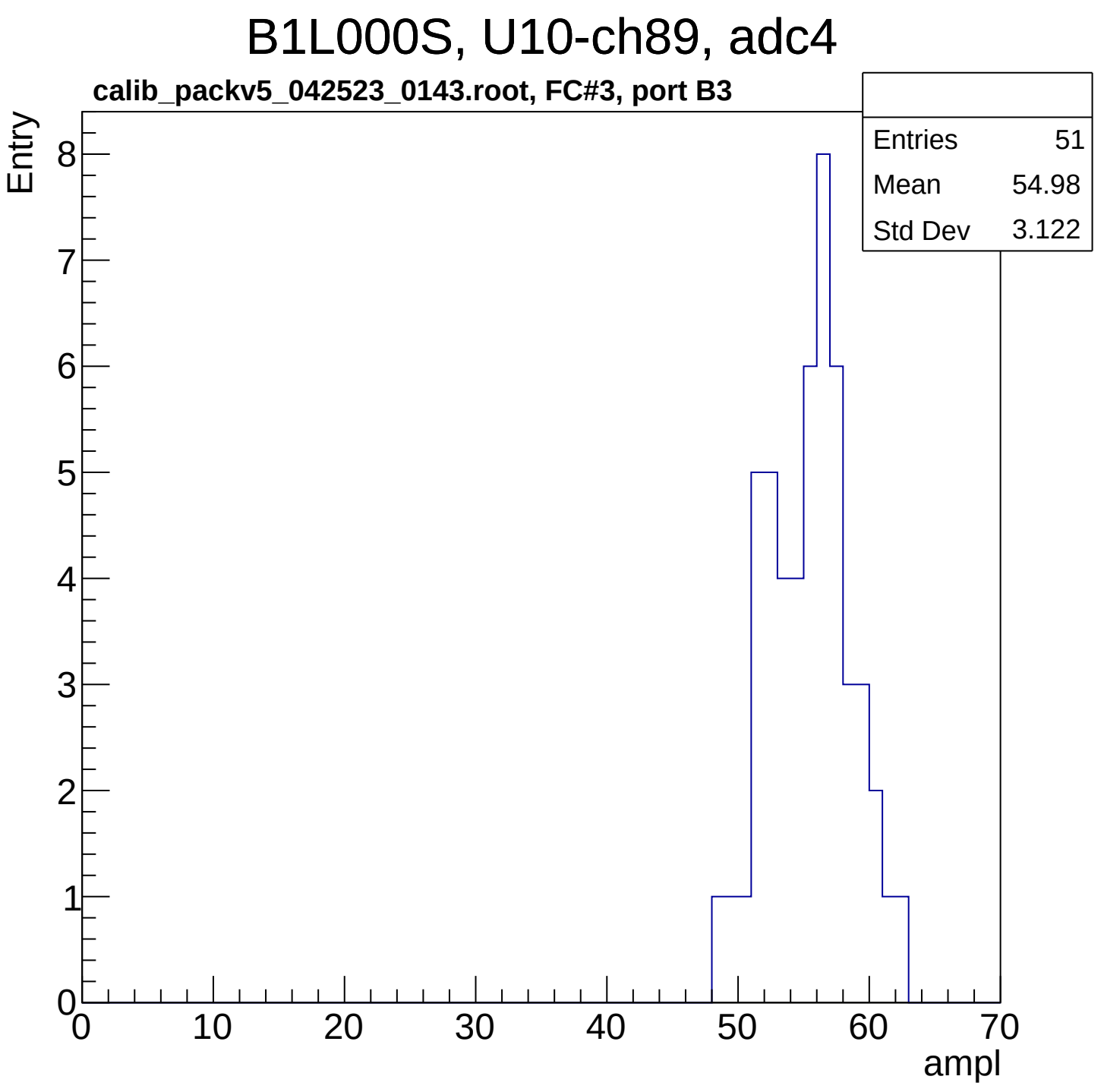
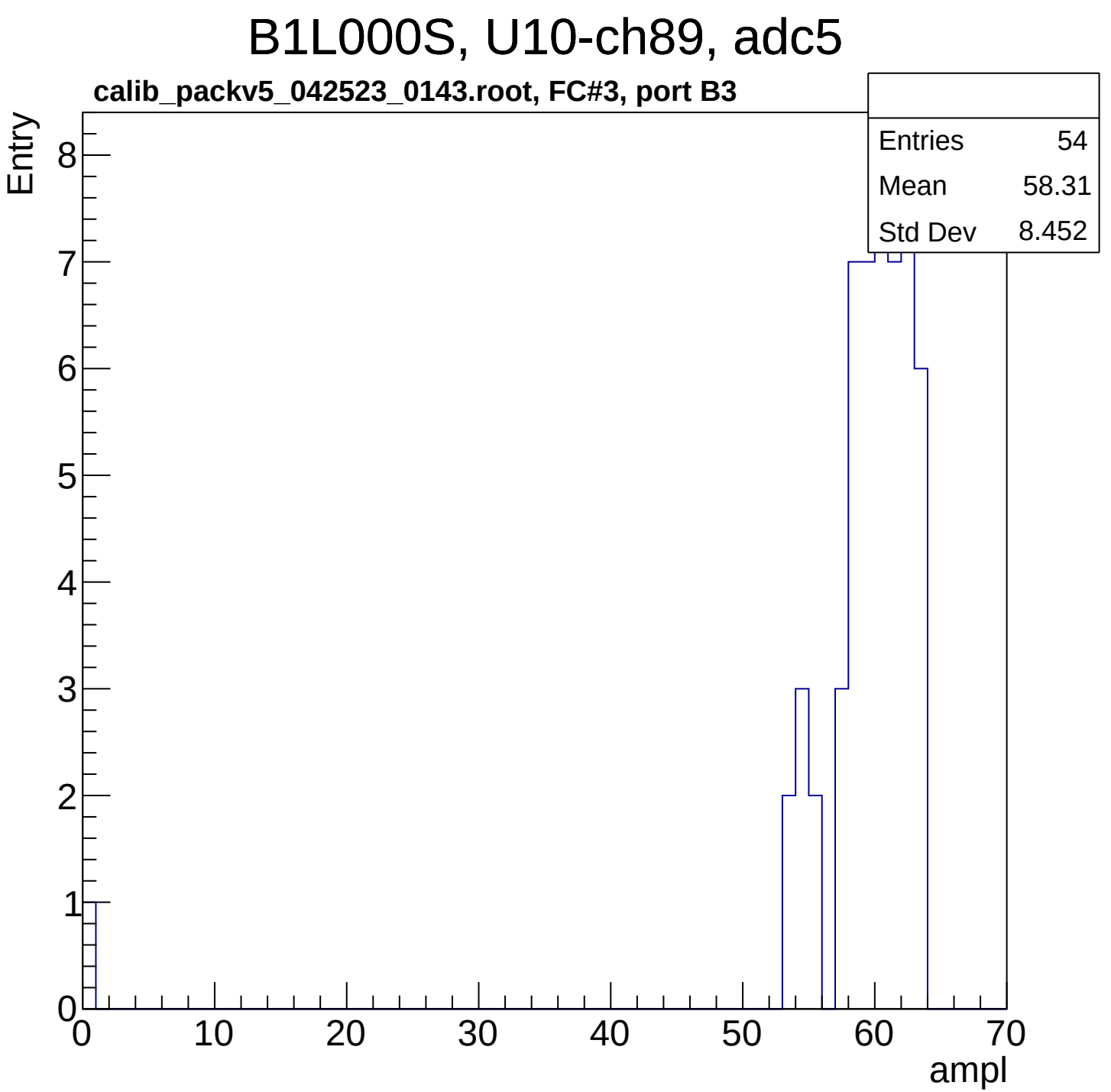

B1L000S, U10-ch89, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.98
Std Dev 3.122
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch89, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 58.31
Std Dev 8.452
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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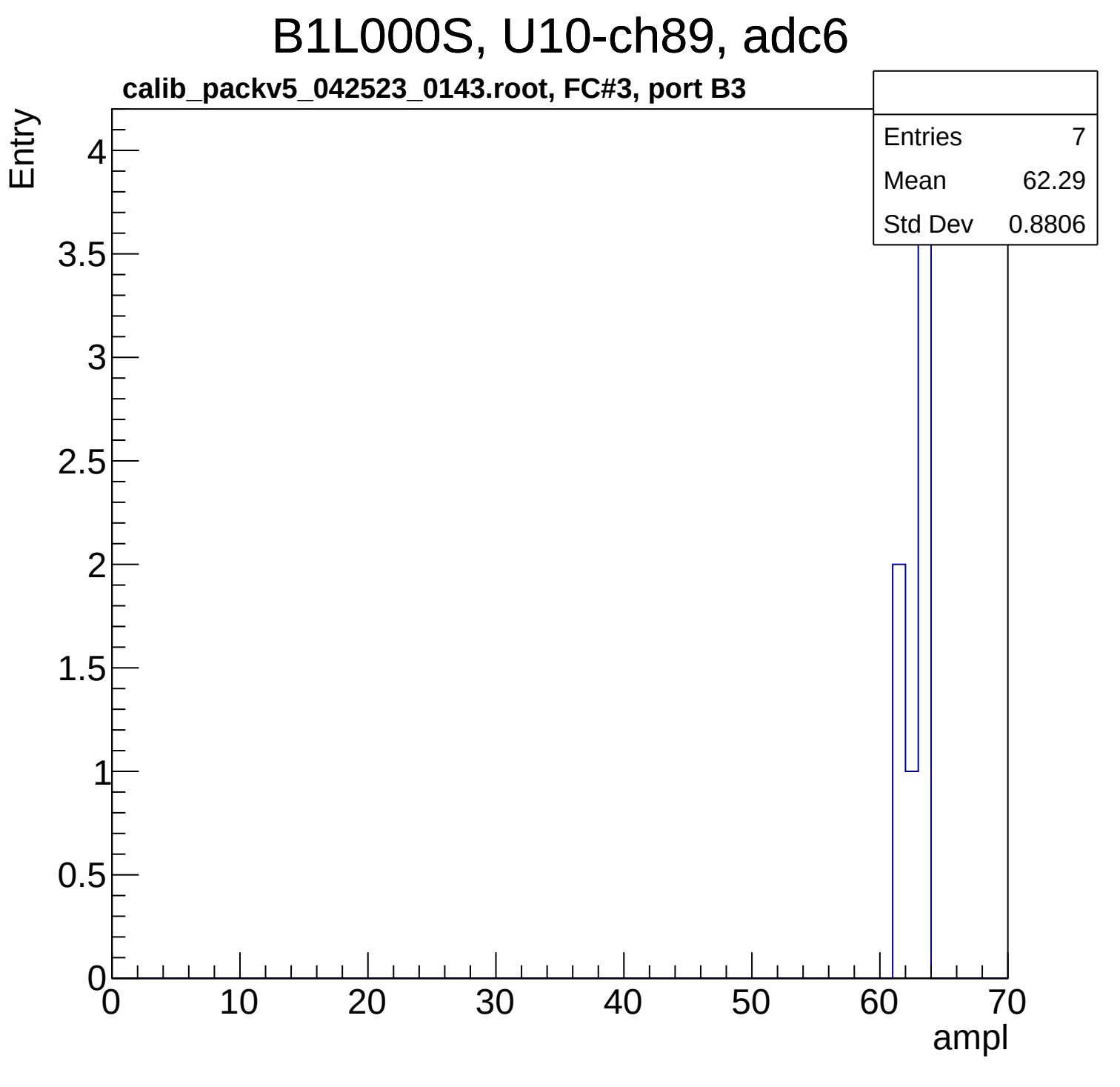
B1L000S, U10-ch89, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.29
Std Dev 0.8806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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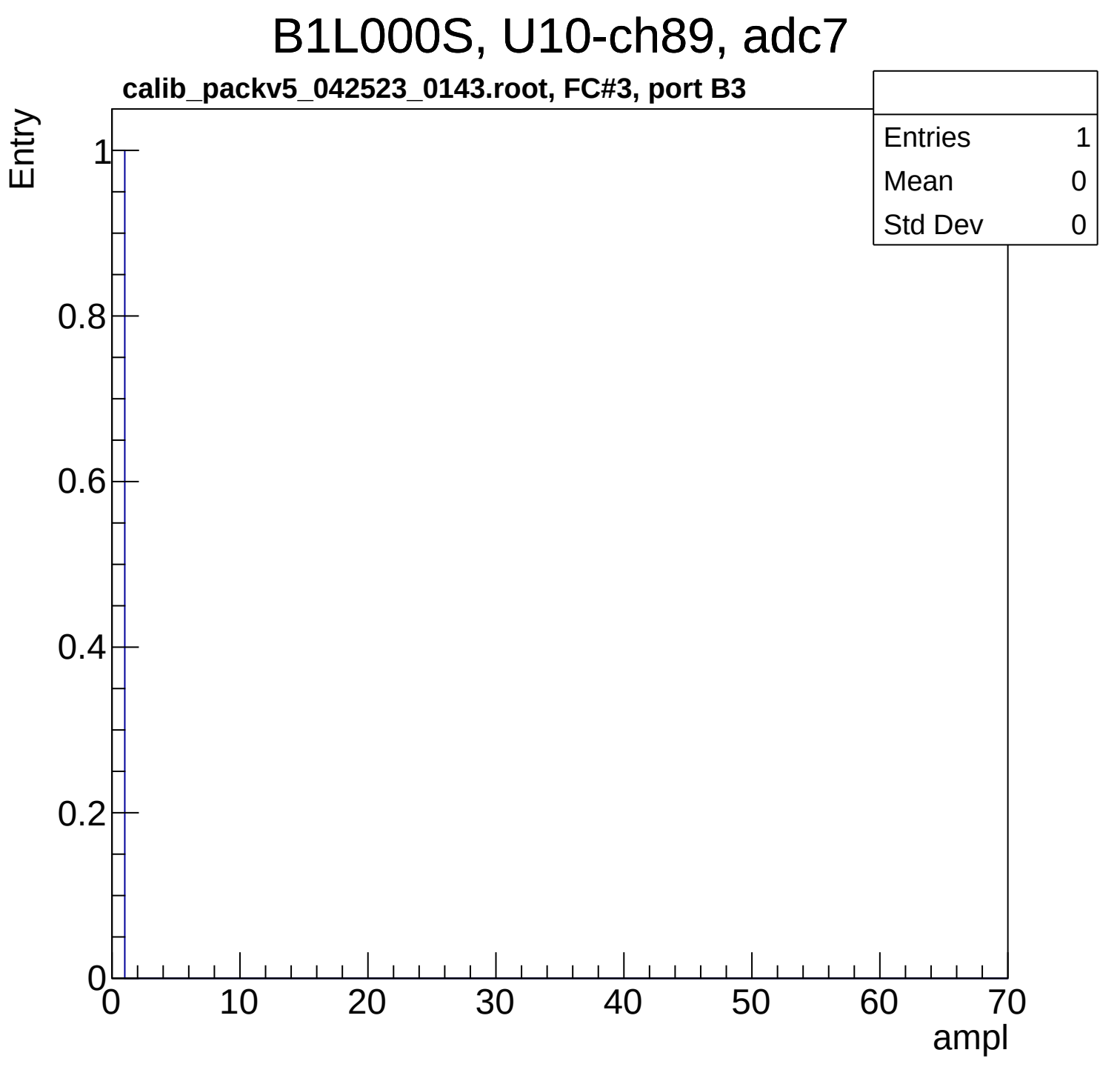
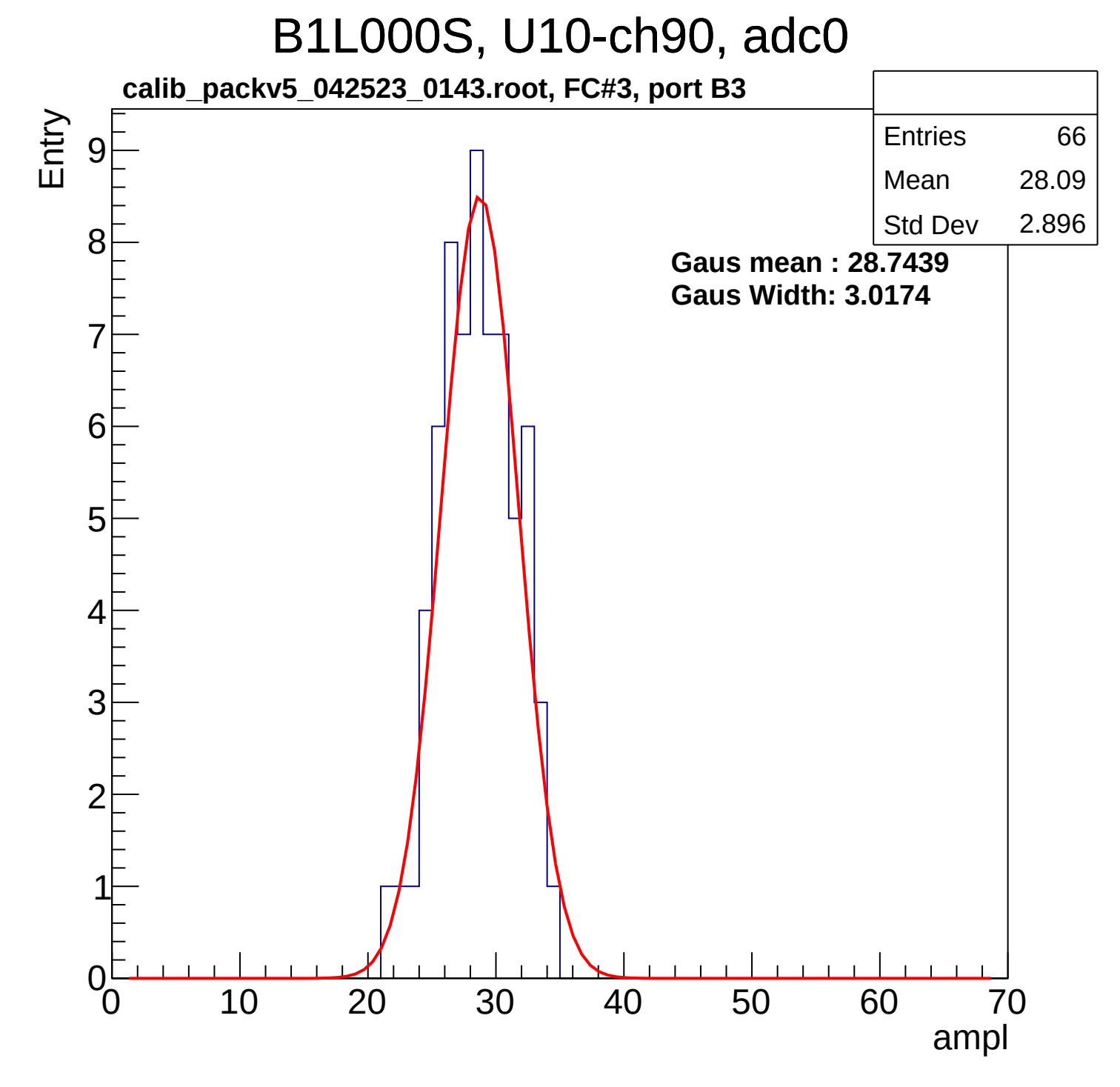
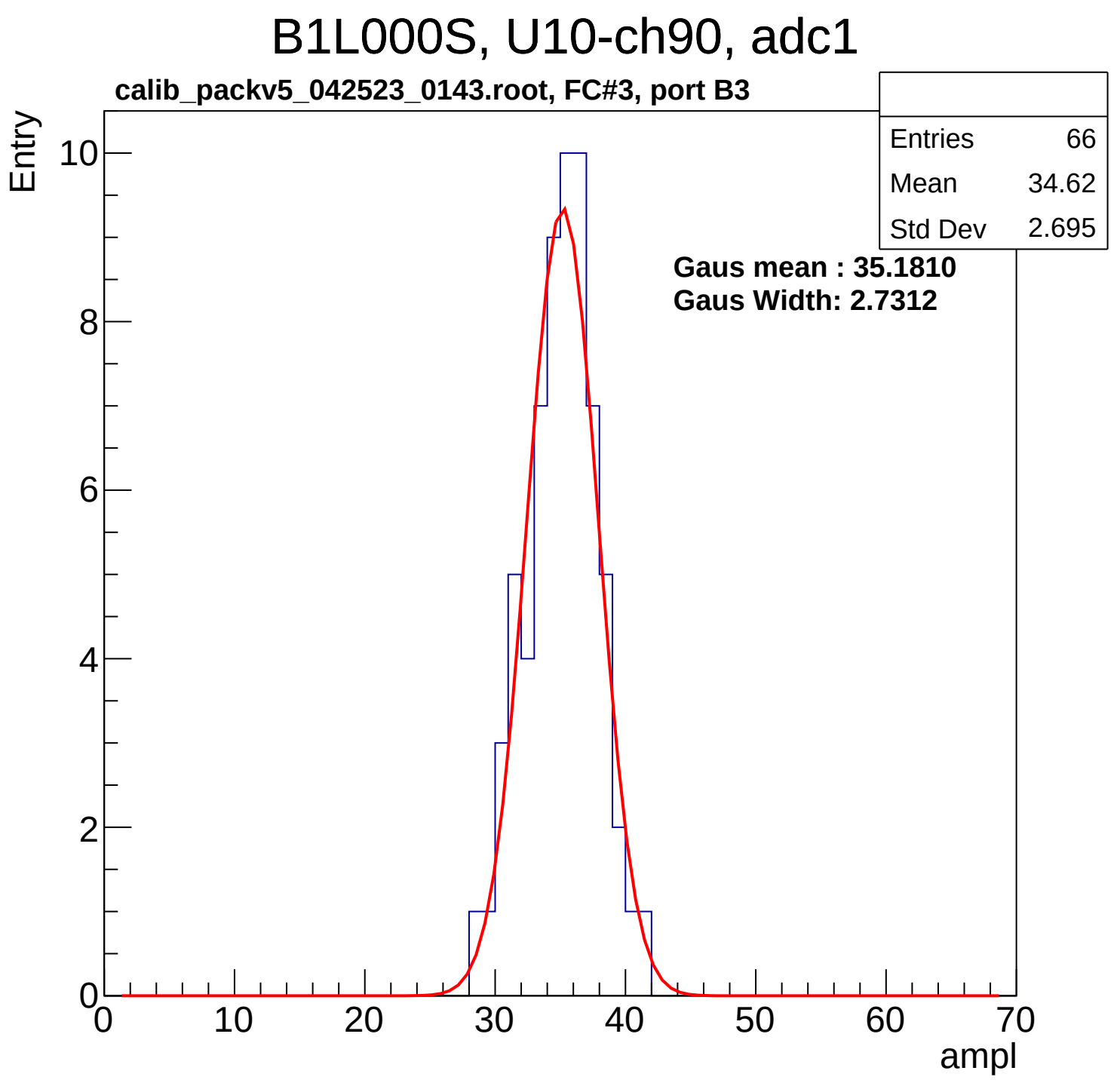
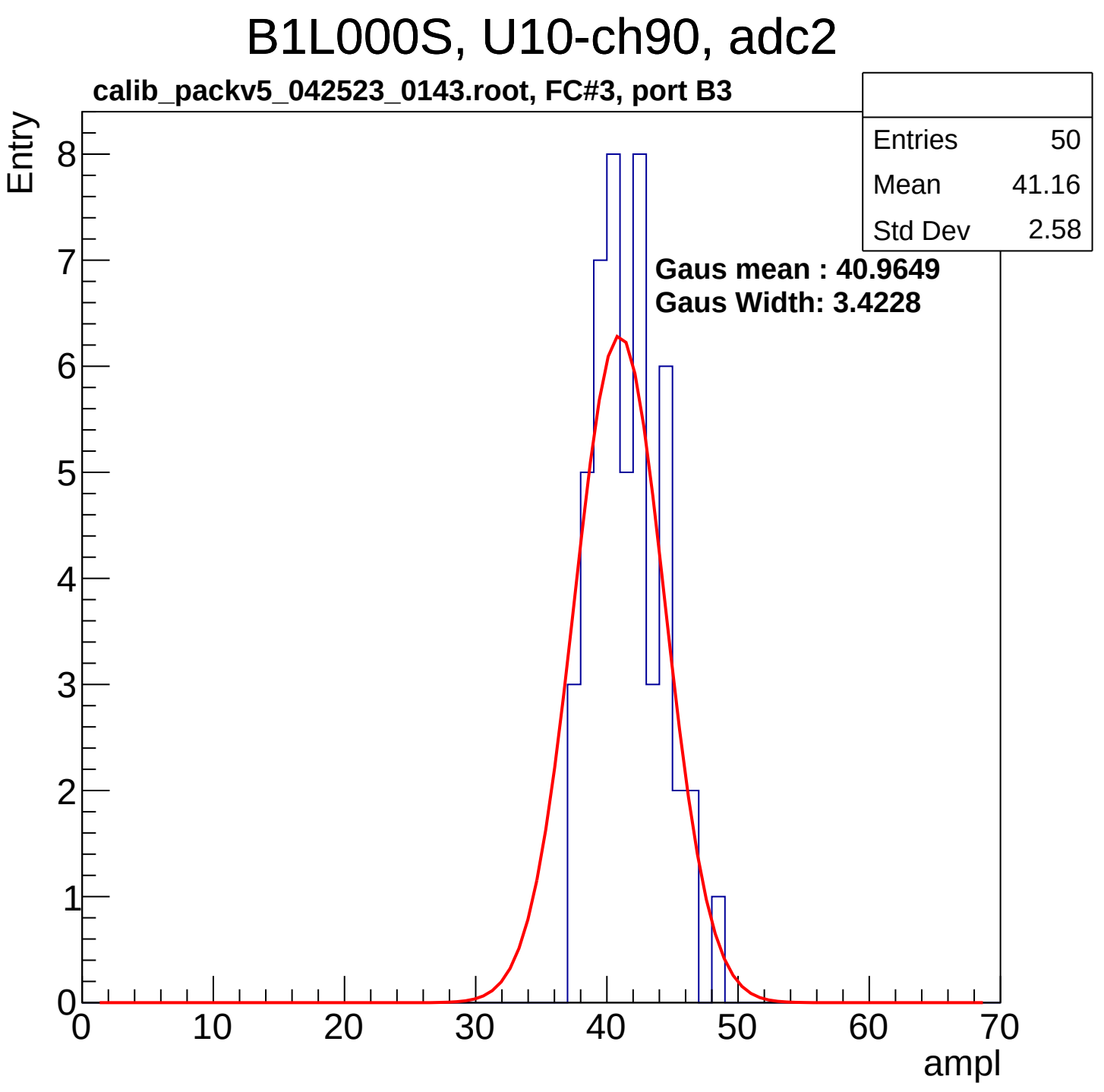
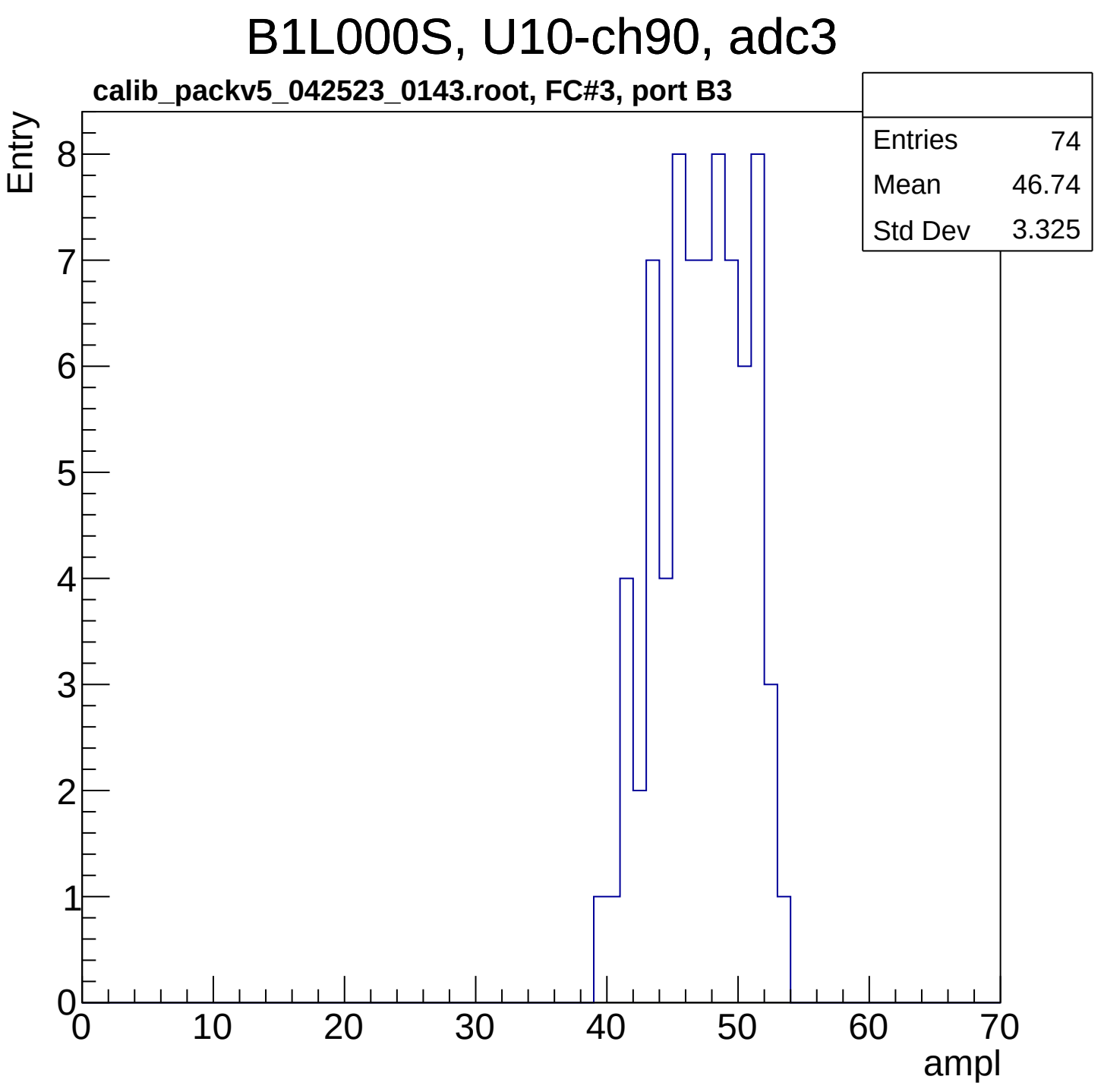

B1L000S, U10-ch89, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch90, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.09
Std Dev 2.896
Gaus mean : 28.7439
Gaus Width: 3.0174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch90, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.62
Std Dev 2.695
Gaus mean : 35.1810
Gaus Width: 2.7312
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch90, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 41.16
Std Dev 2.58
Gaus mean : 40.9649
Gaus Width: 3.4228
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch90, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 46.74
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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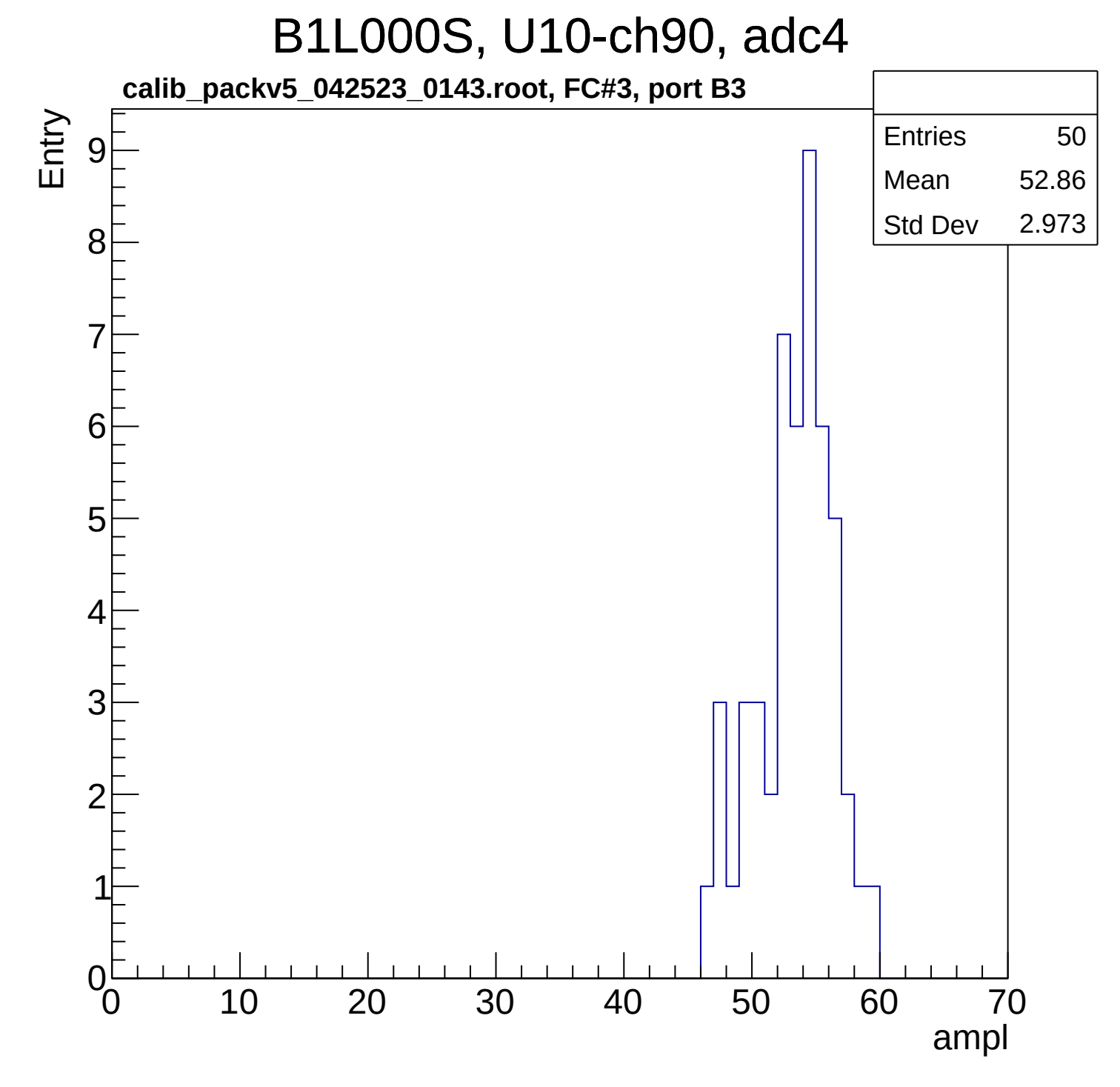
B1L000S, U10-ch90, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 52.86
Std Dev 2.973
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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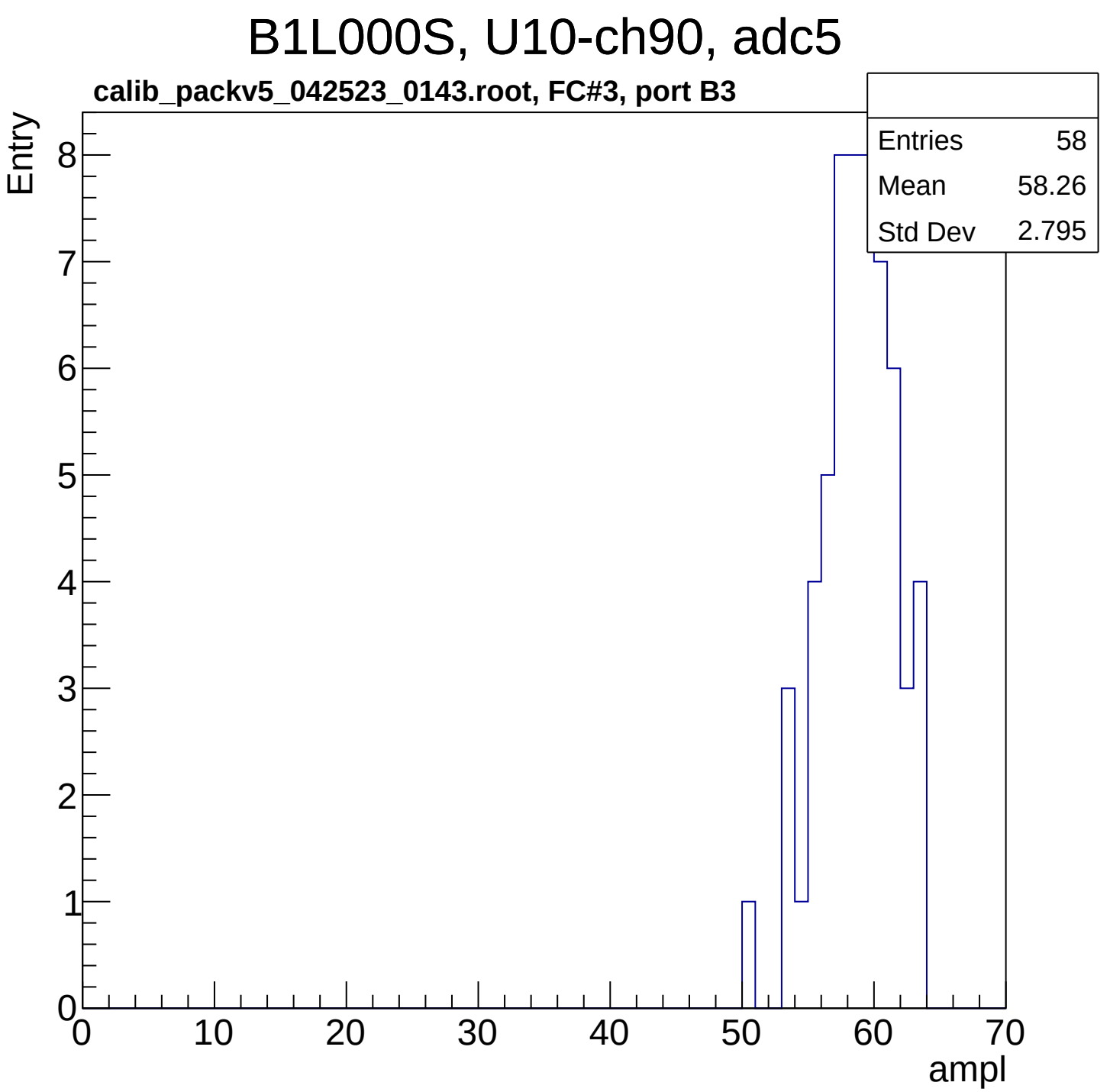
B1L000S, U10-ch90, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.26
Std Dev 2.795
Entry
ampl
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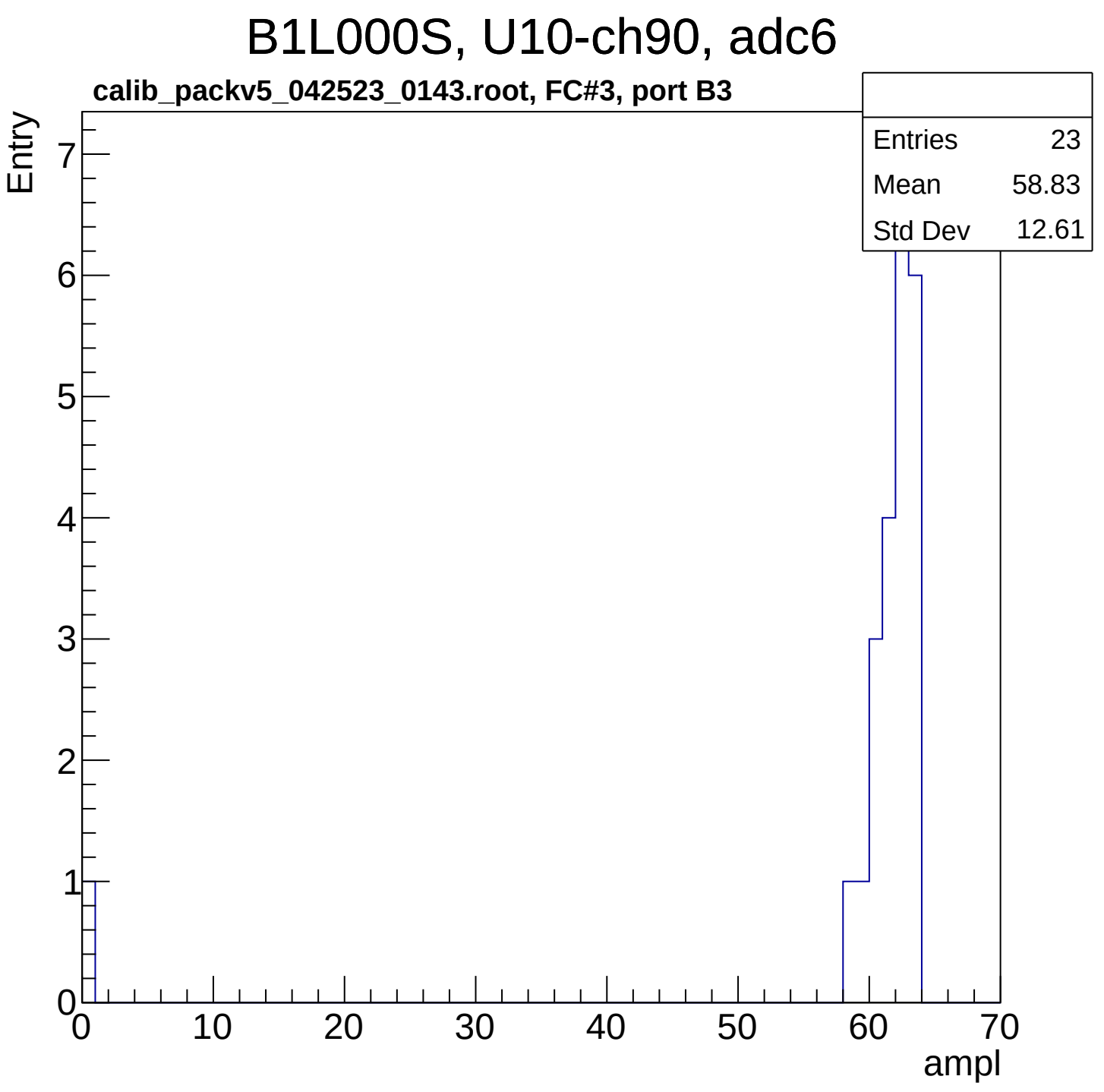
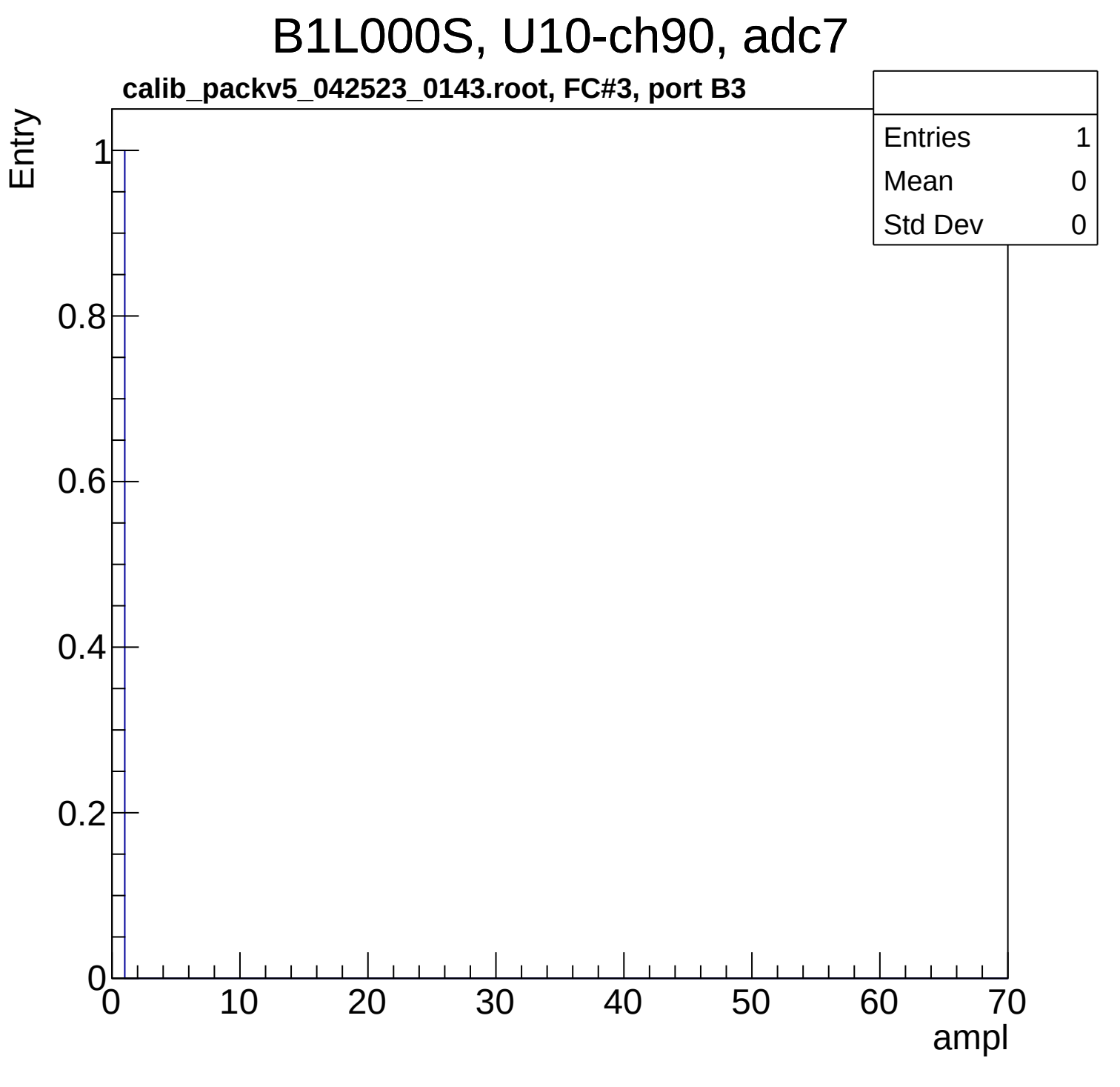
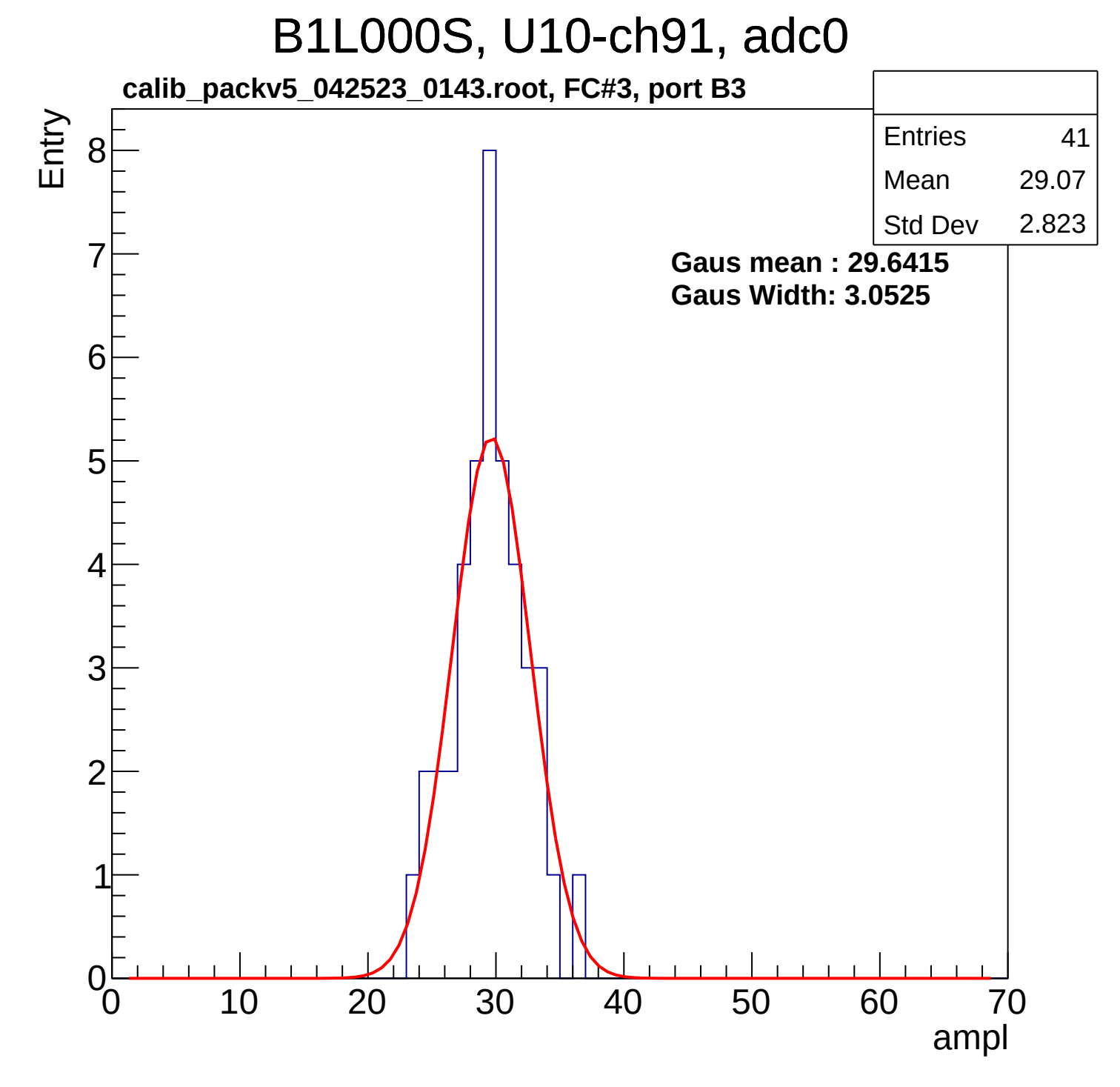
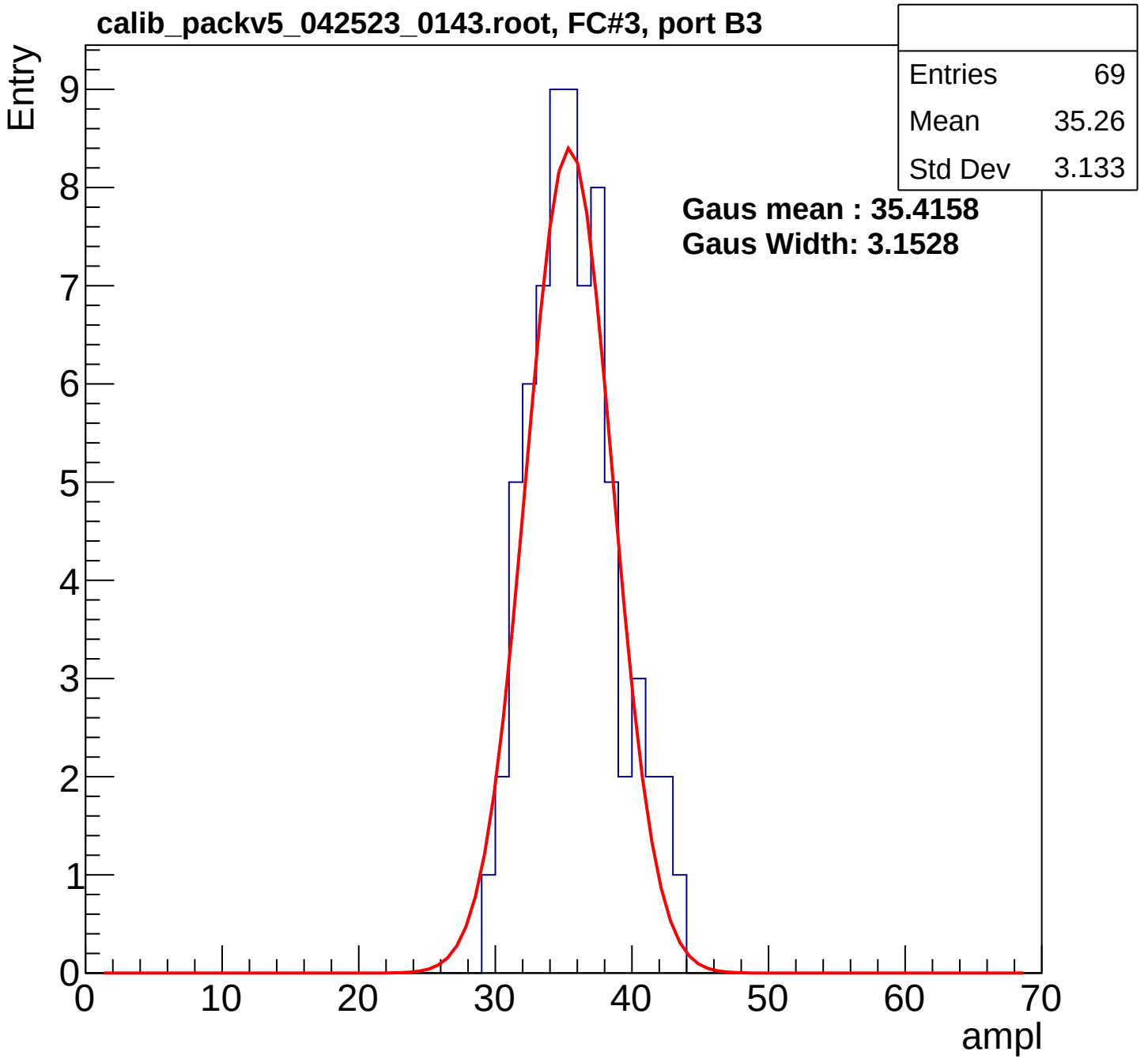
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch90, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 58.83
Std Dev 12.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch90, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch91, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 29.07
Std Dev 2.823
Gaus mean : 29.6415
Gaus Width: 3.0525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch91, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 35.26
Std Dev 3.133
Gaus mean : 35.4158
Gaus Width: 3.1528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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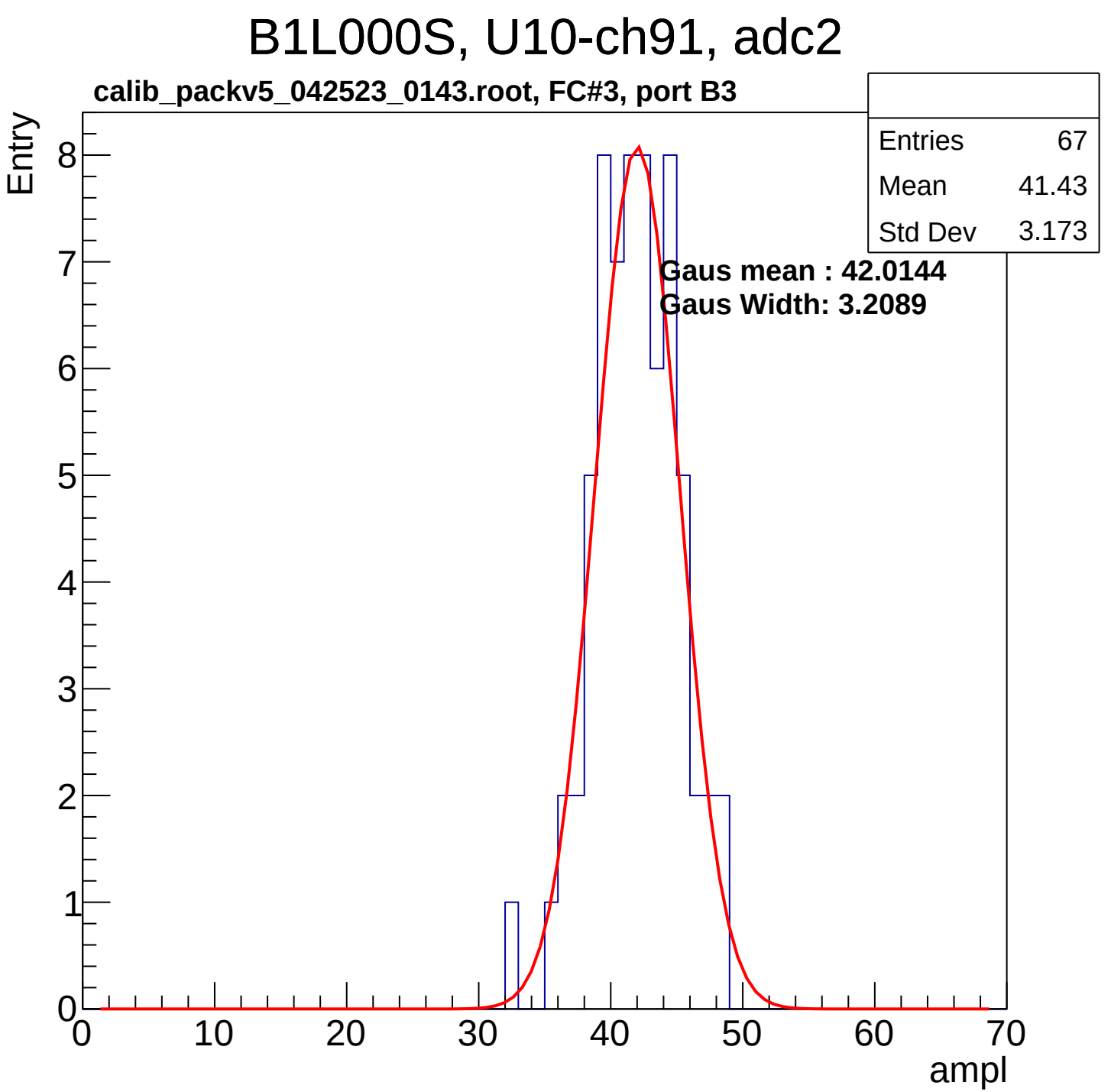
B1L000S, U10-ch91, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.43
Std Dev 3.173
Gaus mean : 42.0144
Gaus Width: 3.2089
Entry
ampl
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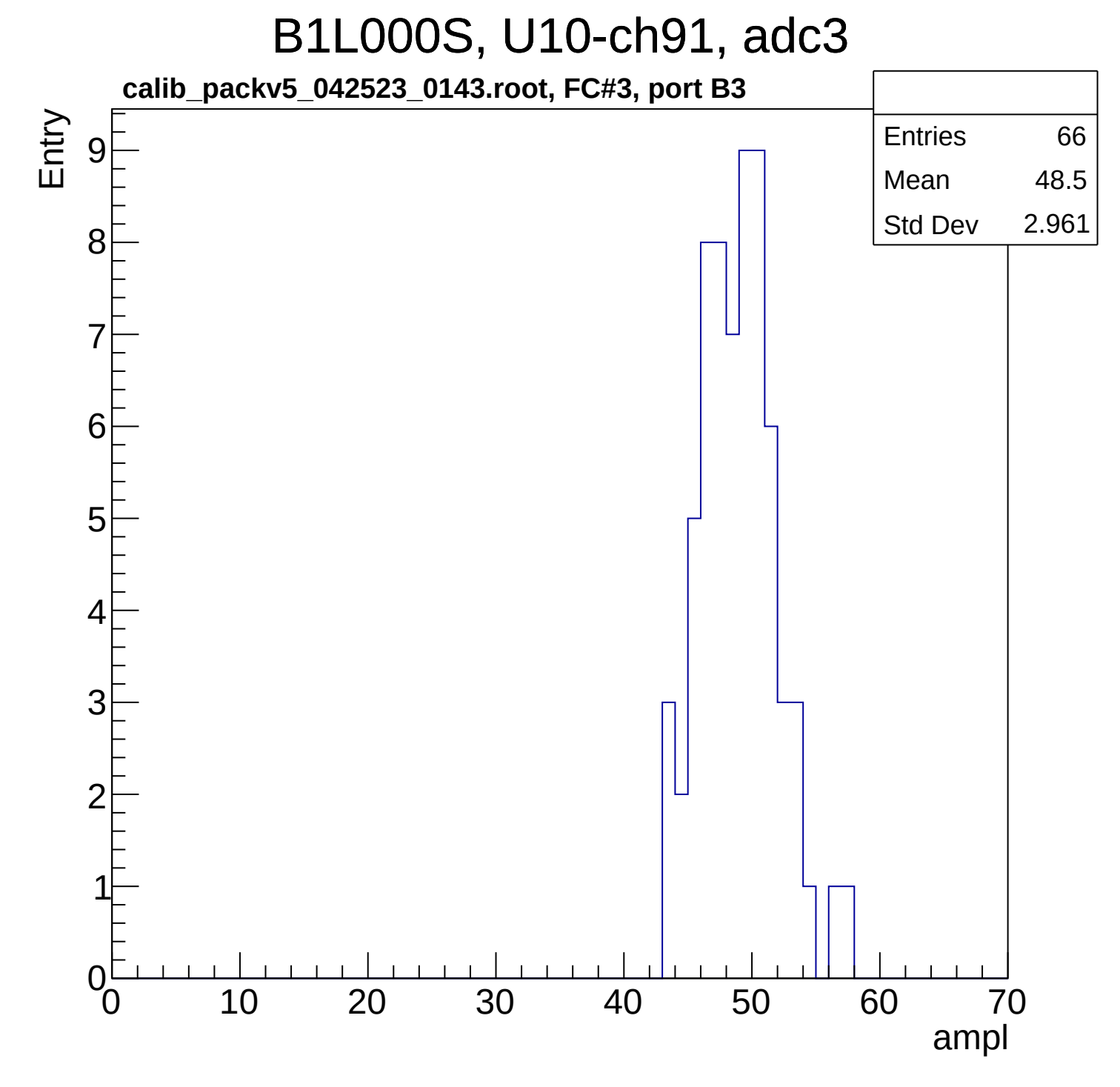
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch91, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 48.5
Std Dev 2.961
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
30
40
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60
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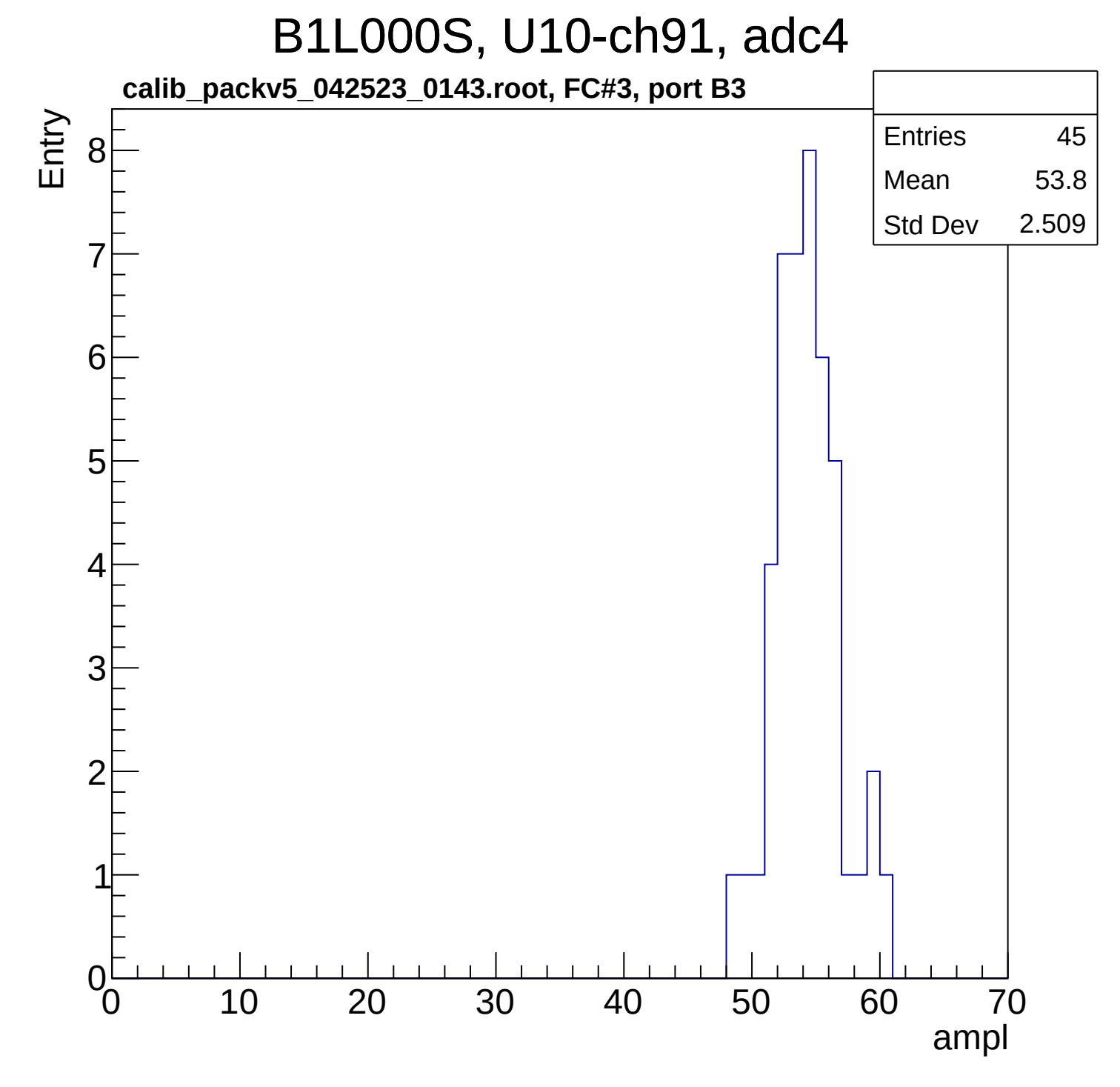
B1L000S, U10-ch91, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.8
Std Dev 2.509
Entry
ampl
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1
2
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4
5
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0
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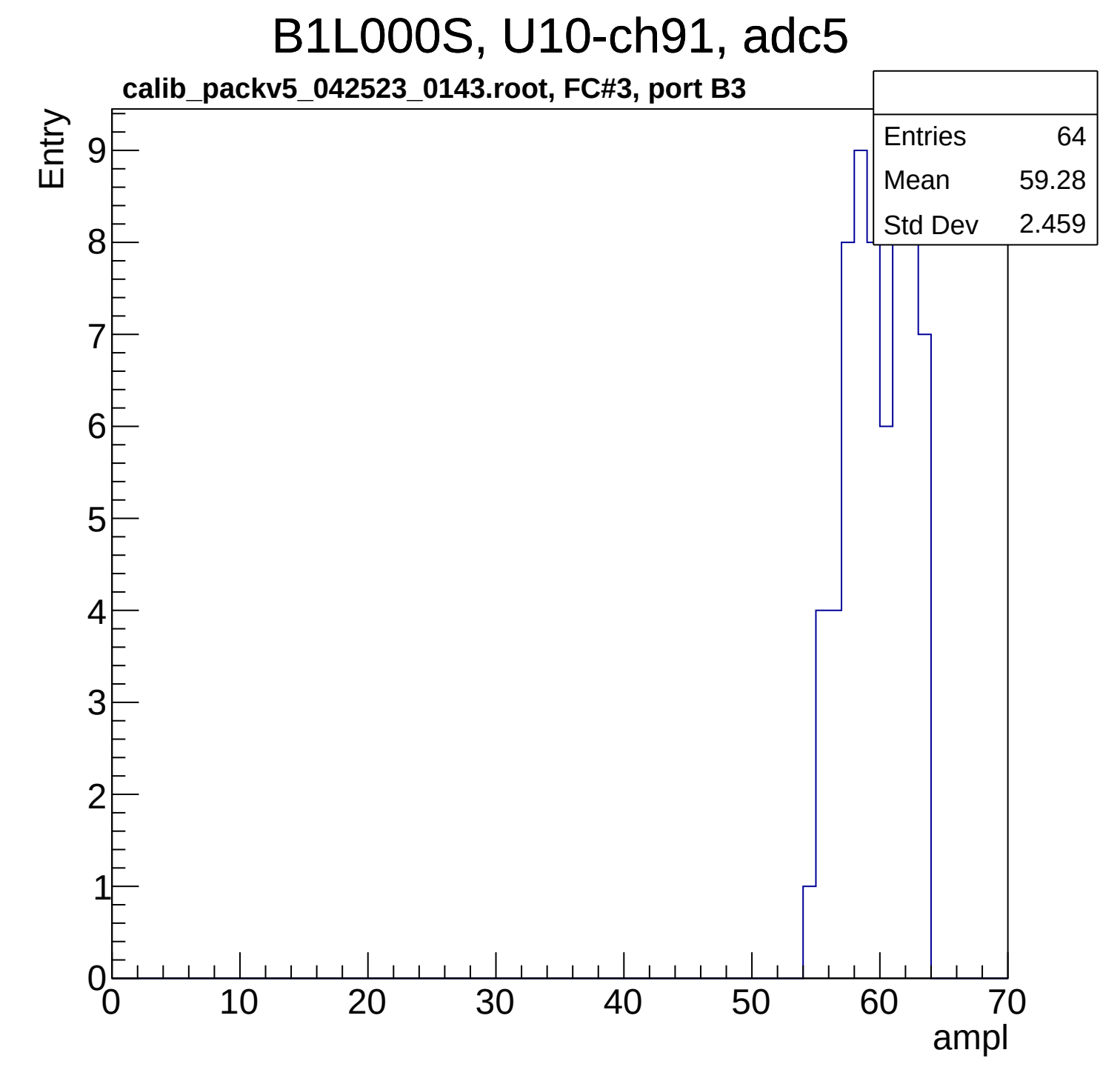
B1L000S, U10-ch91, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 59.28
Std Dev 2.459
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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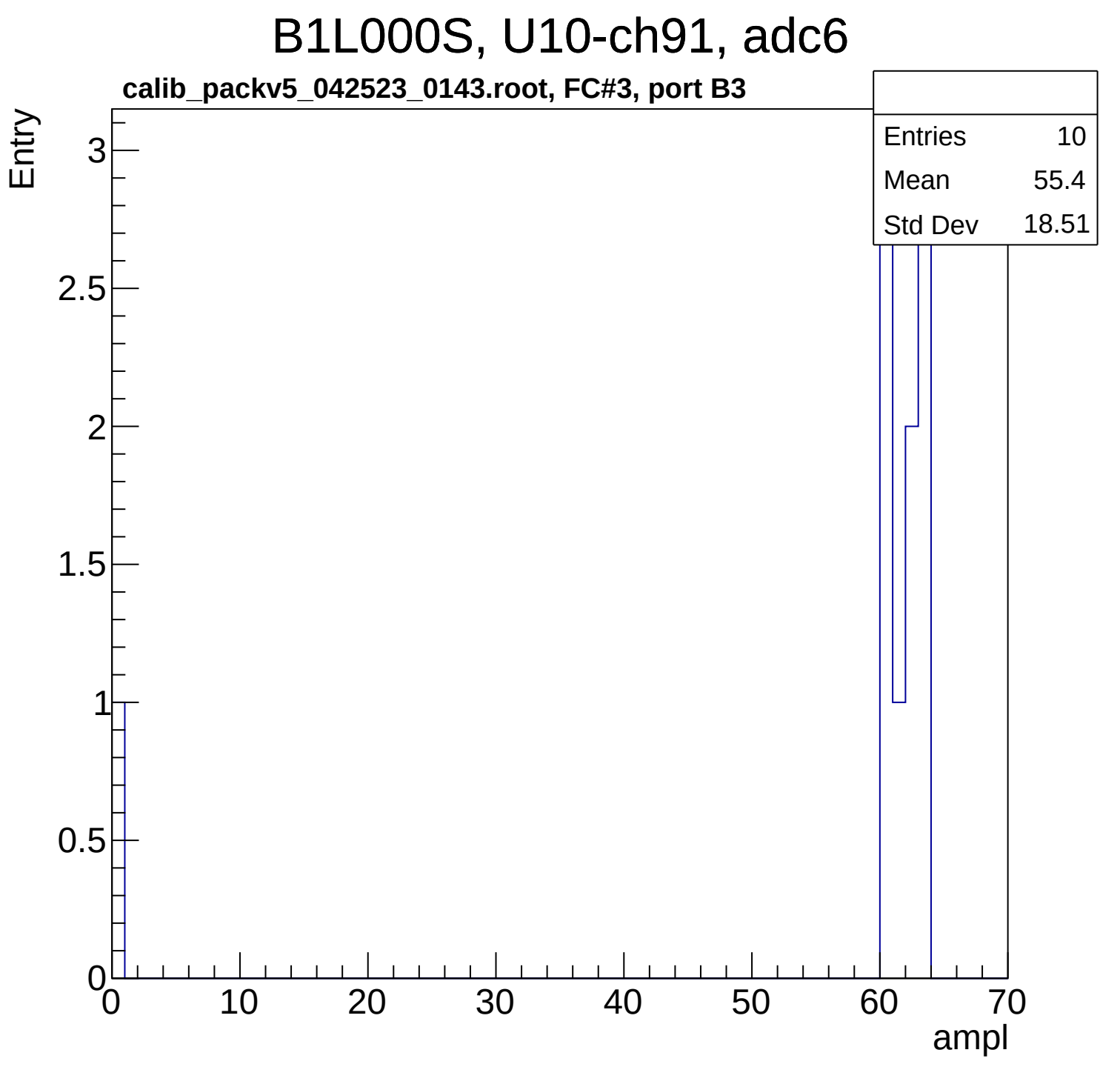
B1L000S, U10-ch91, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.4
Std Dev 18.51
Entry
ampl
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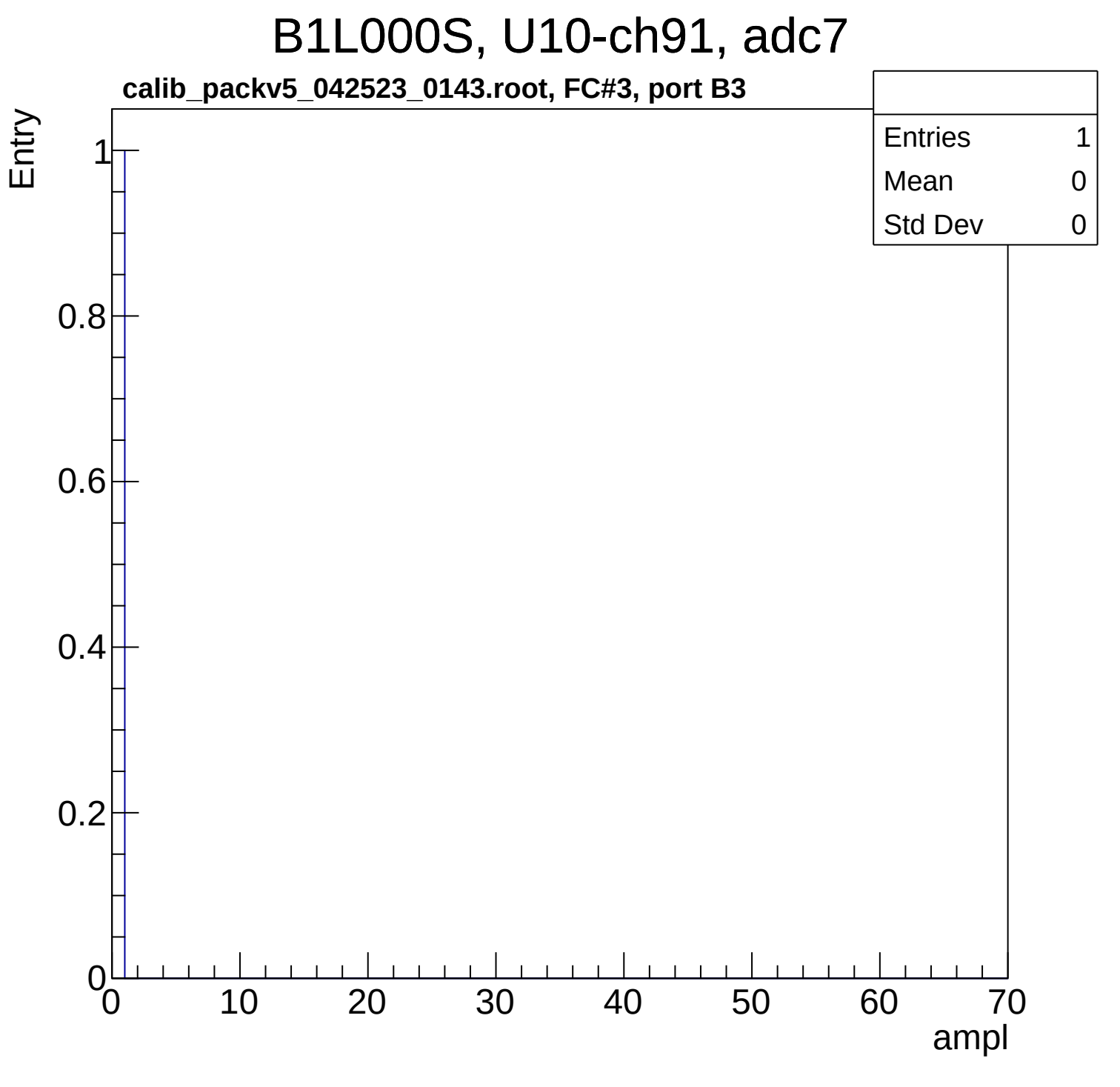
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch91, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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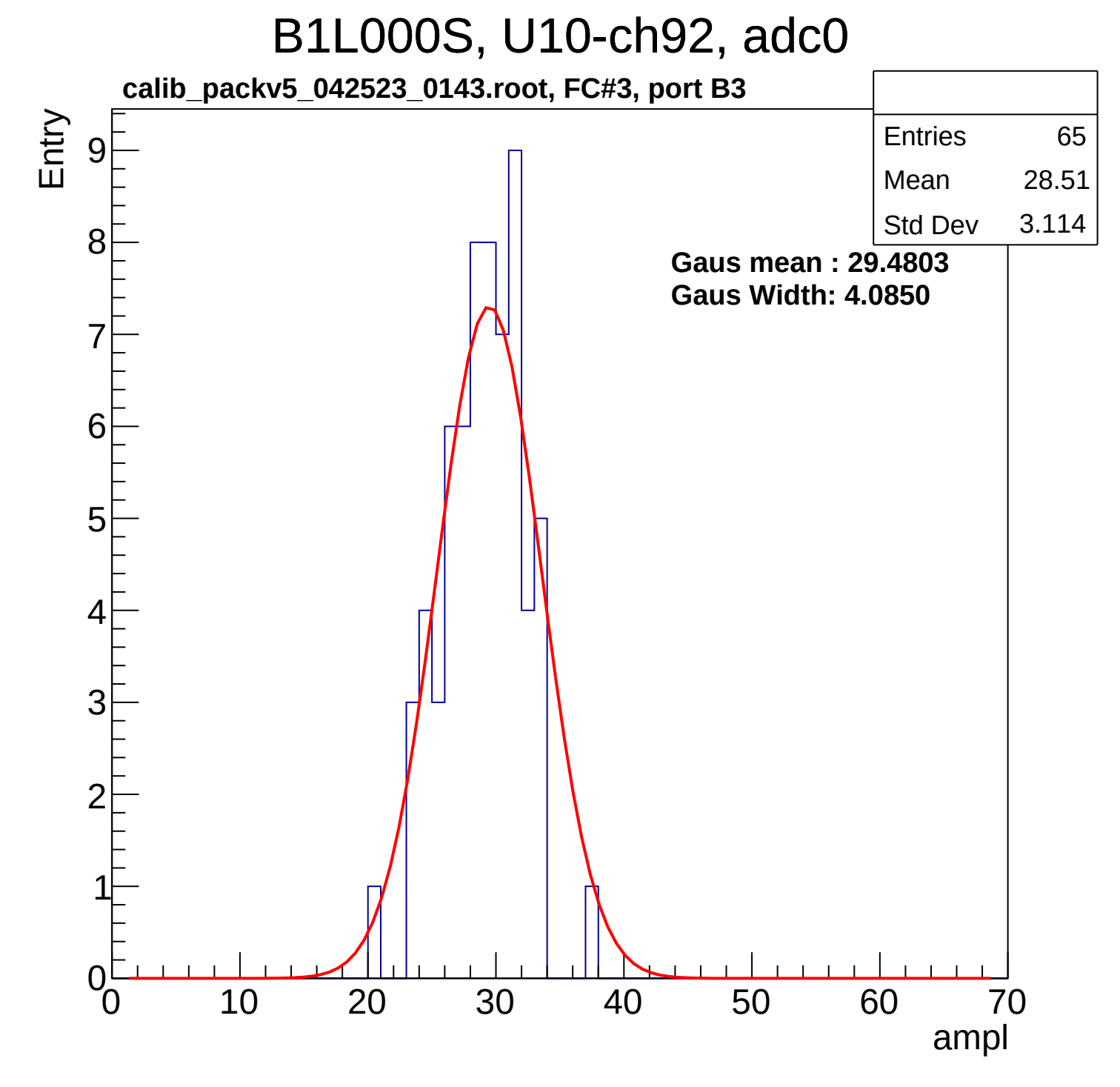
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch92, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.51
Std Dev 3.114
Gaus mean : 29.4803
Gaus Width: 4.0850
Entry
ampl
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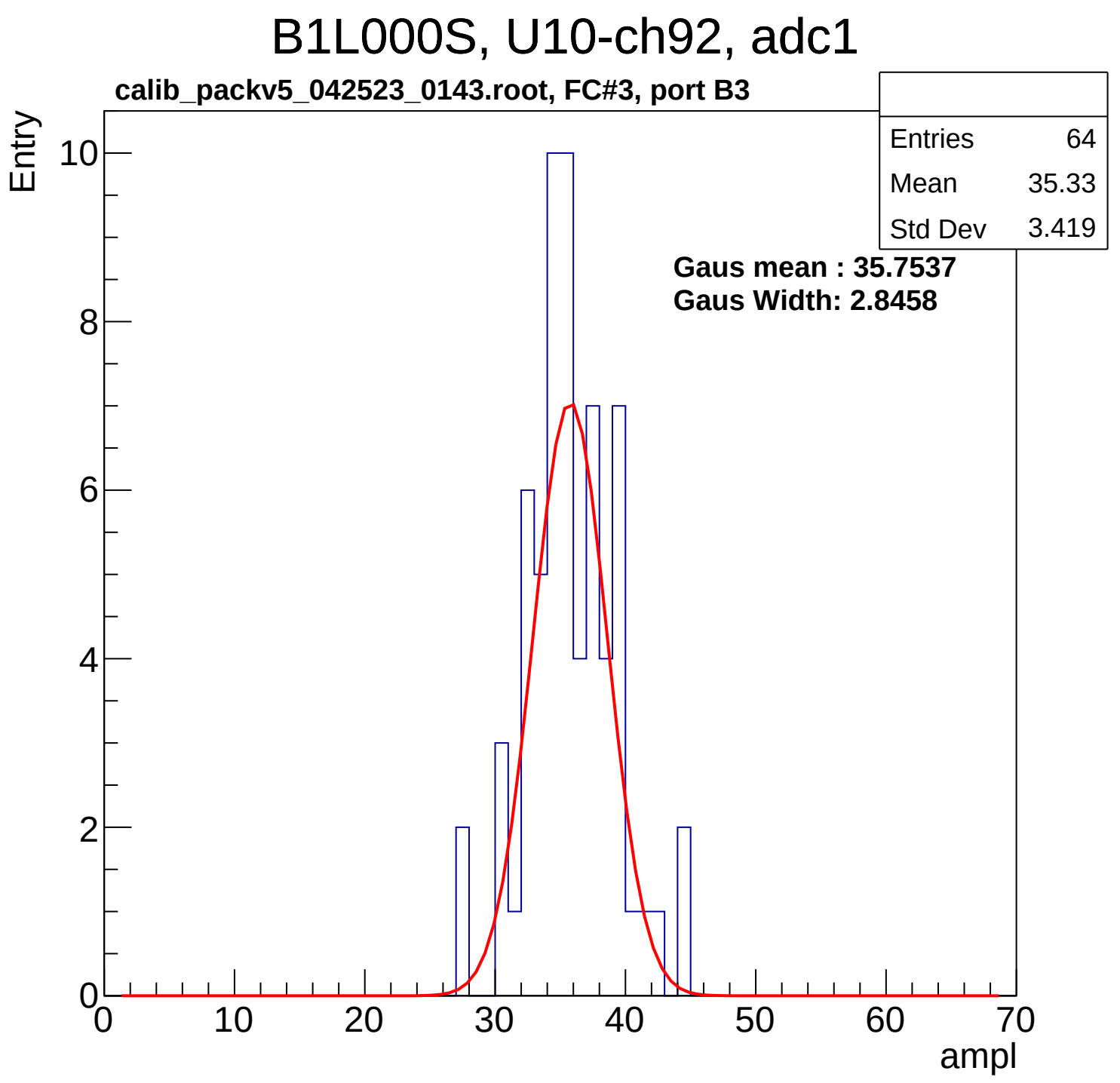
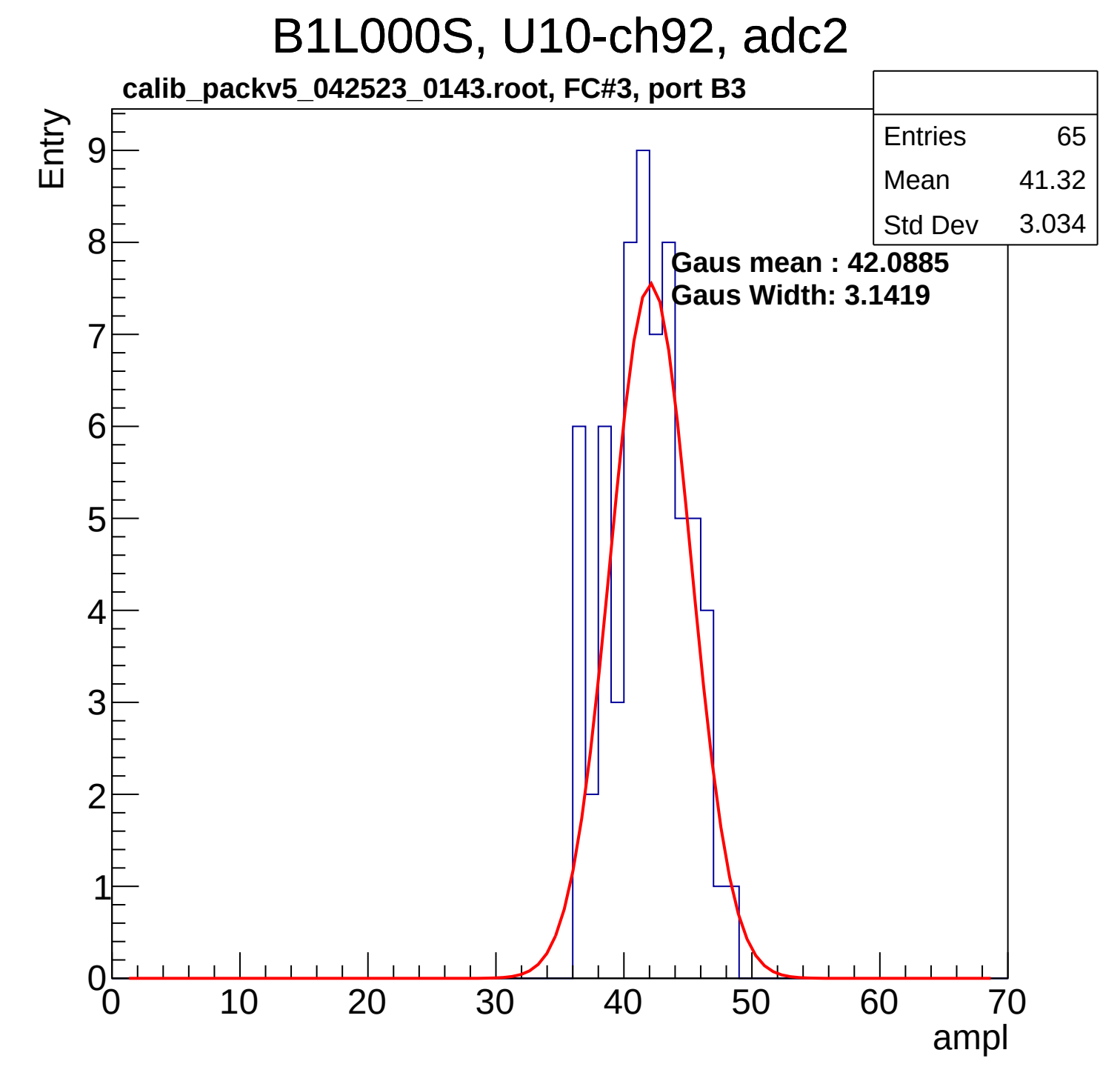
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch92, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.33
Std Dev 3.419
Gaus mean : 35.7537
Gaus Width: 2.8458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch92, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.32
Std Dev 3.034
Gaus mean : 42.0885
Gaus Width: 3.1419
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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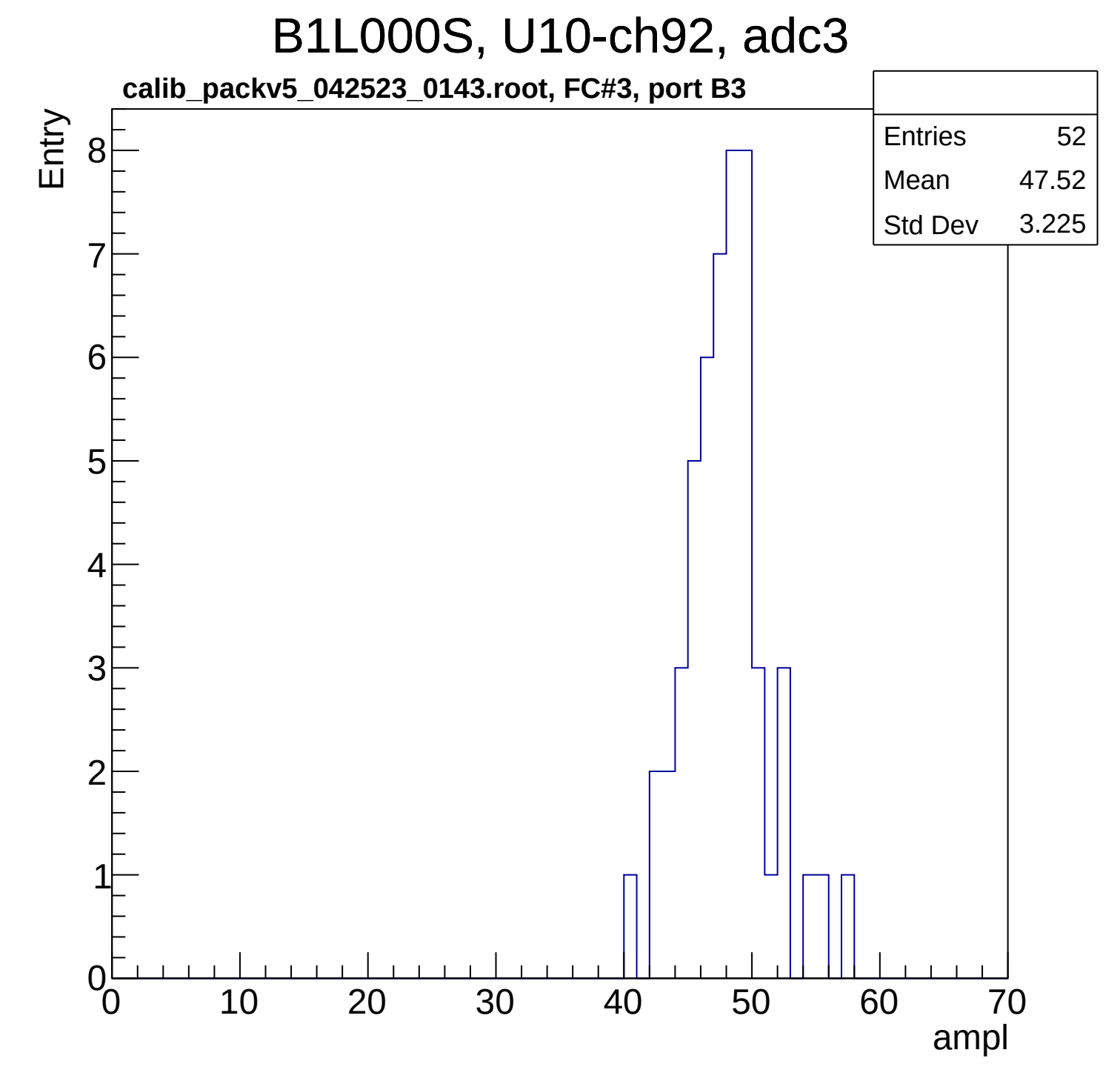
B1L000S, U10-ch92, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 47.52
Std Dev 3.225
Entry
ampl
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4
5
6
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0
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60
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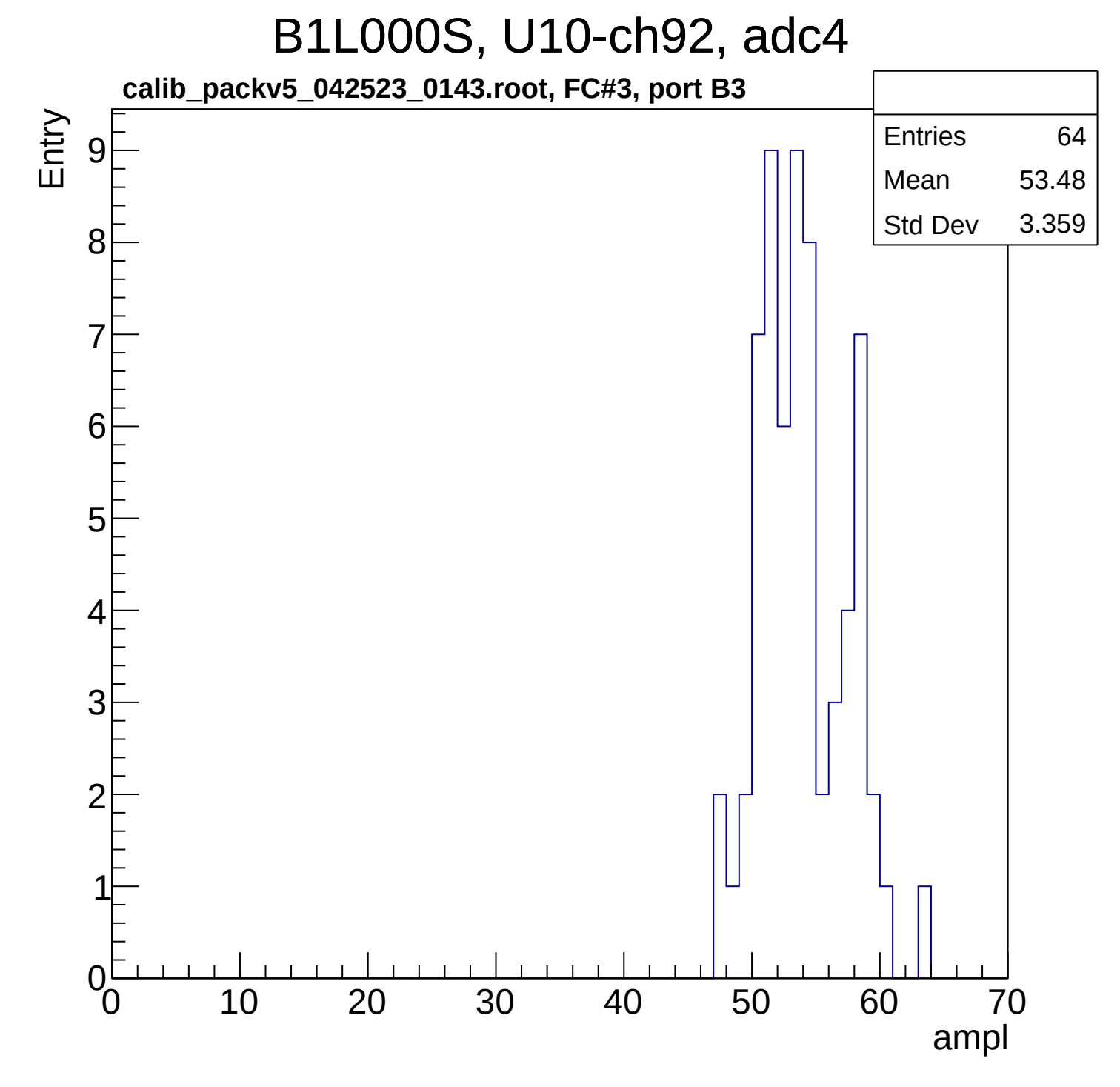
B1L000S, U10-ch92, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 53.48
Std Dev 3.359
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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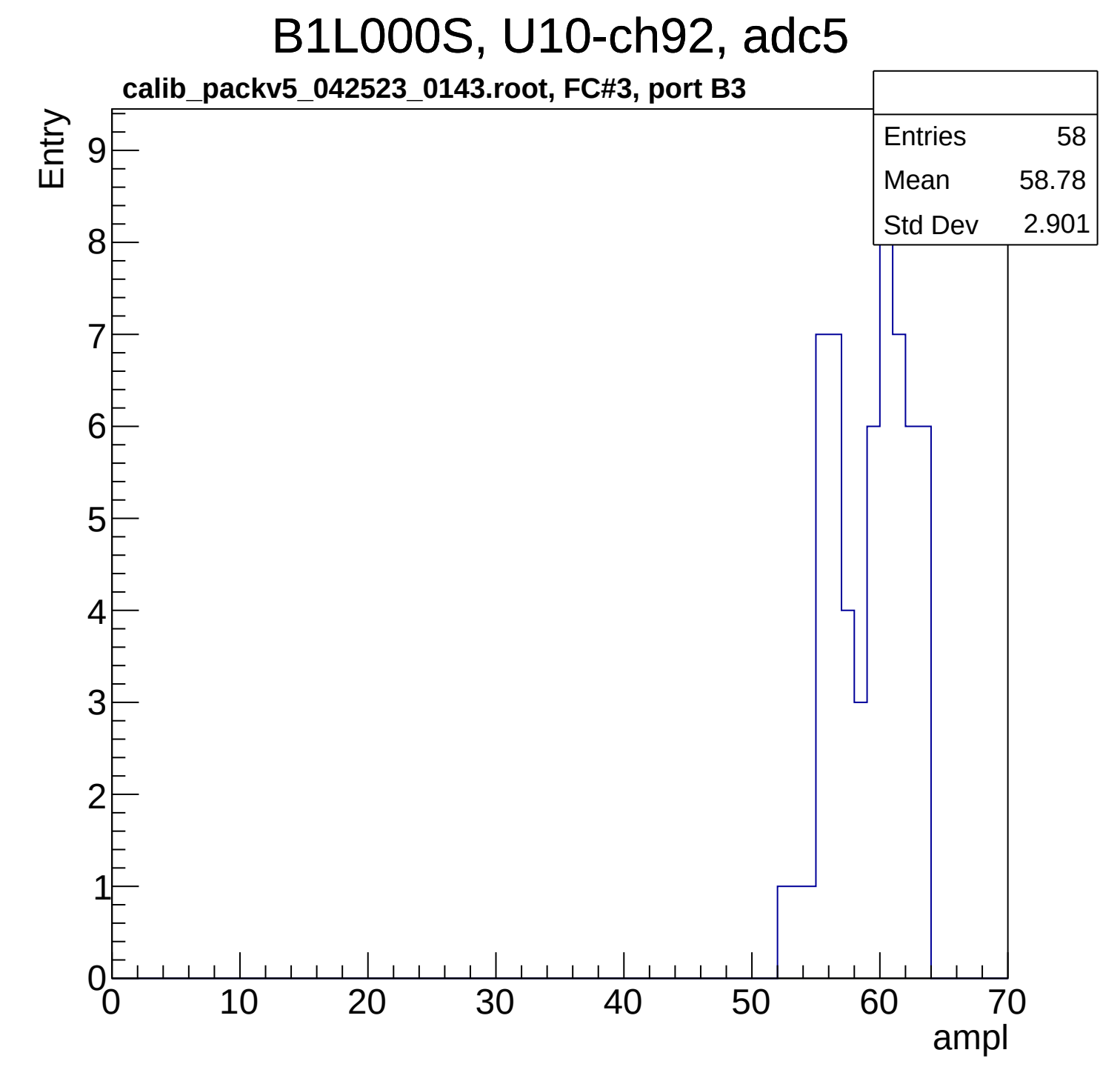
B1L000S, U10-ch92, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.78
Std Dev 2.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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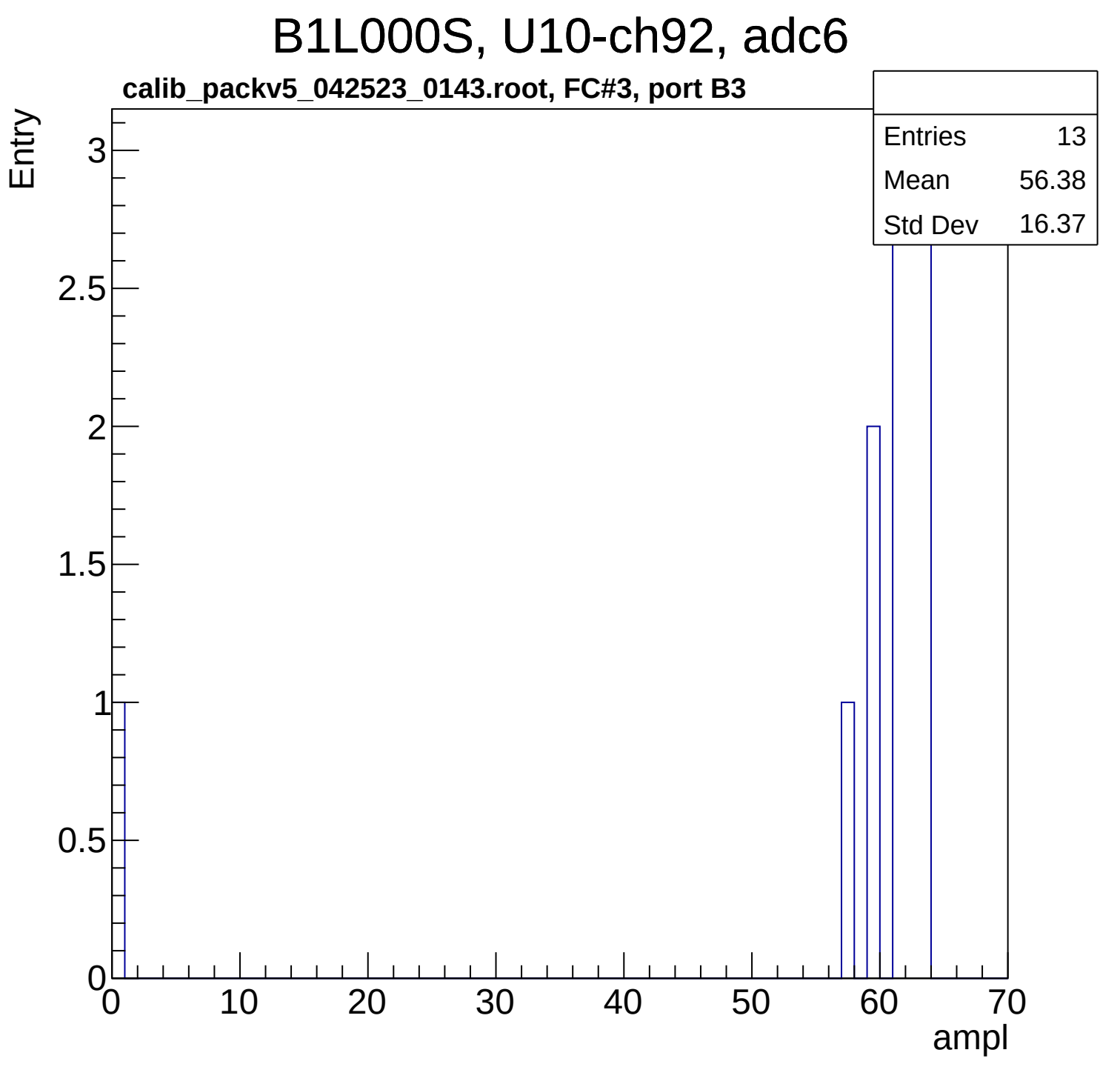
B1L000S, U10-ch92, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.38
Std Dev 16.37
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
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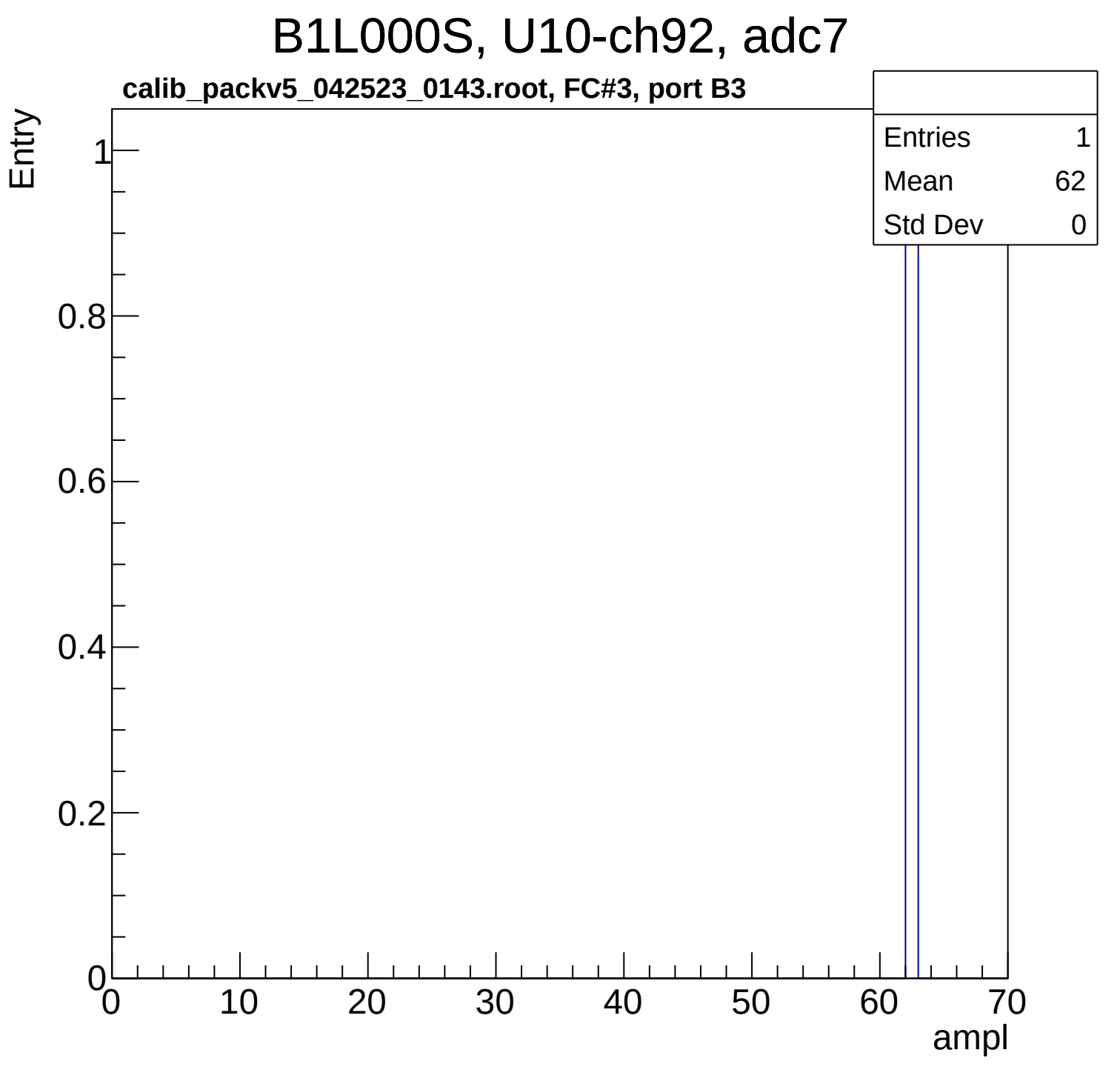
40
50
60
70


B1L000S, U10-ch92, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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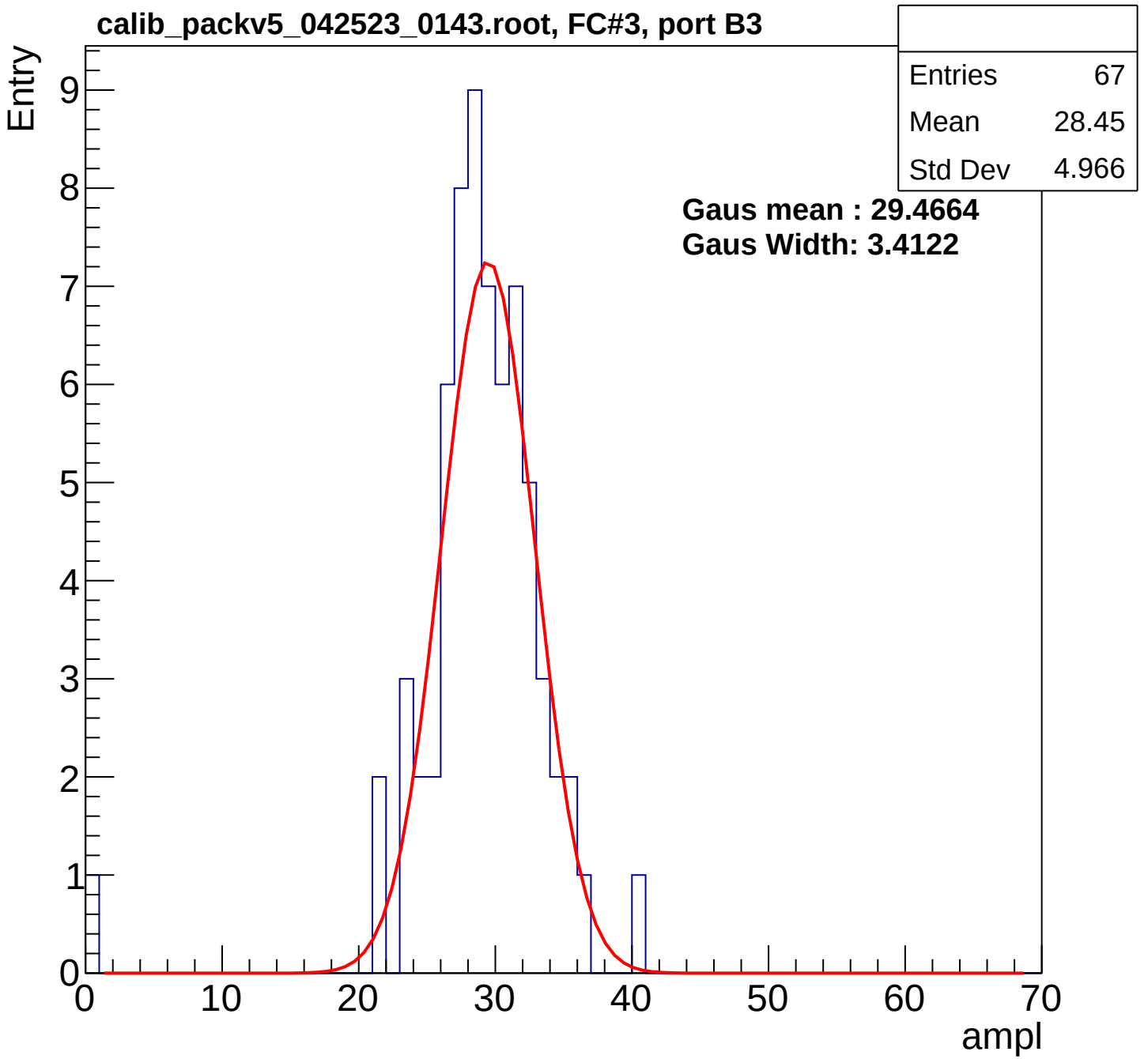
50
60
70
ampl


B1L000S, U10-ch93, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 28.45
Std Dev 4.966
Gaus mean : 29.4664
Gaus Width: 3.4122
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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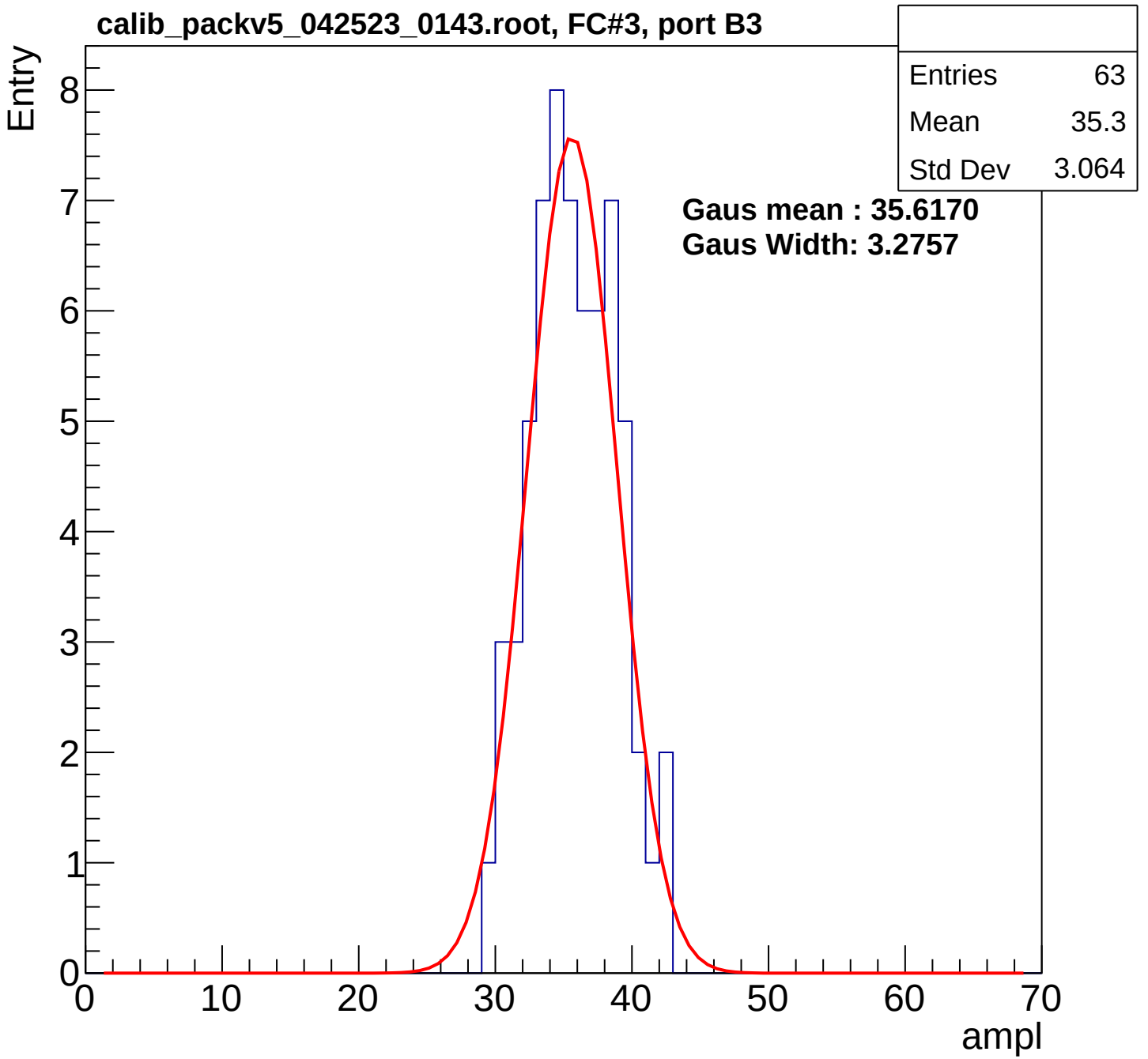
B1L000S, U10-ch93, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.3
Std Dev 3.064
Gaus mean : 35.6170
Gaus Width: 3.2757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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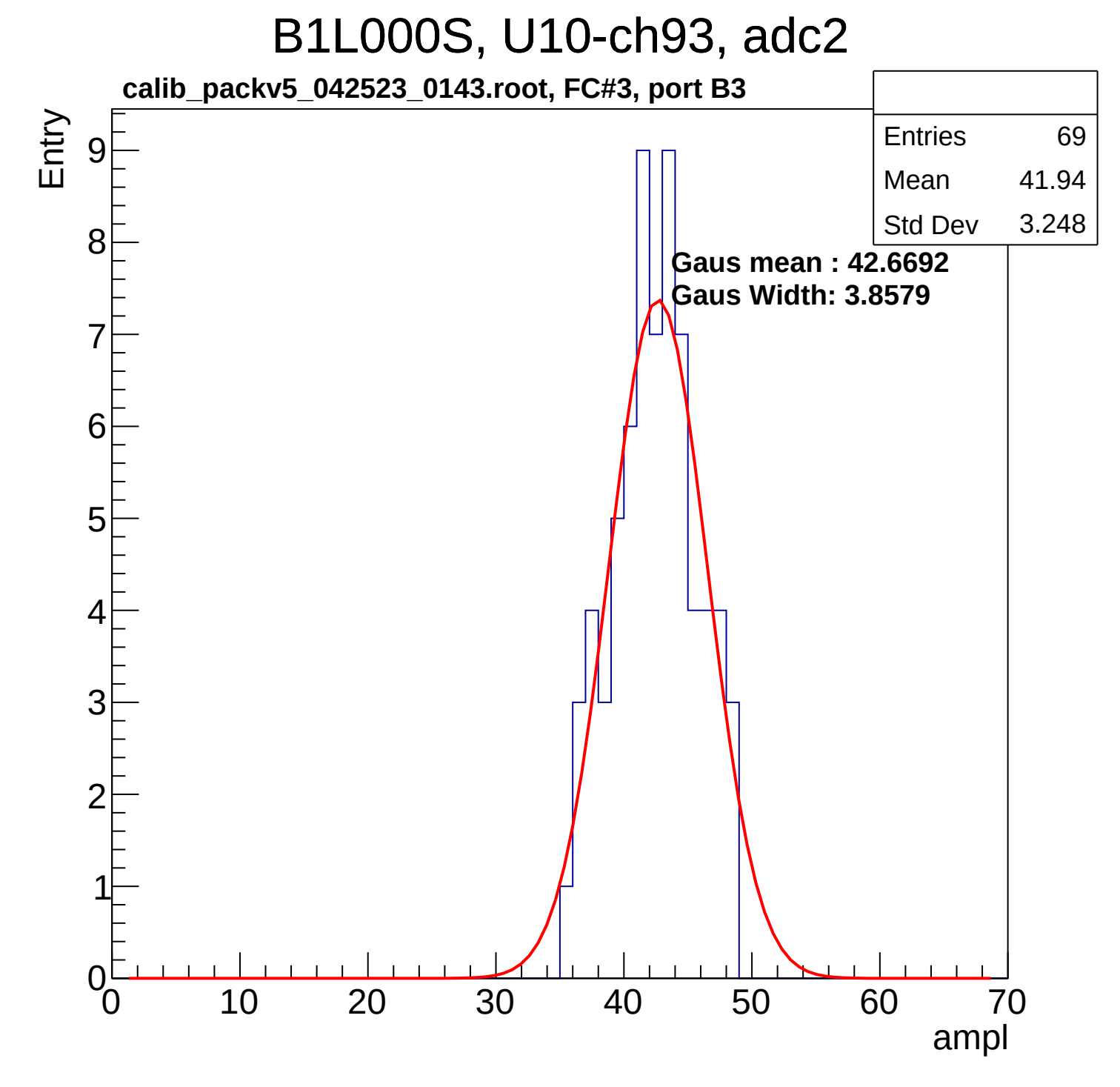
B1L000S, U10-ch93, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.94
Std Dev 3.248
Gaus mean : 42.6692
Gaus Width: 3.8579
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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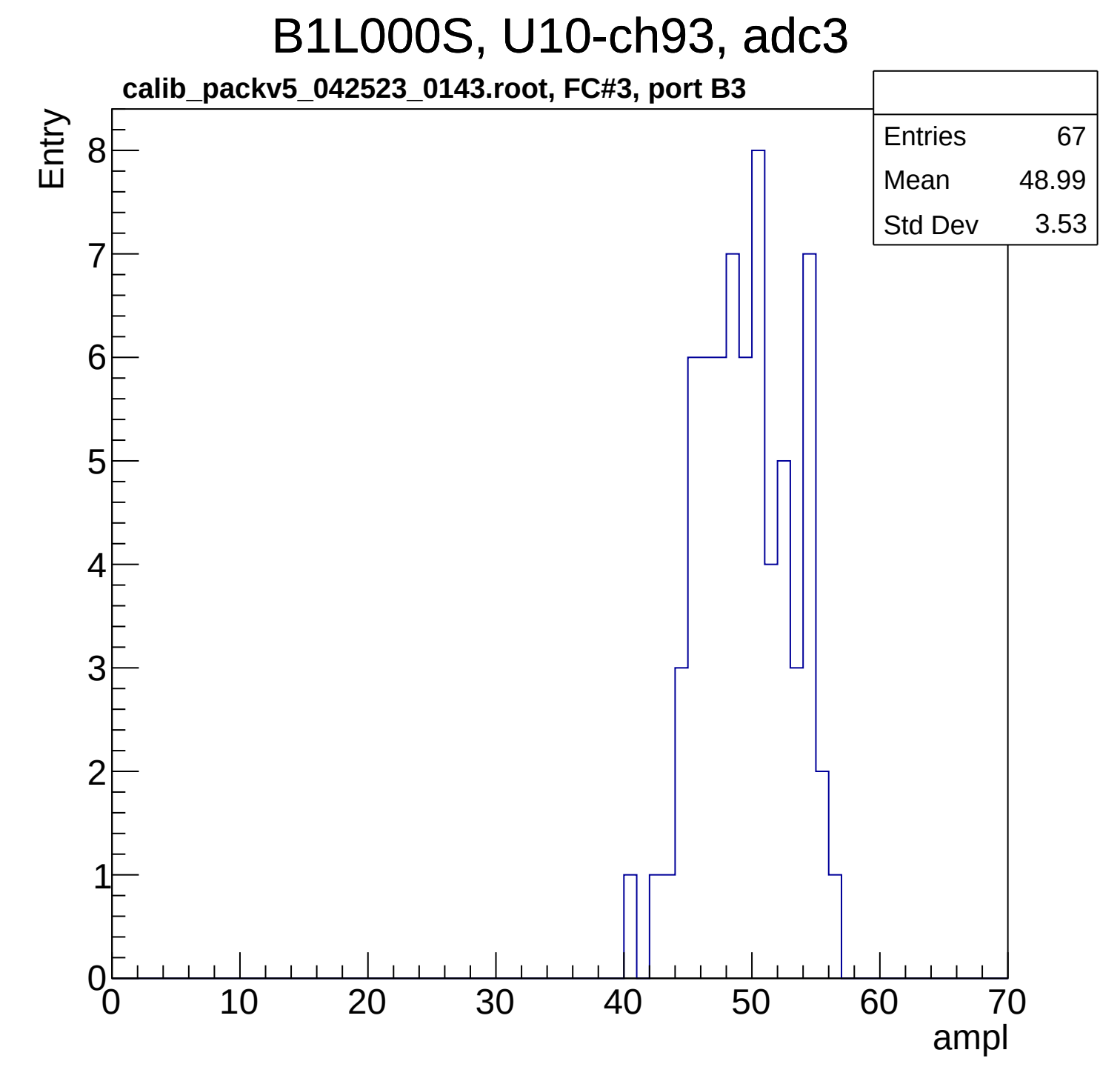
B1L000S, U10-ch93, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 48.99
Std Dev 3.53
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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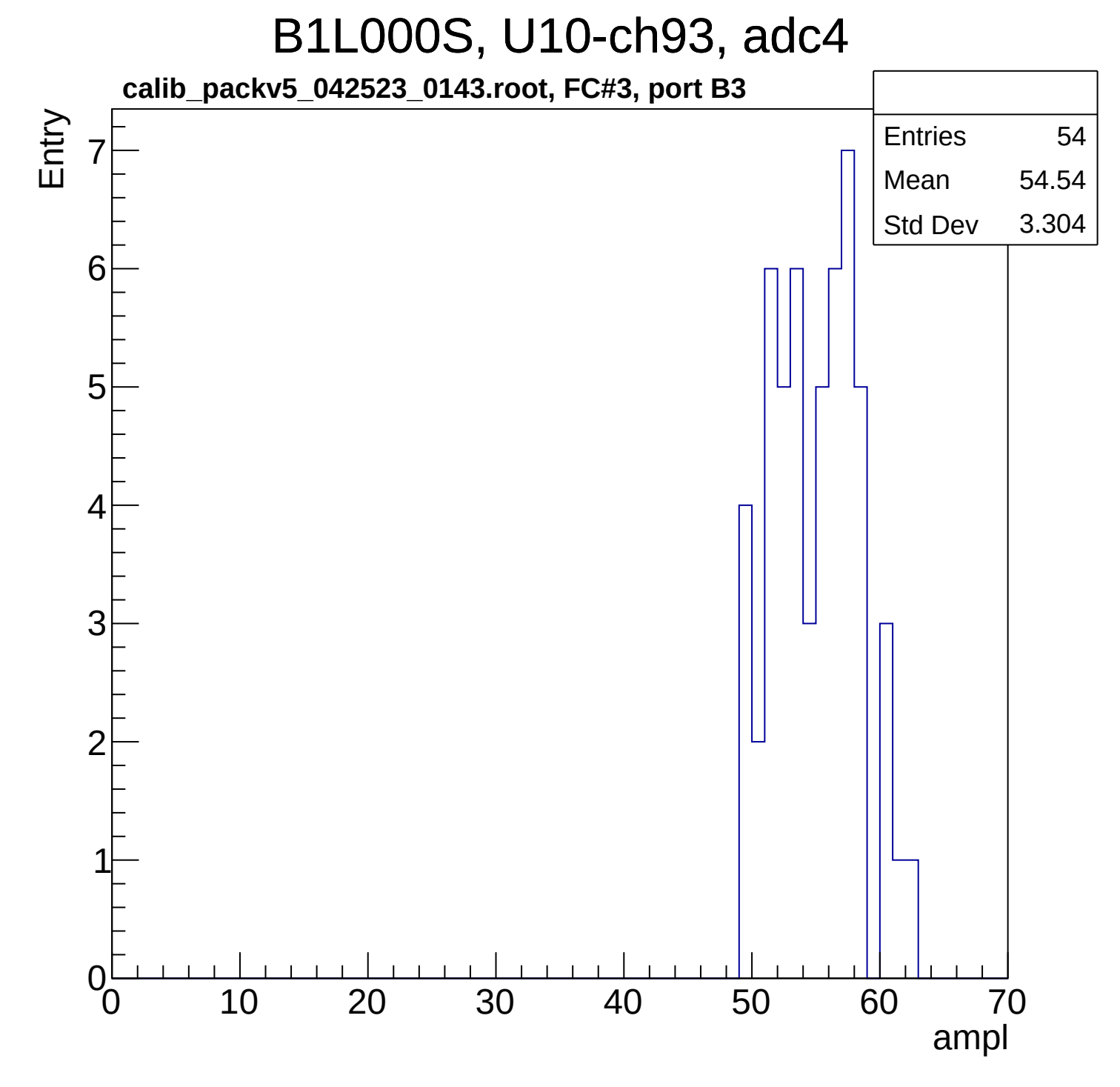
B1L000S, U10-ch93, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.54
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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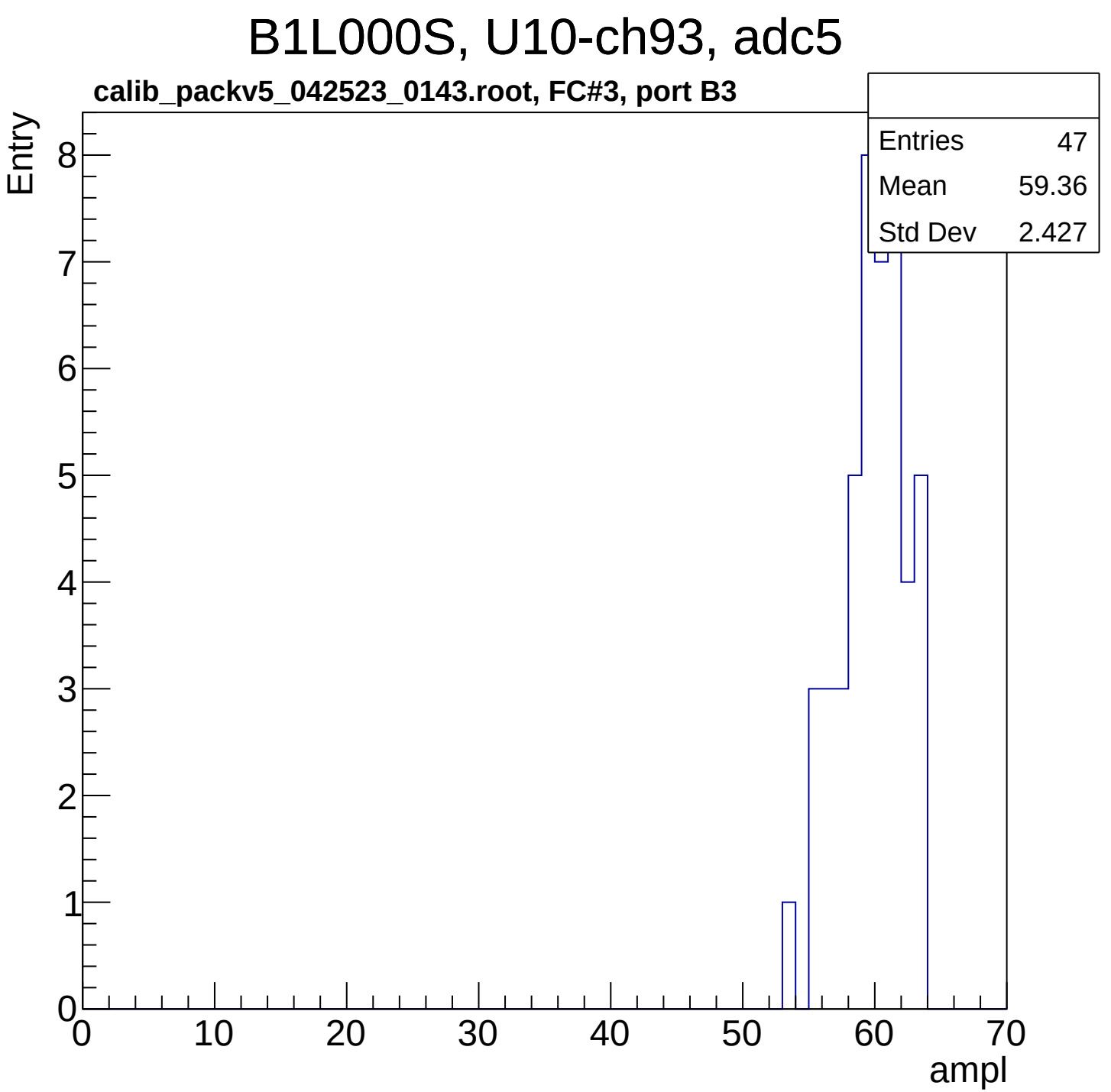
B1L000S, U10-ch93, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.36
Std Dev 2.427
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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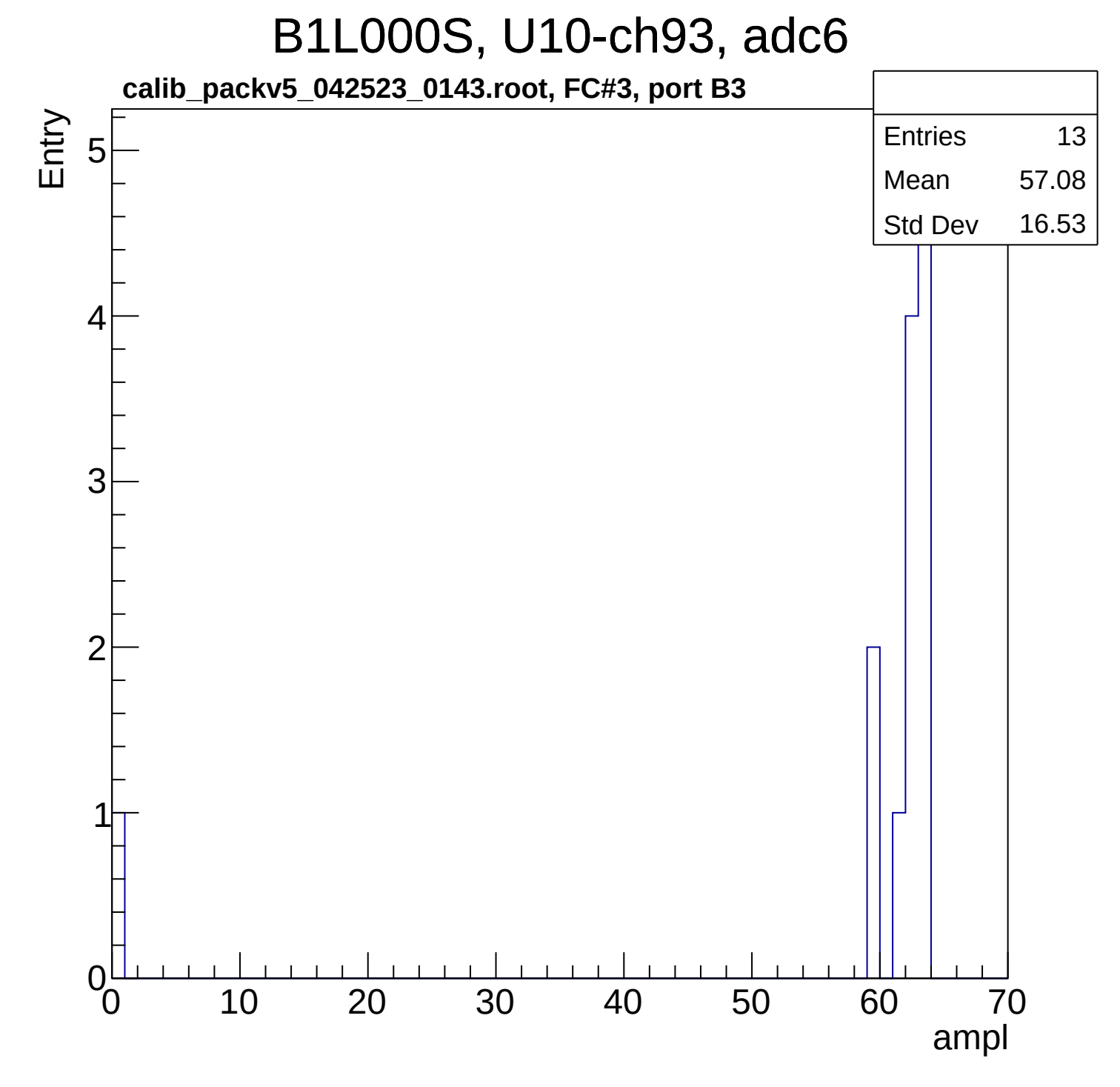
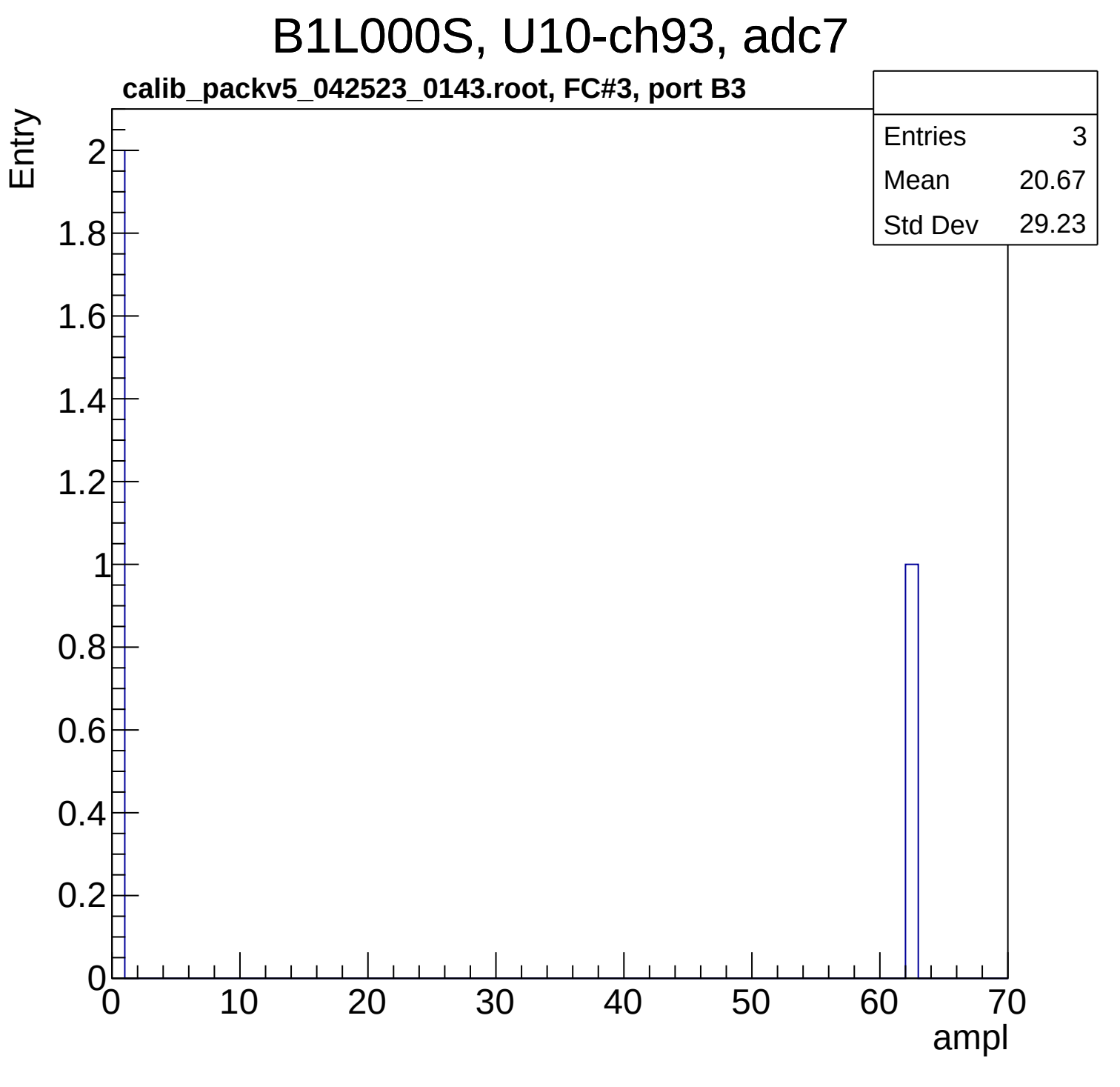

B1L000S, U10-ch93, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.08
Std Dev 16.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch93, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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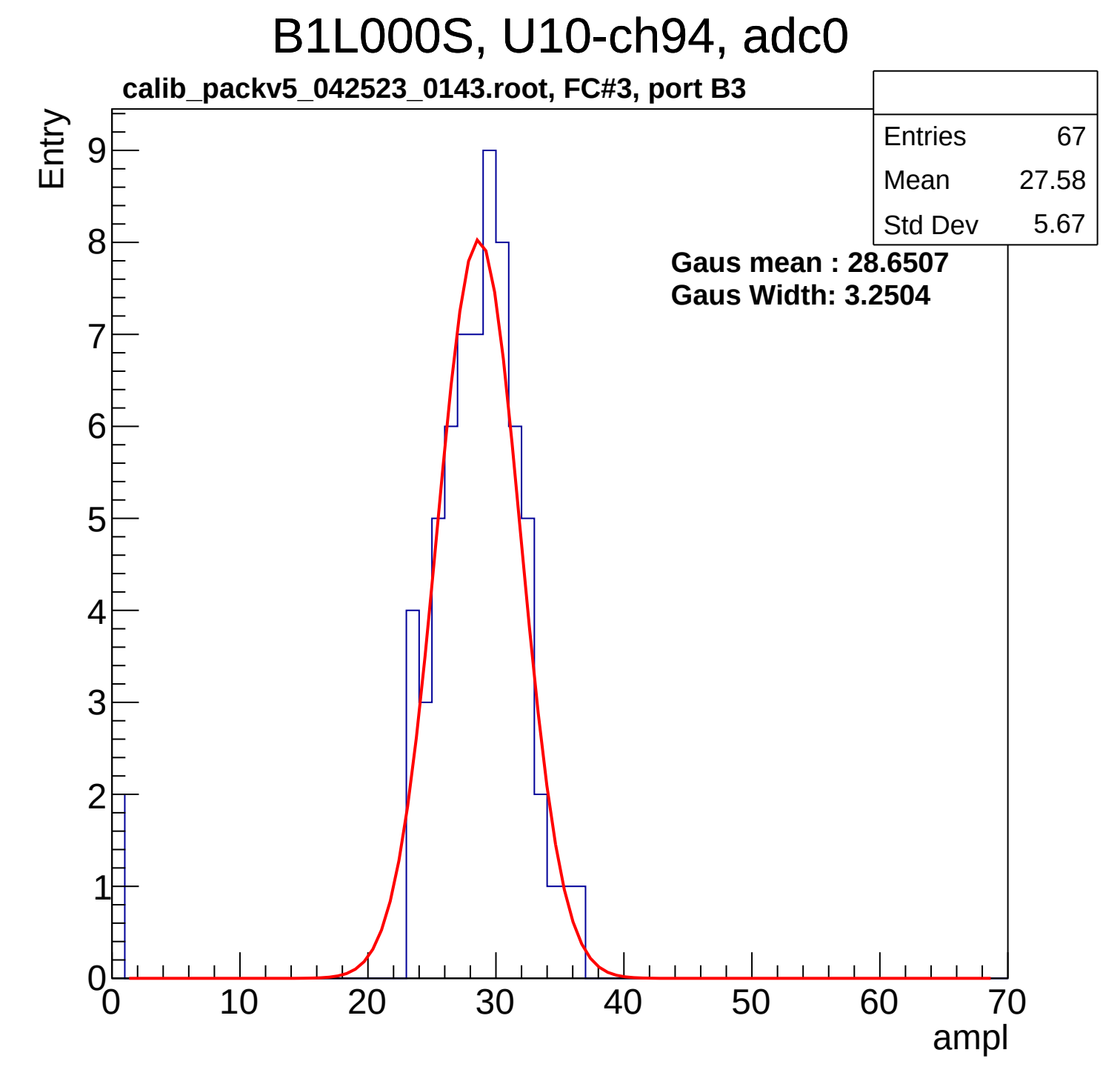
B1L000S, U10-ch94, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.58
Std Dev 5.67
Gaus mean : 28.6507
Gaus Width: 3.2504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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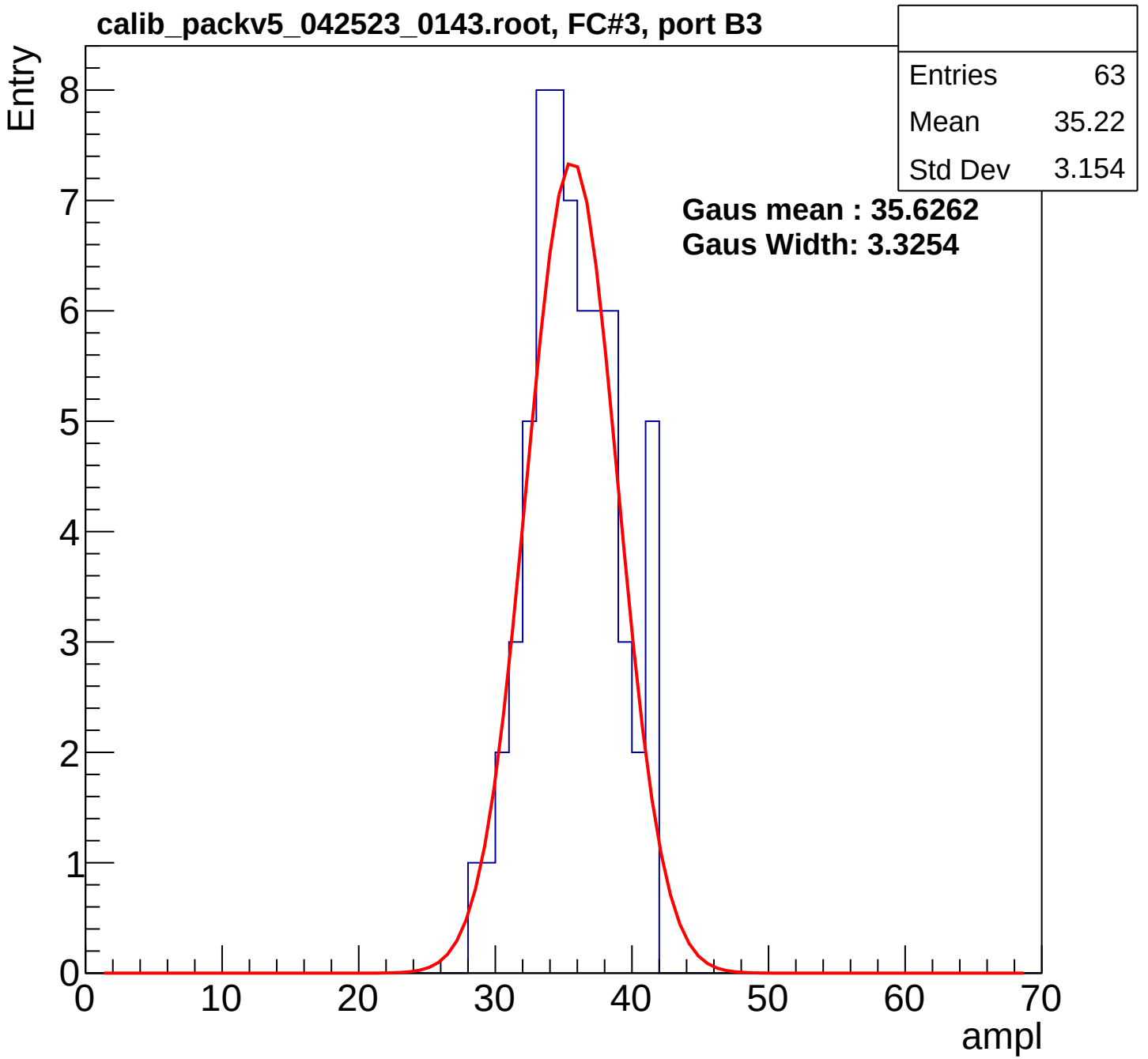
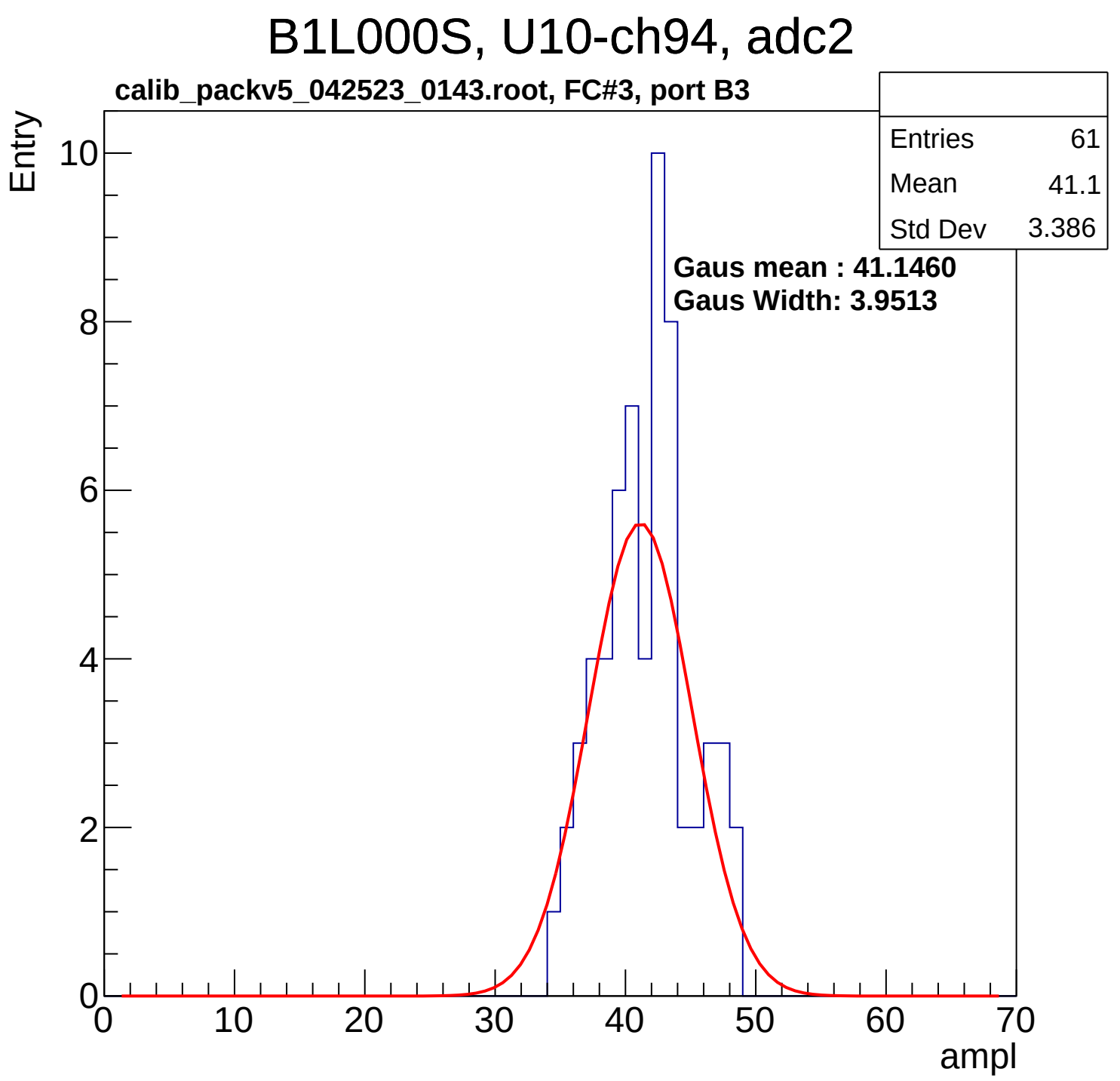
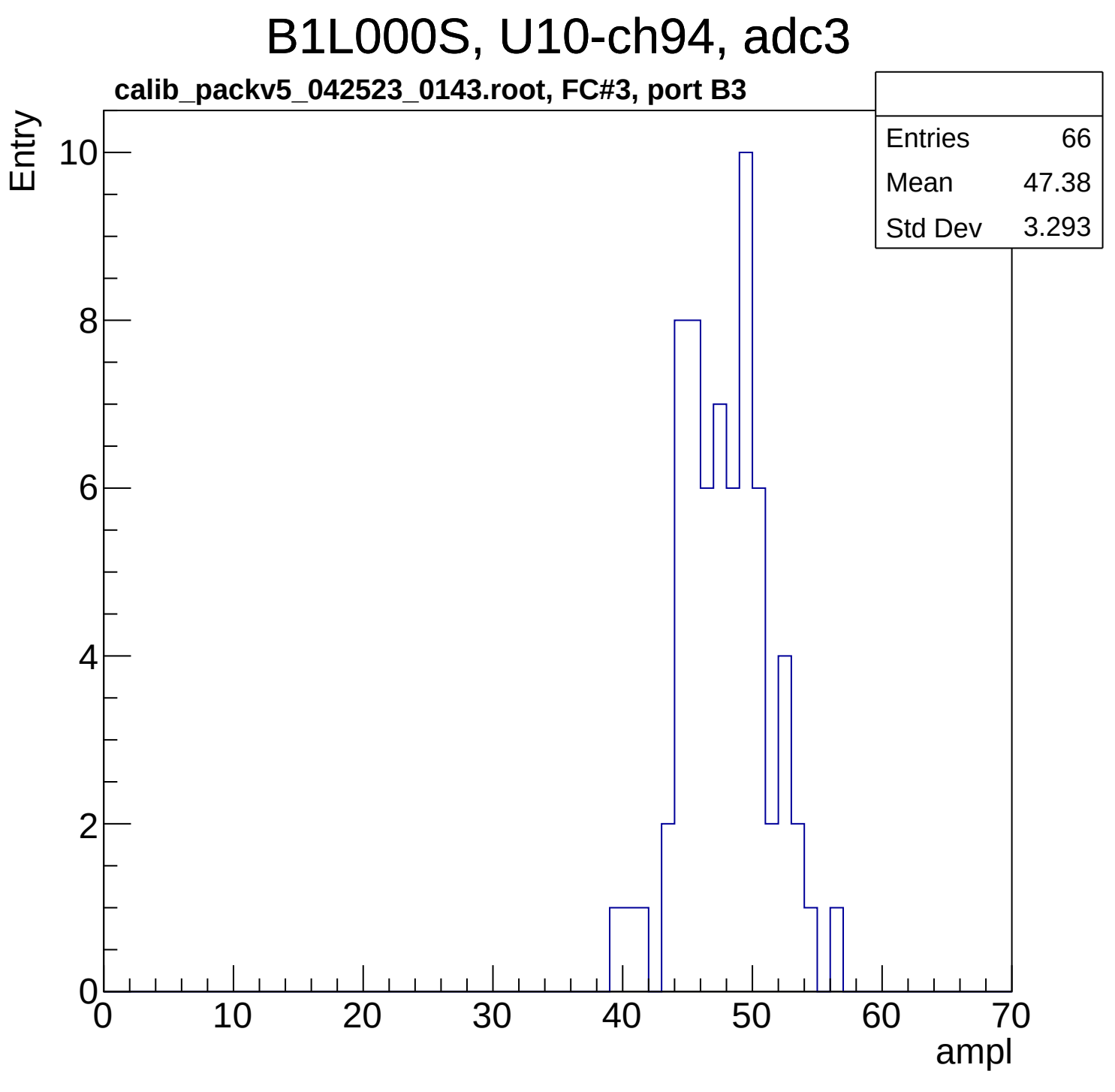
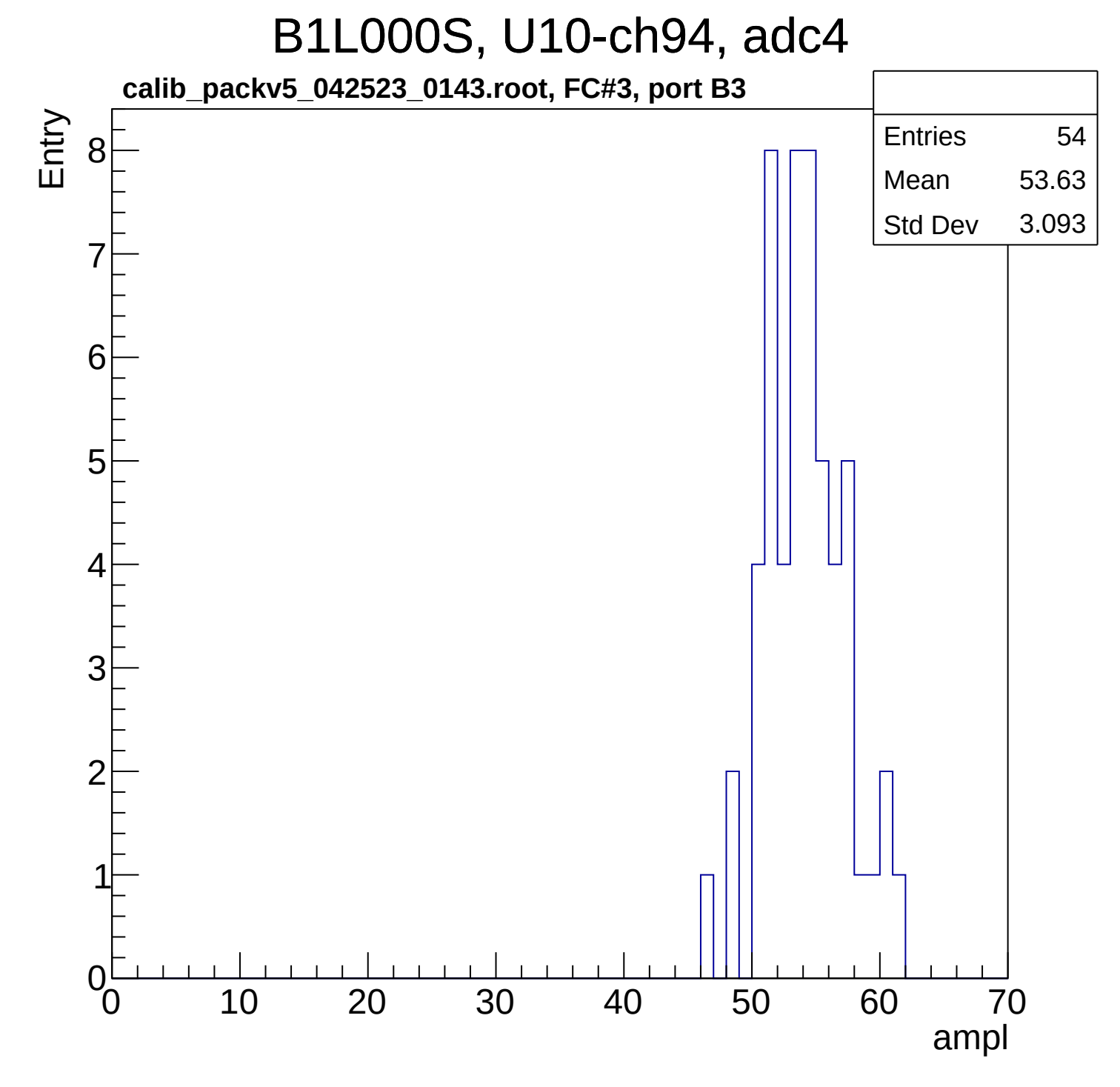

B1L000S, U10-ch94, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.22
Std Dev 3.154
Gaus mean : 35.6262
Gaus Width: 3.3254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch94, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.1
Std Dev 3.386
Gaus mean : 41.1460
Gaus Width: 3.9513
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch94, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.38
Std Dev 3.293
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch94, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.63
Std Dev 3.093
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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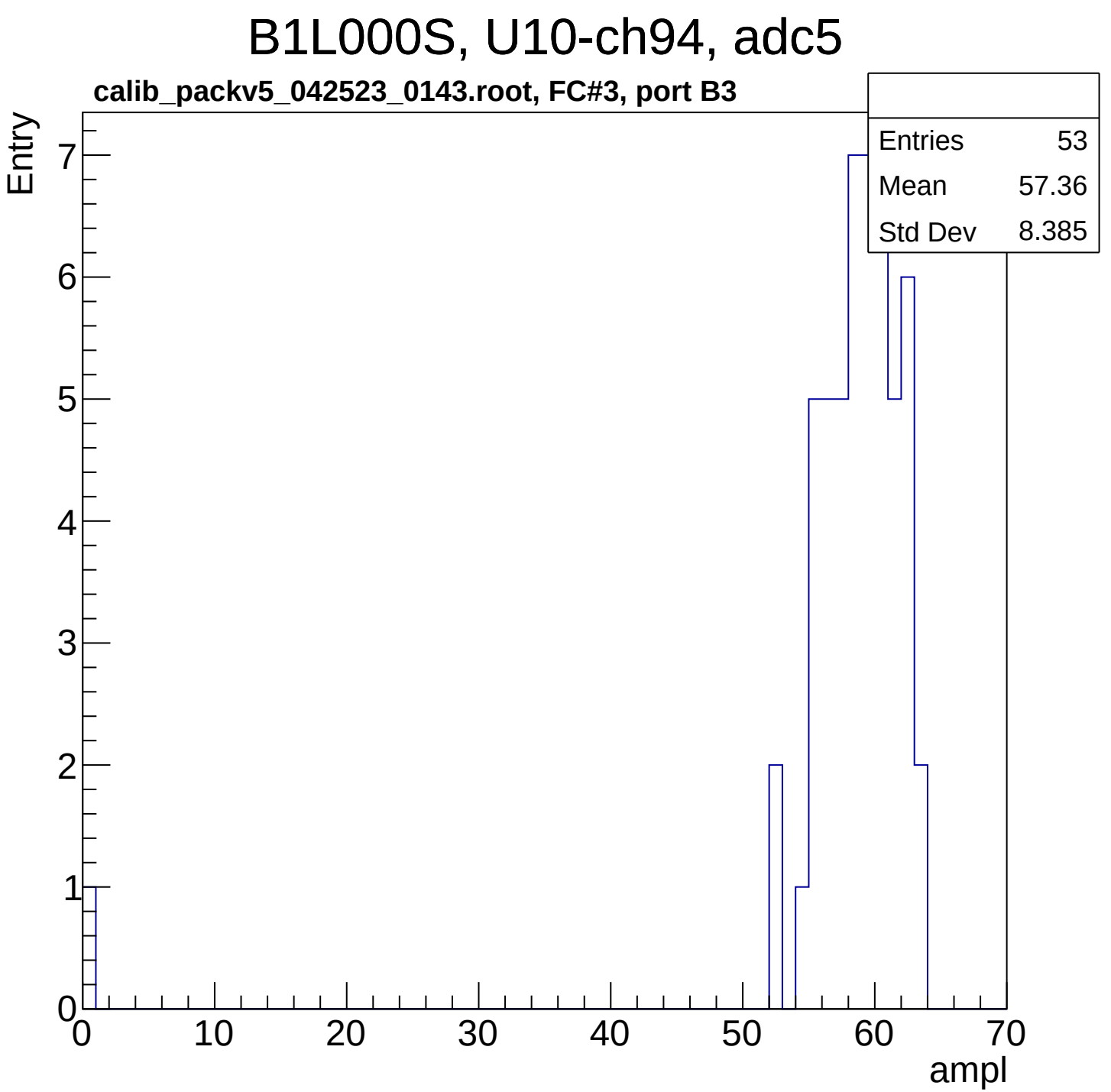
B1L000S, U10-ch94, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 57.36
Std Dev 8.385
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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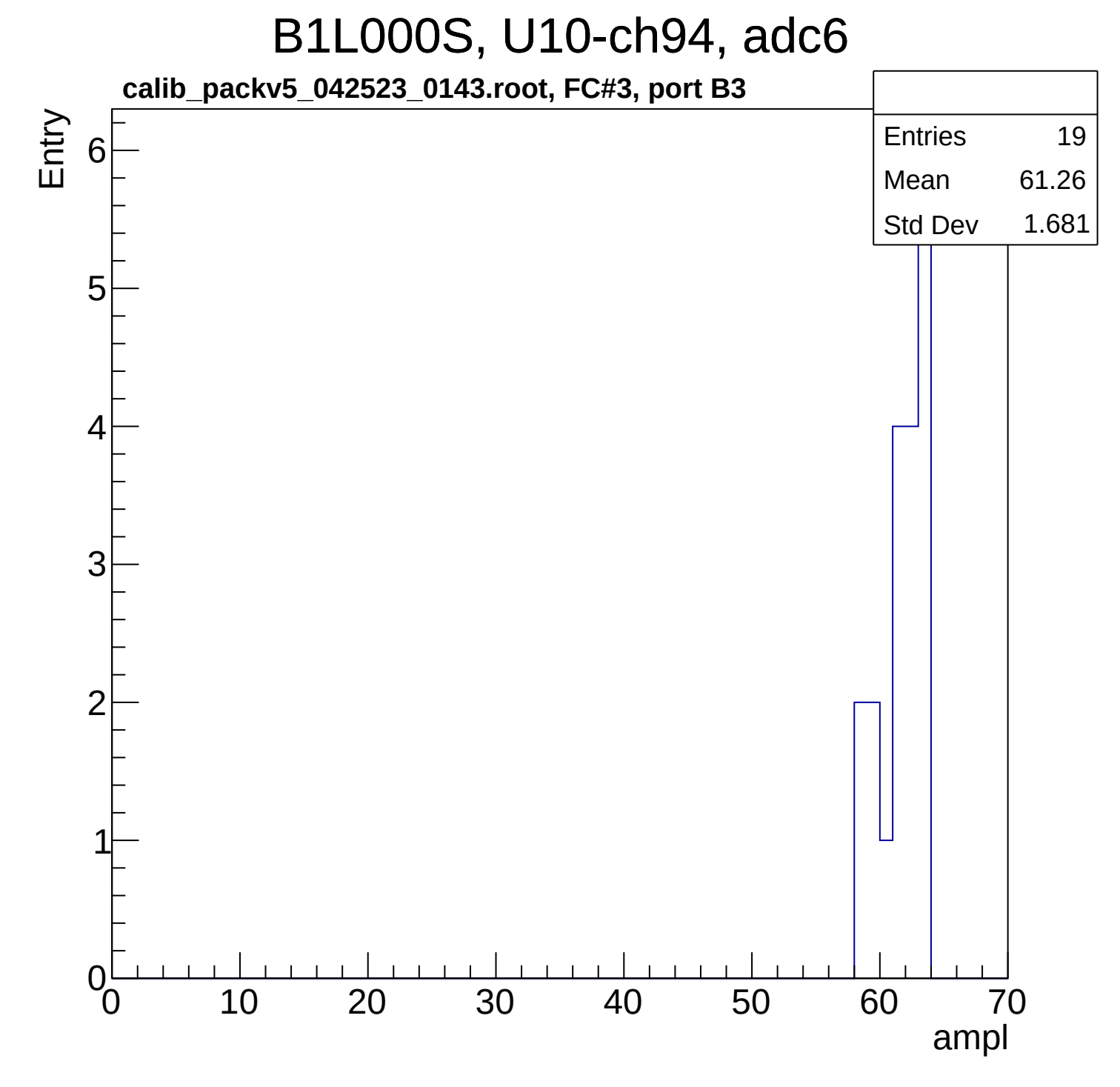
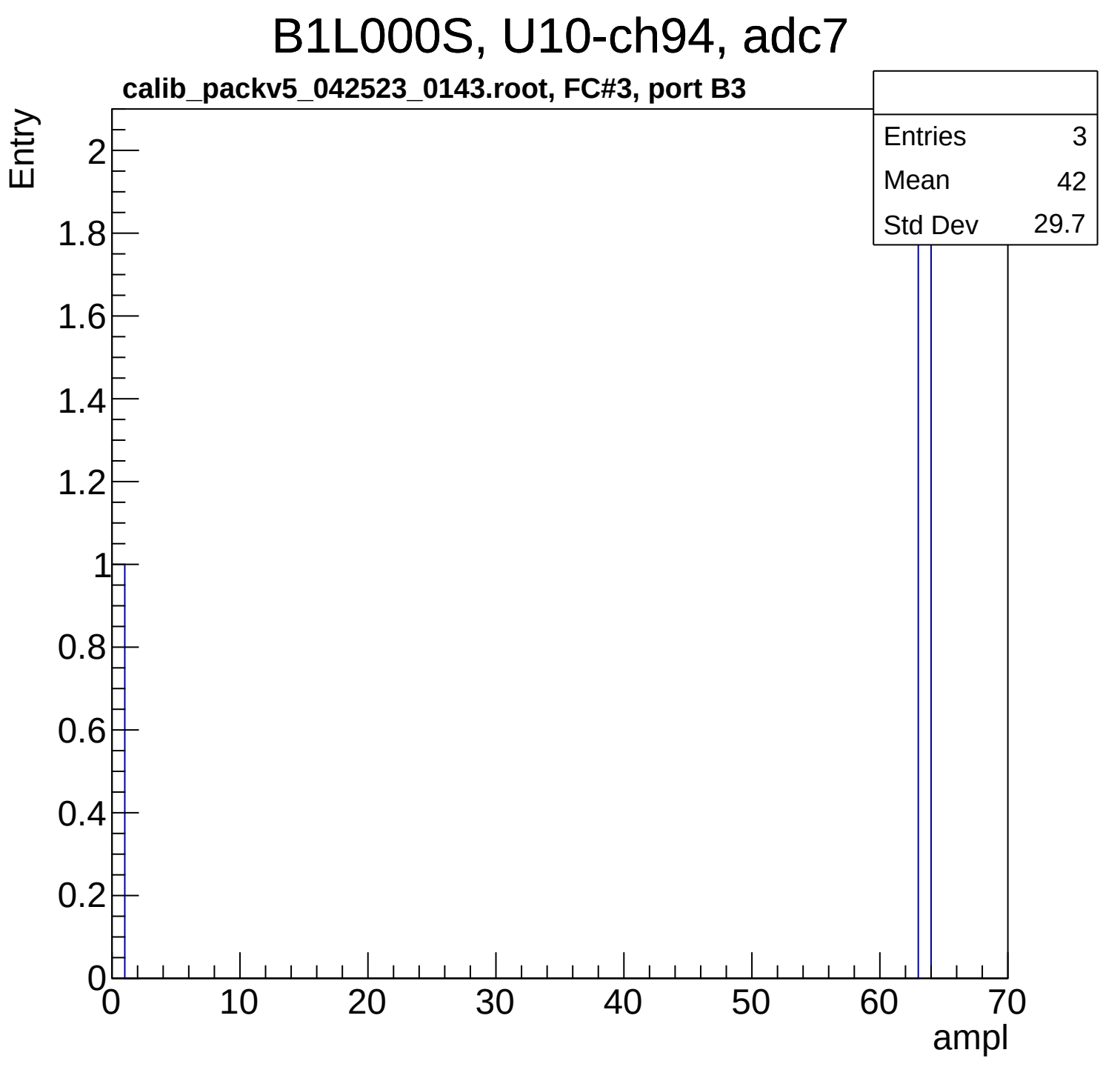
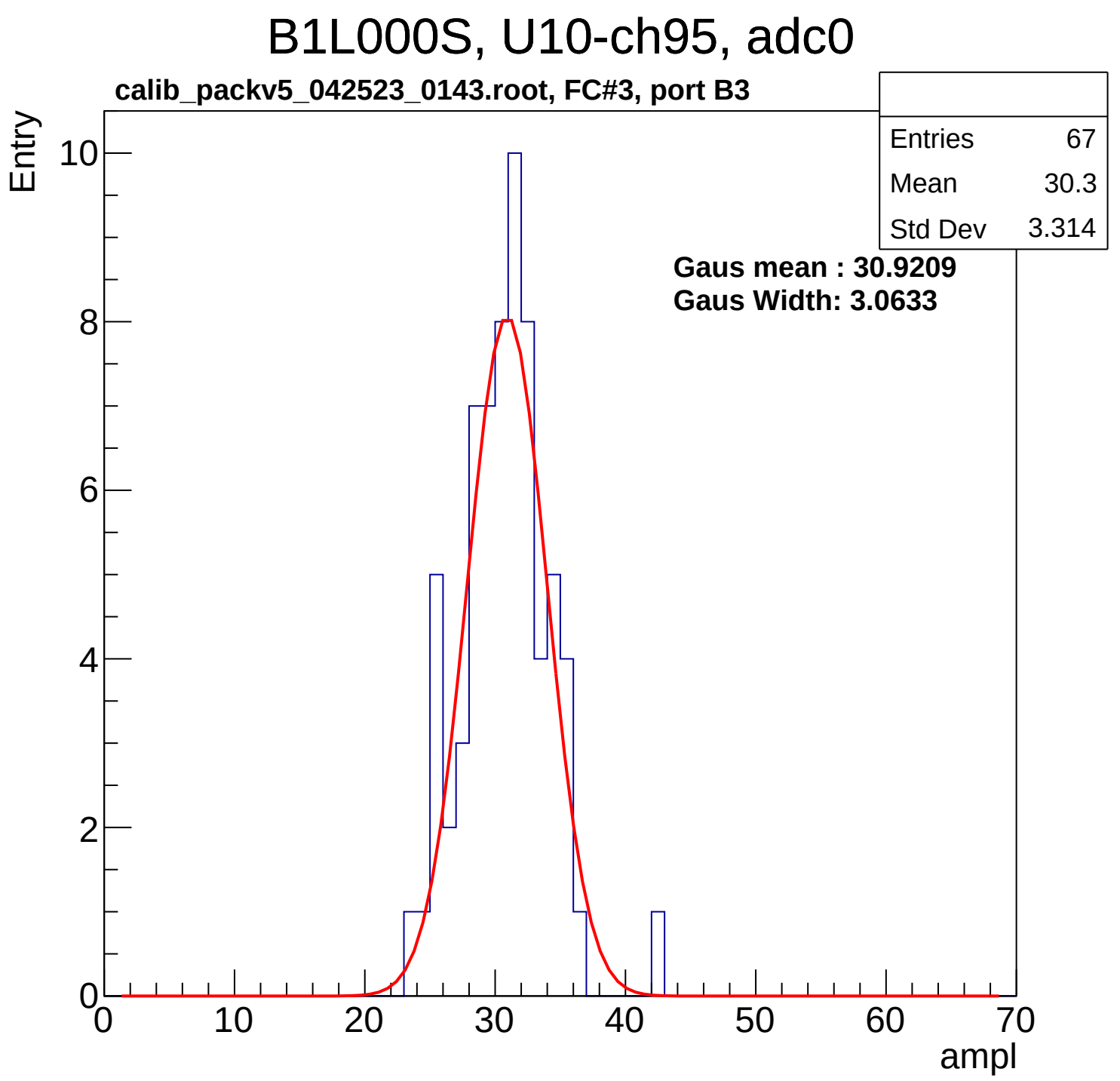
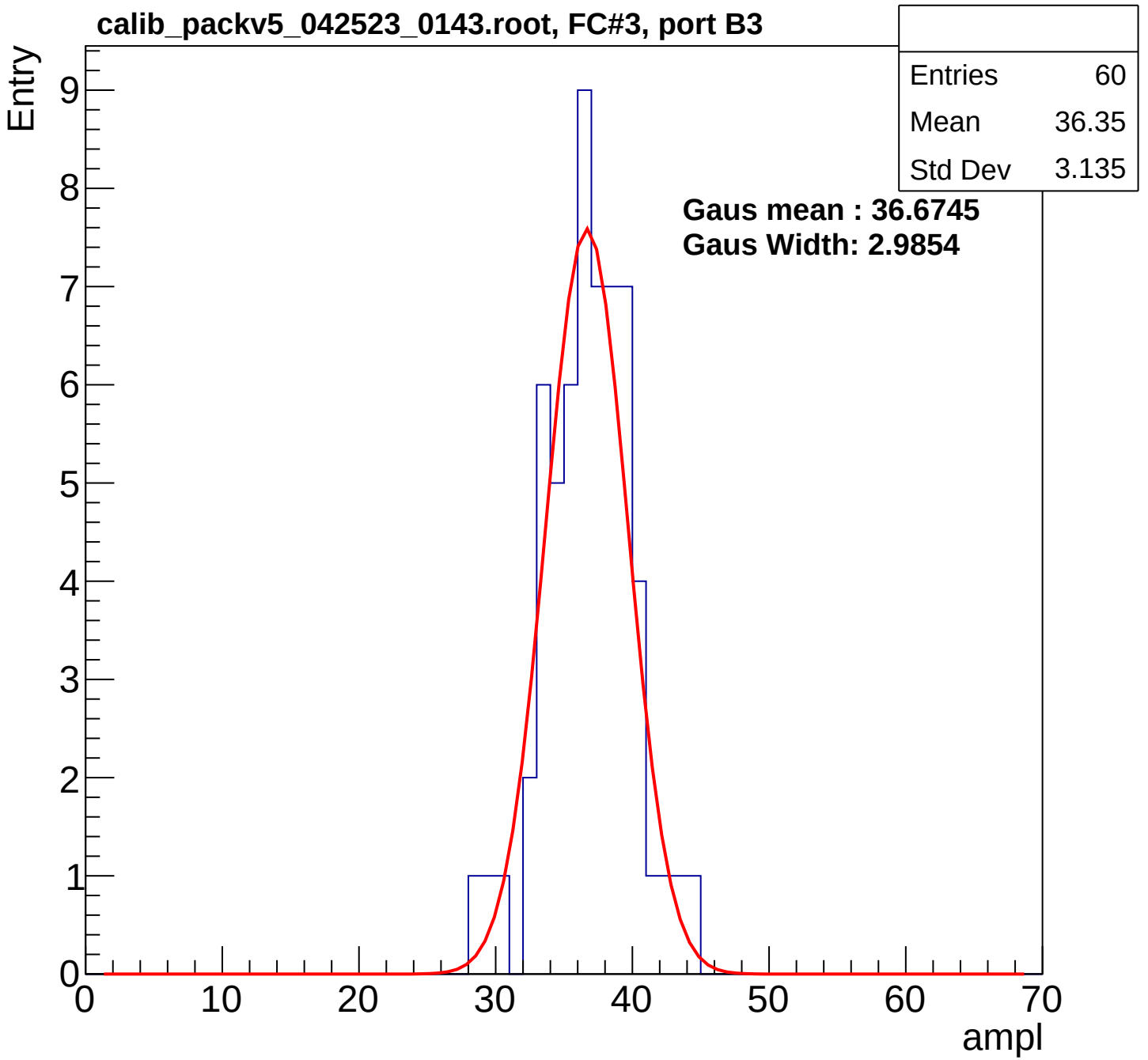

B1L000S, U10-ch94, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 61.26
Std Dev 1.681
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch94, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch95, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 30.3
Std Dev 3.314
Gaus mean : 30.9209
Gaus Width: 3.0633
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch95, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 36.35
Std Dev 3.135
Gaus mean : 36.6745
Gaus Width: 2.9854
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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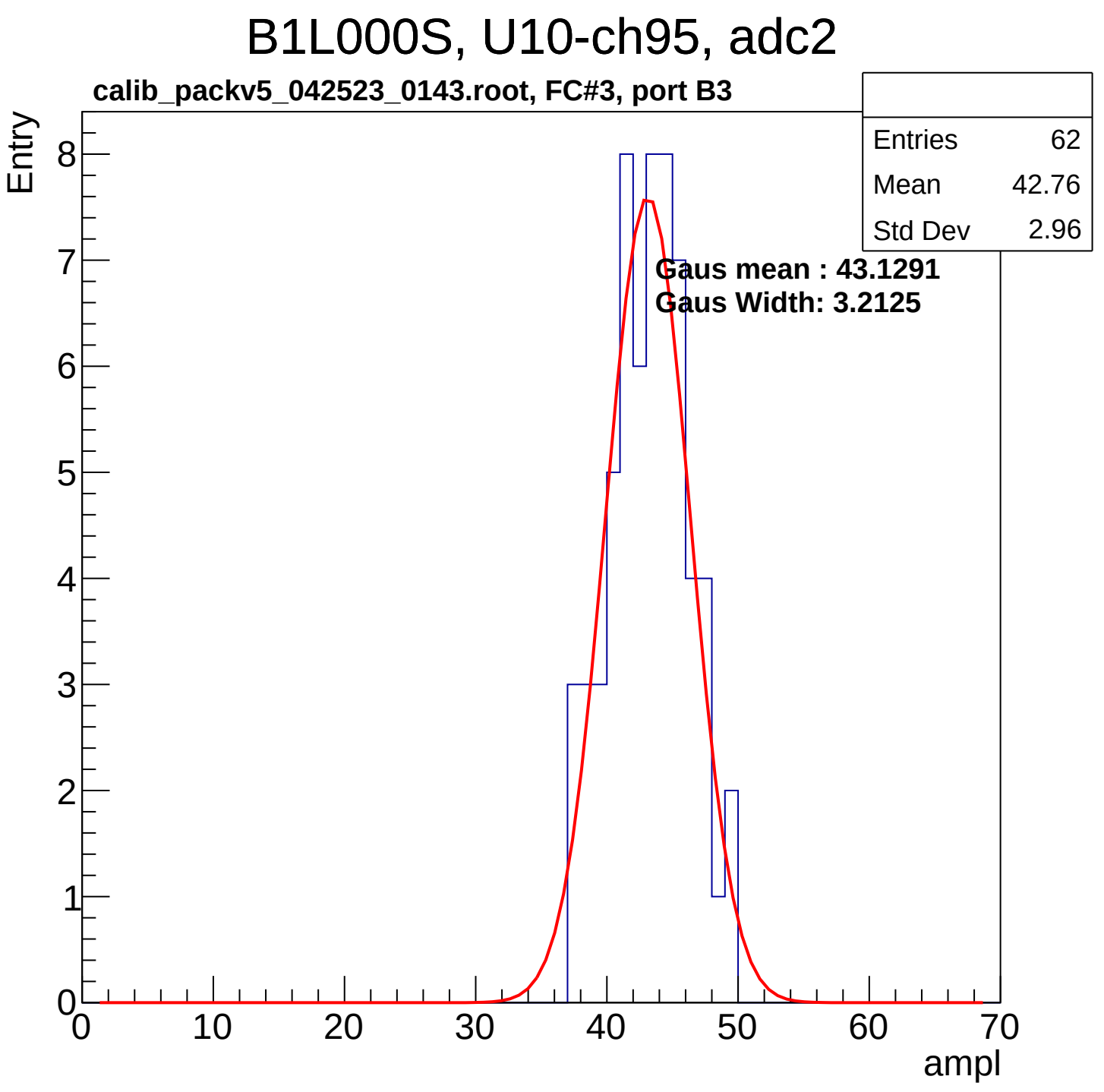
B1L000S, U10-ch95, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 42.76
Std Dev 2.96
Gaus mean : 43.1291
Gaus Width: 3.2125
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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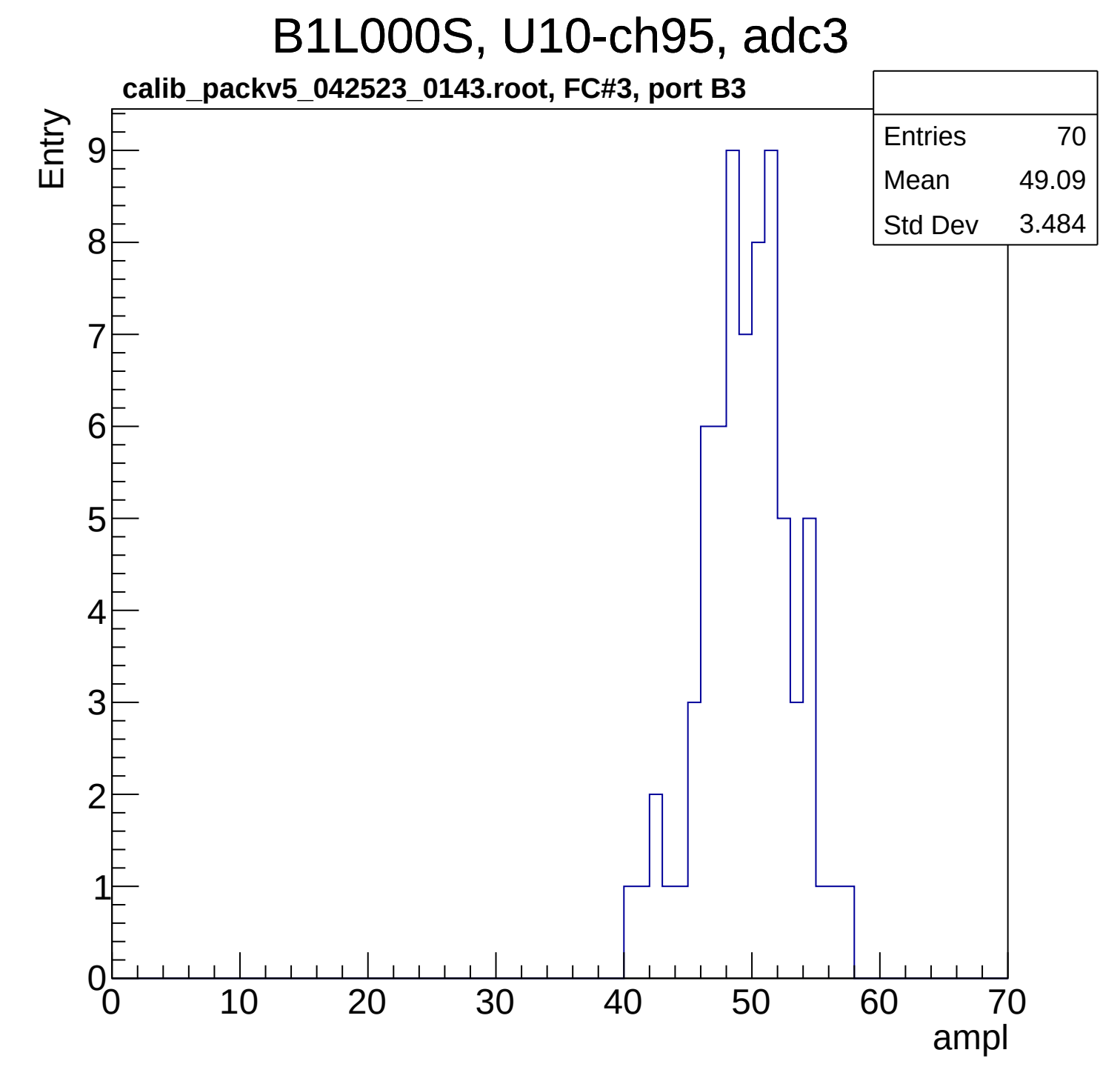
B1L000S, U10-ch95, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 49.09
Std Dev 3.484
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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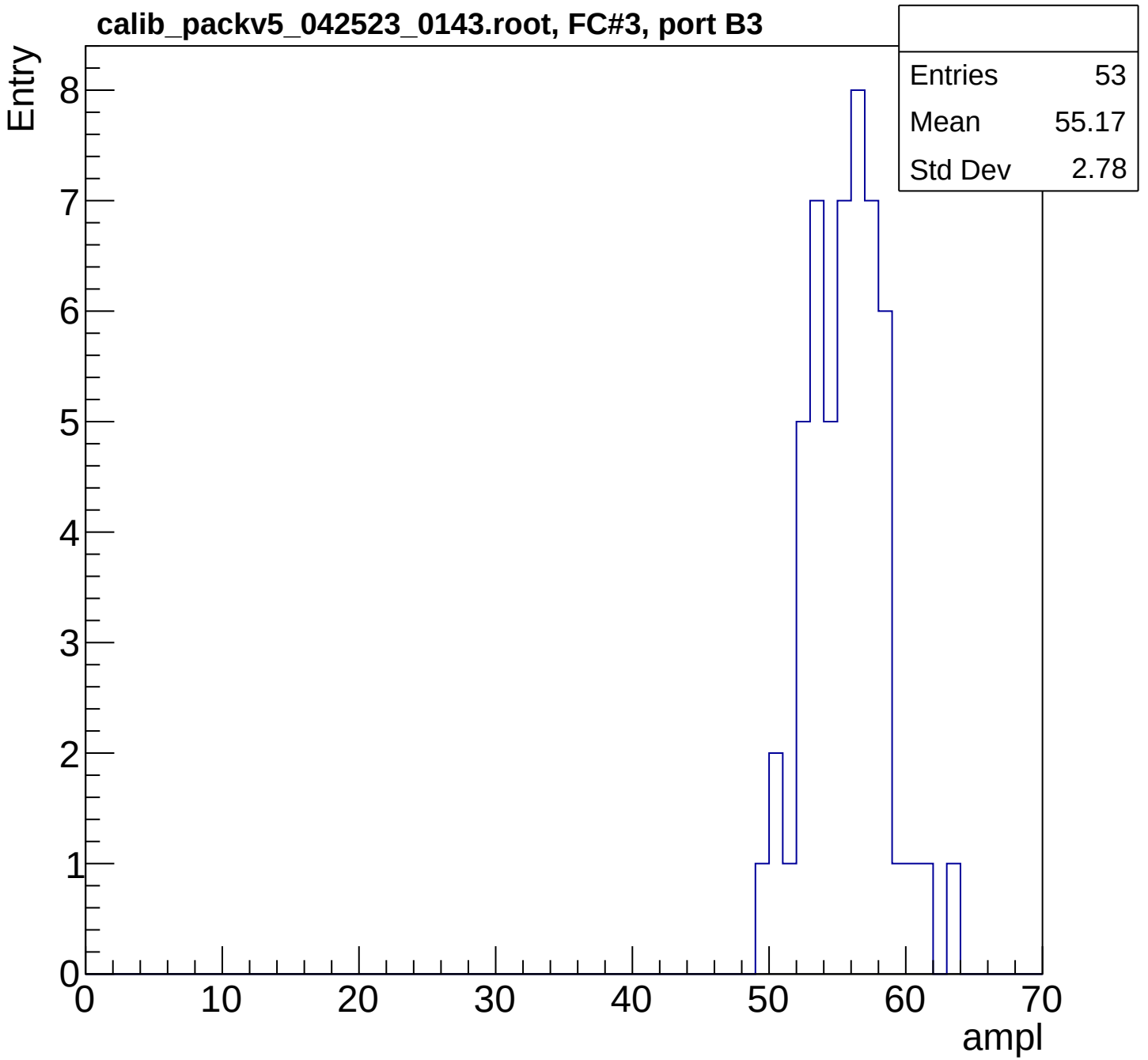
B1L000S, U10-ch95, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 55.17
Std Dev 2.78
Entry
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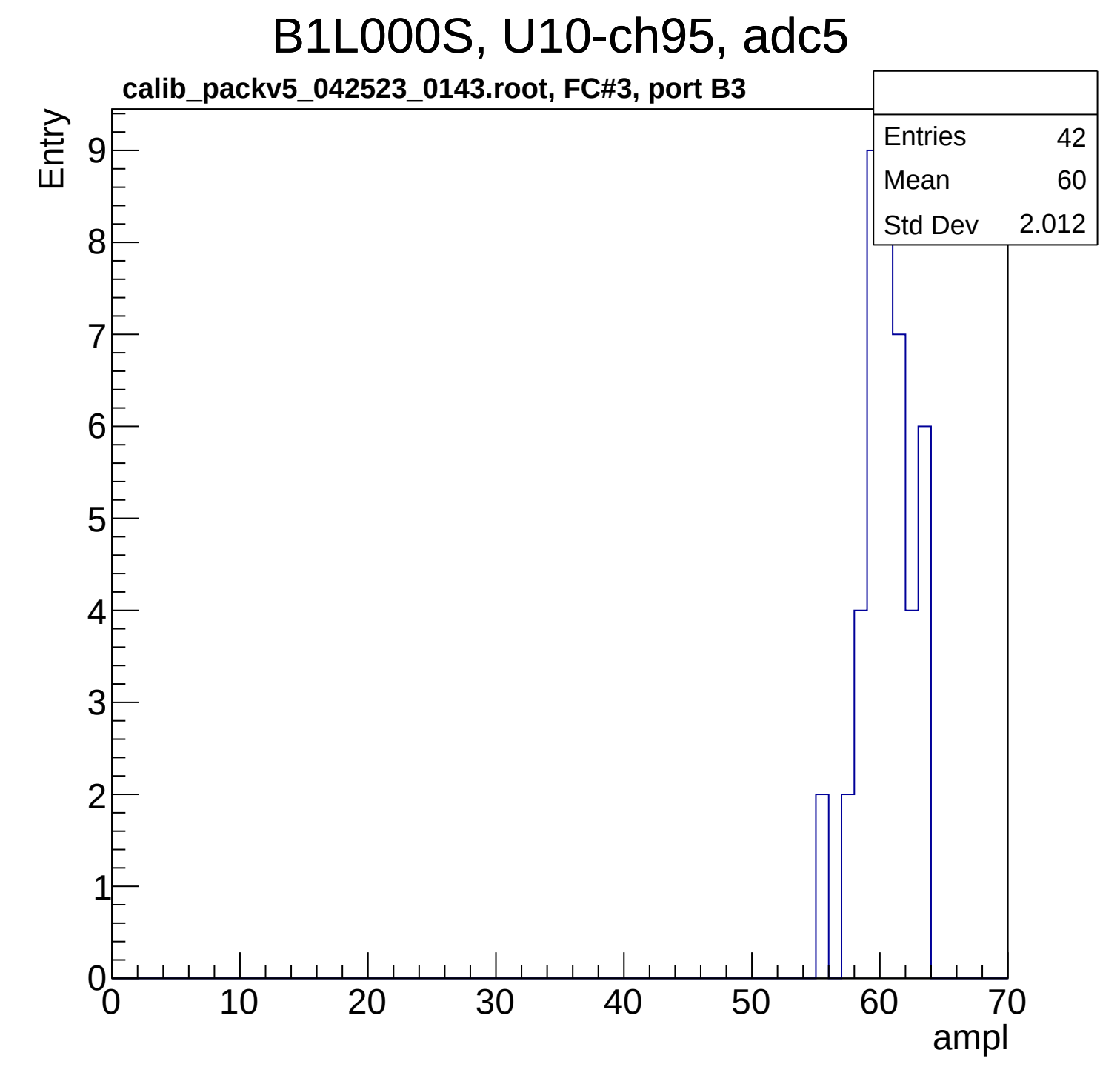
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch95, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 60
Std Dev 2.012
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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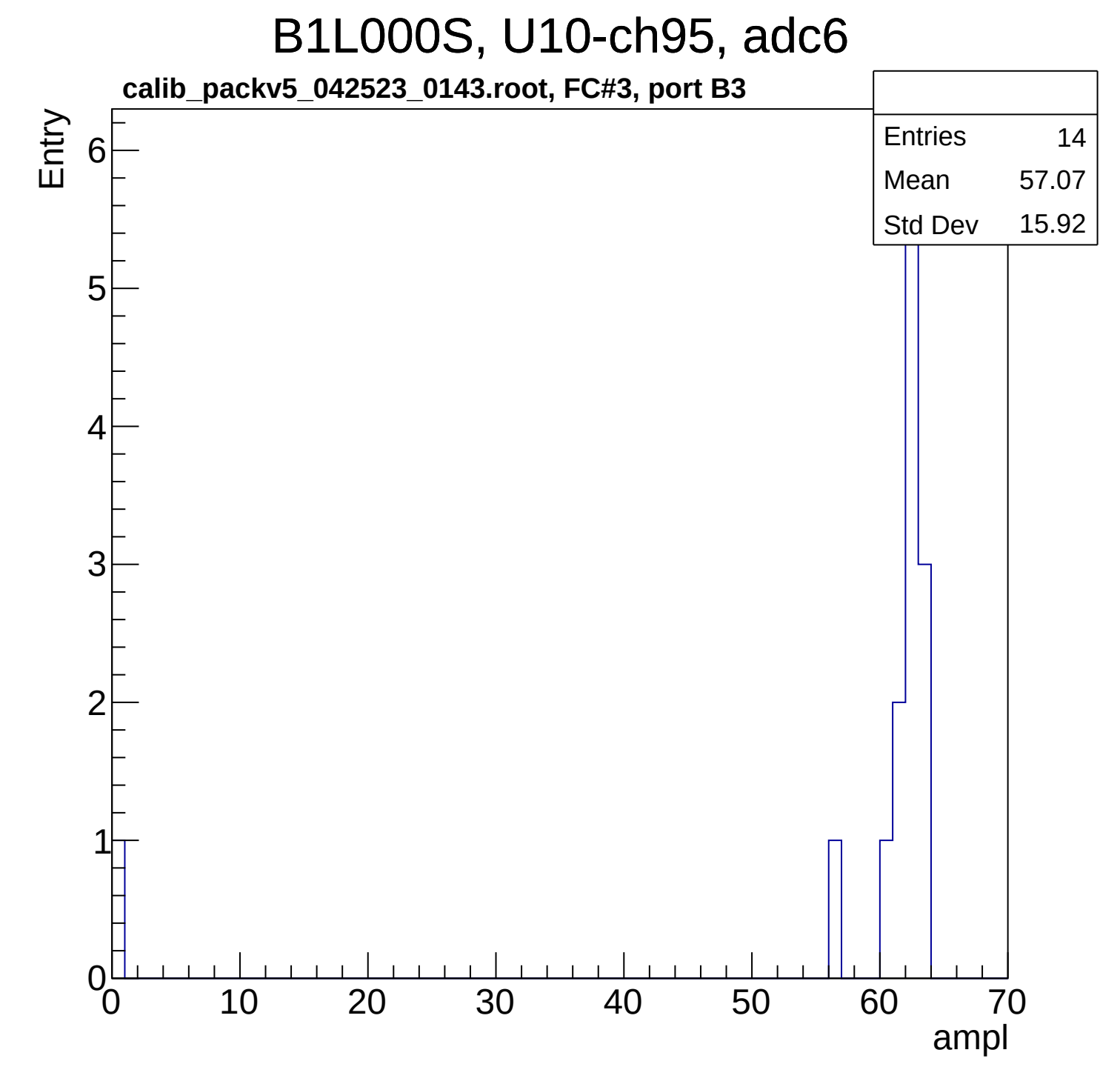
60
70


B1L000S, U10-ch95, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.07
Std Dev 15.92
Entry
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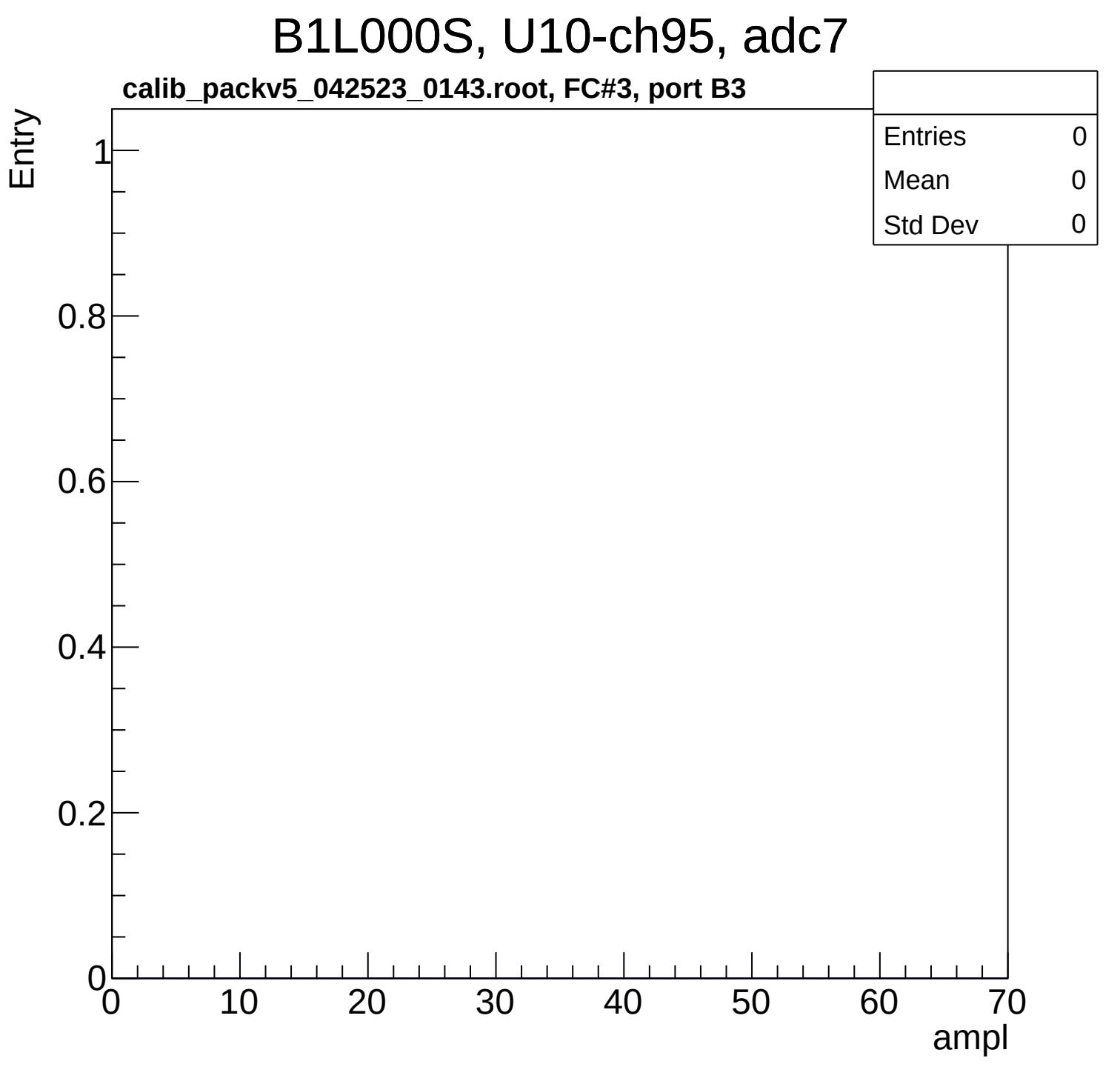
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch95, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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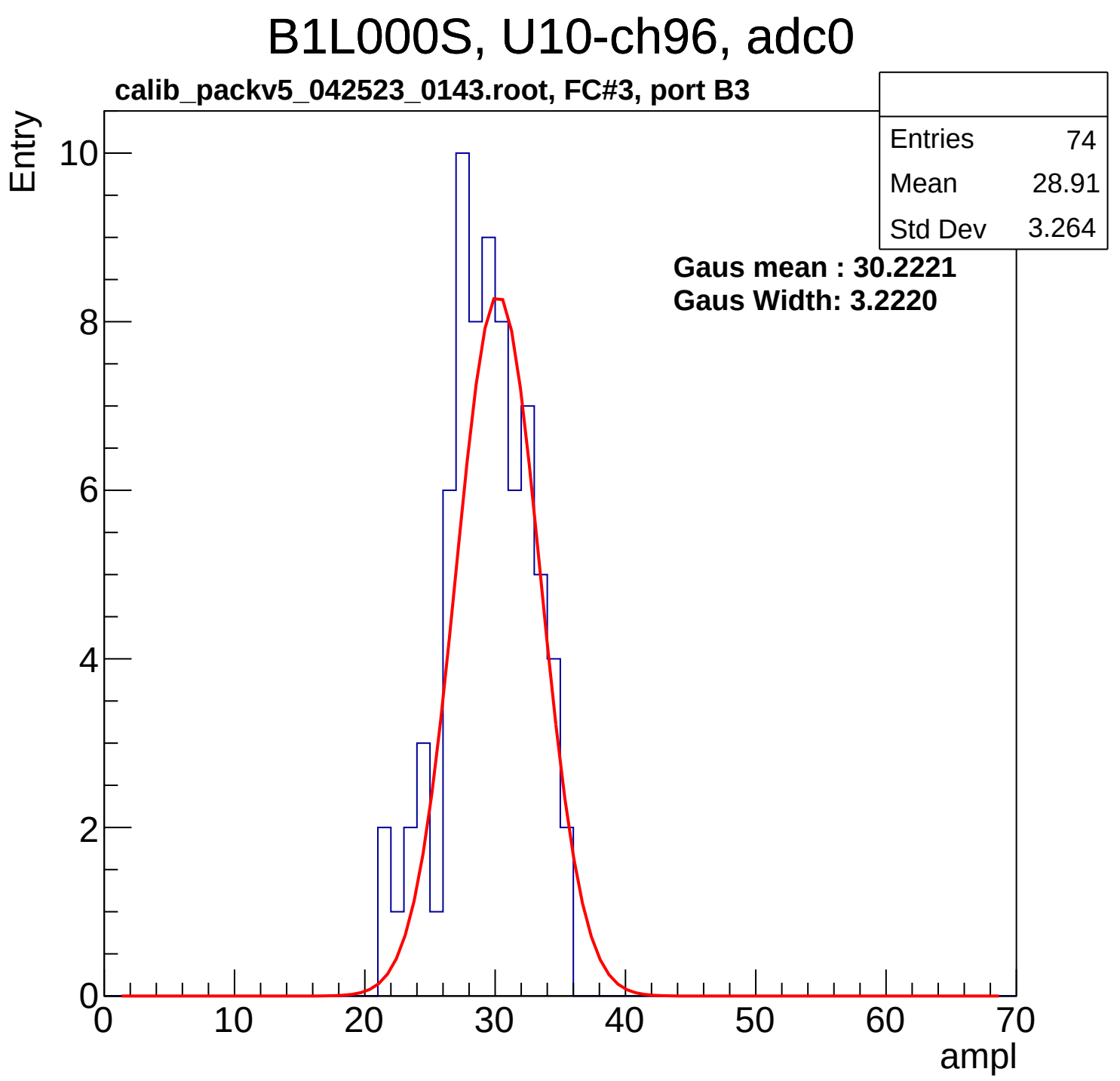
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch96, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 28.91
Std Dev 3.264
Gaus mean : 30.2221
Gaus Width: 3.2220
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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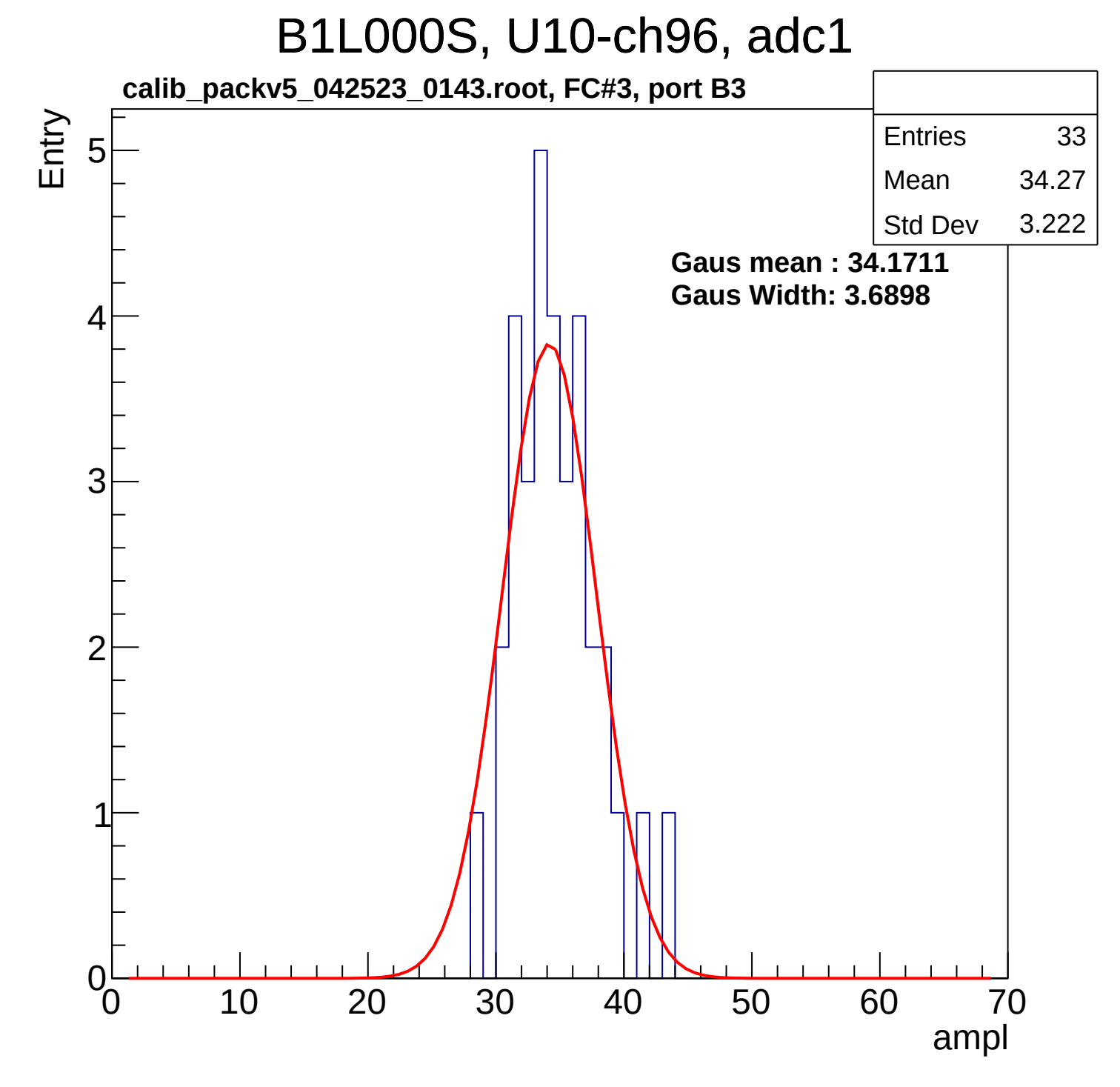
B1L000S, U10-ch96, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 33
Mean 34.27
Std Dev 3.222
Gaus mean : 34.1711
Gaus Width: 3.6898
Entry
ampl
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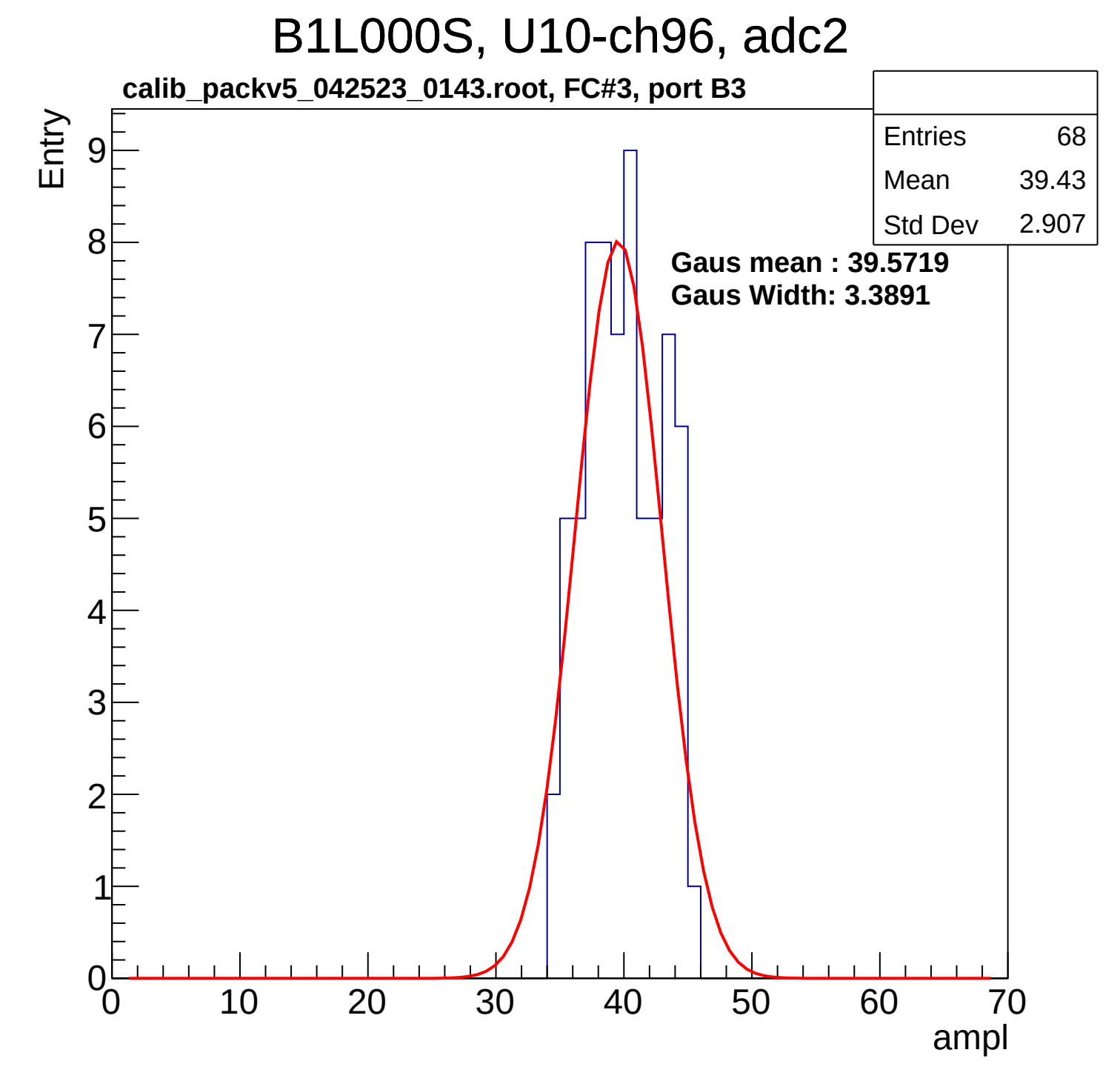
2
3
4
5
0
10
20
30
40
50
60
70


B1L000S, U10-ch96, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 39.43
Std Dev 2.907
Gaus mean : 39.5719
Gaus Width: 3.3891
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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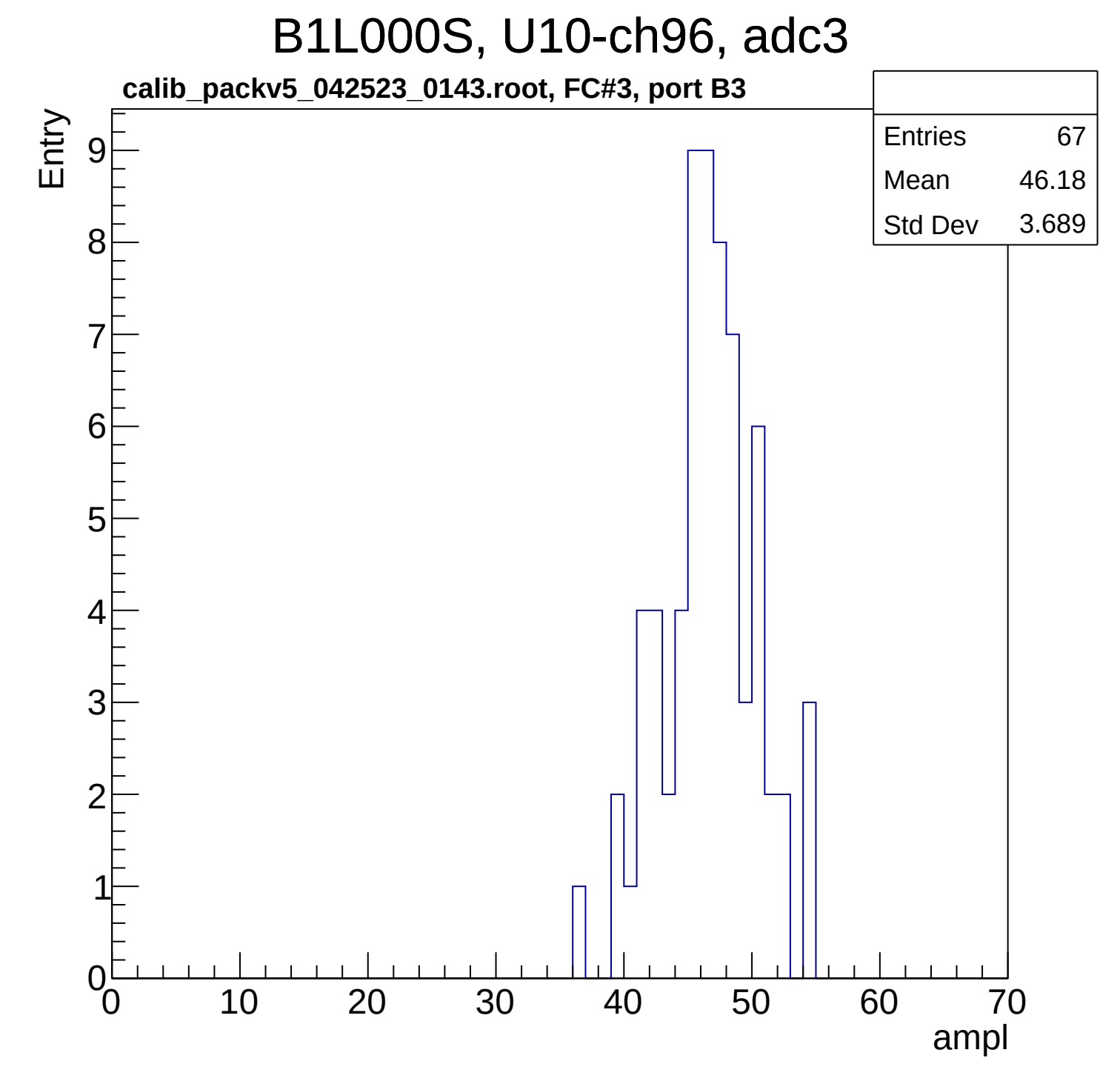
B1L000S, U10-ch96, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 46.18
Std Dev 3.689
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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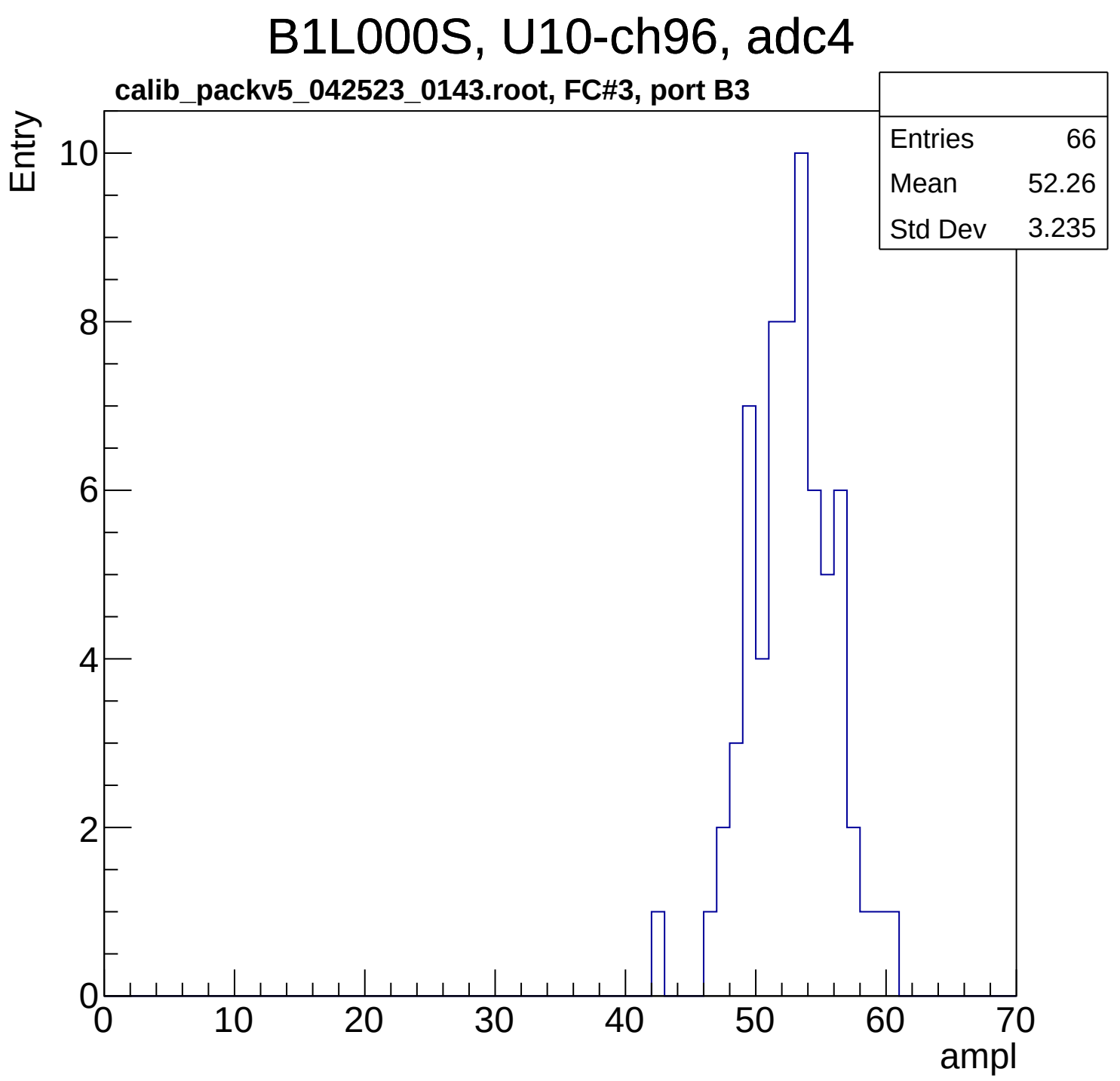
B1L000S, U10-ch96, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 52.26
Std Dev 3.235
Entry
ampl
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4
6
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10
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10
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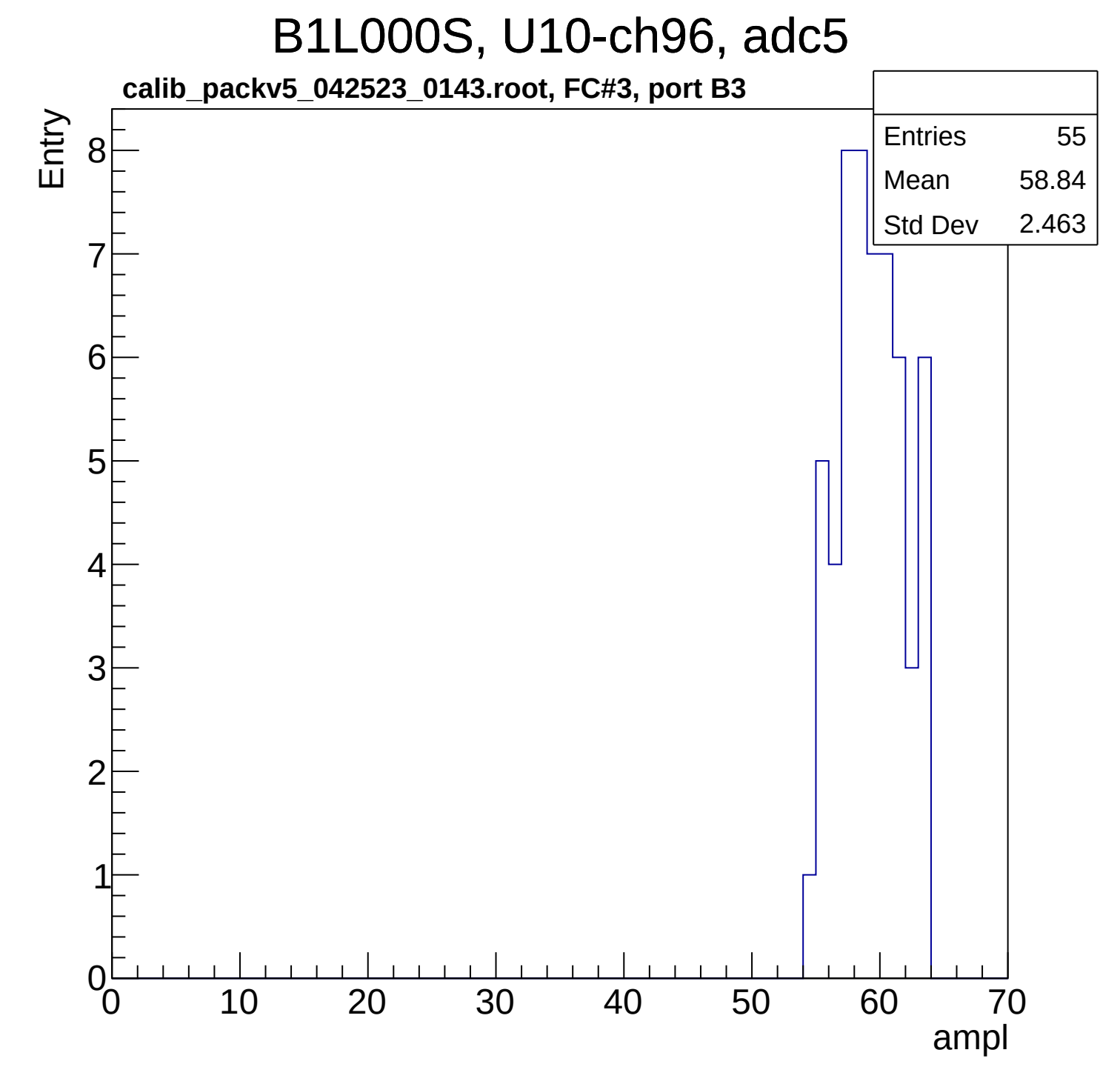
B1L000S, U10-ch96, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.84
Std Dev 2.463
Entry
ampl
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3
4
5
6
7
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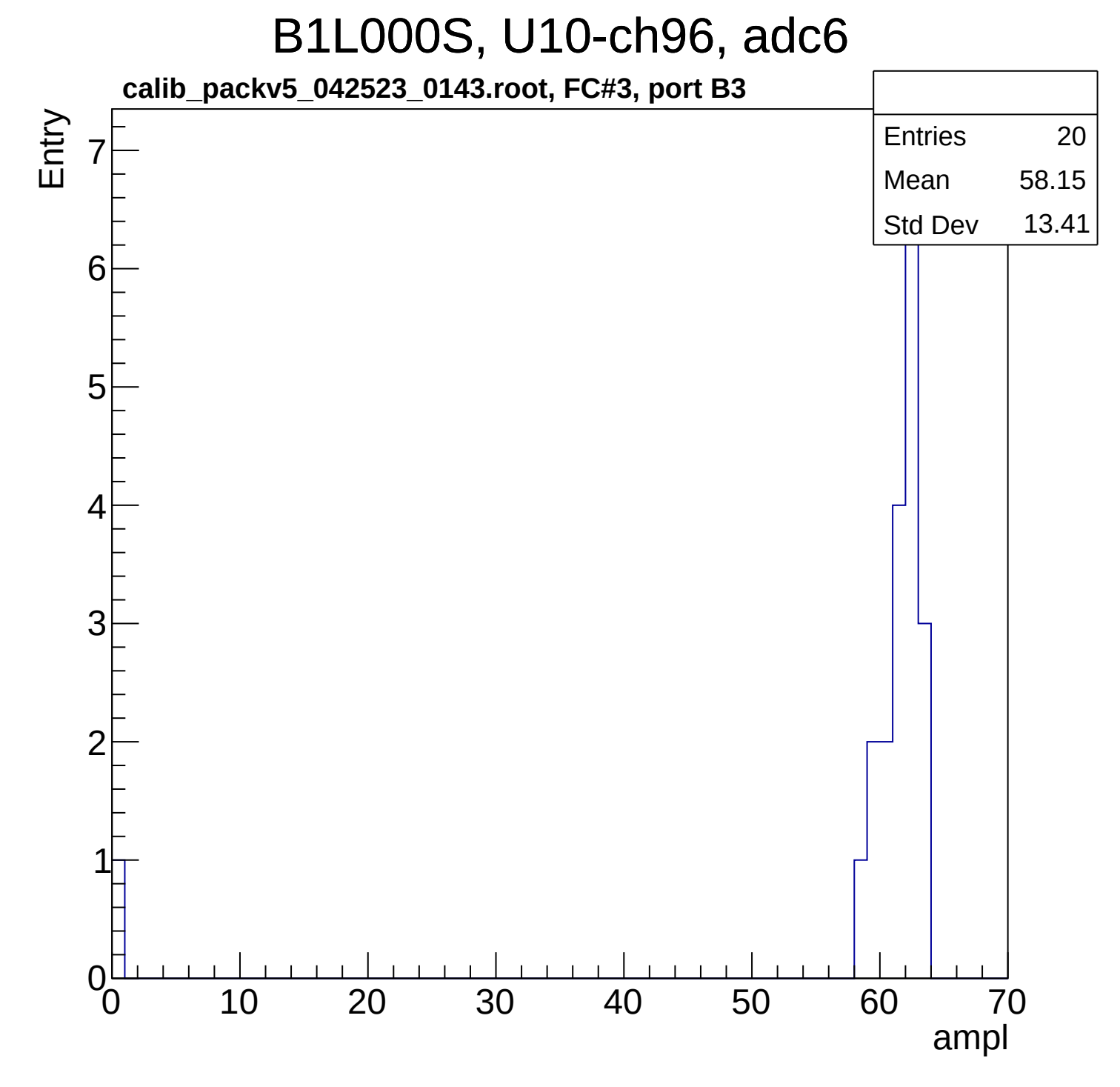
B1L000S, U10-ch96, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.15
Std Dev 13.41
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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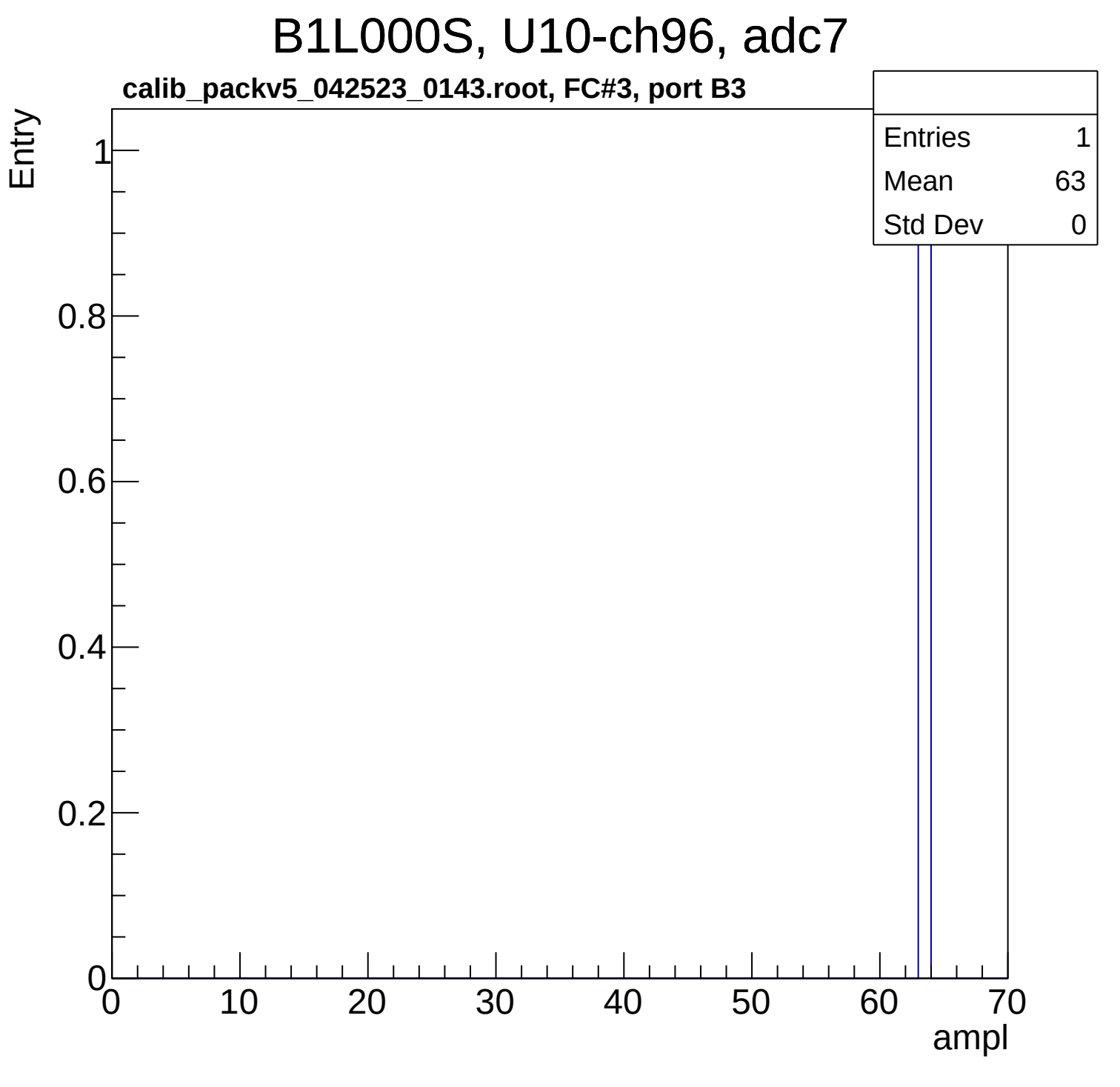
B1L000S, U10-ch96, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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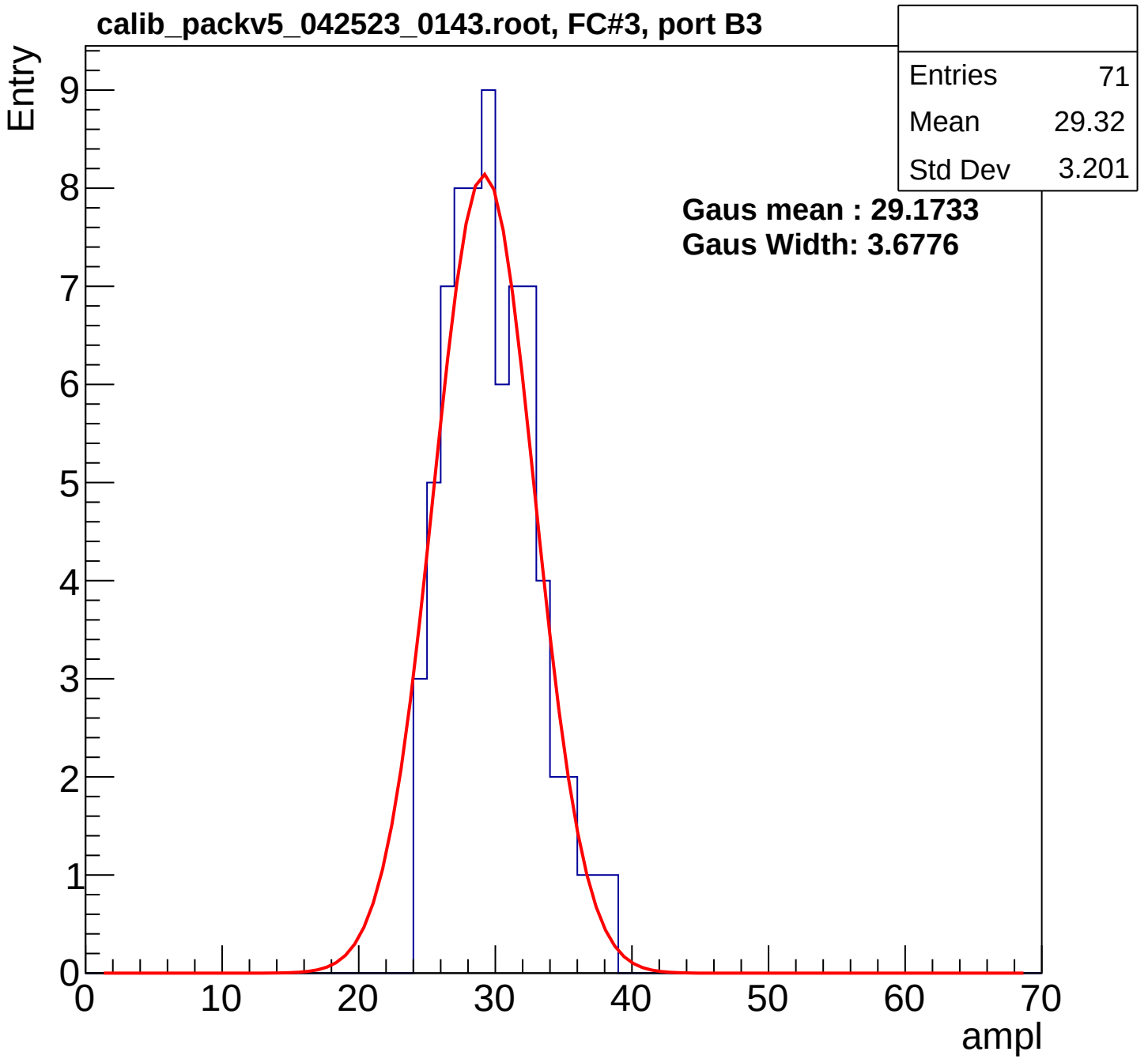
50
60
70
ampl


B1L000S, U10-ch97, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 29.32
Std Dev 3.201
Gaus mean : 29.1733
Gaus Width: 3.6776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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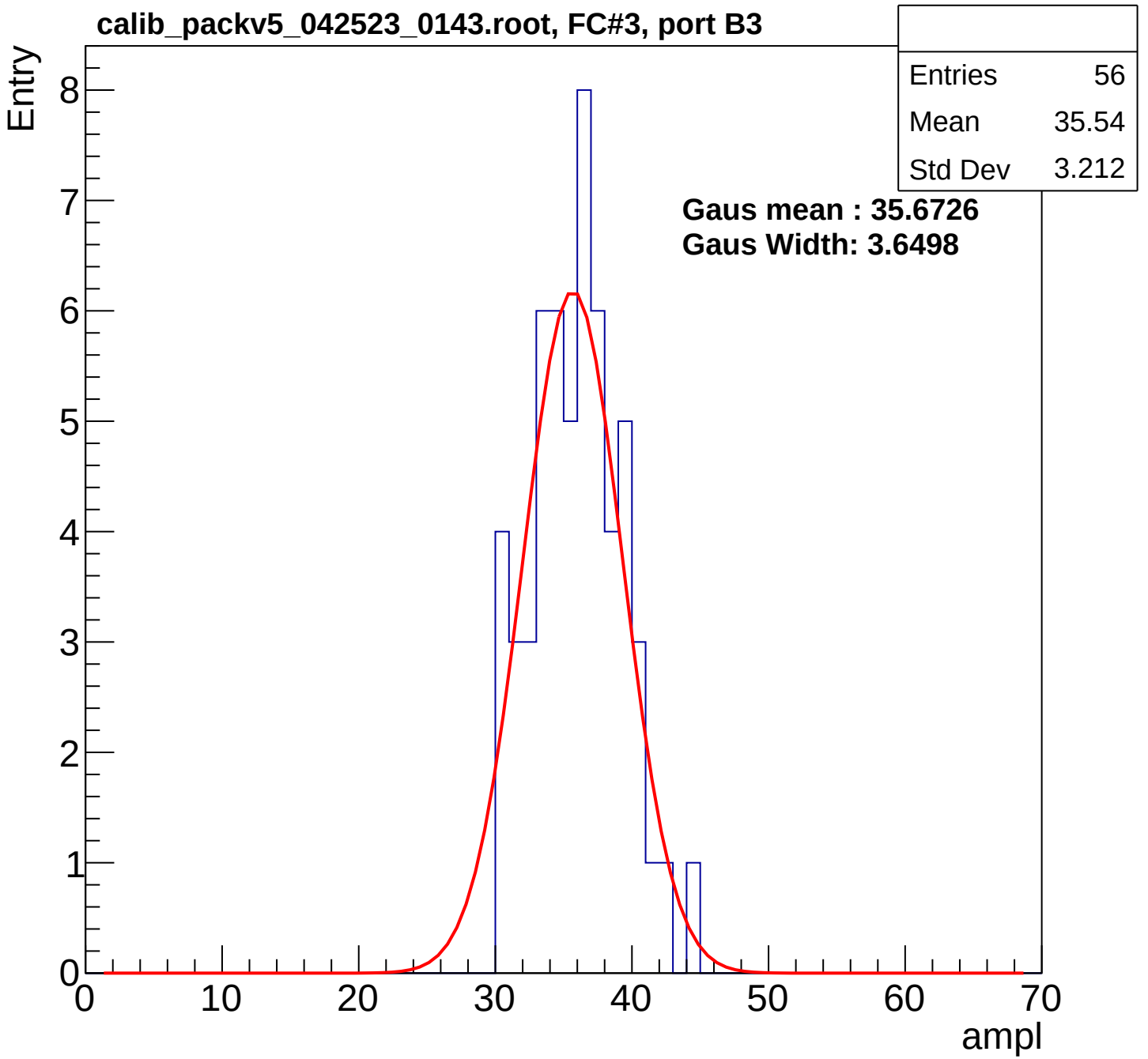
B1L000S, U10-ch97, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.54
Std Dev 3.212
Gaus mean : 35.6726
Gaus Width: 3.6498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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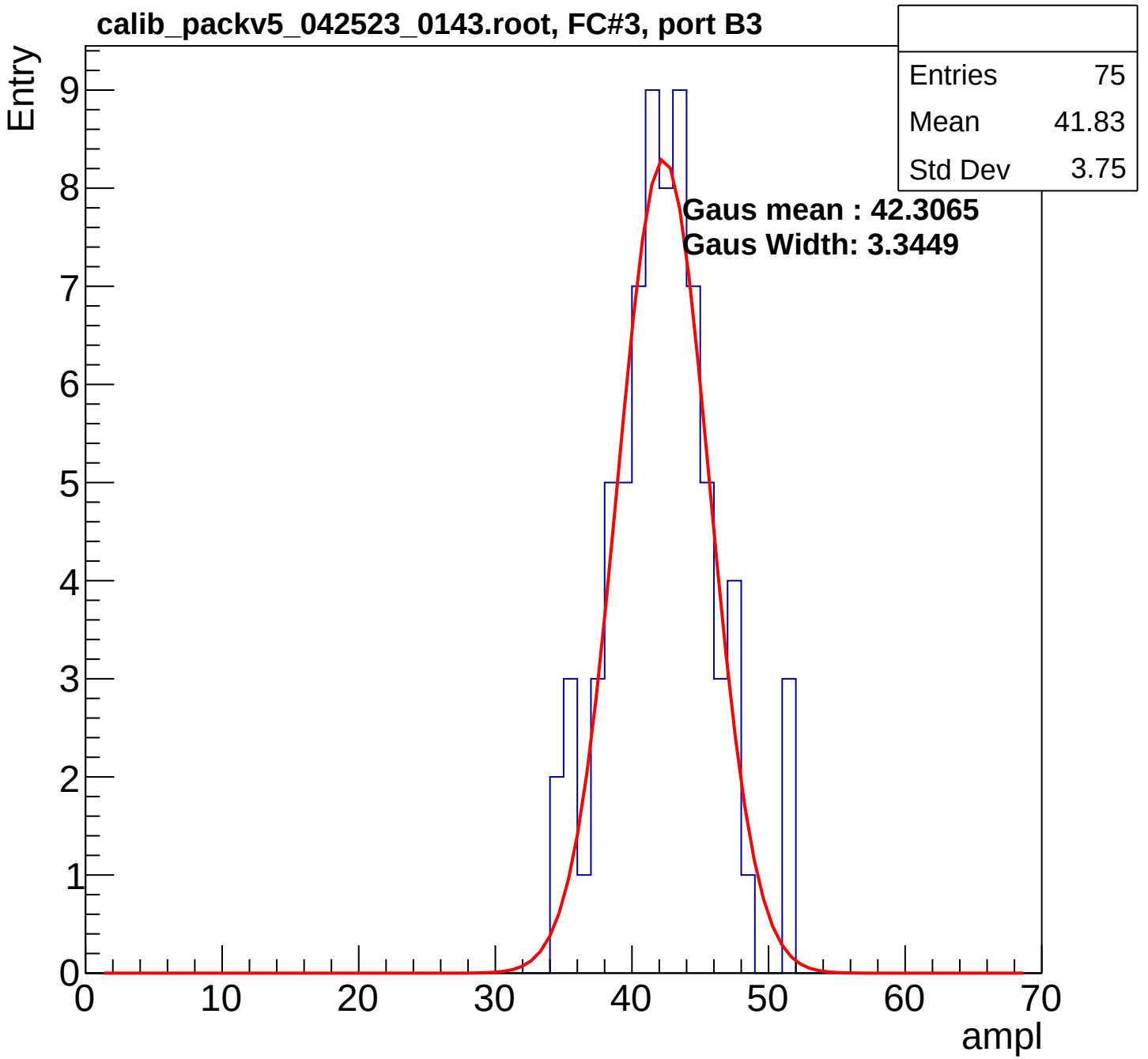
B1L000S, U10-ch97, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 41.83
Std Dev 3.75
Gaus mean : 42.3065
Gaus Width: 3.3449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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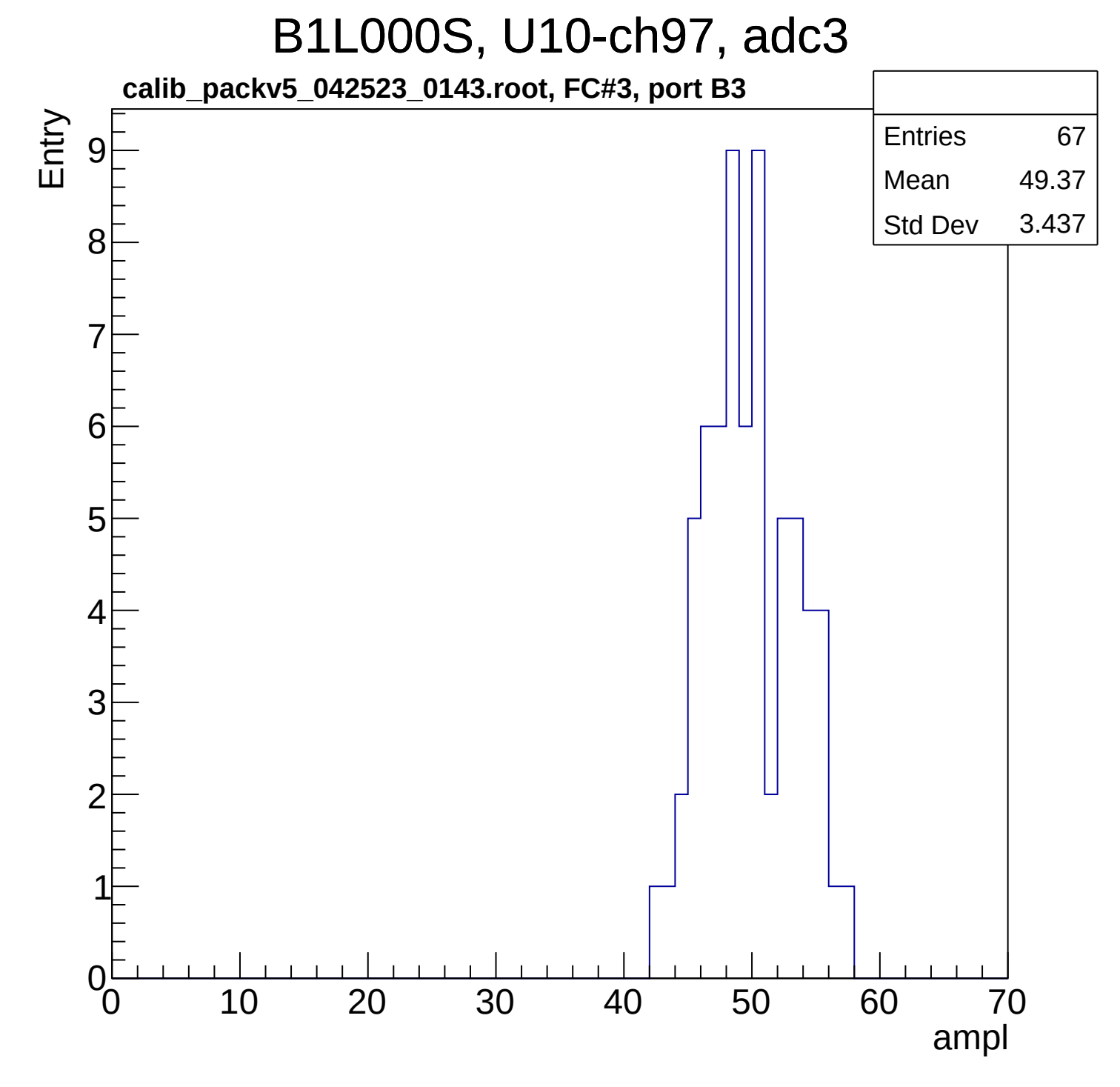
B1L000S, U10-ch97, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 49.37
Std Dev 3.437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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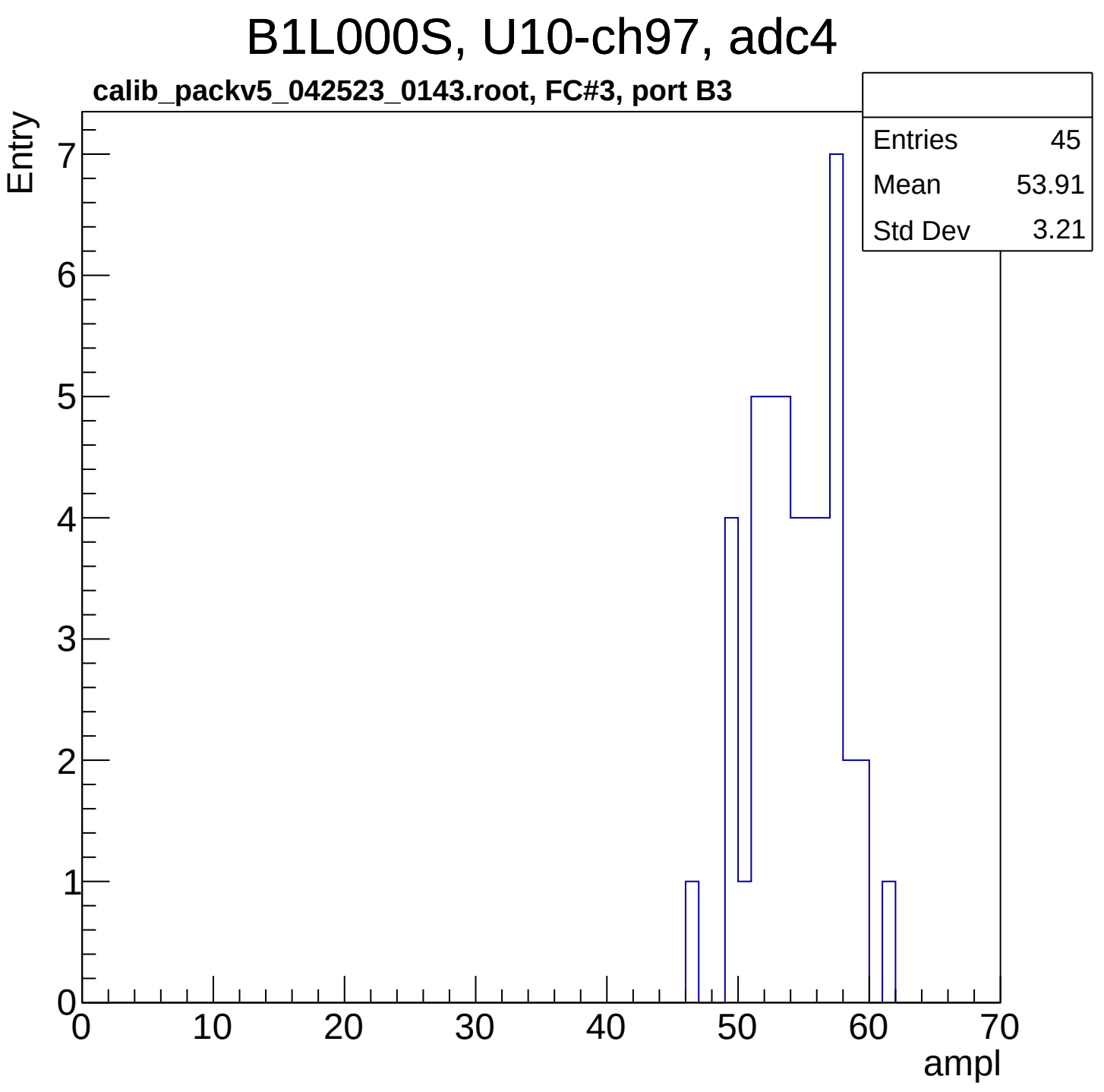
B1L000S, U10-ch97, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.91
Std Dev 3.21
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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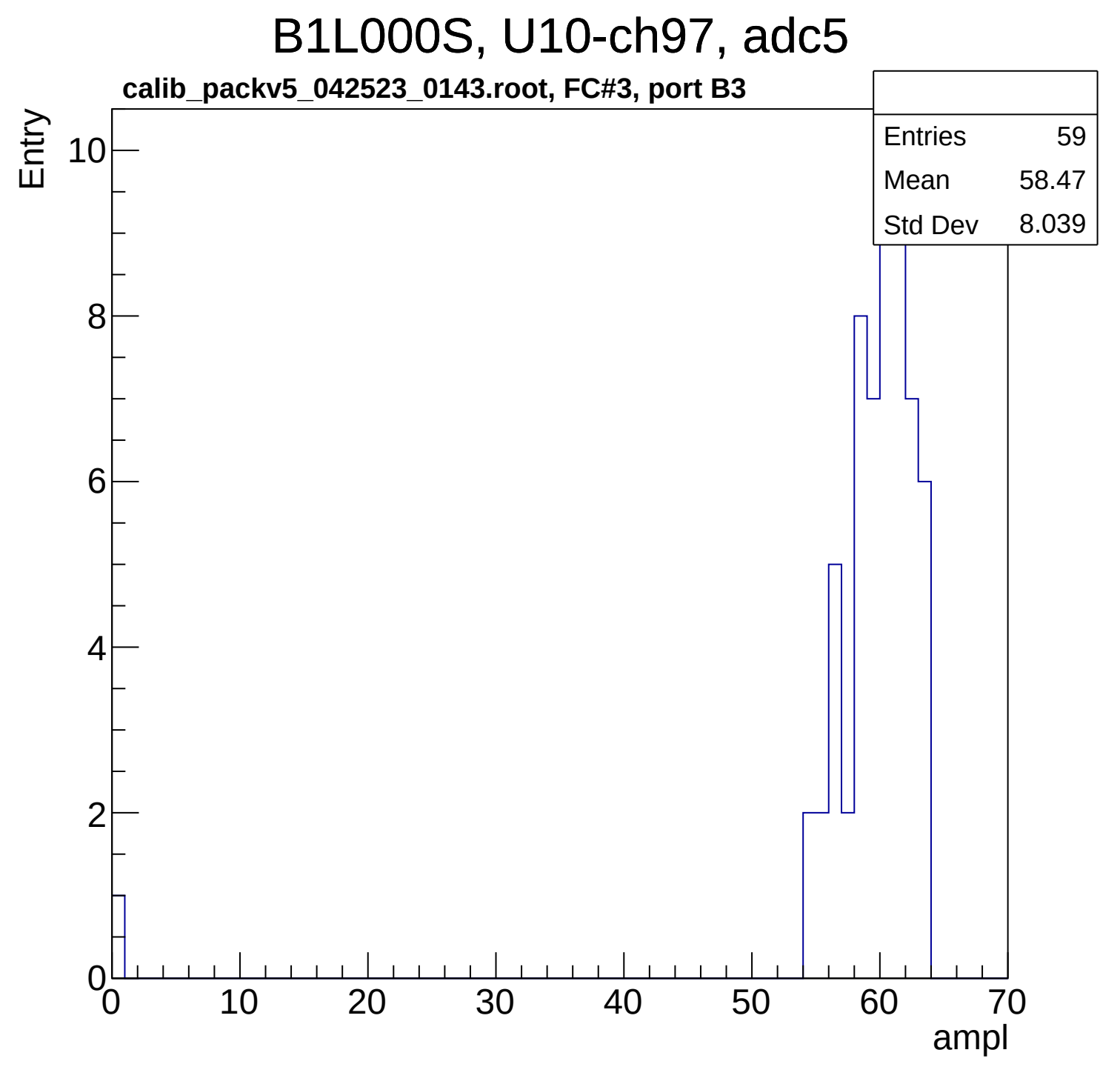
B1L000S, U10-ch97, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.47
Std Dev 8.039
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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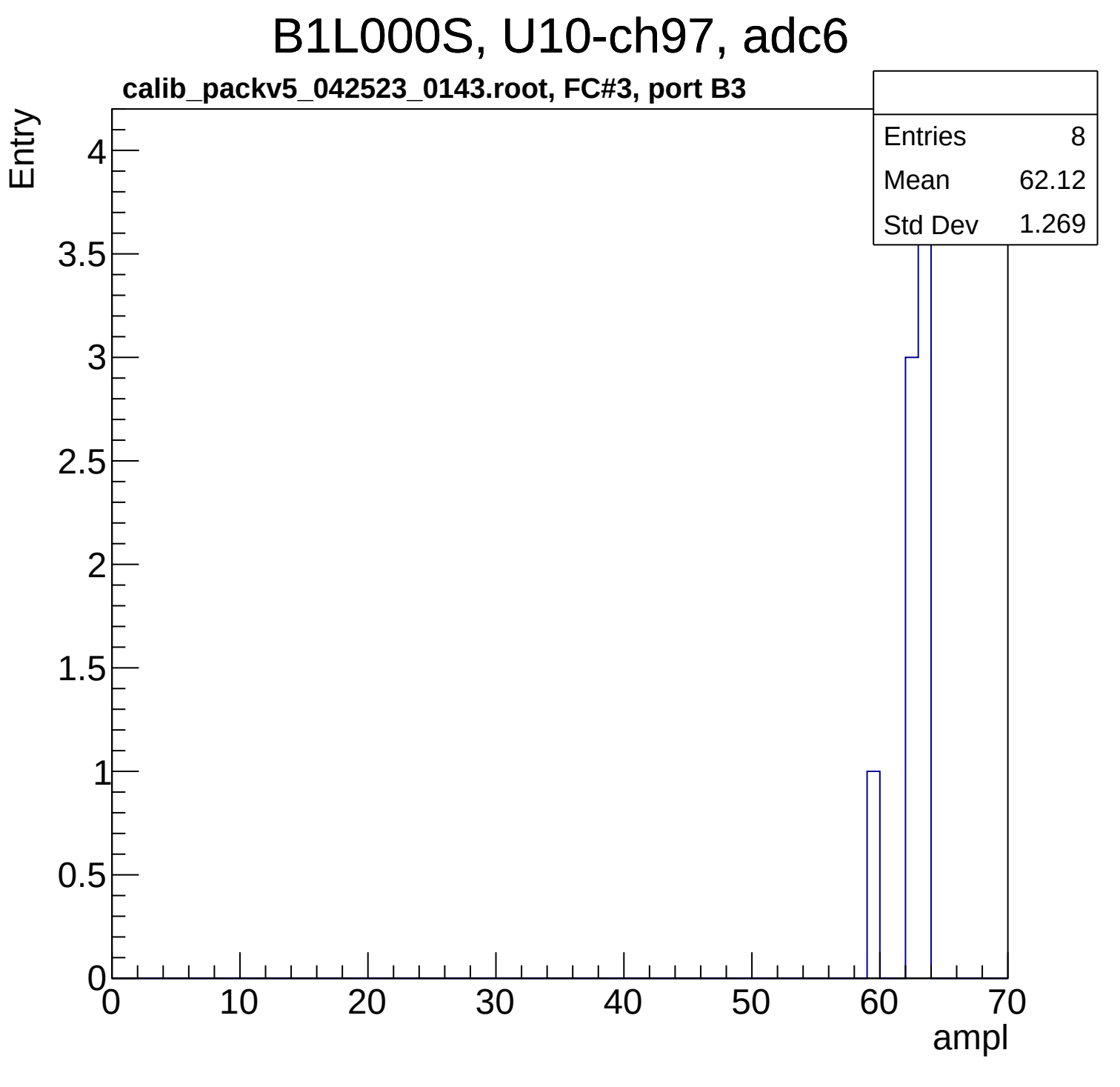
B1L000S, U10-ch97, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.12
Std Dev 1.269
Entry
ampl
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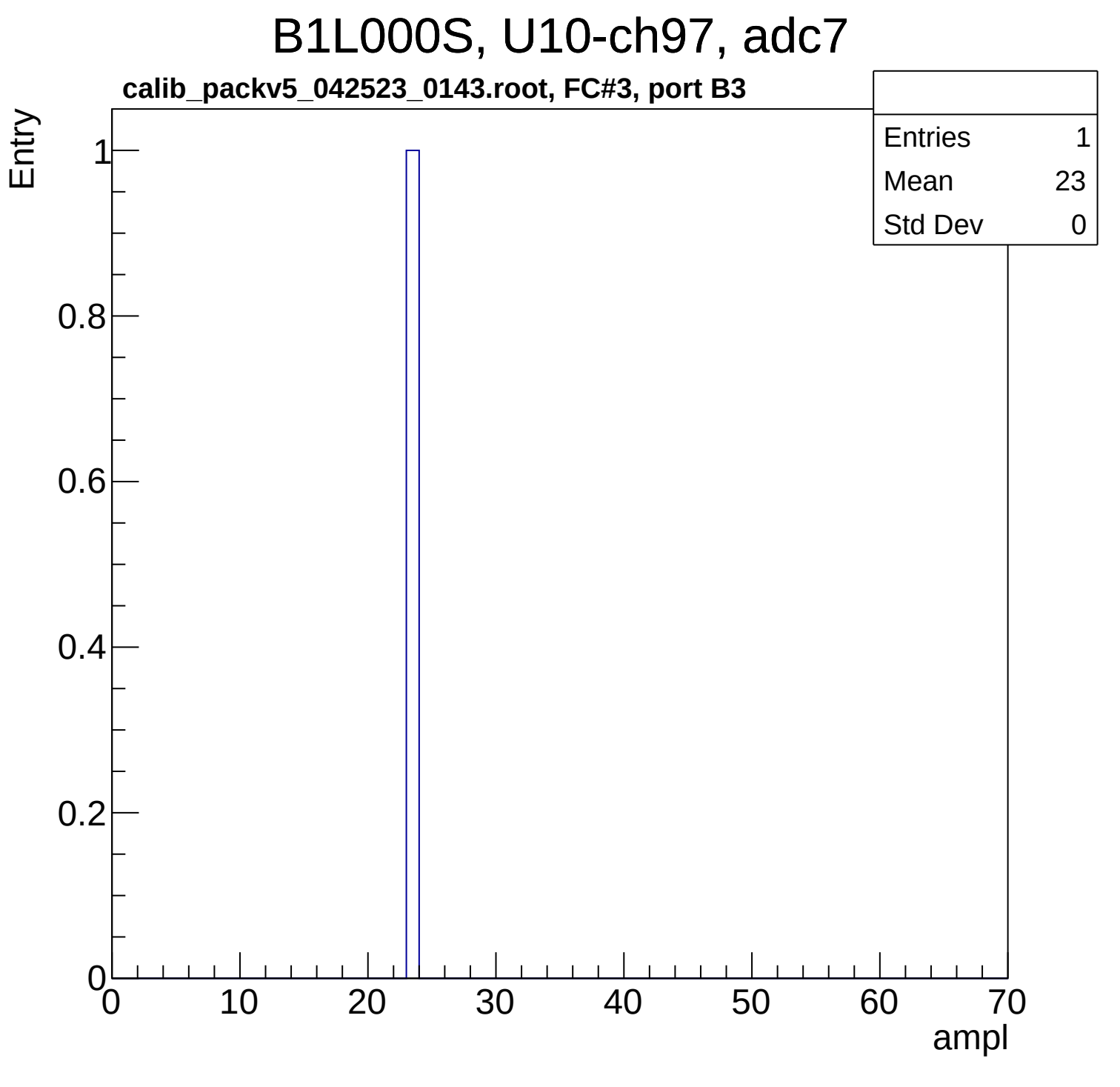
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch97, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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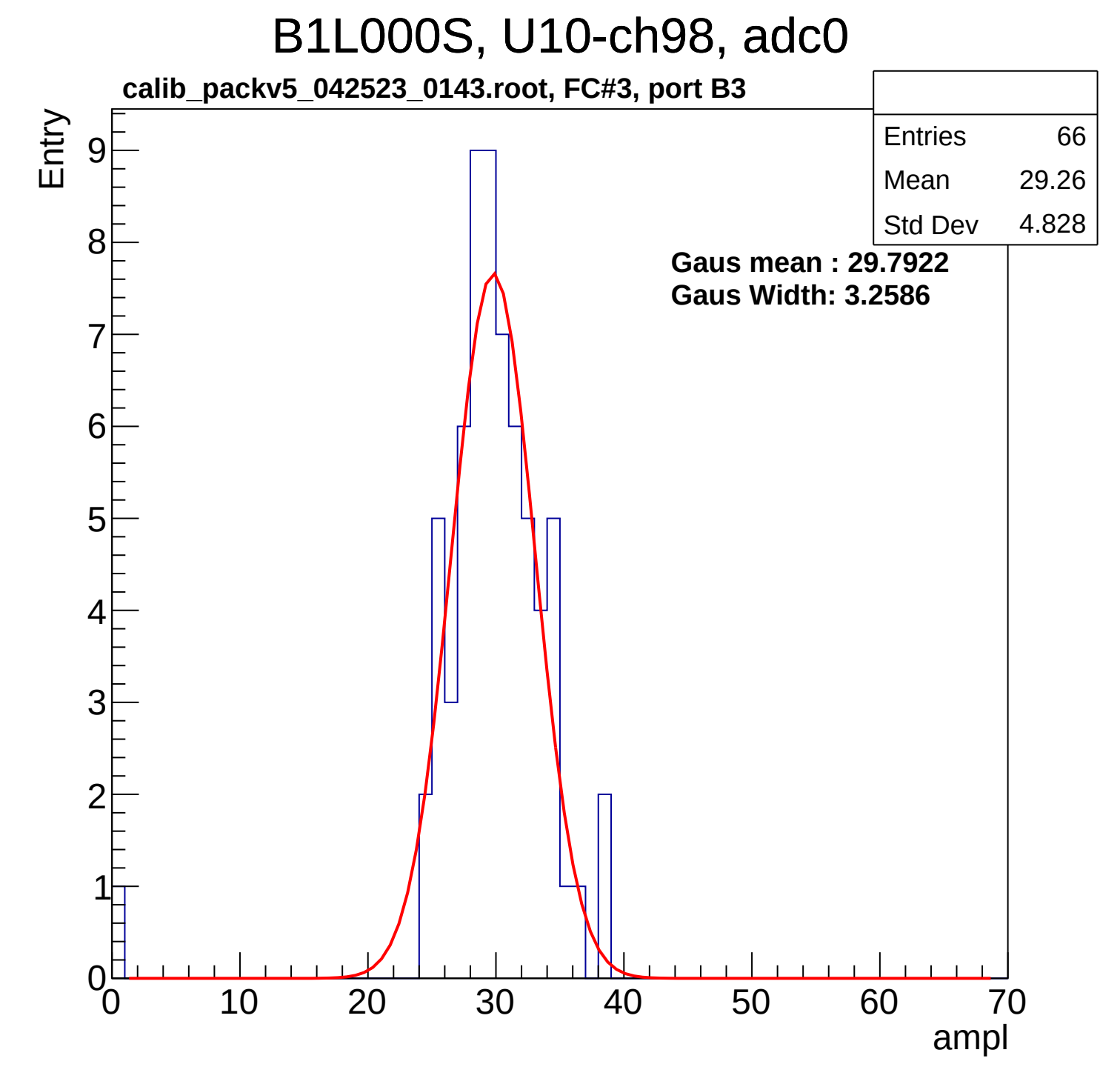
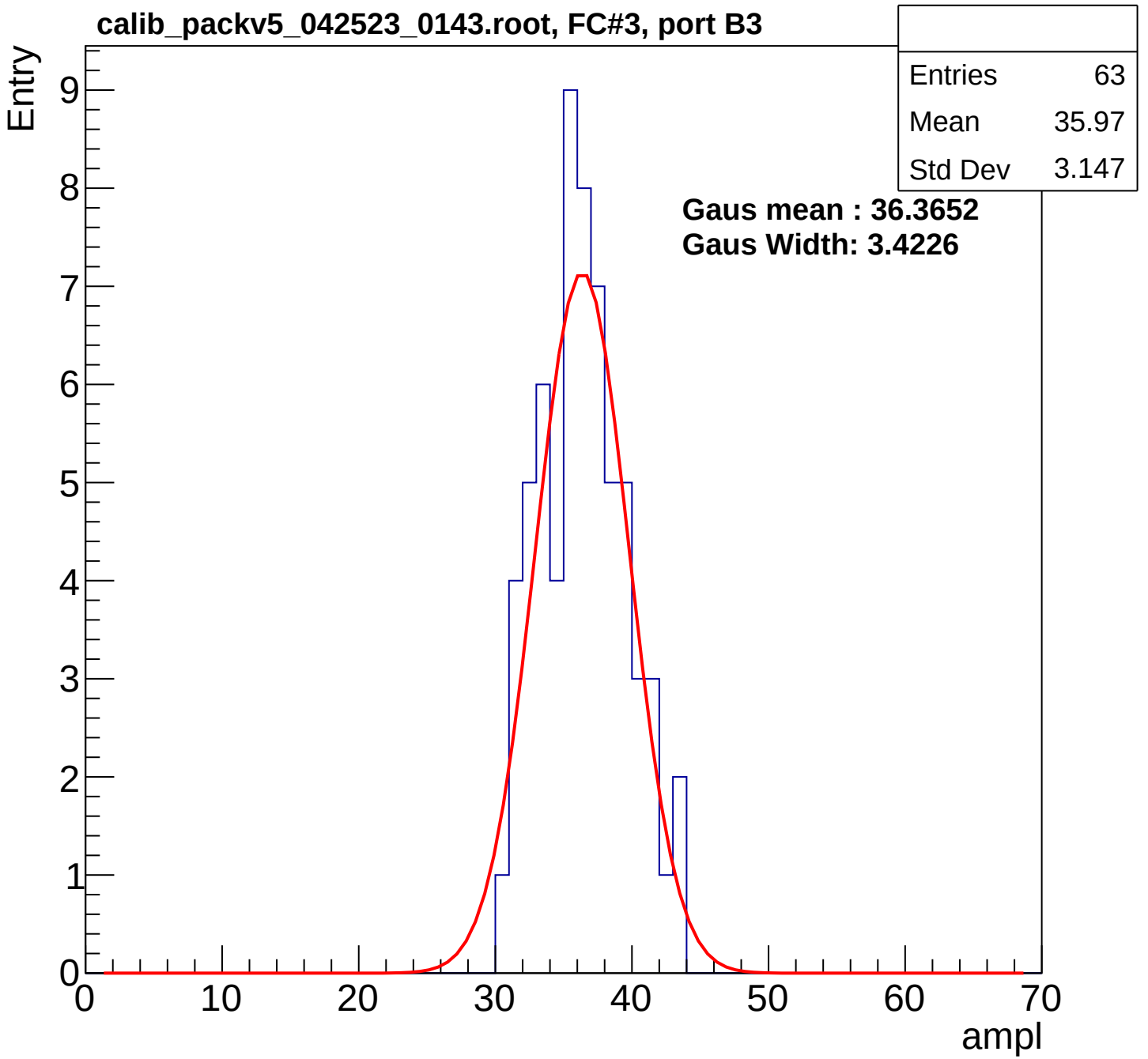
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch98, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 29.26
Std Dev 4.828
Gaus mean : 29.7922
Gaus Width: 3.2586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch98, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.97
Std Dev 3.147
Gaus mean : 36.3652
Gaus Width: 3.4226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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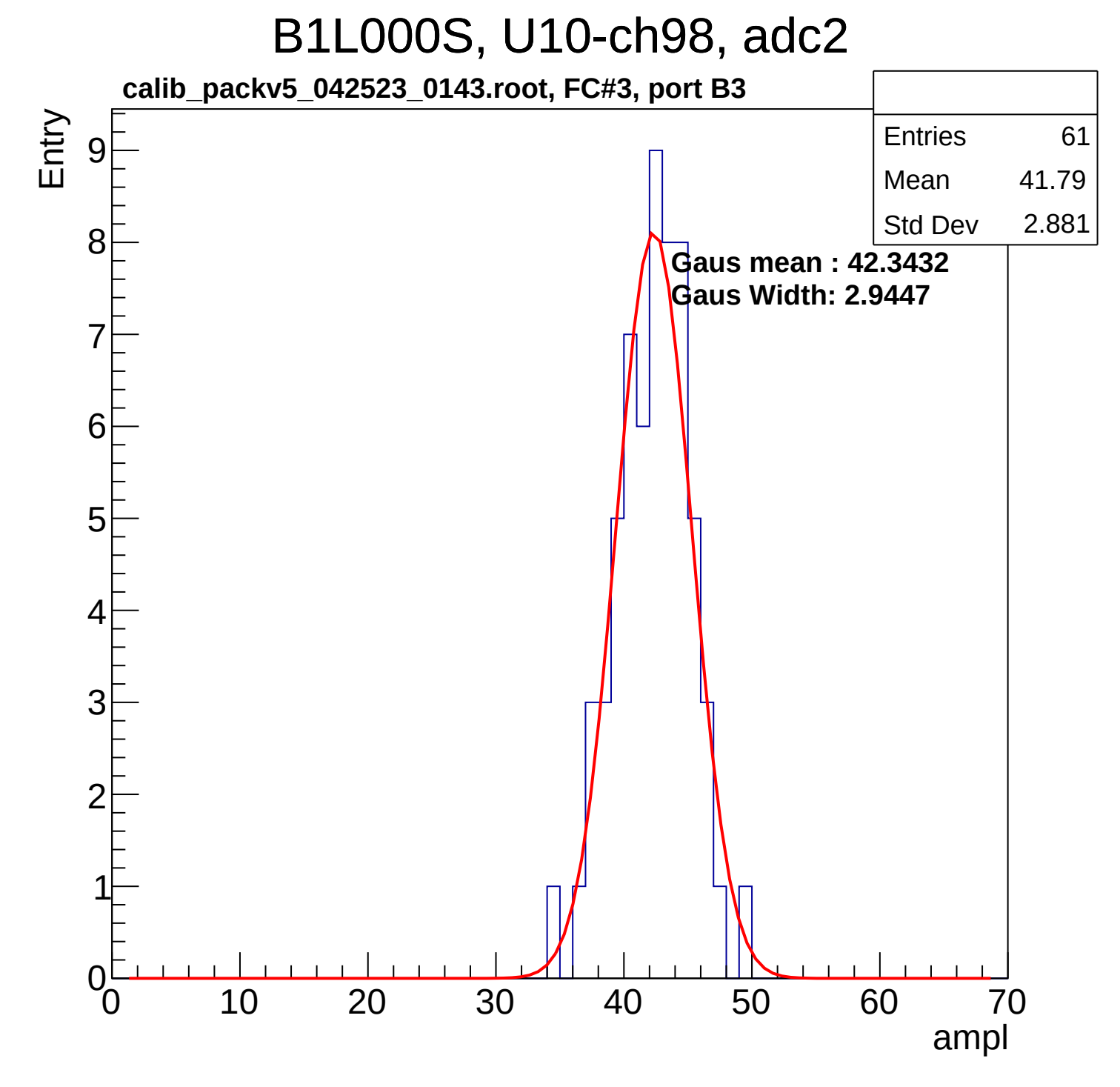
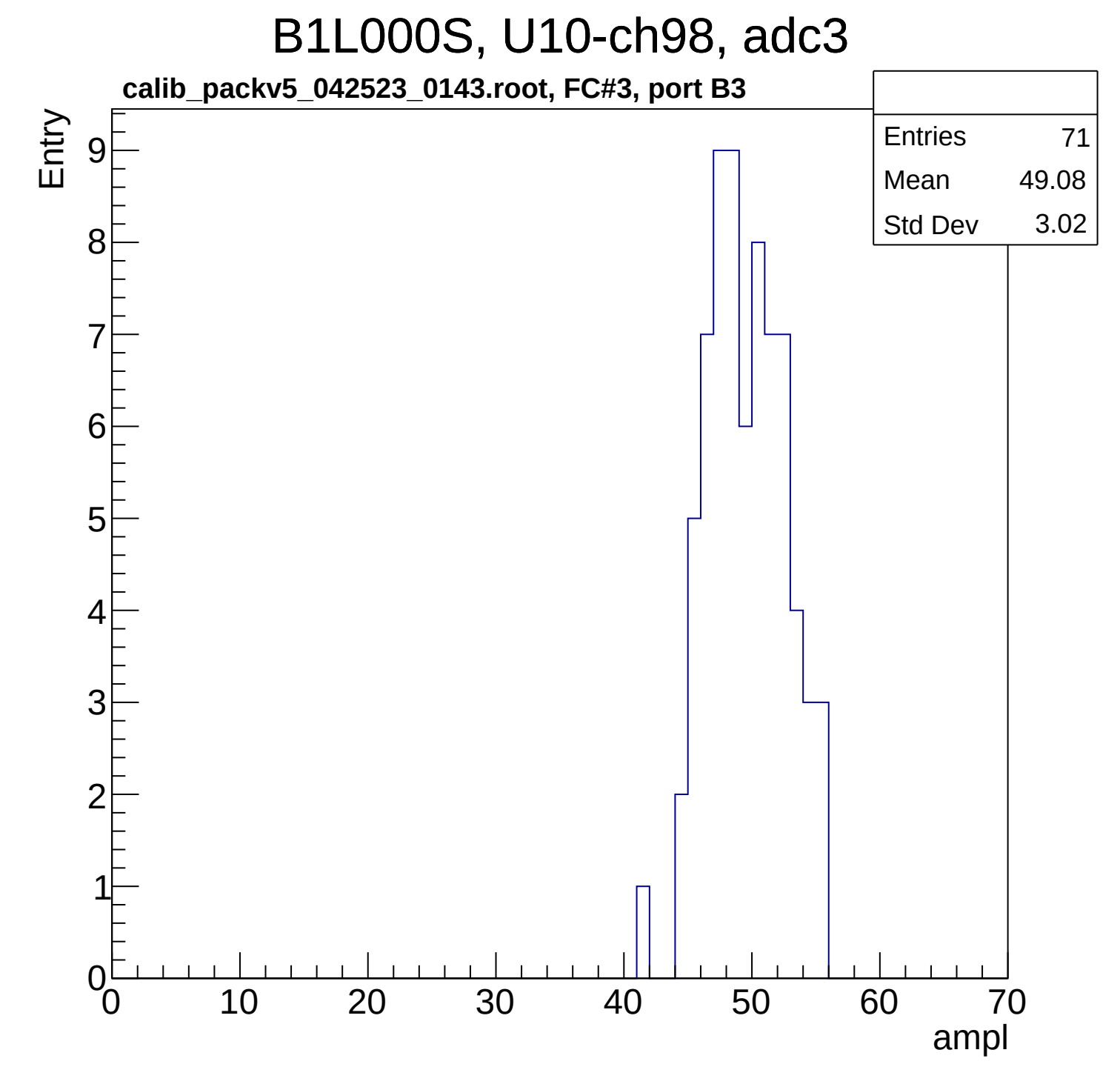

B1L000S, U10-ch98, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.79
Std Dev 2.881
Gaus mean : 42.3432
Gaus Width: 2.9447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch98, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 49.08
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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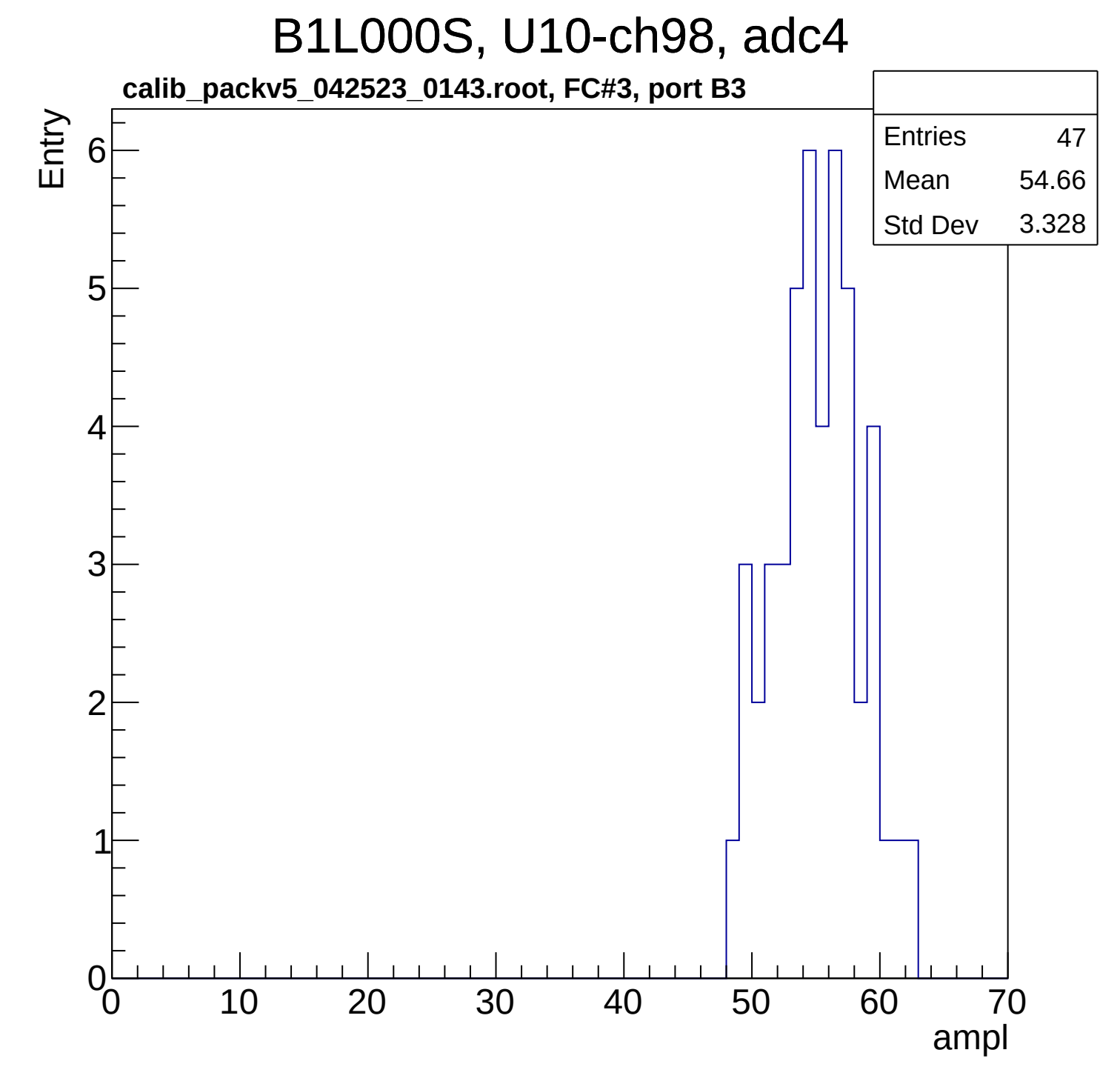
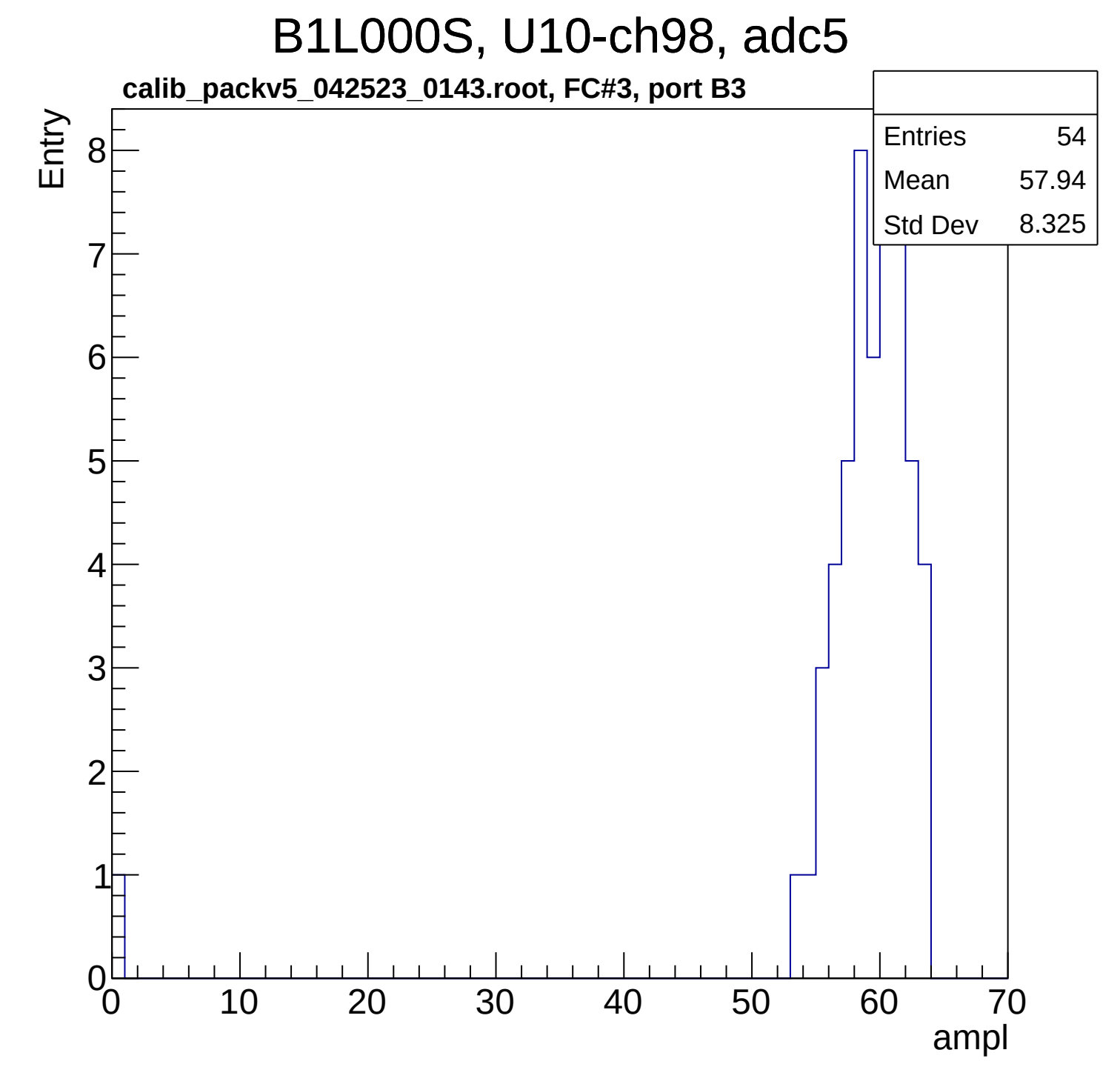
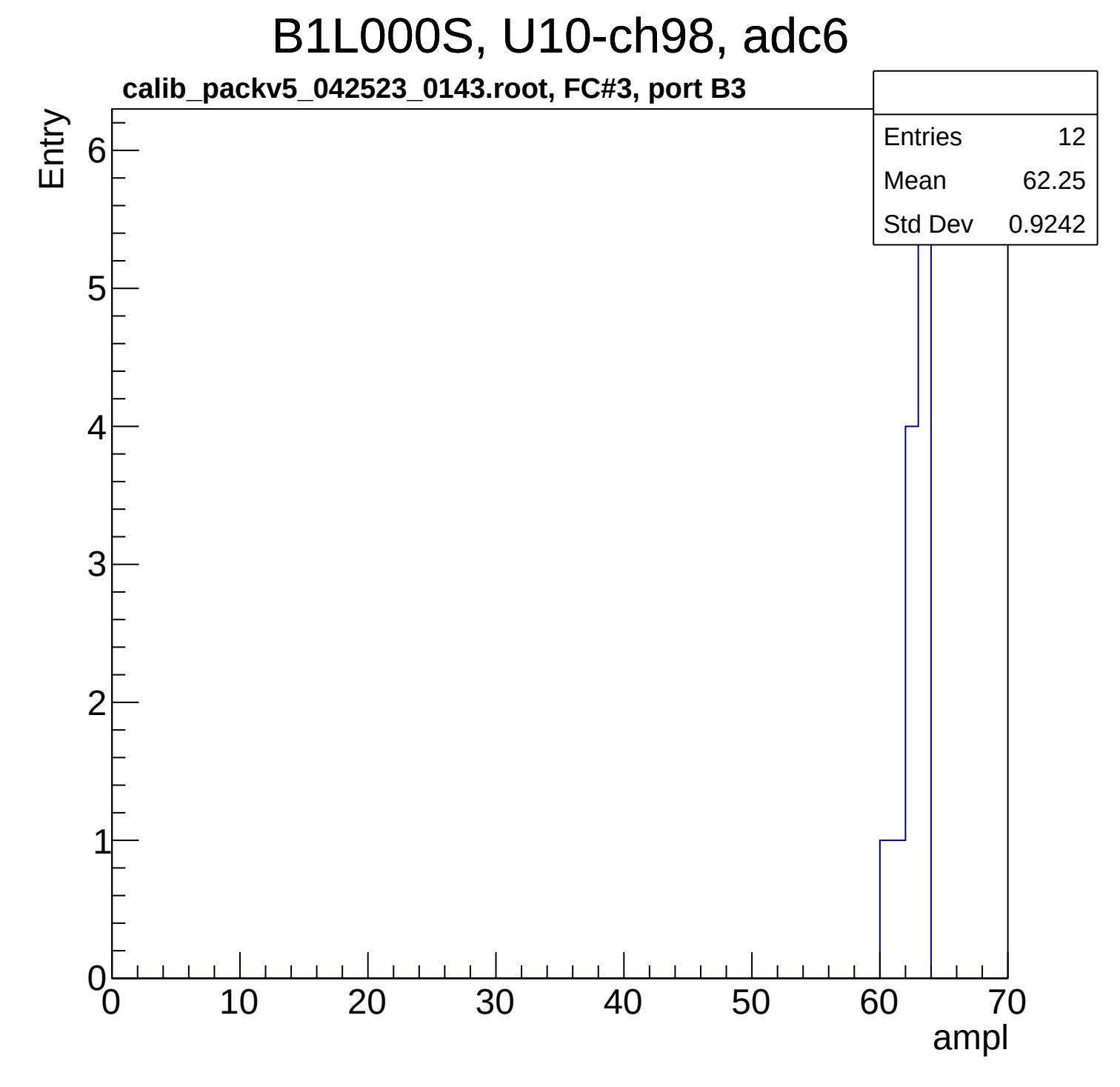
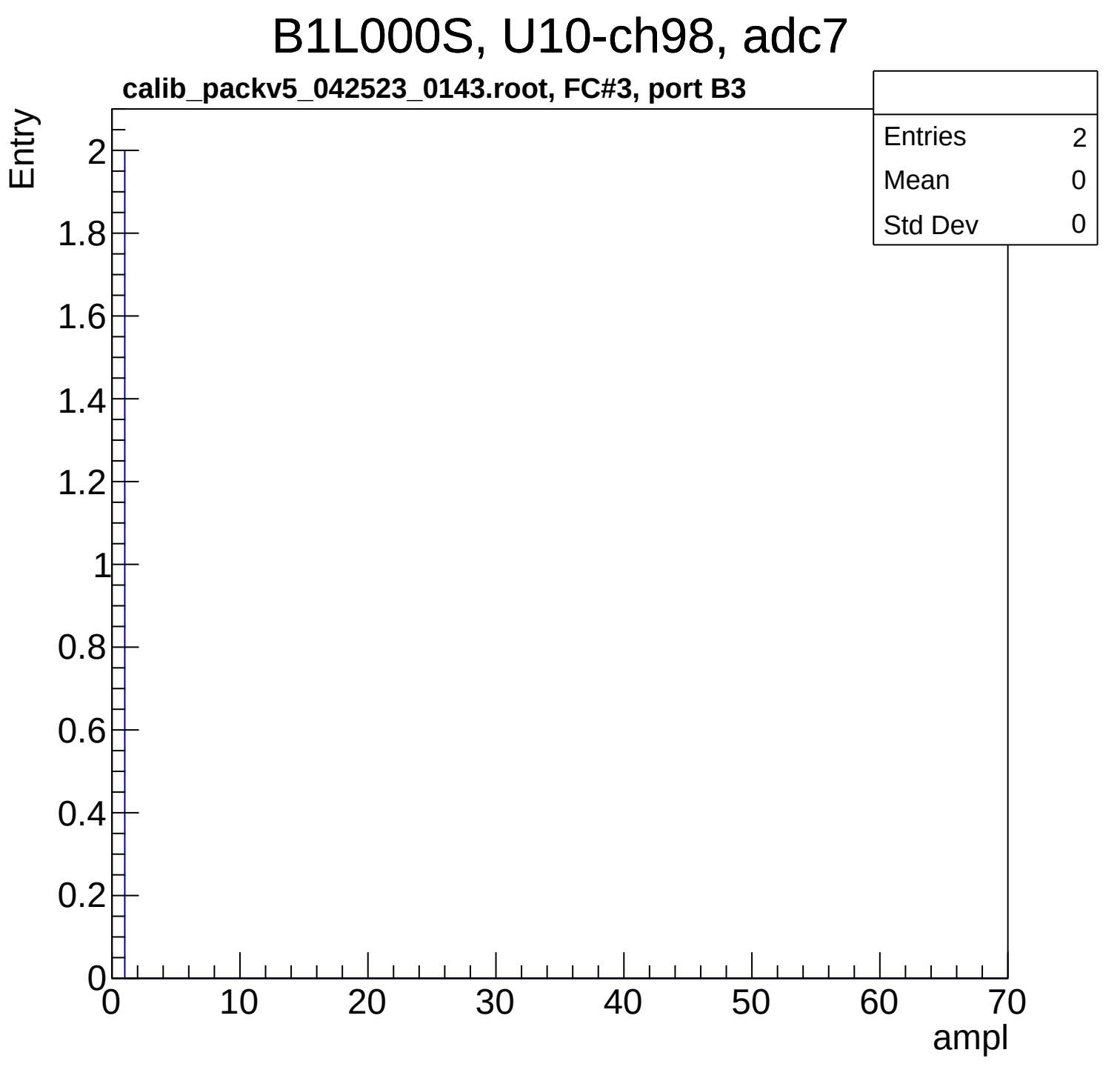
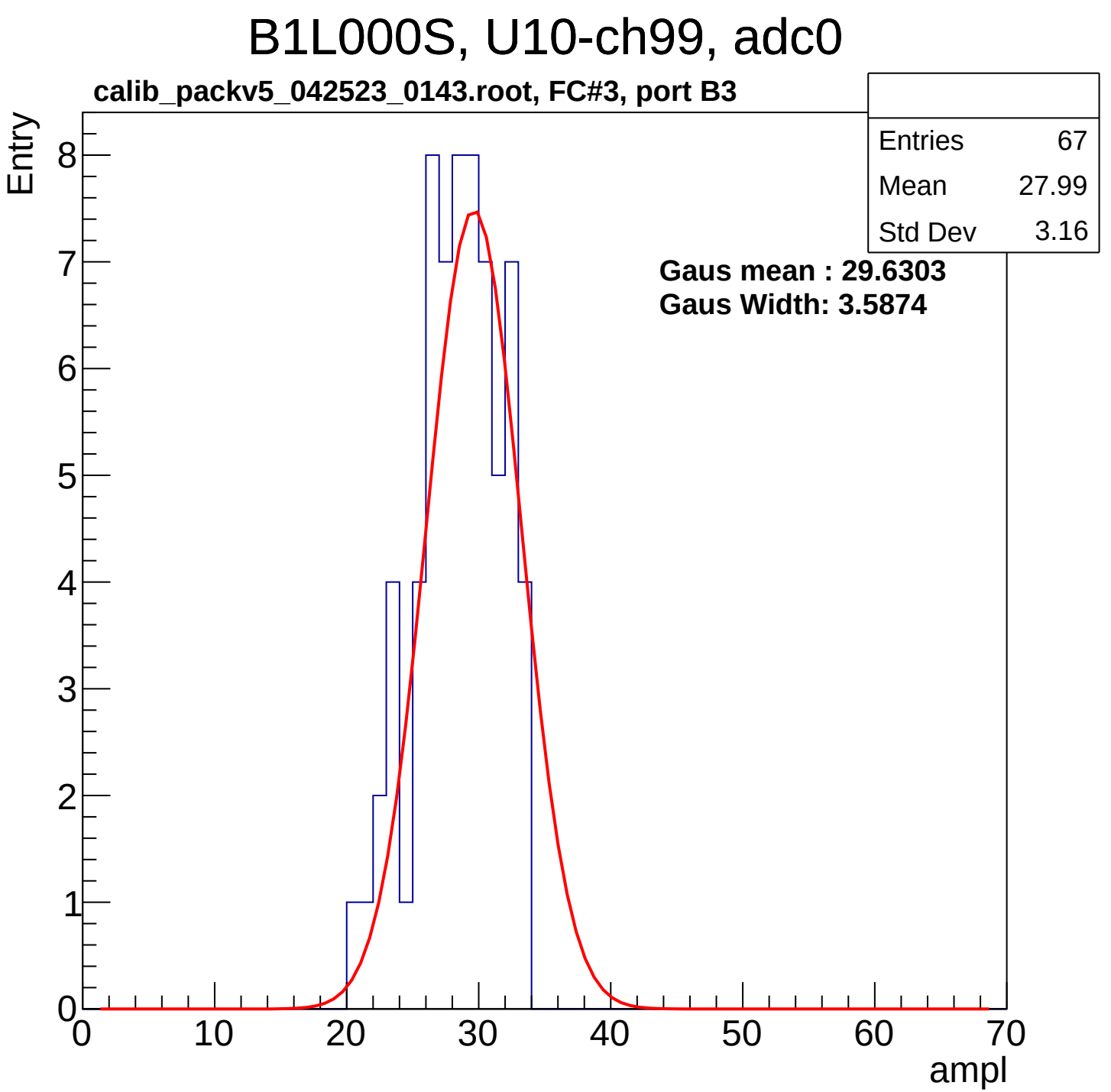
40
50
60
70


B1L000S, U10-ch98, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 54.66
Std Dev 3.328
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch98, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.94
Std Dev 8.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch98, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 62.25
Std Dev 0.9242
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch98, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch99, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.99
Std Dev 3.16
Gaus mean : 29.6303
Gaus Width: 3.5874
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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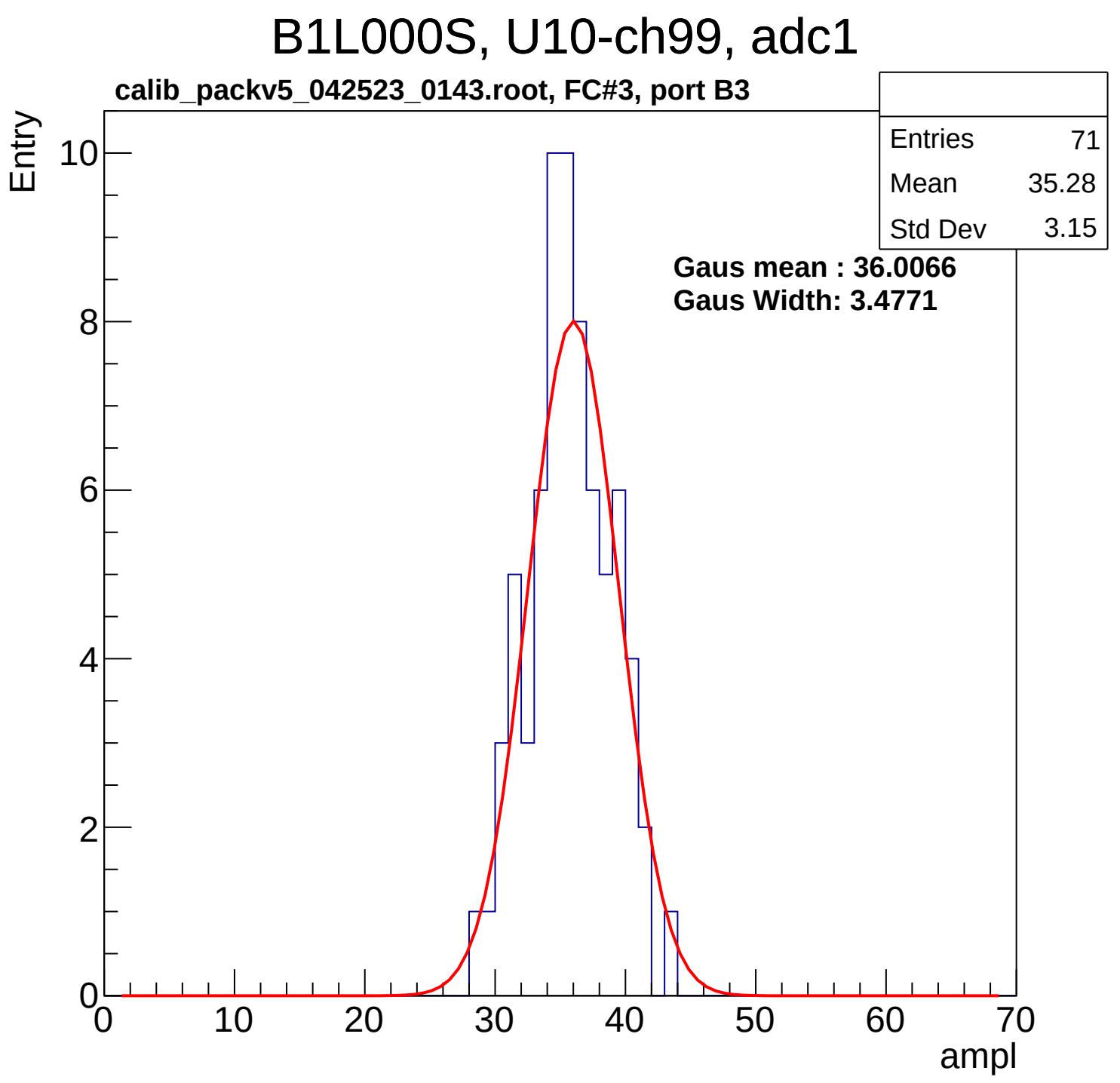
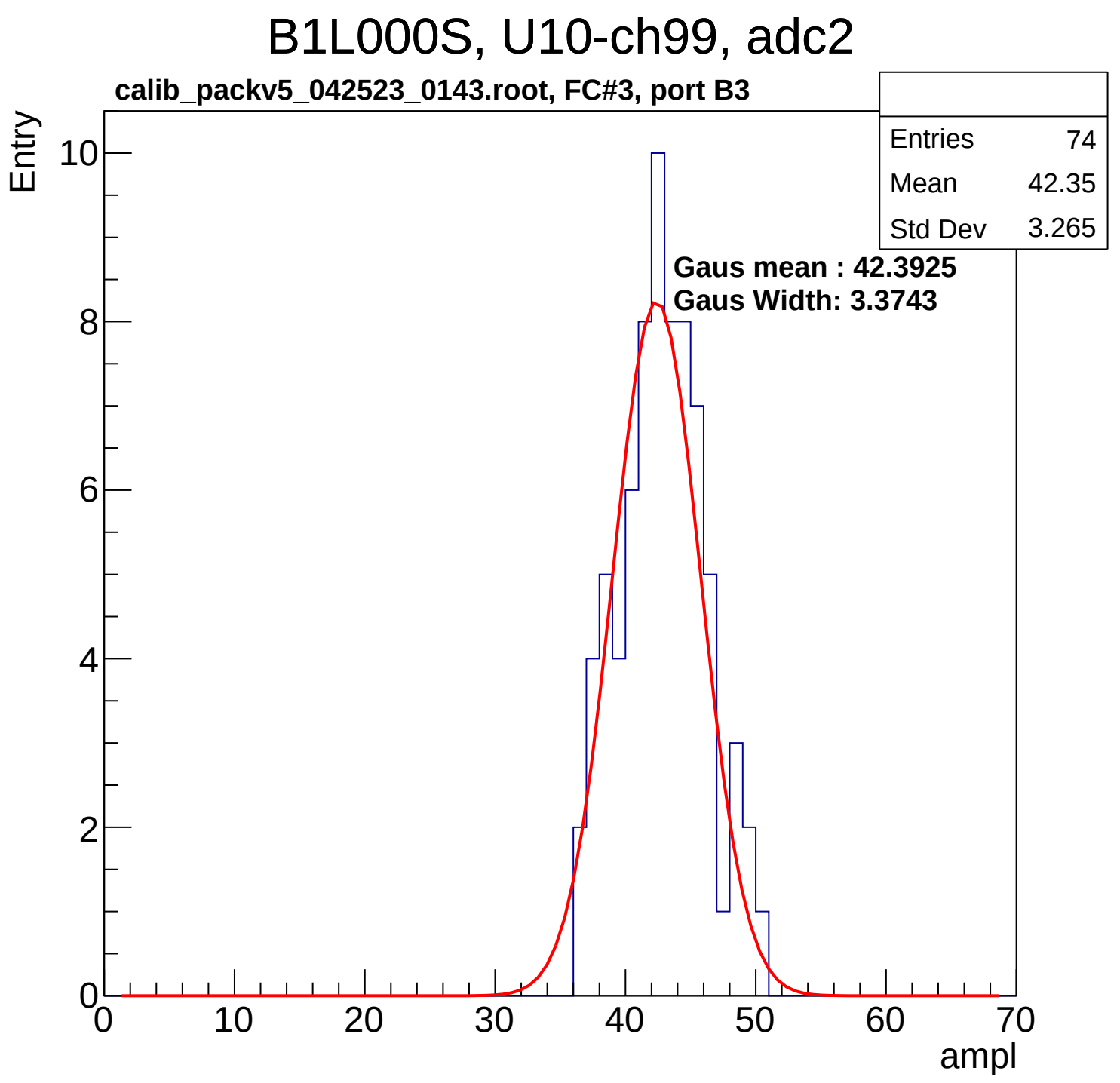
20
30
40
50
60
70


B1L000S, U10-ch99, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.28
Std Dev 3.15
Gaus mean : 36.0066
Gaus Width: 3.4771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch99, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 42.35
Std Dev 3.265
Gaus mean : 42.3925
Gaus Width: 3.3743
Entry
ampl
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6
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0
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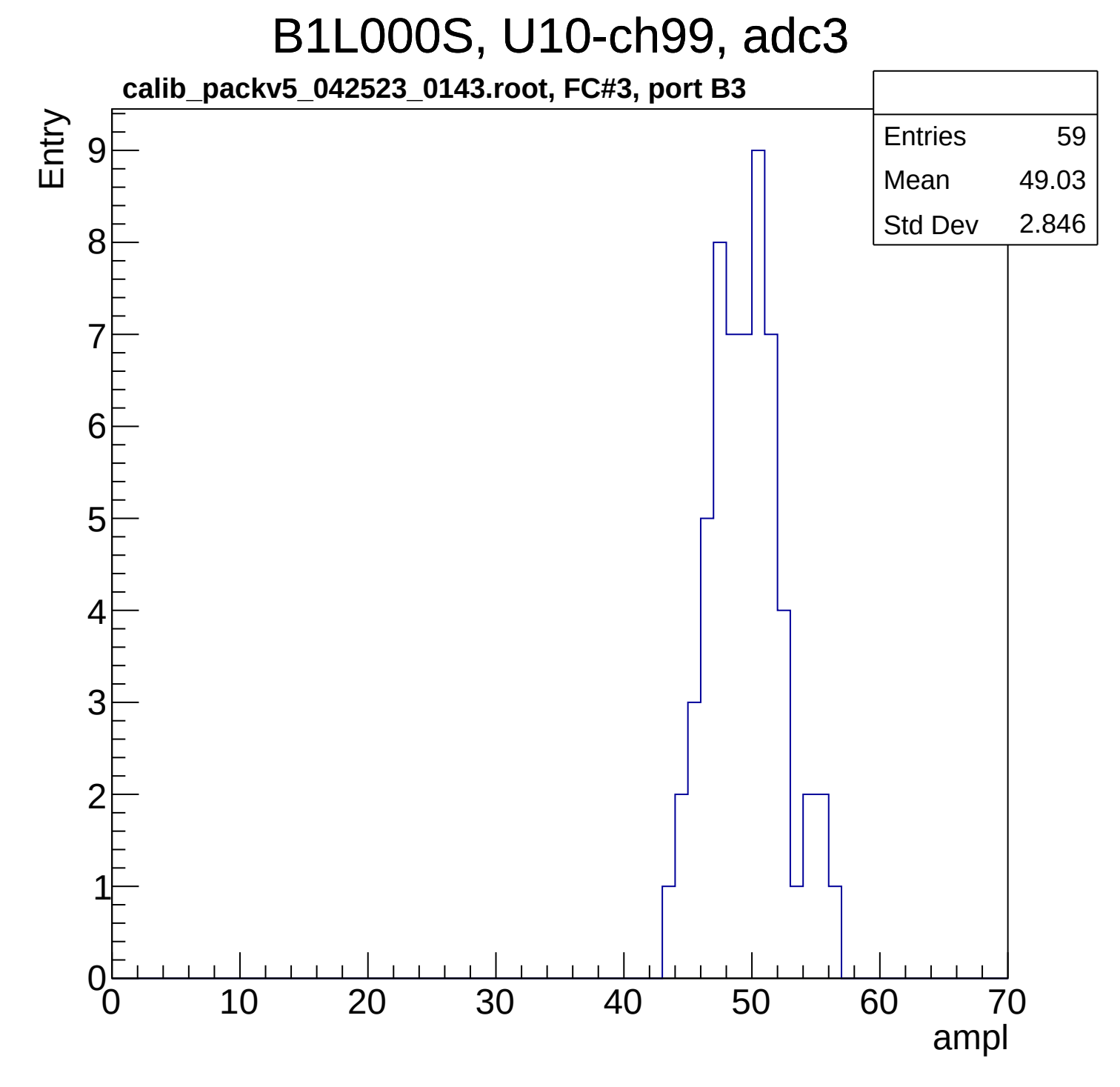
40
50
60
70


B1L000S, U10-ch99, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 49.03
Std Dev 2.846
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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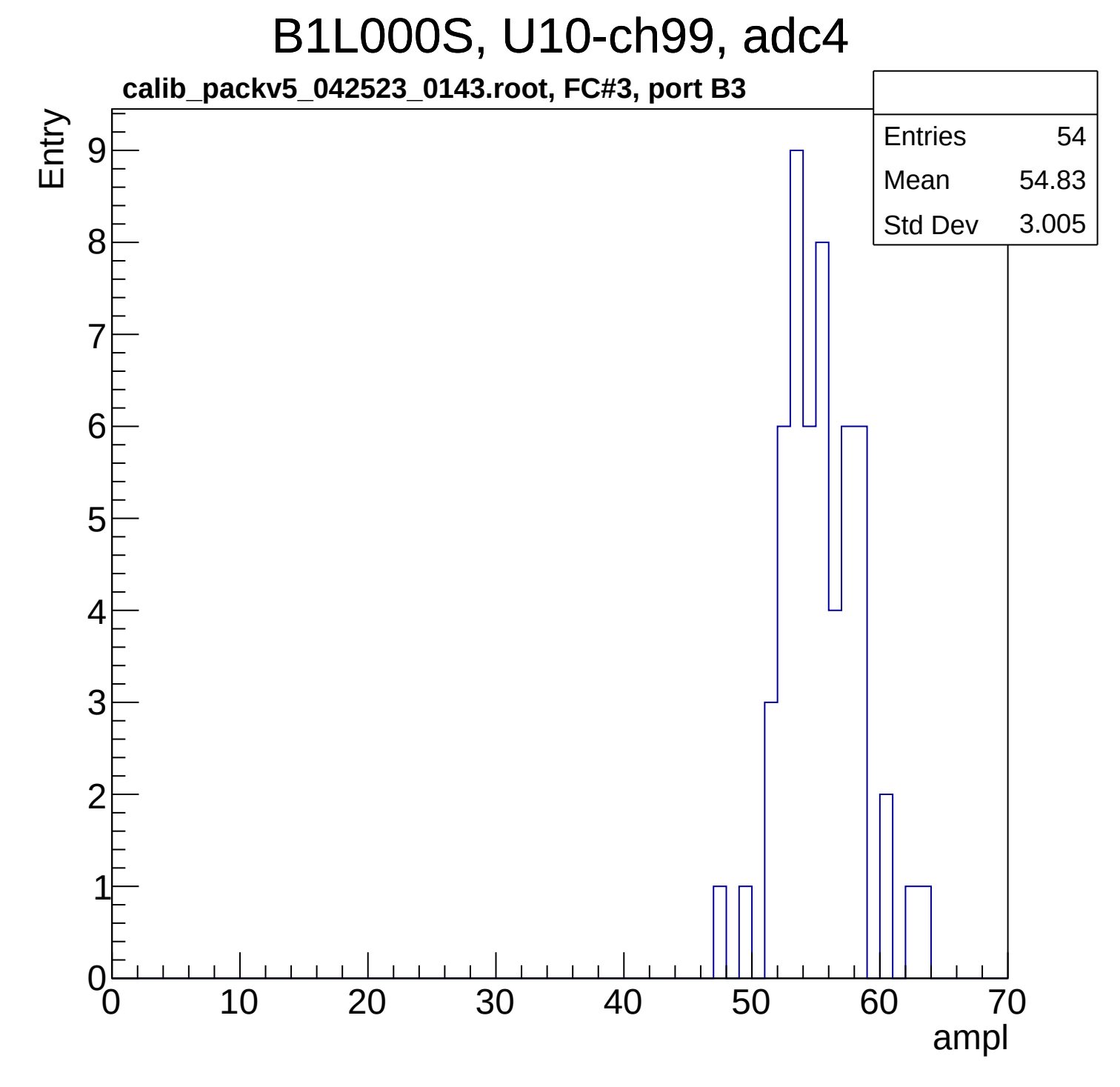
60
70


B1L000S, U10-ch99, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.83
Std Dev 3.005
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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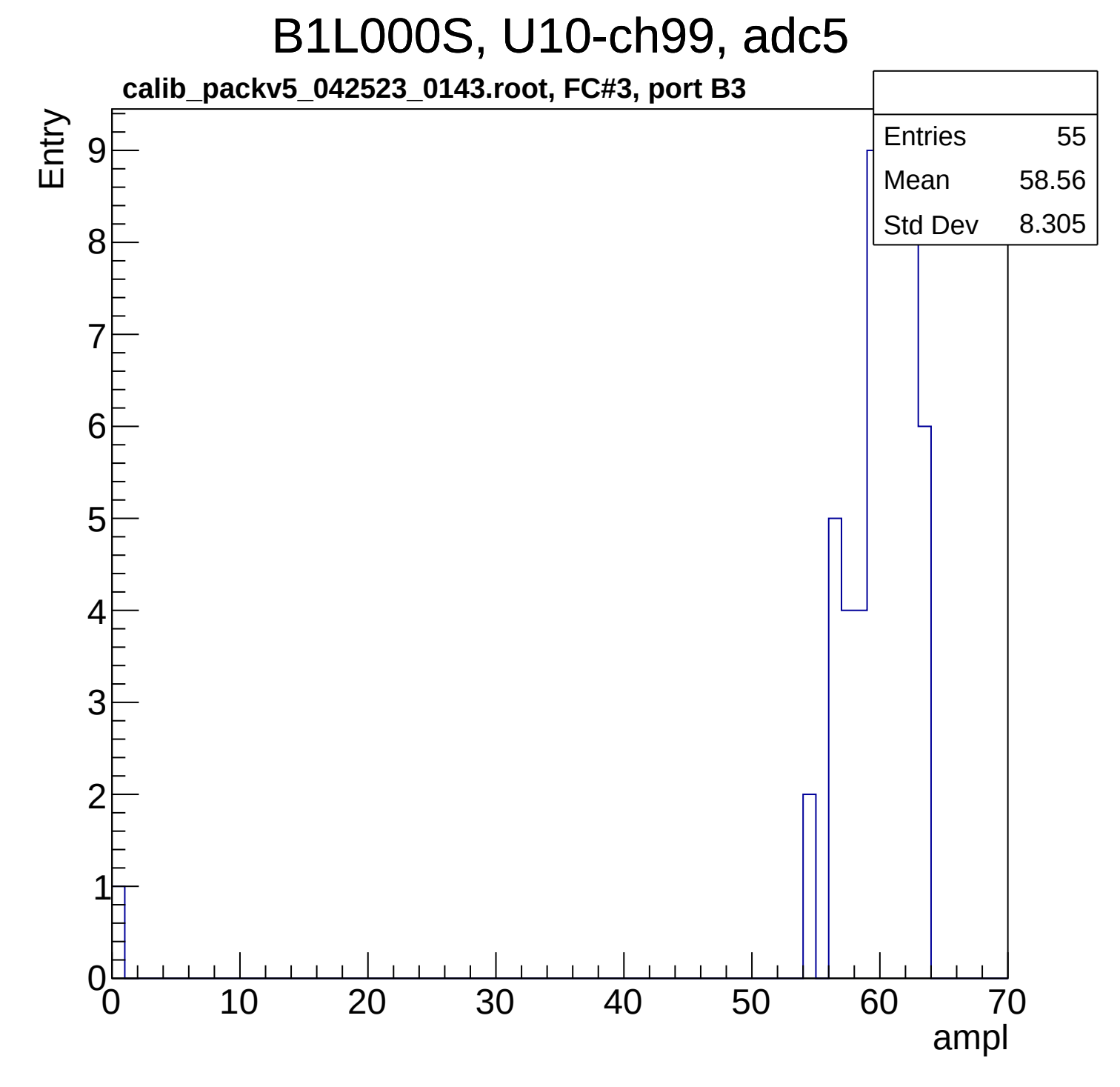
B1L000S, U10-ch99, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.56
Std Dev 8.305
Entry
ampl
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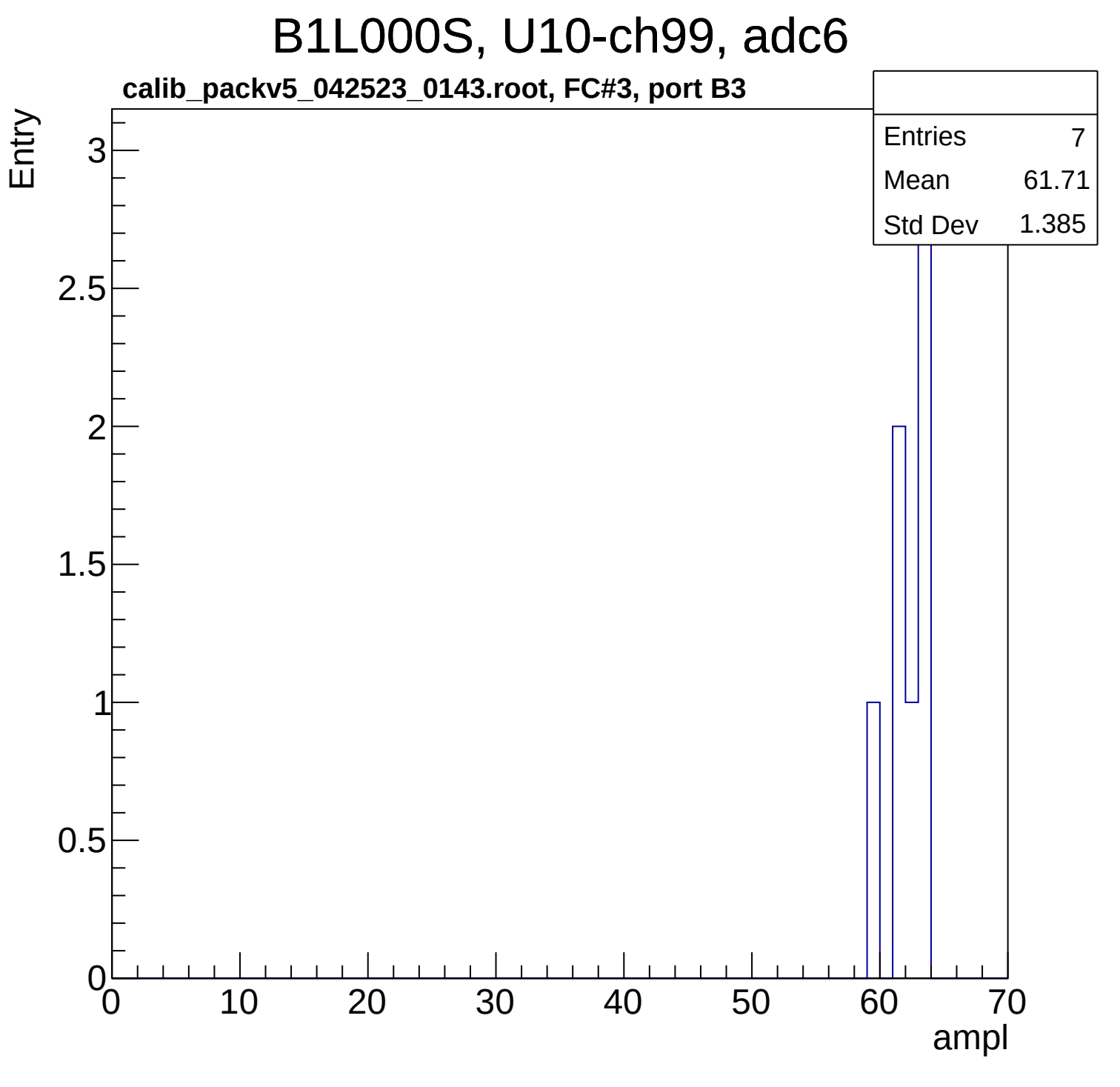
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch99, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 61.71
Std Dev 1.385
Entry
ampl
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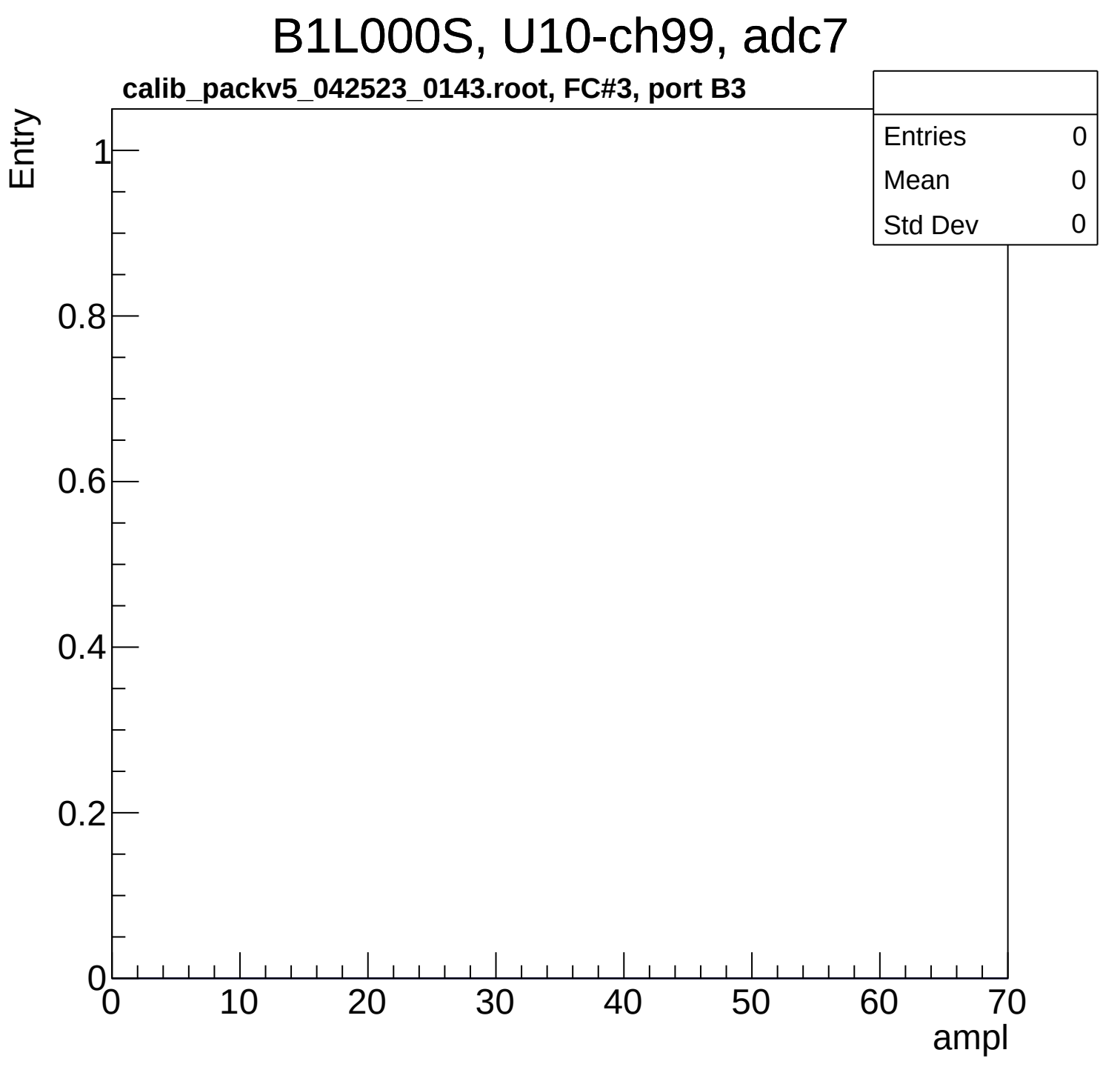
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch99, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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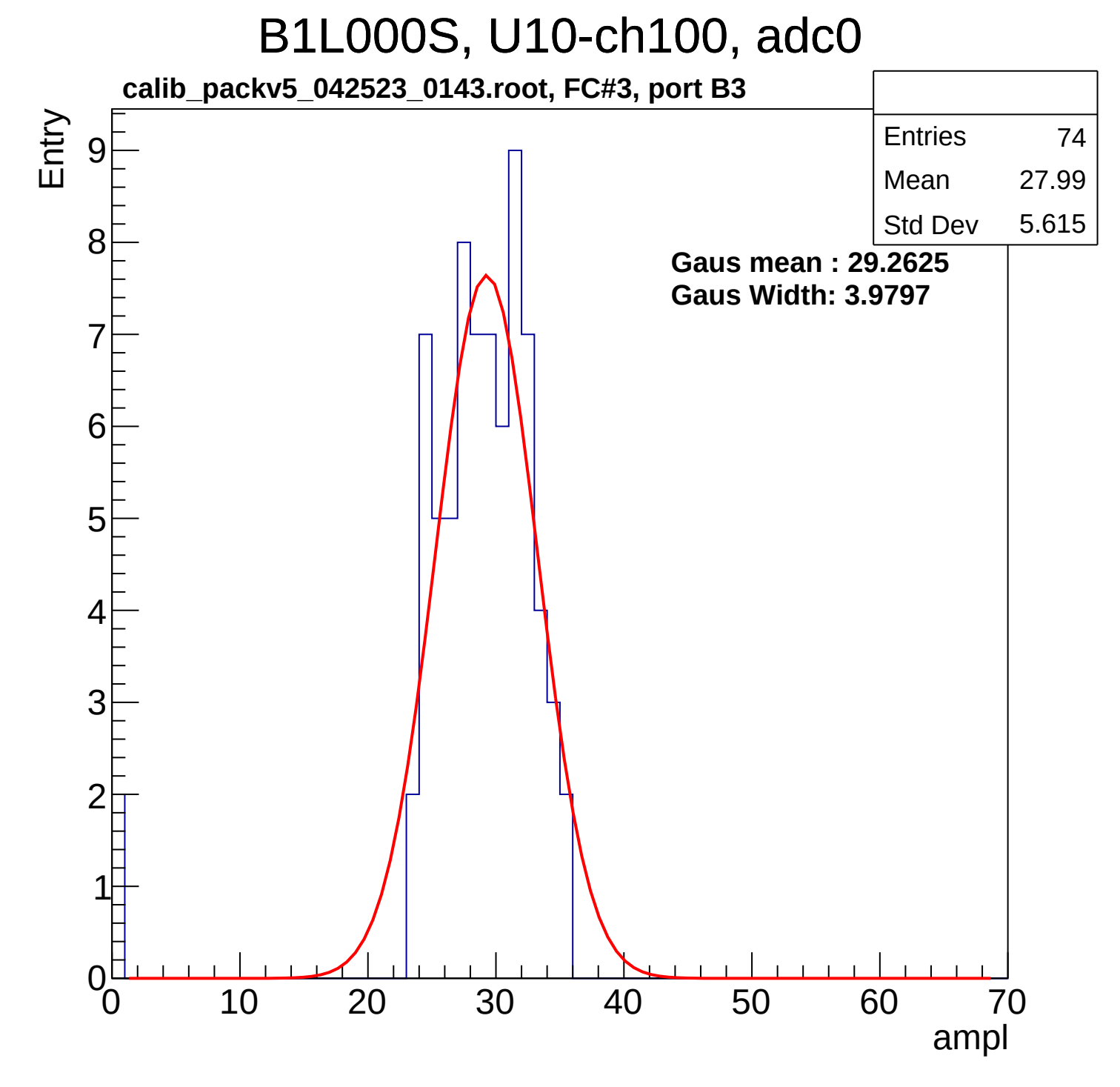
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch100, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 27.99
Std Dev 5.615
Gaus mean : 29.2625
Gaus Width: 3.9797
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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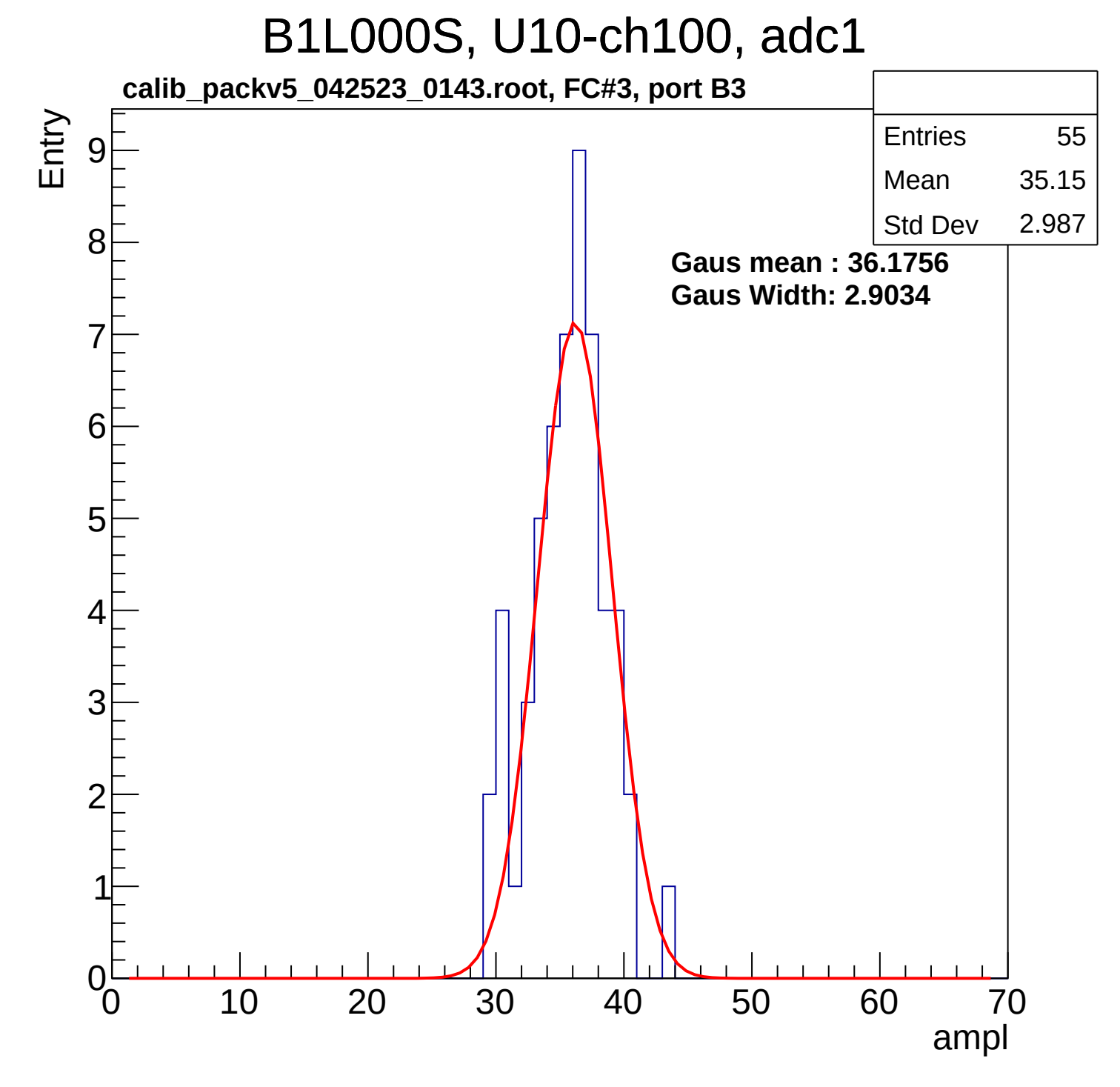
40
50
60
70


B1L000S, U10-ch100, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 35.15
Std Dev 2.987
Gaus mean : 36.1756
Gaus Width: 2.9034
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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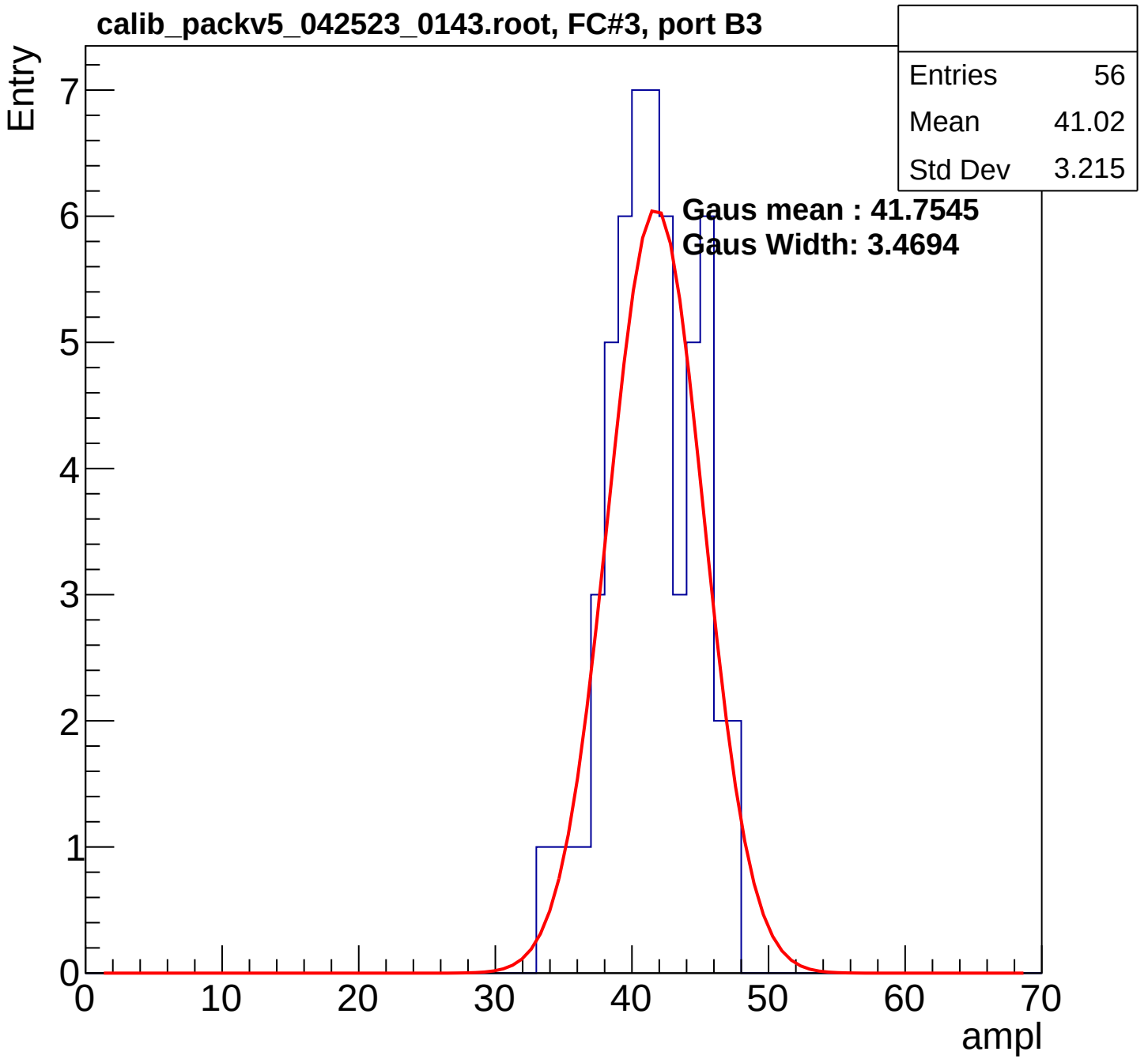
B1L000S, U10-ch100, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.02
Std Dev 3.215
Gaus mean : 41.7545
Gaus Width: 3.4694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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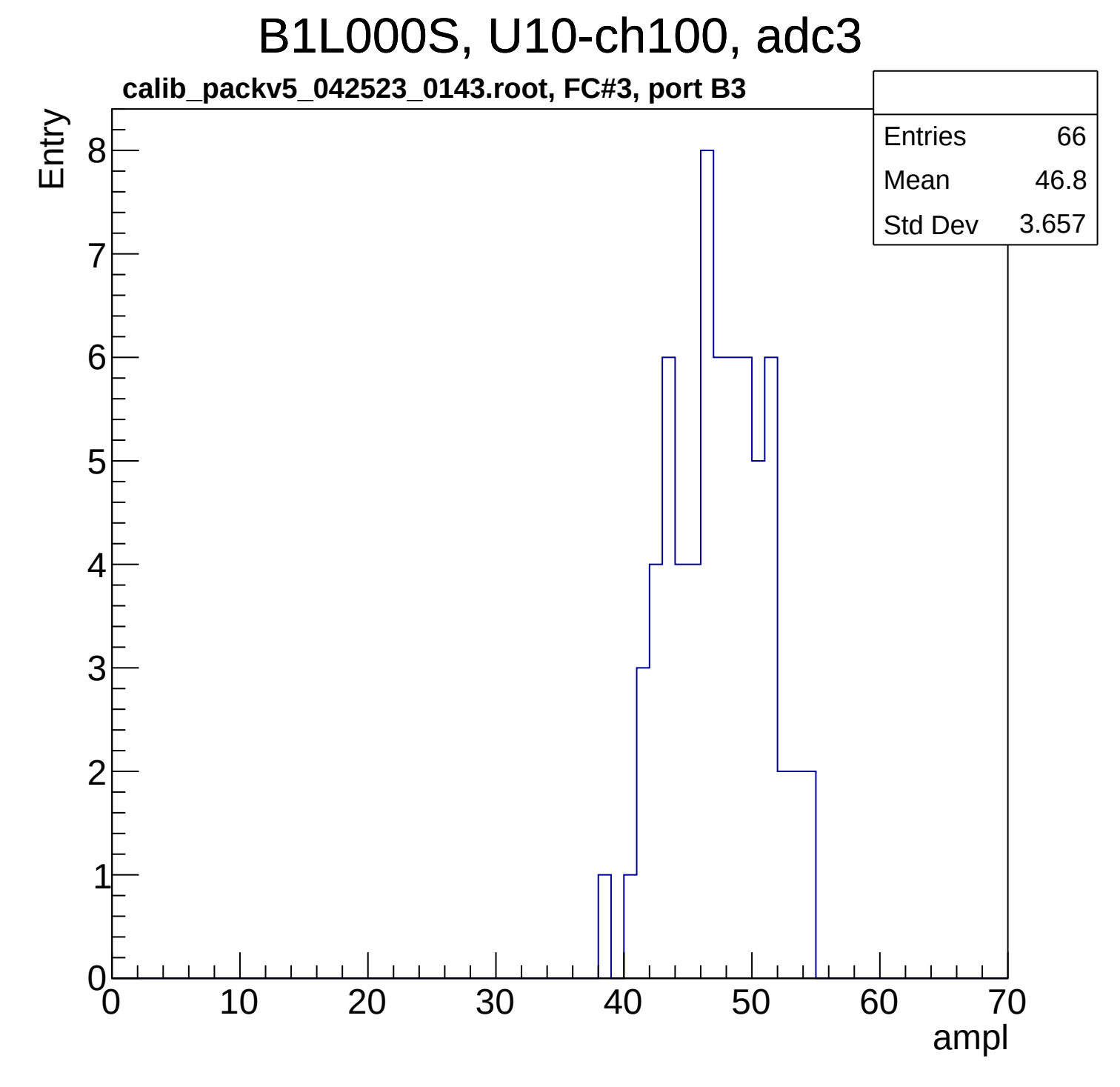
B1L000S, U10-ch100, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 46.8
Std Dev 3.657
Entry
ampl
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2
3
4
5
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0
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20
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40
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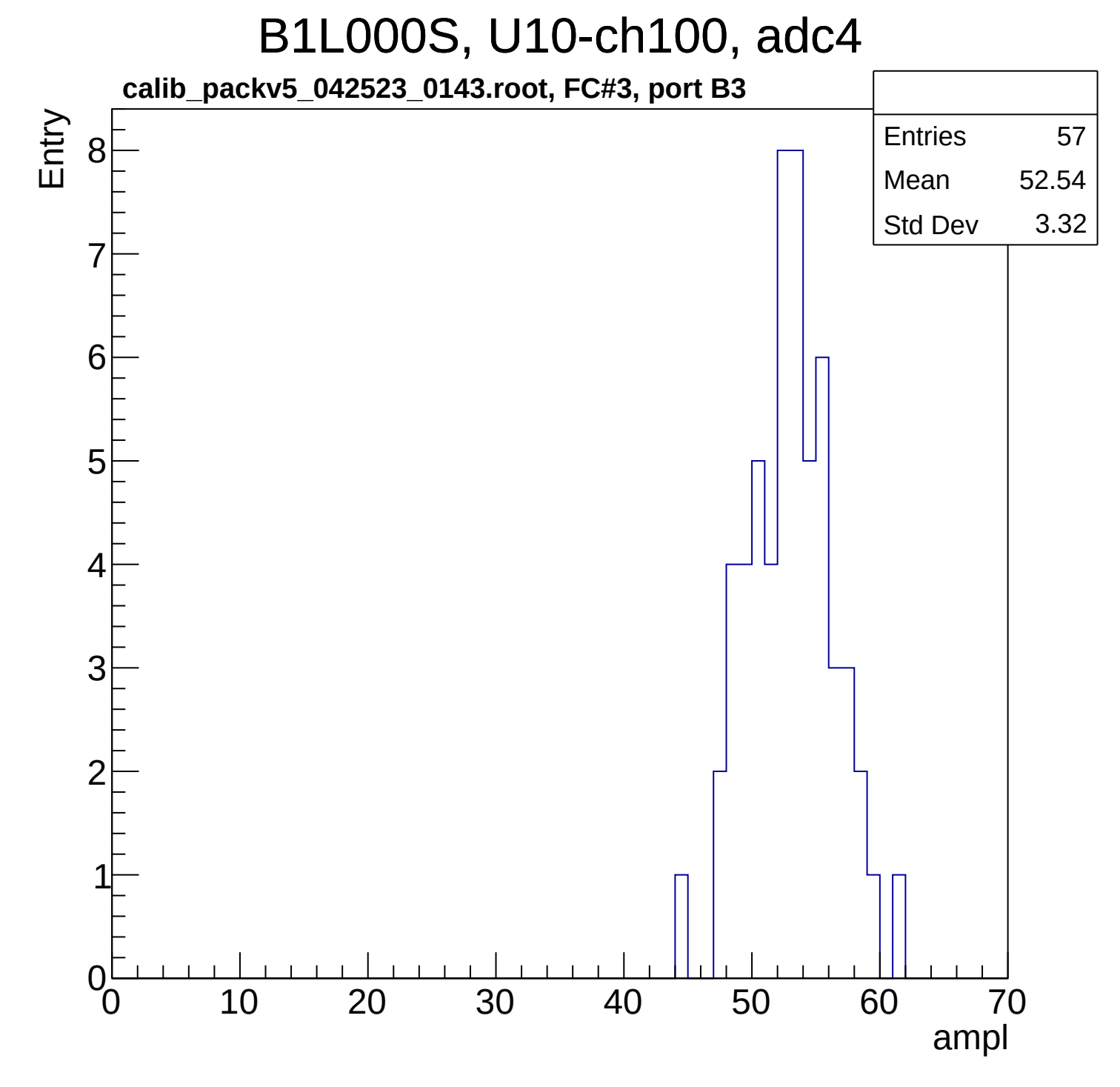
B1L000S, U10-ch100, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 52.54
Std Dev 3.32
Entry
ampl
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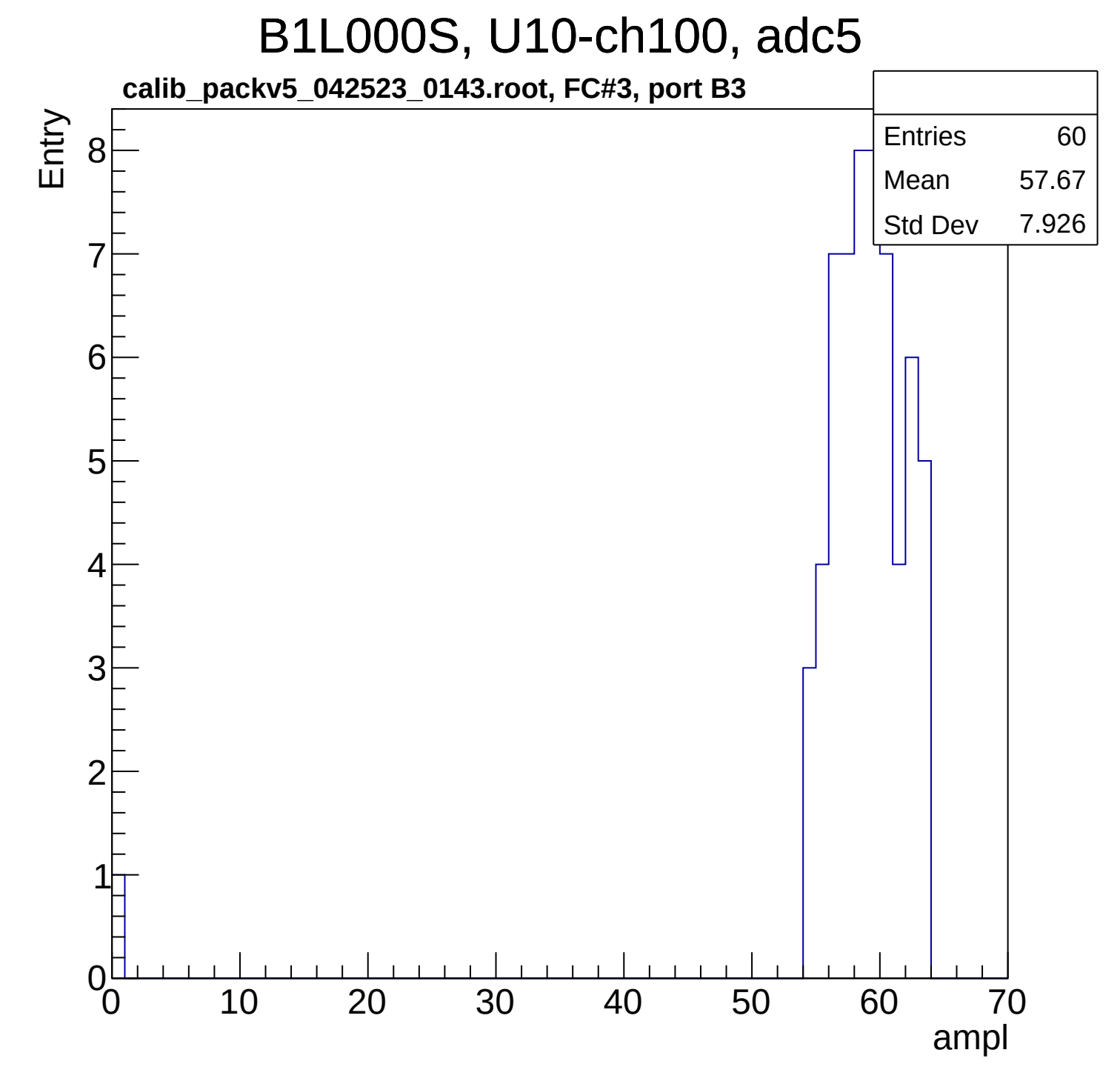
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch100, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 57.67
Std Dev 7.926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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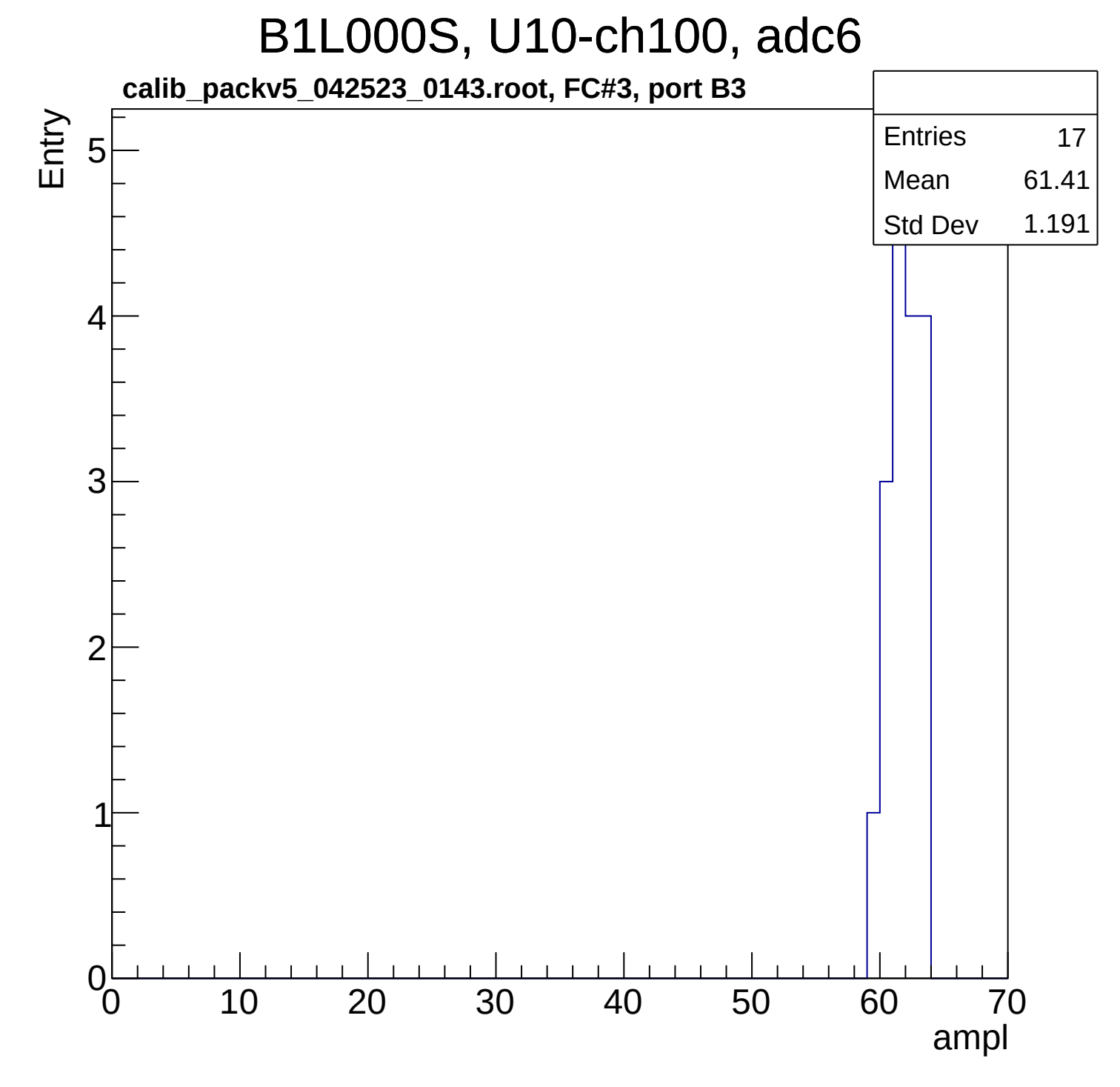
B1L000S, U10-ch100, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.41
Std Dev 1.191
Entry
ampl
0
1
2
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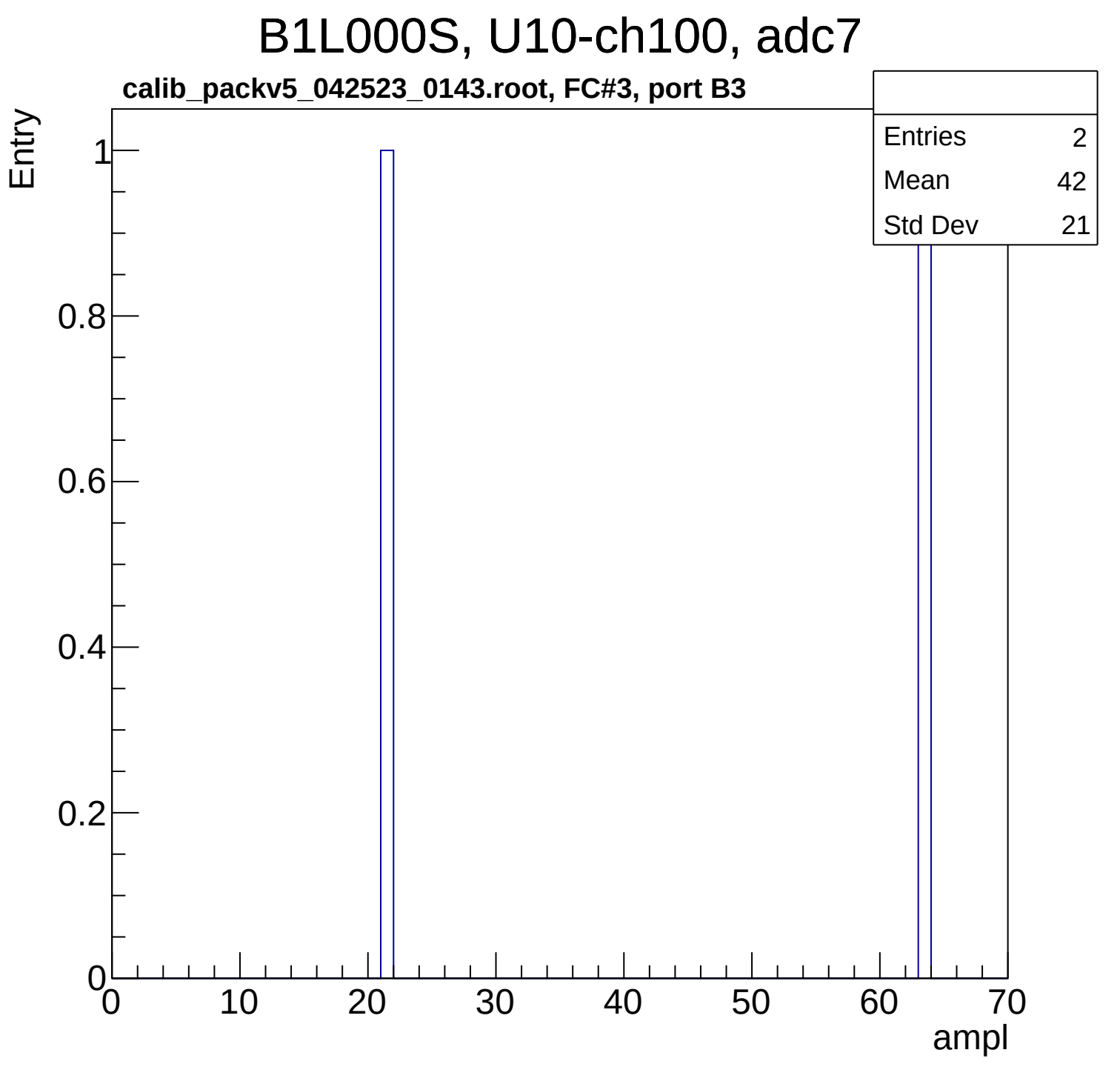
B1L000S, U10-ch100, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 42
Std Dev 21
Entry
ampl
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0.2
0.4
0.6
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0
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40
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60
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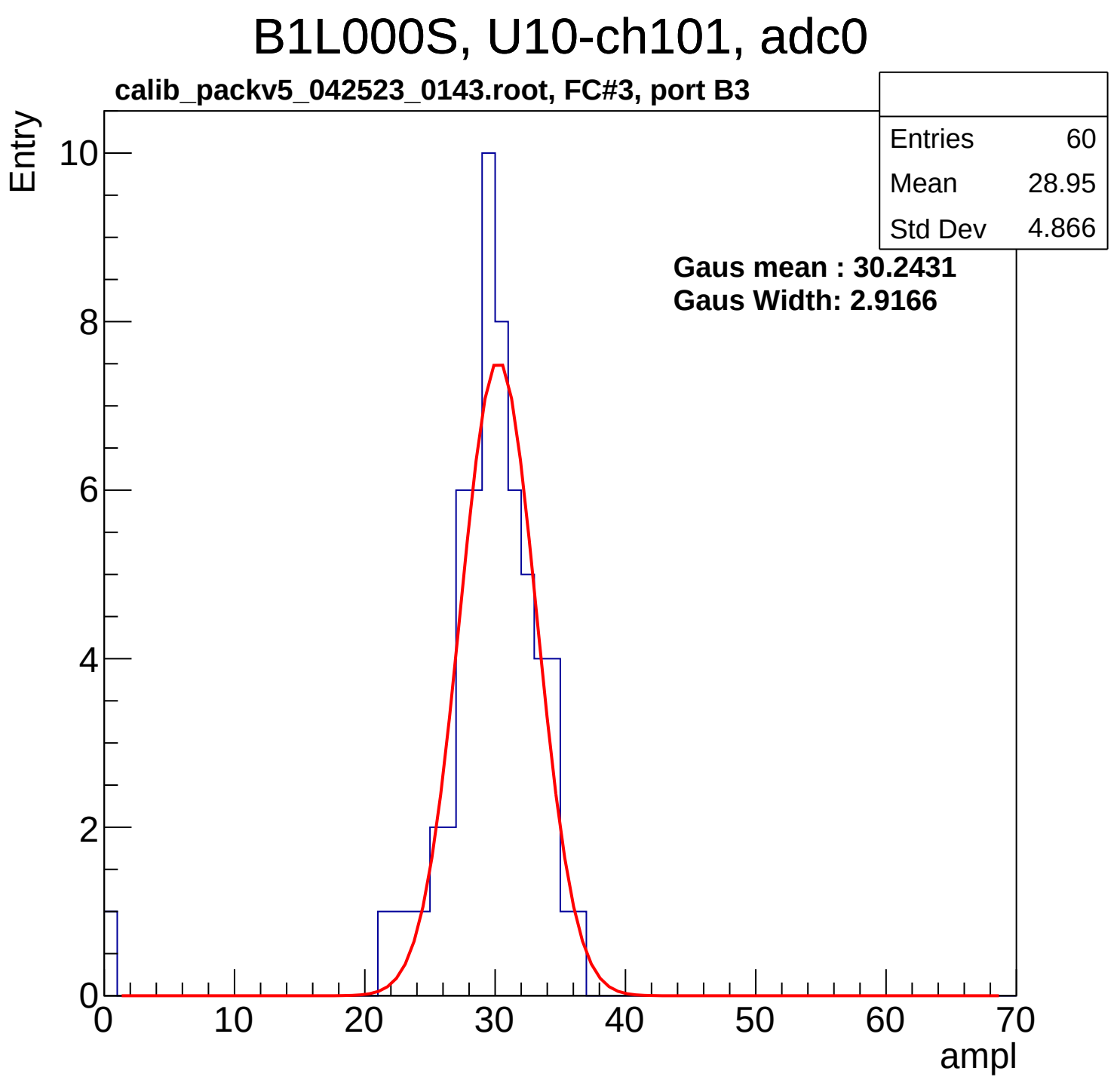
B1L000S, U10-ch101, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.95
Std Dev 4.866
Gaus mean : 30.2431
Gaus Width: 2.9166
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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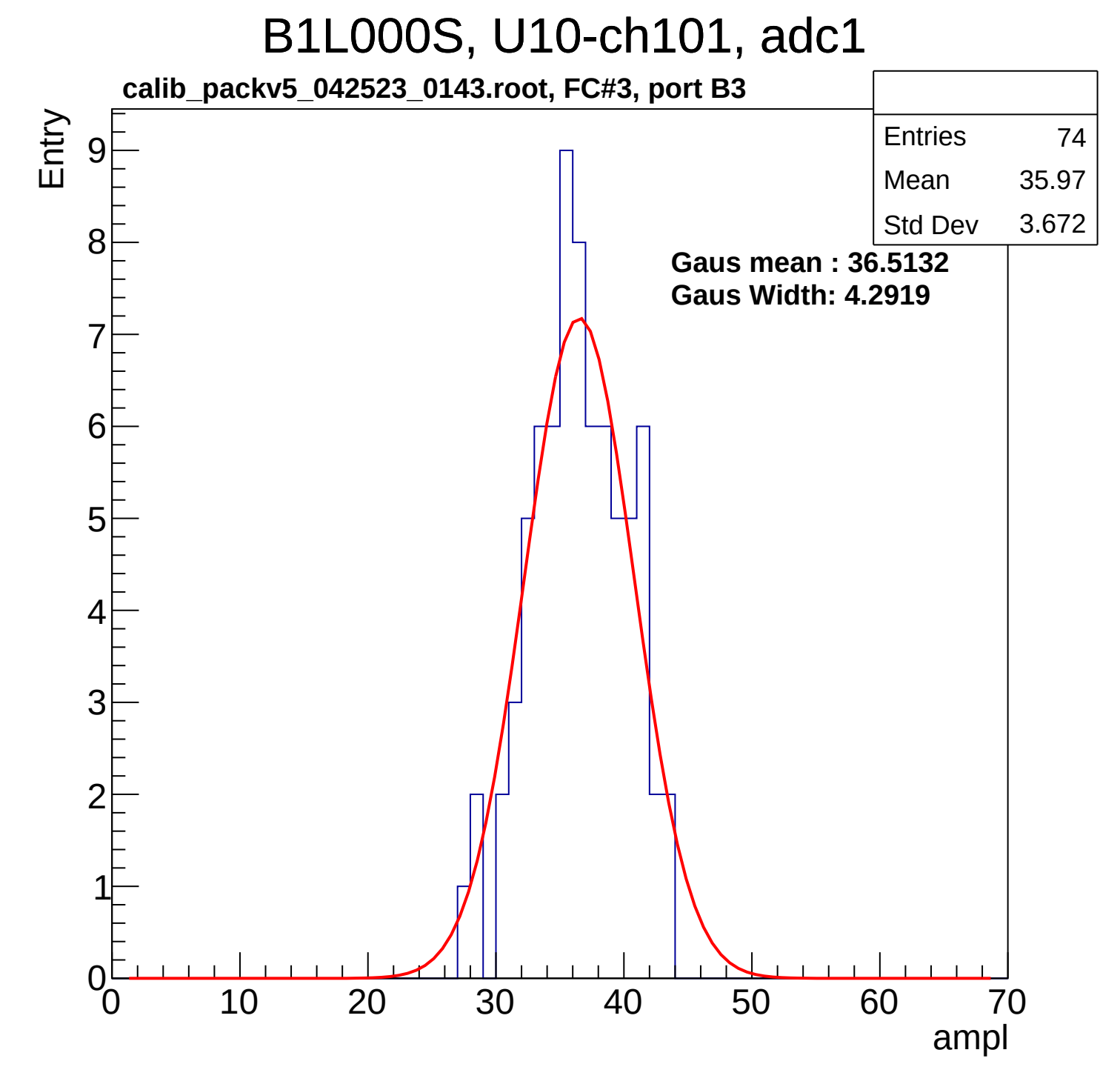
B1L000S, U10-ch101, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 35.97
Std Dev 3.672
Gaus mean : 36.5132
Gaus Width: 4.2919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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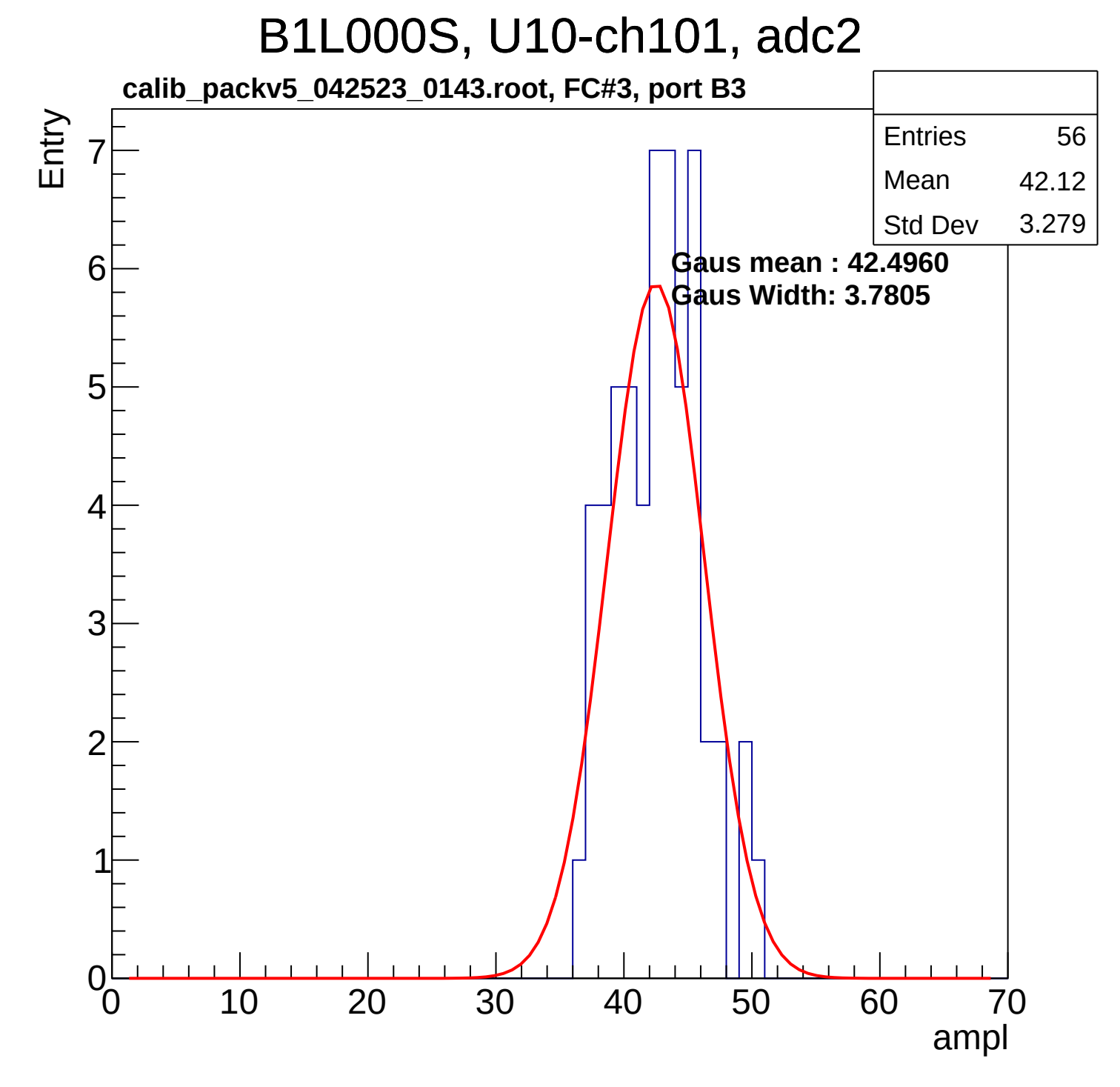
B1L000S, U10-ch101, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.12
Std Dev 3.279
Gaus mean : 42.4960
Gaus Width: 3.7805
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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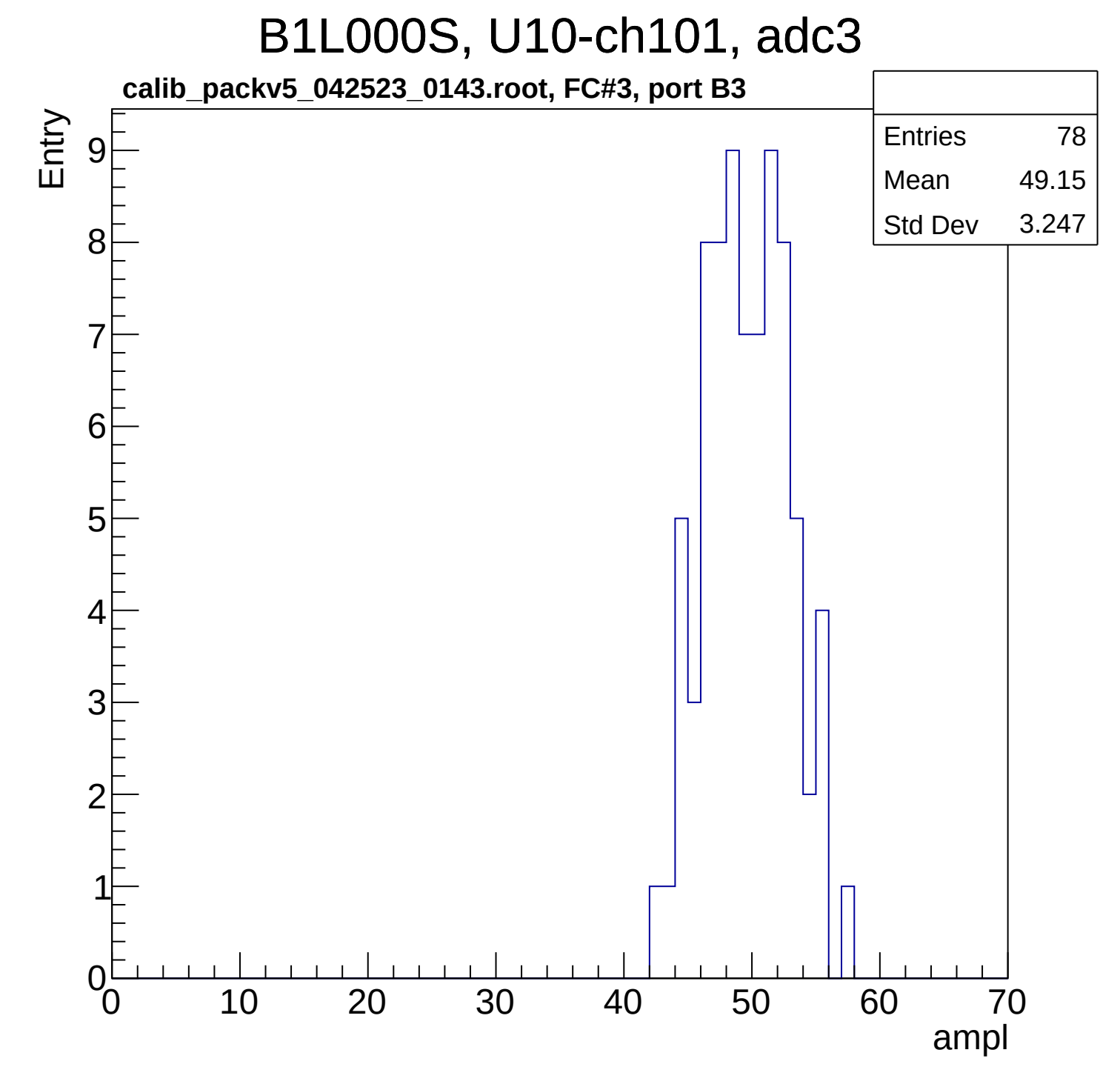
B1L000S, U10-ch101, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 49.15
Std Dev 3.247
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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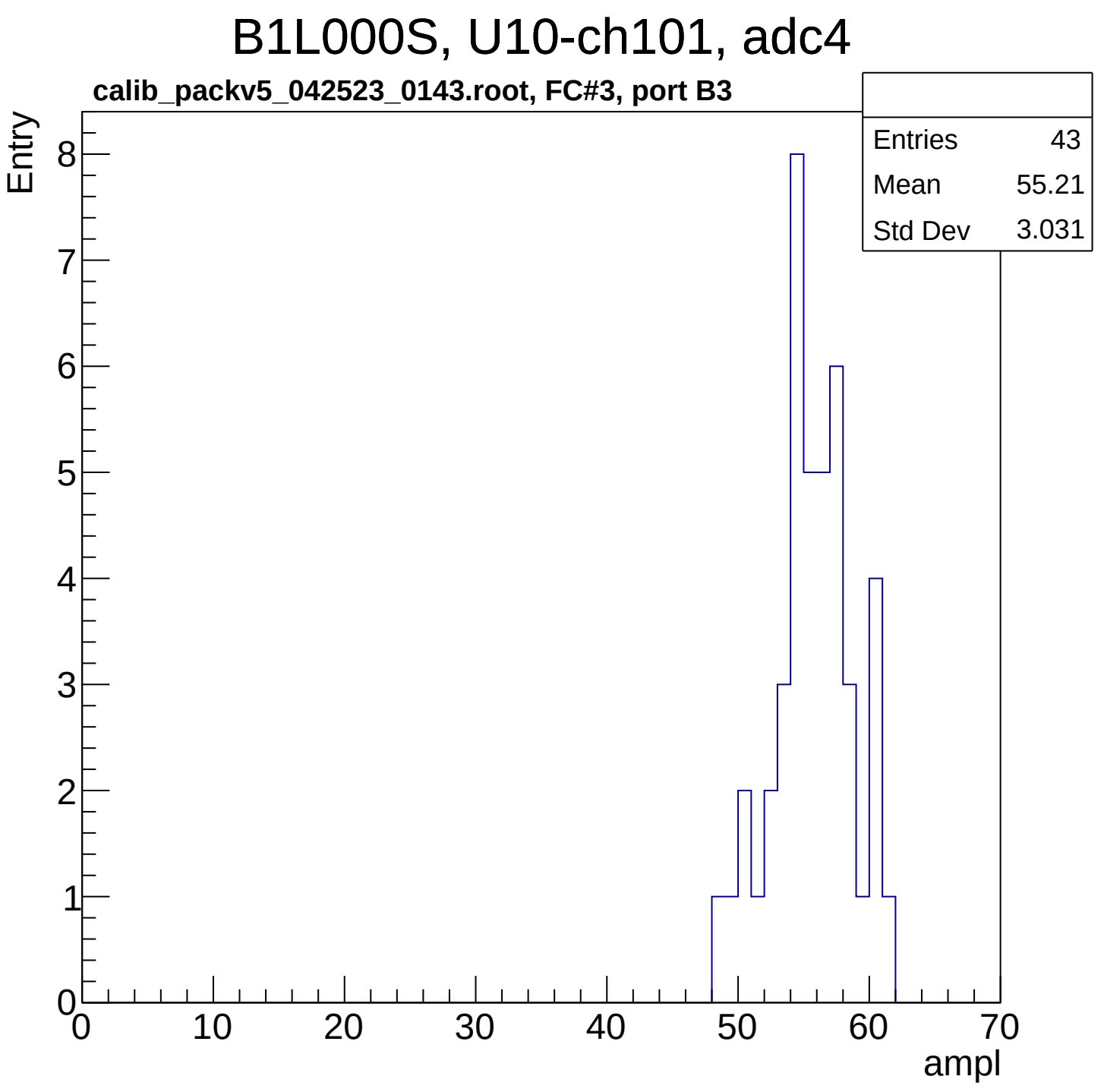
B1L000S, U10-ch101, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 55.21
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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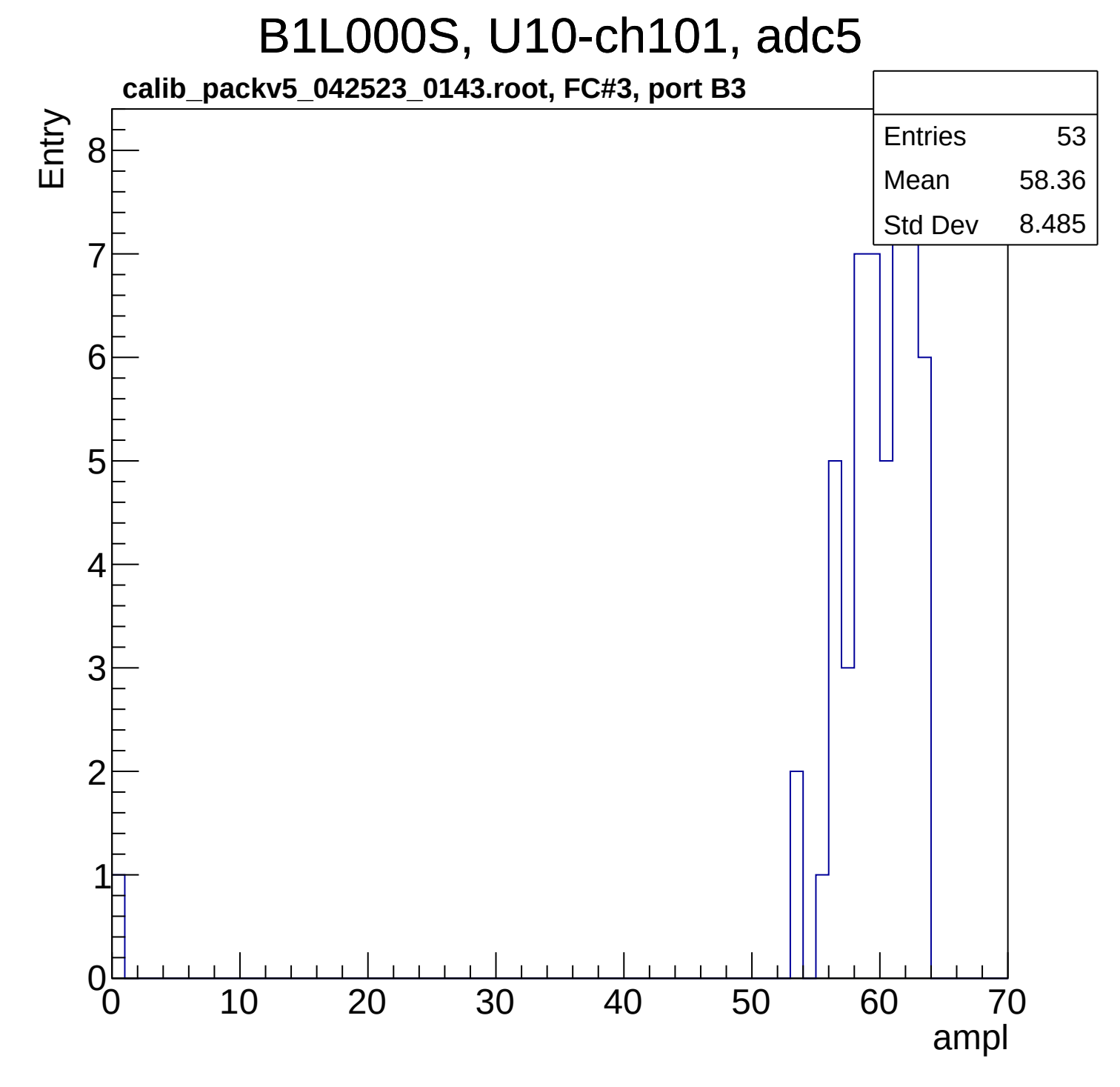
B1L000S, U10-ch101, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.36
Std Dev 8.485
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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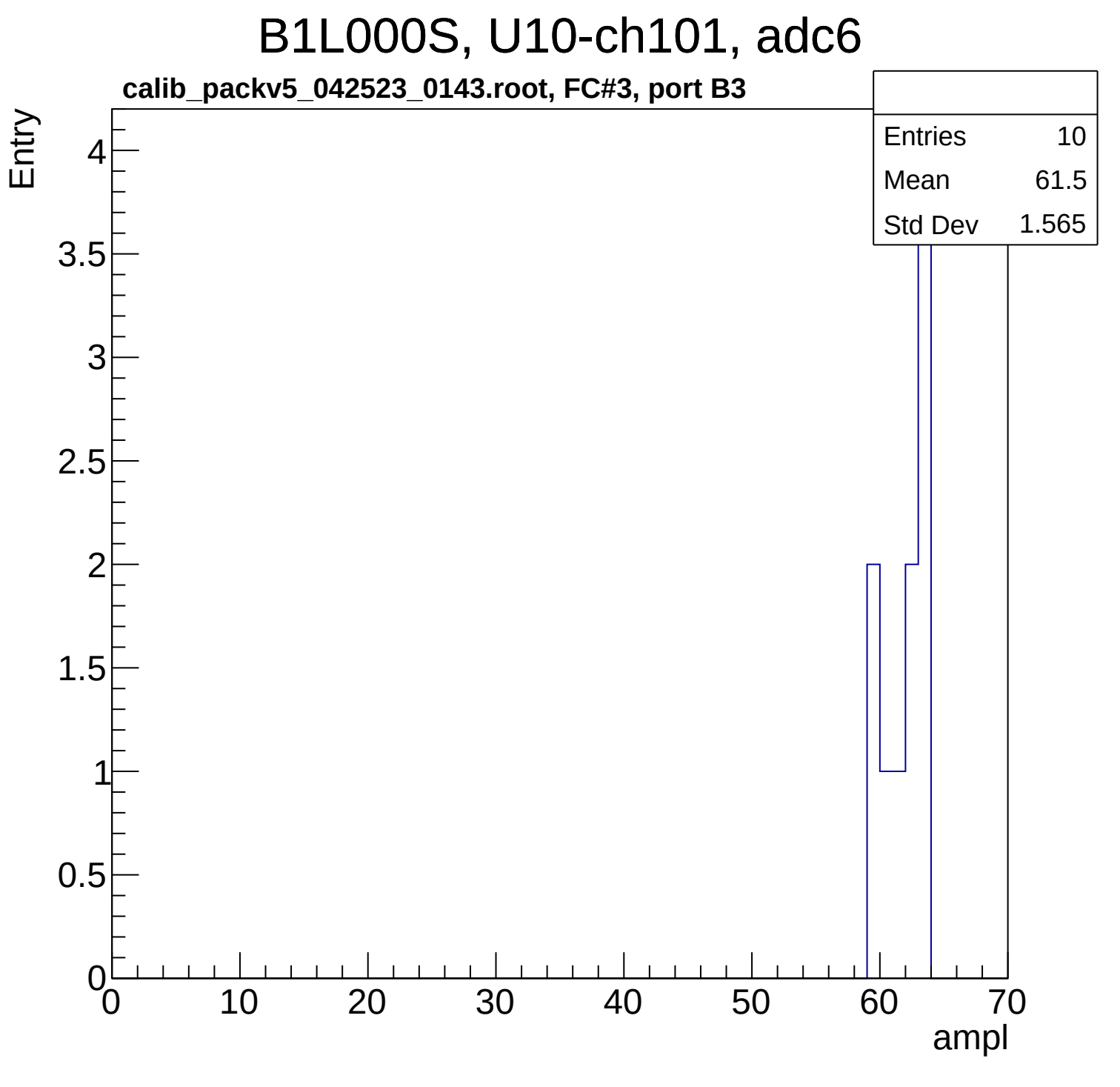
B1L000S, U10-ch101, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.5
Std Dev 1.565
Entry
ampl
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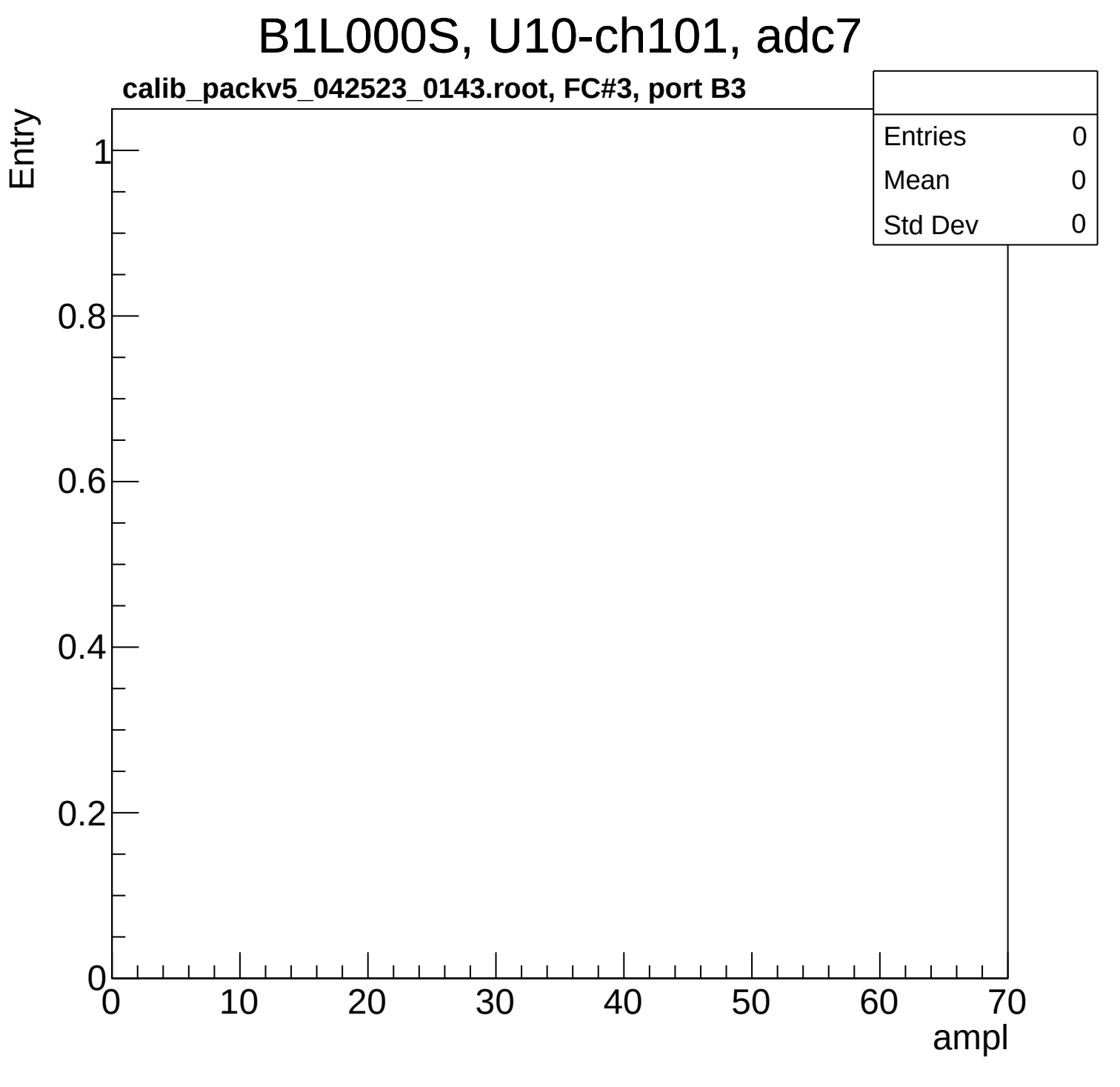
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch101, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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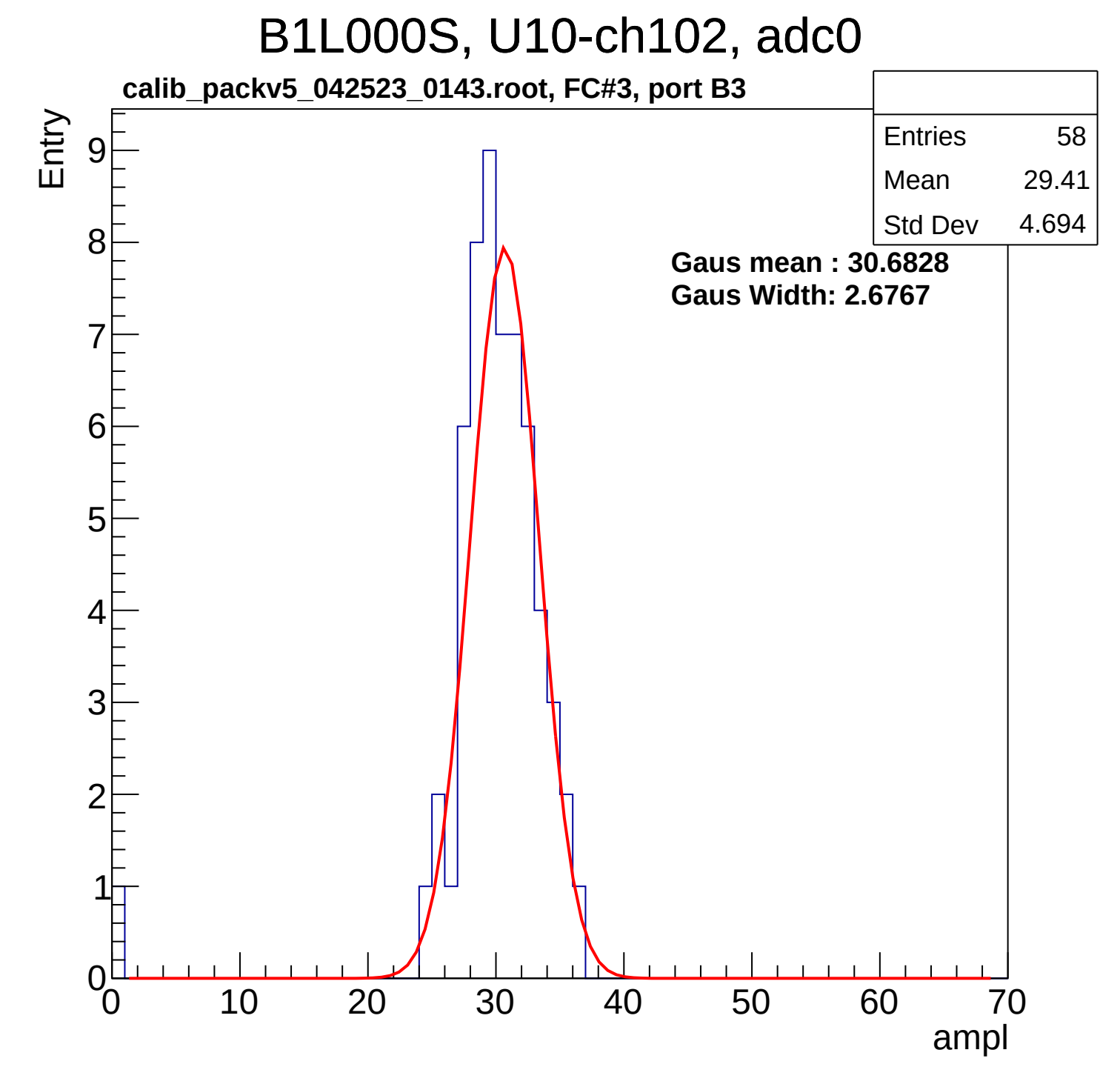
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch102, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 29.41
Std Dev 4.694
Gaus mean : 30.6828
Gaus Width: 2.6767
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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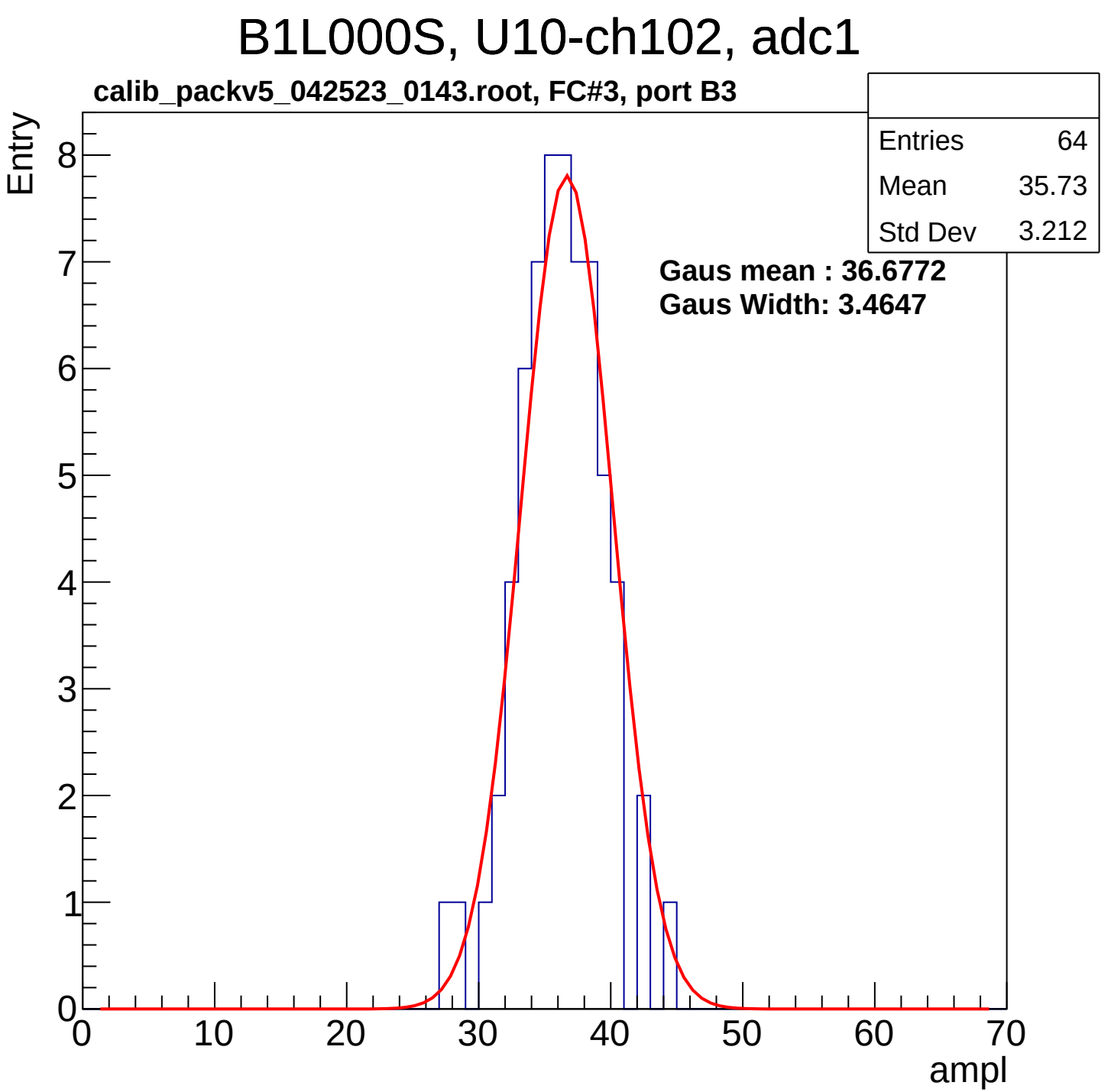
B1L000S, U10-ch102, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.73
Std Dev 3.212
Gaus mean : 36.6772
Gaus Width: 3.4647
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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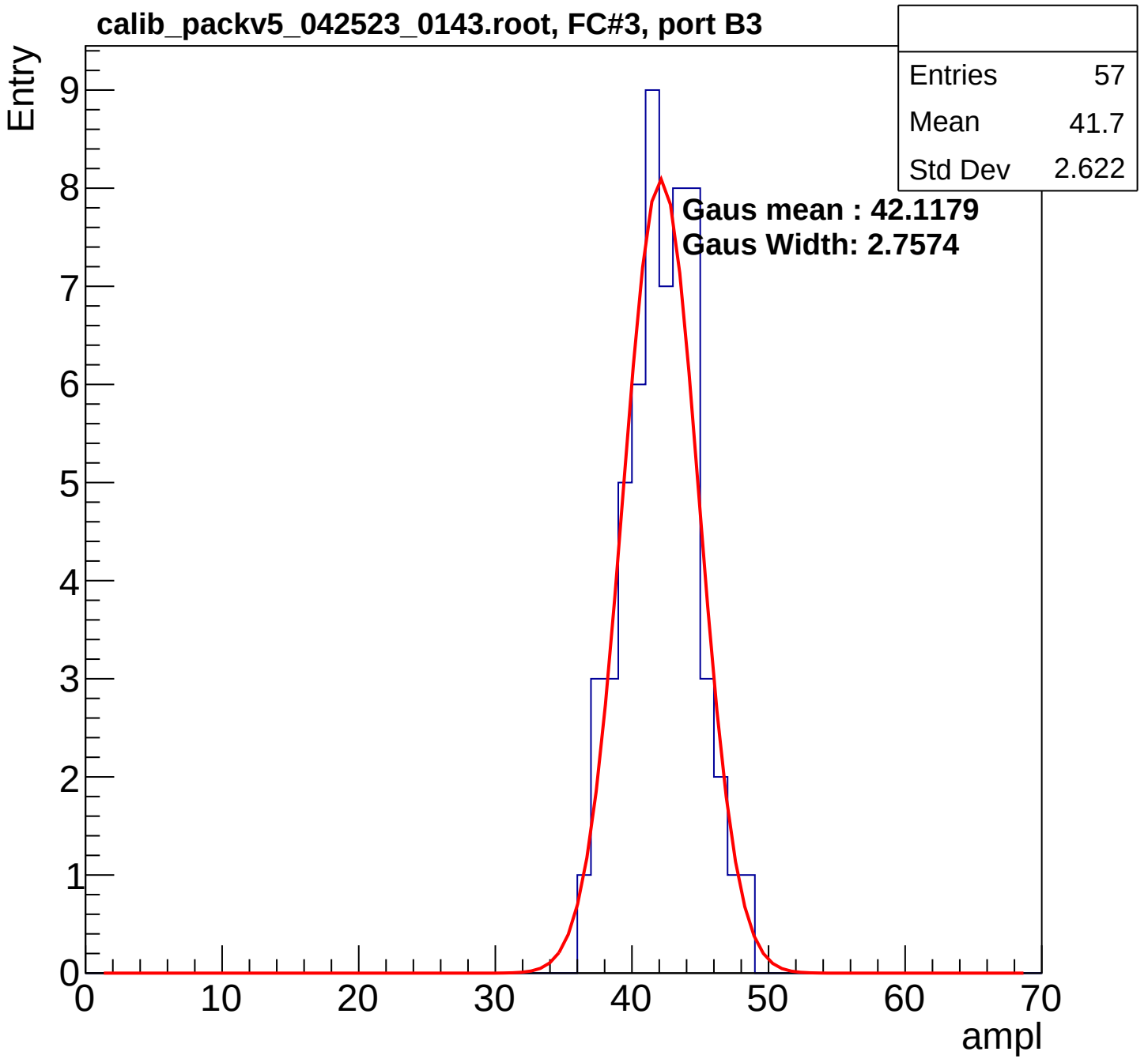
B1L000S, U10-ch102, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.7
Std Dev 2.622
Gaus mean : 42.1179
Gaus Width: 2.7574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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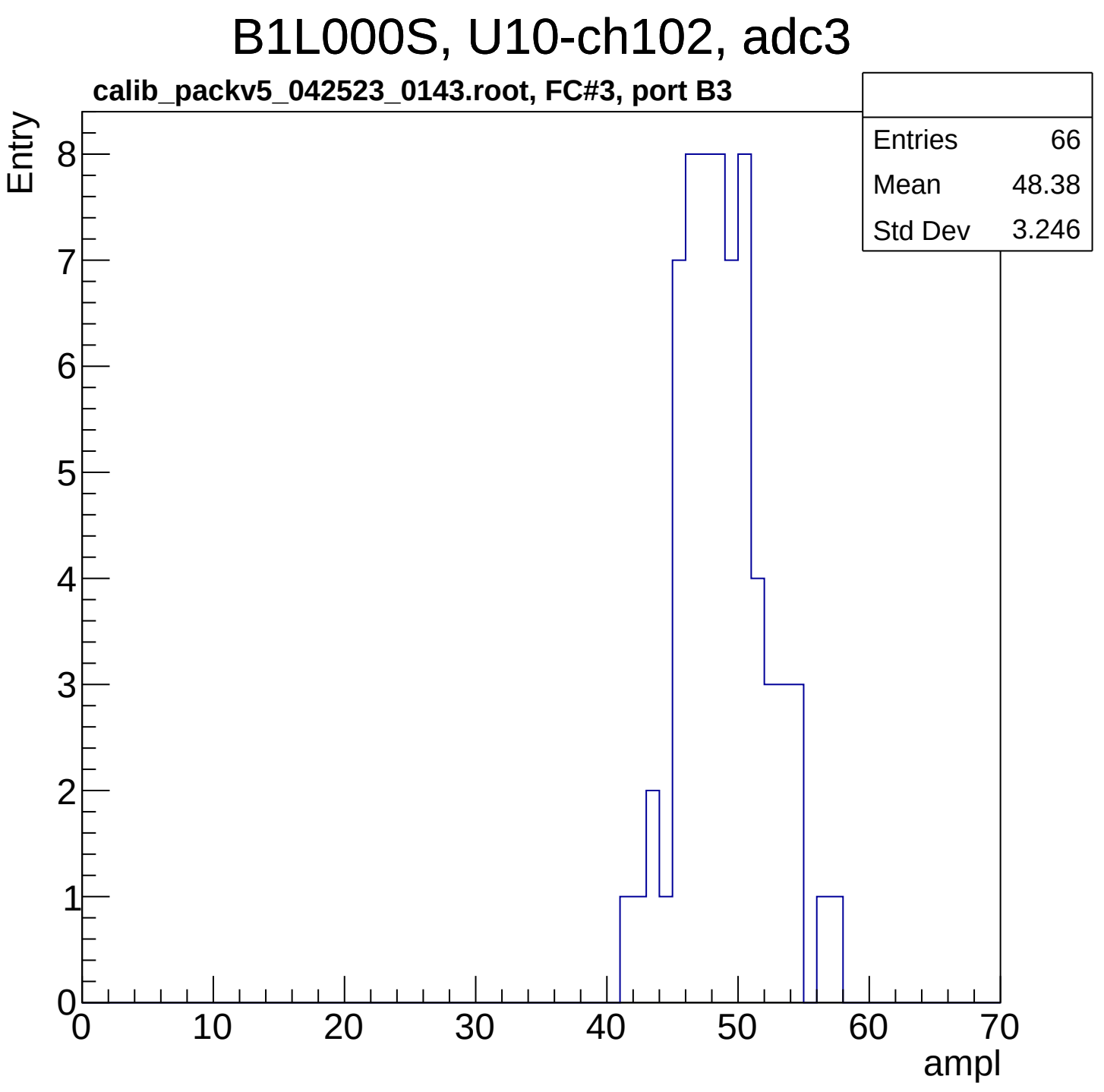
B1L000S, U10-ch102, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 48.38
Std Dev 3.246
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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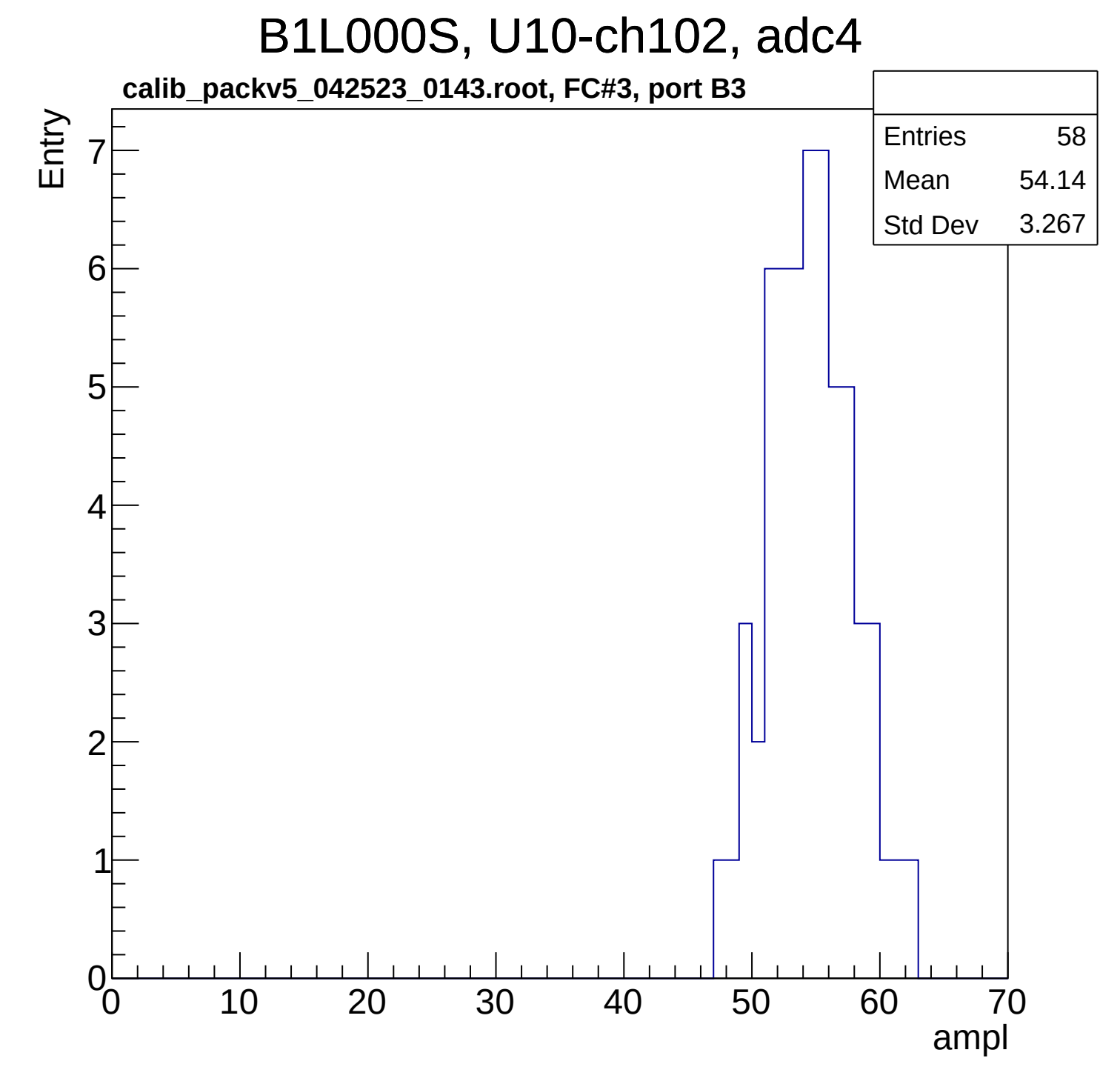
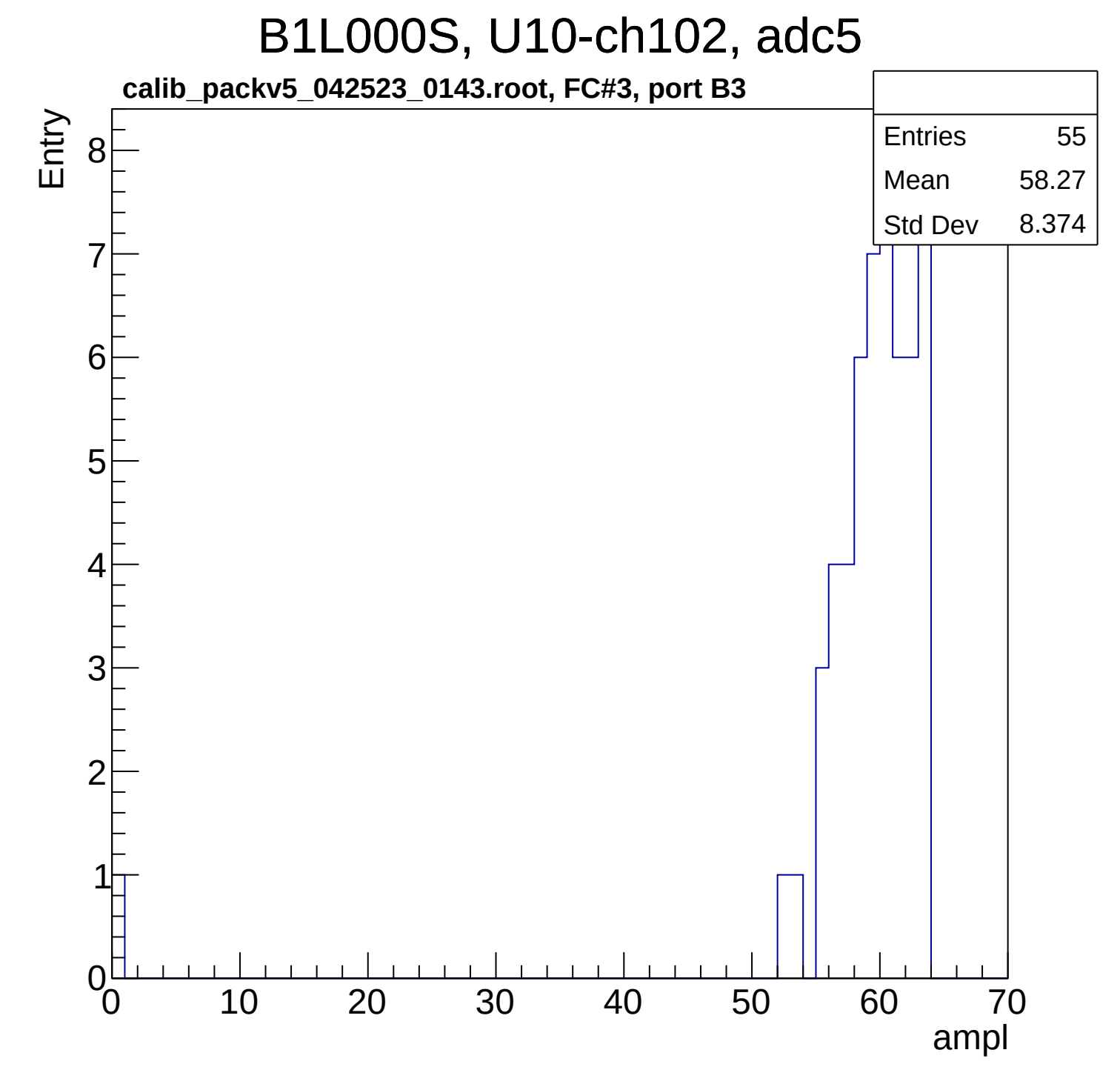

B1L000S, U10-ch102, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.14
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch102, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.27
Std Dev 8.374
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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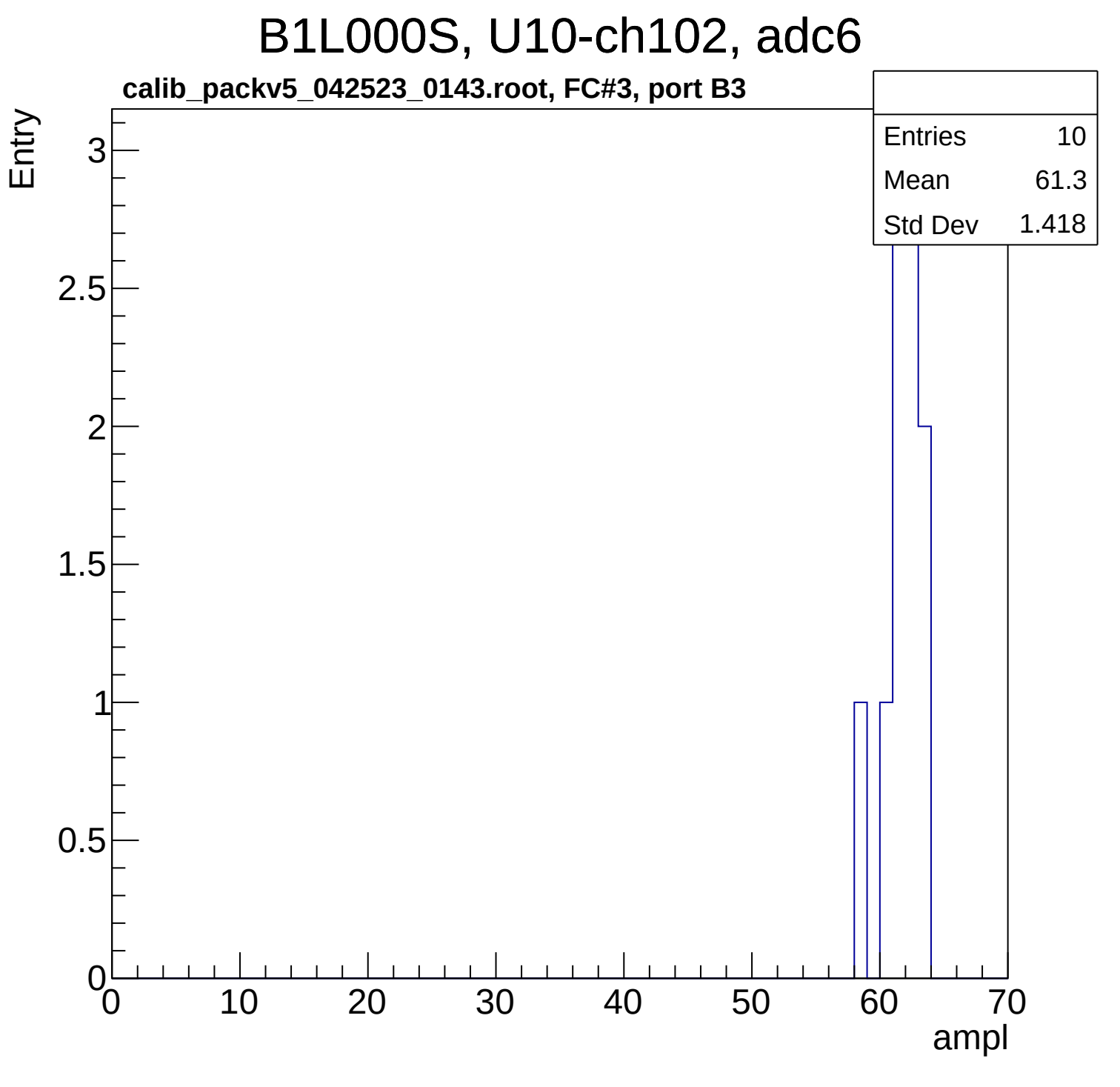
B1L000S, U10-ch102, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.3
Std Dev 1.418
Entry
ampl
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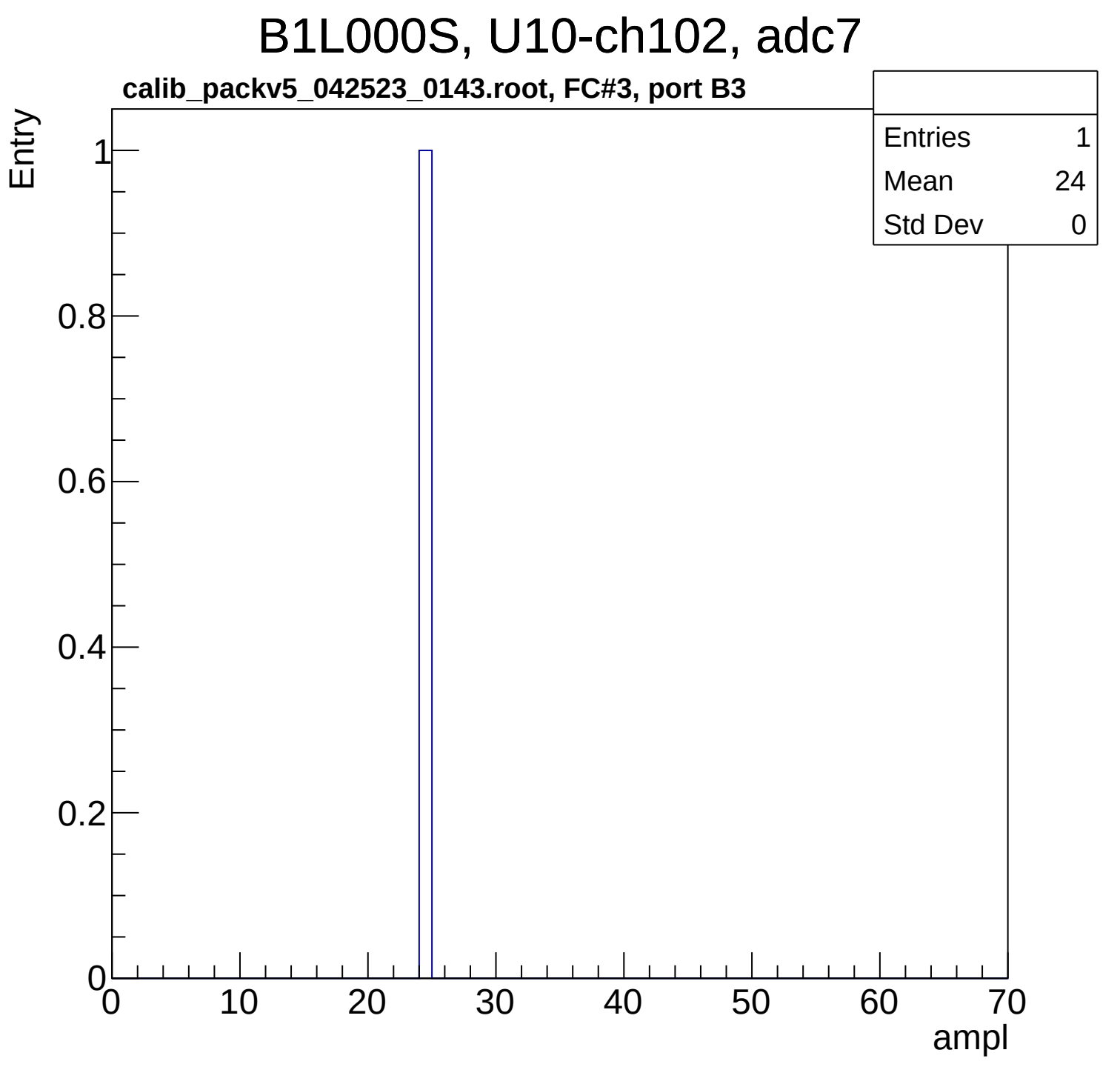
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch102, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 24
Std Dev 0
Entry
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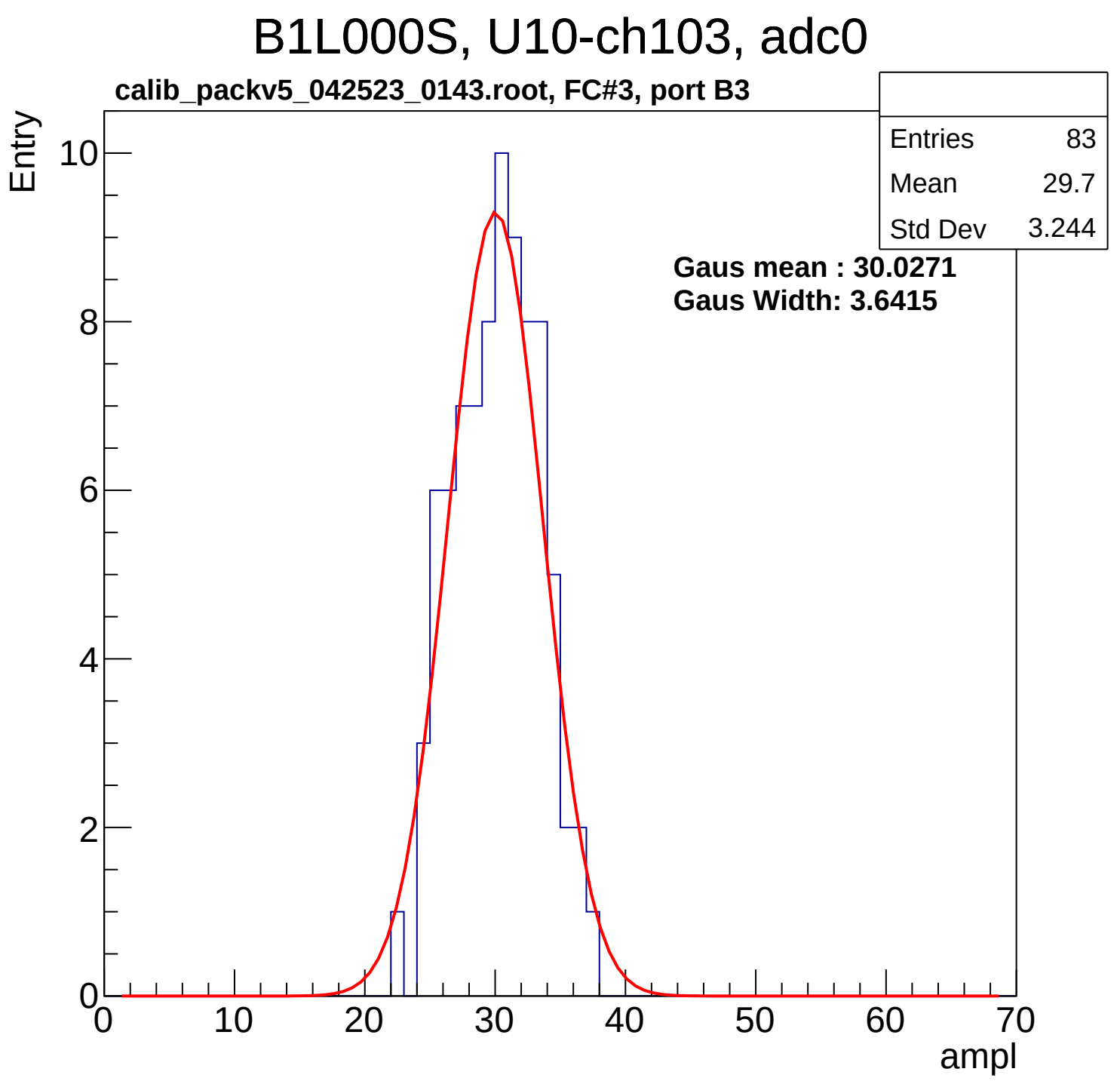
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch103, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 29.7
Std Dev 3.244
Gaus mean : 30.0271
Gaus Width: 3.6415
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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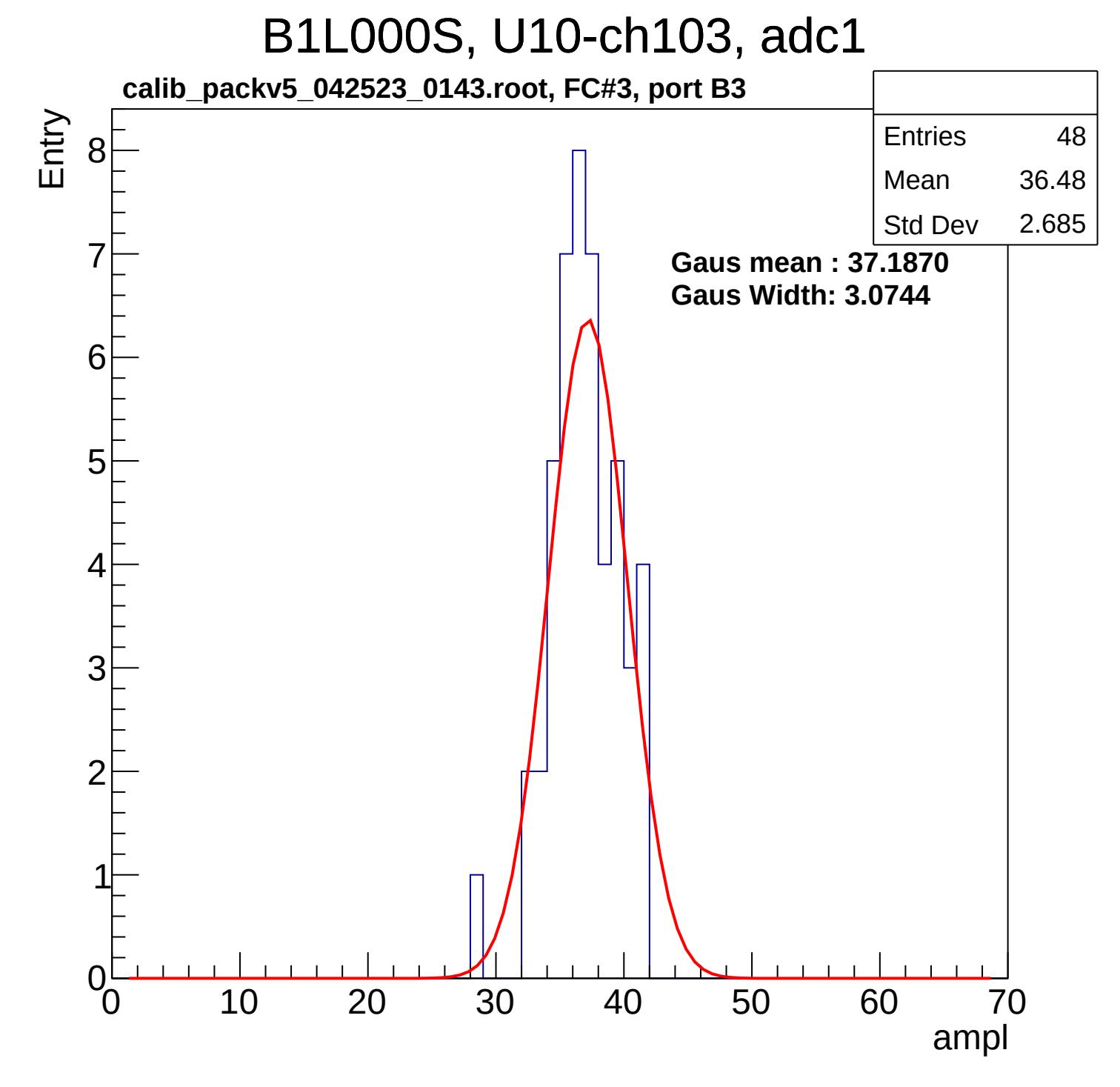
B1L000S, U10-ch103, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 36.48
Std Dev 2.685
Gaus mean : 37.1870
Gaus Width: 3.0744
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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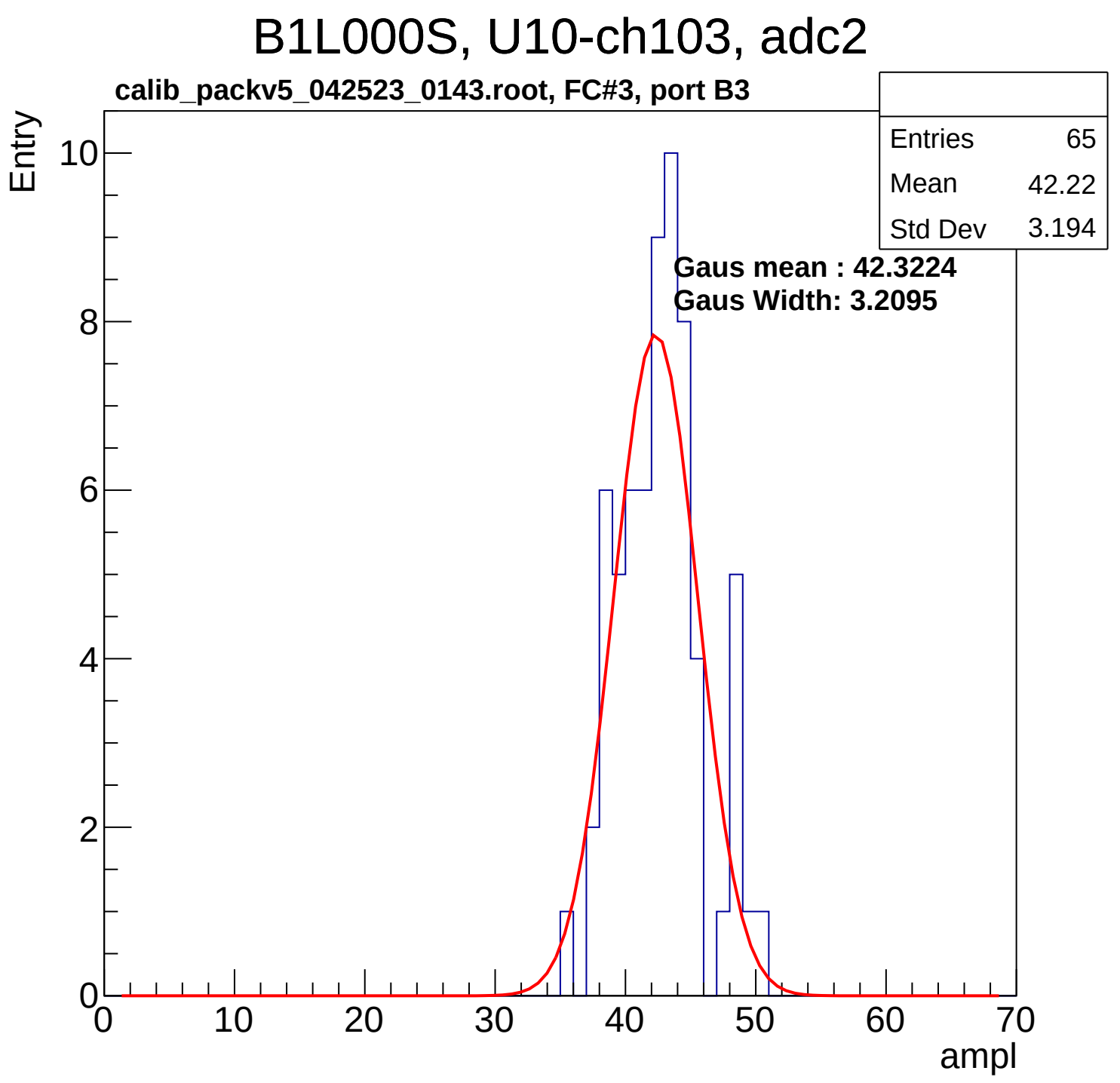
B1L000S, U10-ch103, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 42.22
Std Dev 3.194
Gaus mean : 42.3224
Gaus Width: 3.2095
Entry
ampl
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8
10
0
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20
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60
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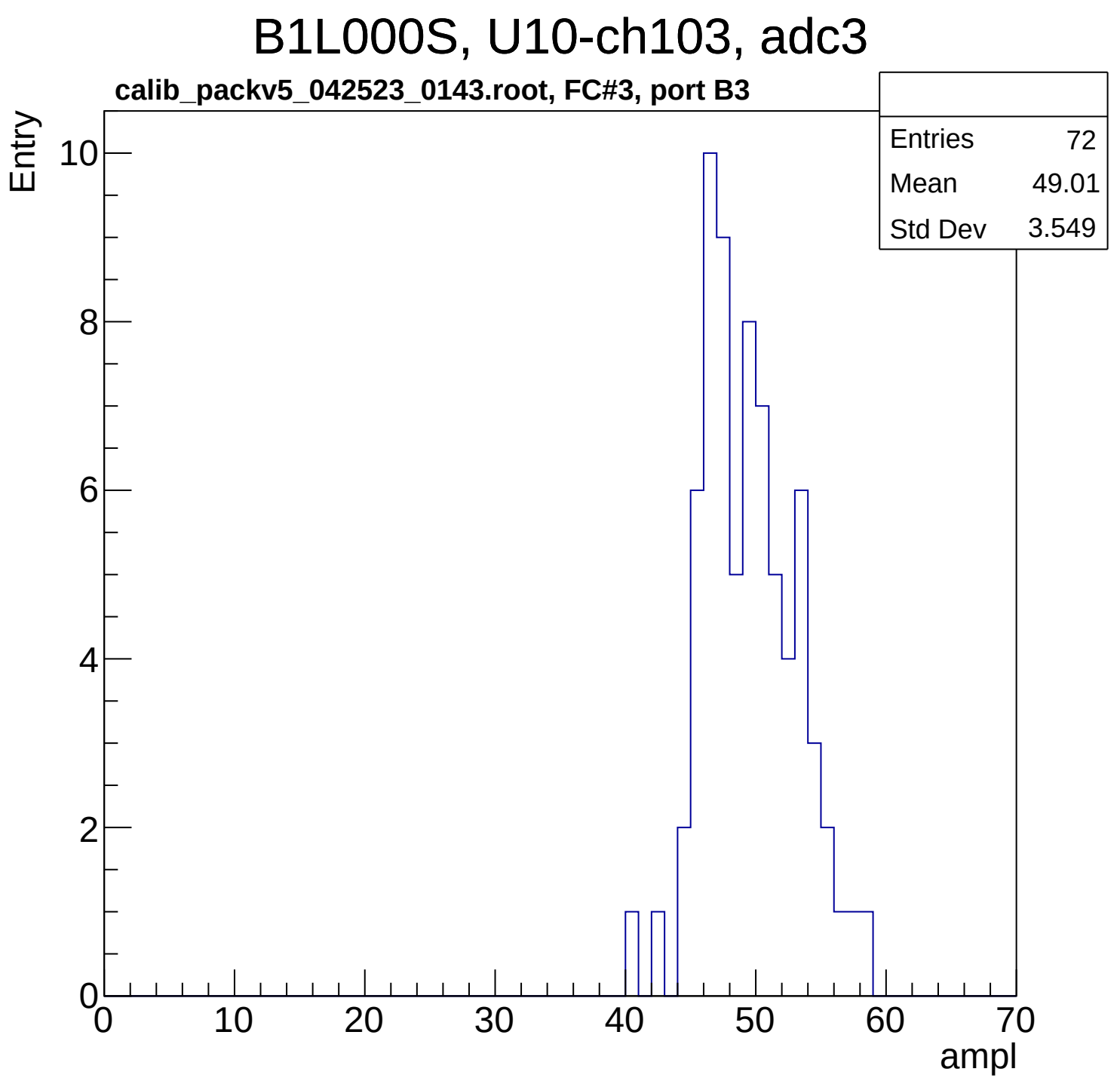
B1L000S, U10-ch103, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 49.01
Std Dev 3.549
Entry
ampl
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60
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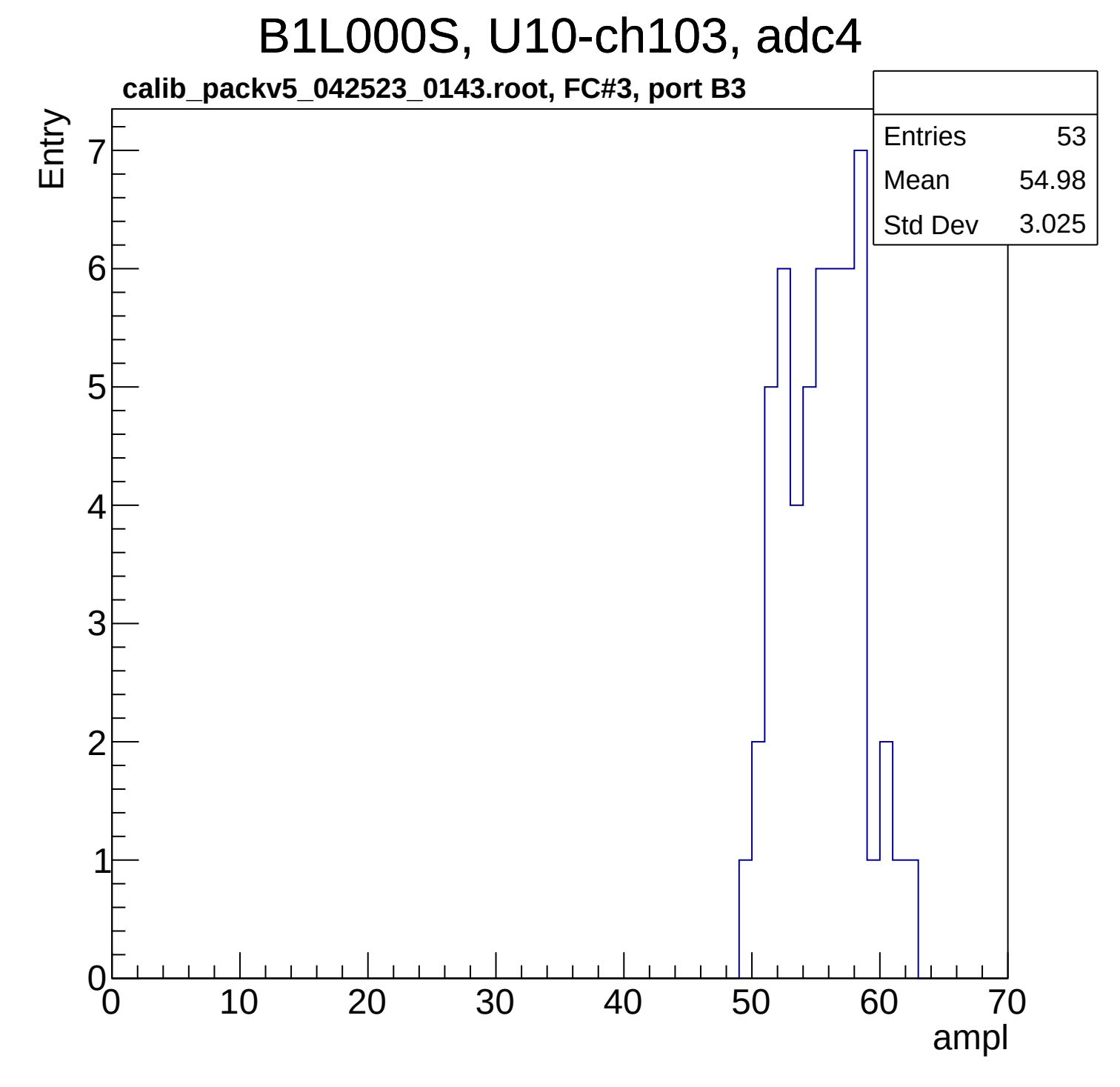
B1L000S, U10-ch103, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 54.98
Std Dev 3.025
Entry
ampl
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3
4
5
6
7
0
10
20
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40
50
60
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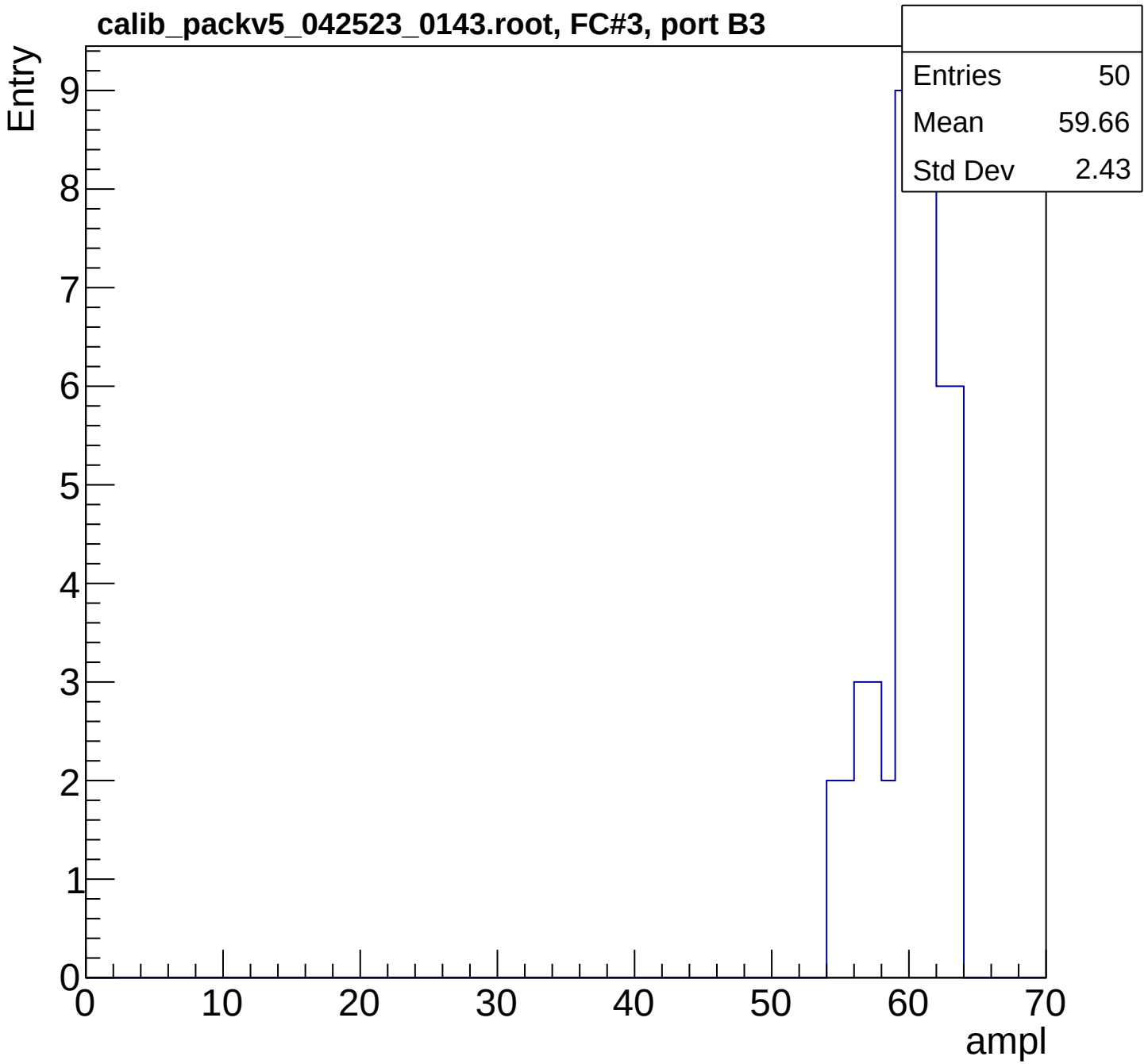
B1L000S, U10-ch103, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 59.66
Std Dev 2.43
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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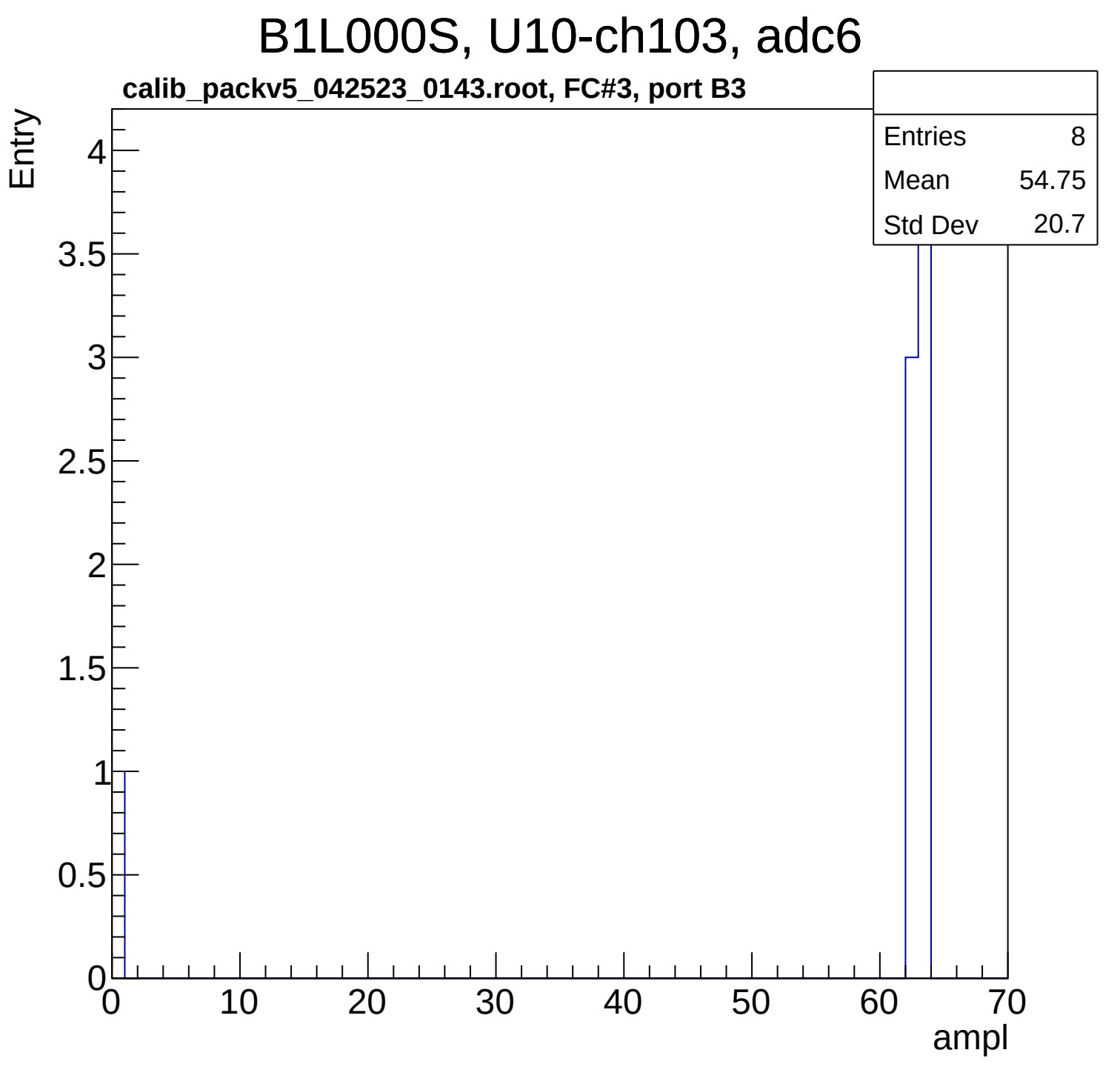
B1L000S, U10-ch103, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 54.75
Std Dev 20.7
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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60
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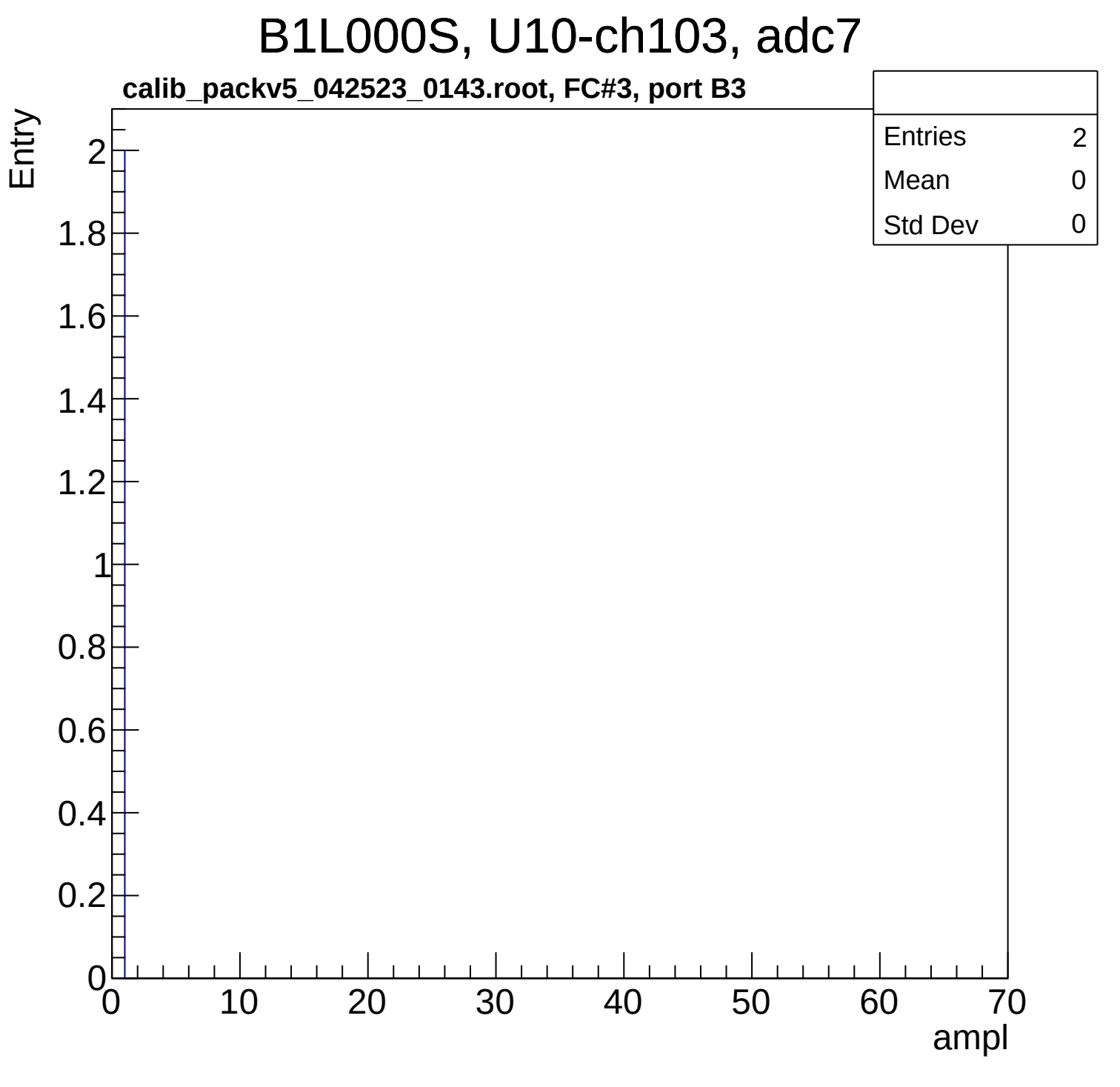
B1L000S, U10-ch103, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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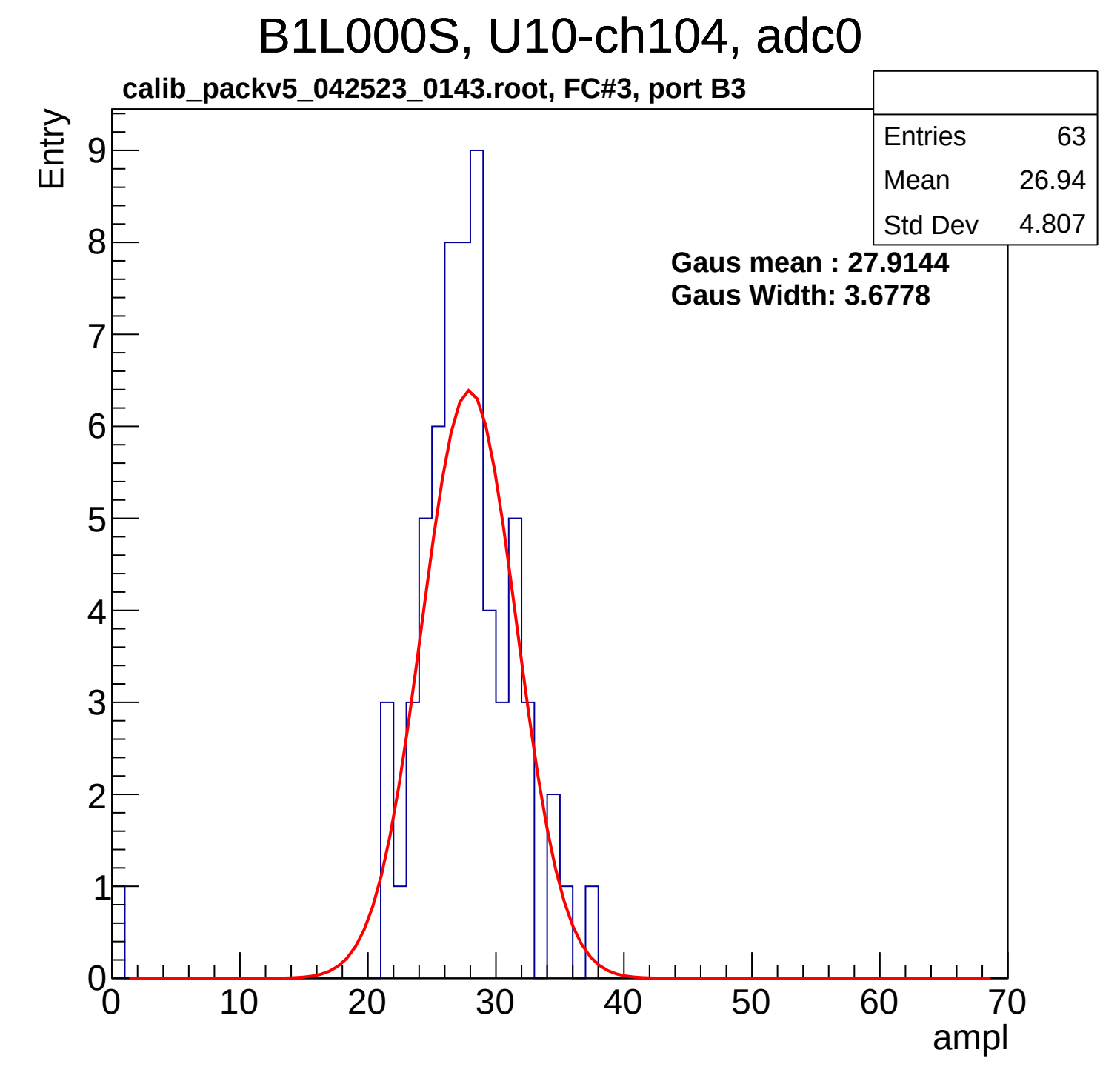
B1L000S, U10-ch104, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 26.94
Std Dev 4.807
Gaus mean : 27.9144
Gaus Width: 3.6778
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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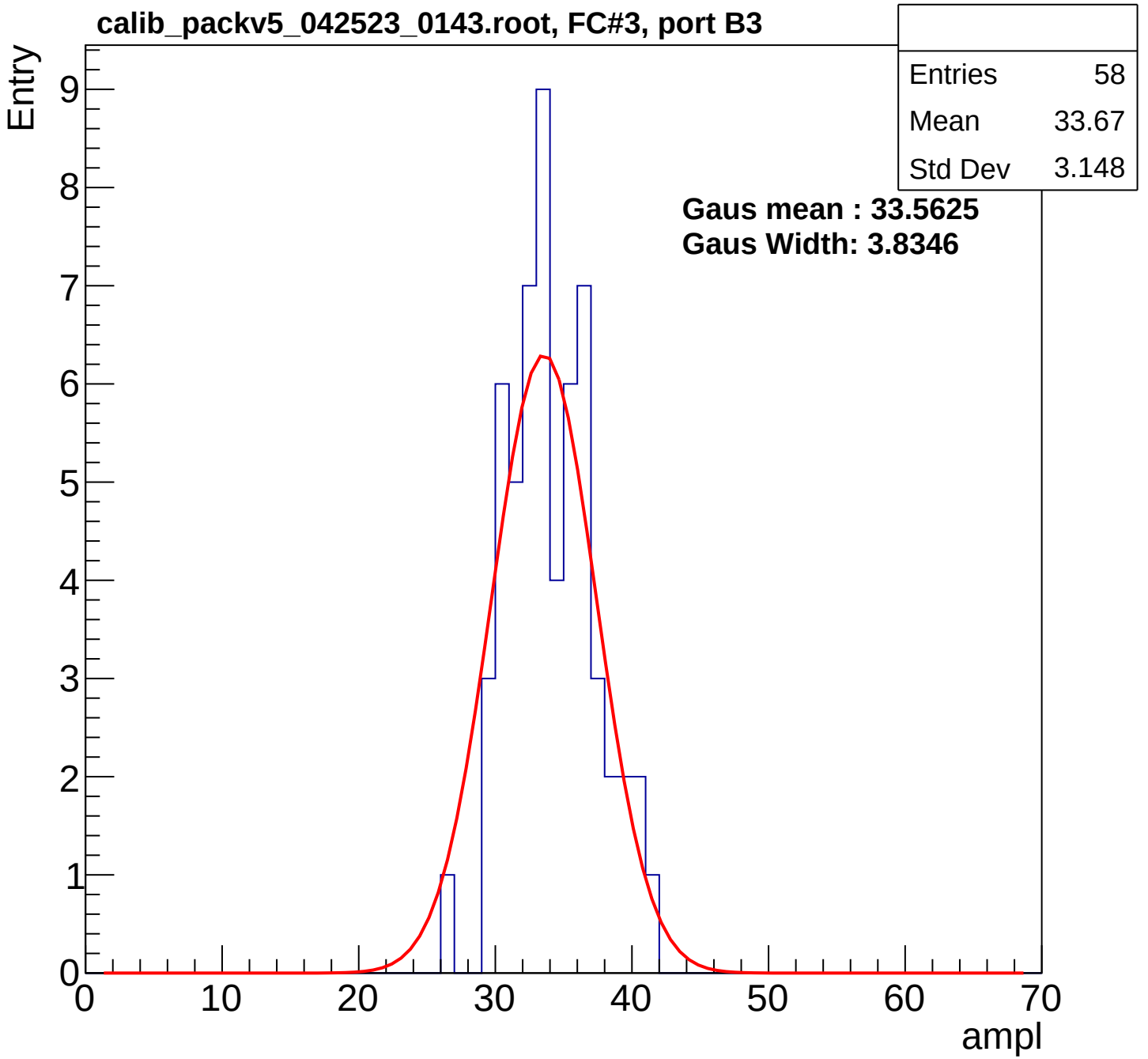
B1L000S, U10-ch104, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 33.67
Std Dev 3.148
Gaus mean : 33.5625
Gaus Width: 3.8346
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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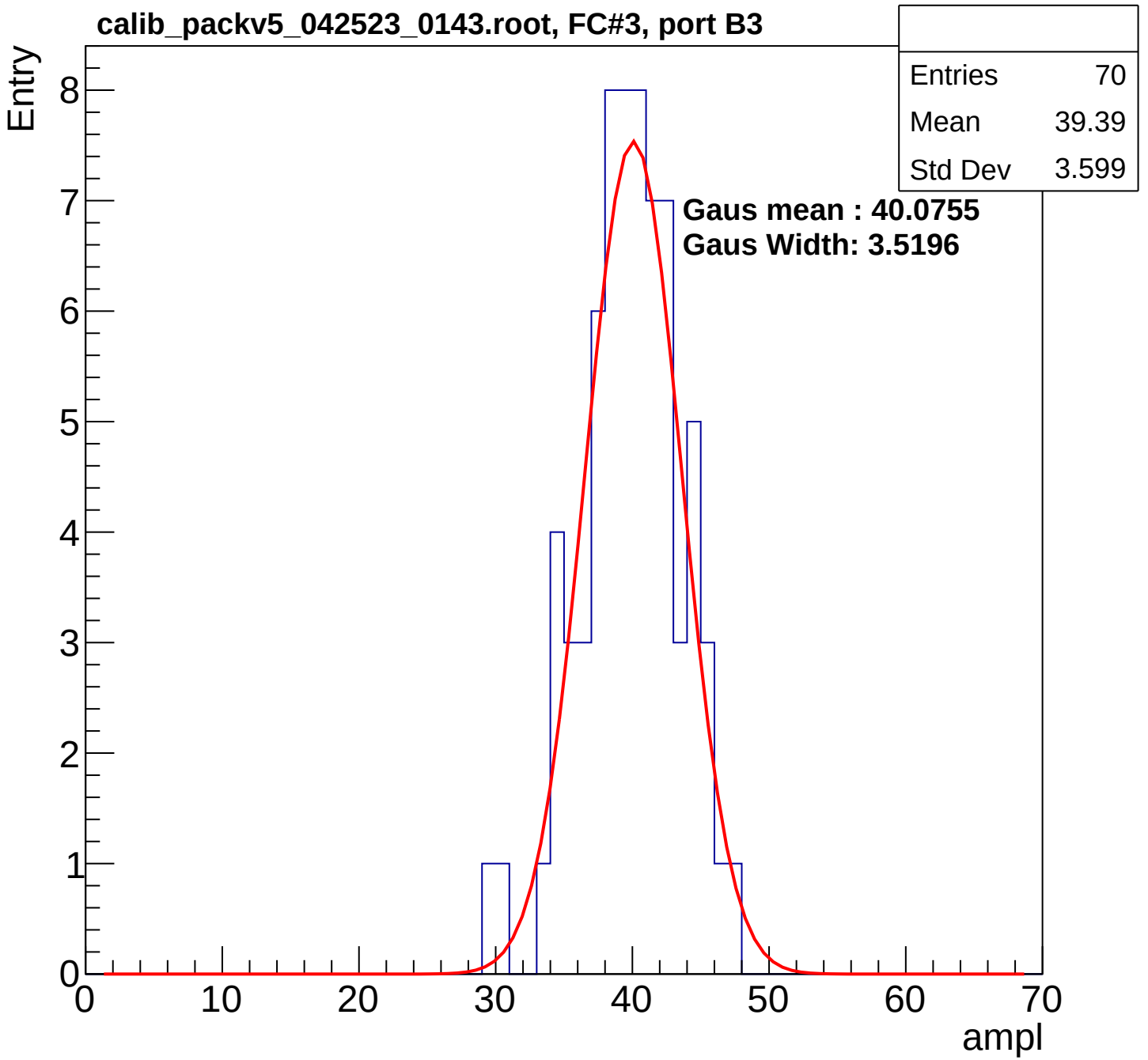
40
50
60
70


B1L000S, U10-ch104, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 39.39
Std Dev 3.599
Gaus mean : 40.0755
Gaus Width: 3.5196
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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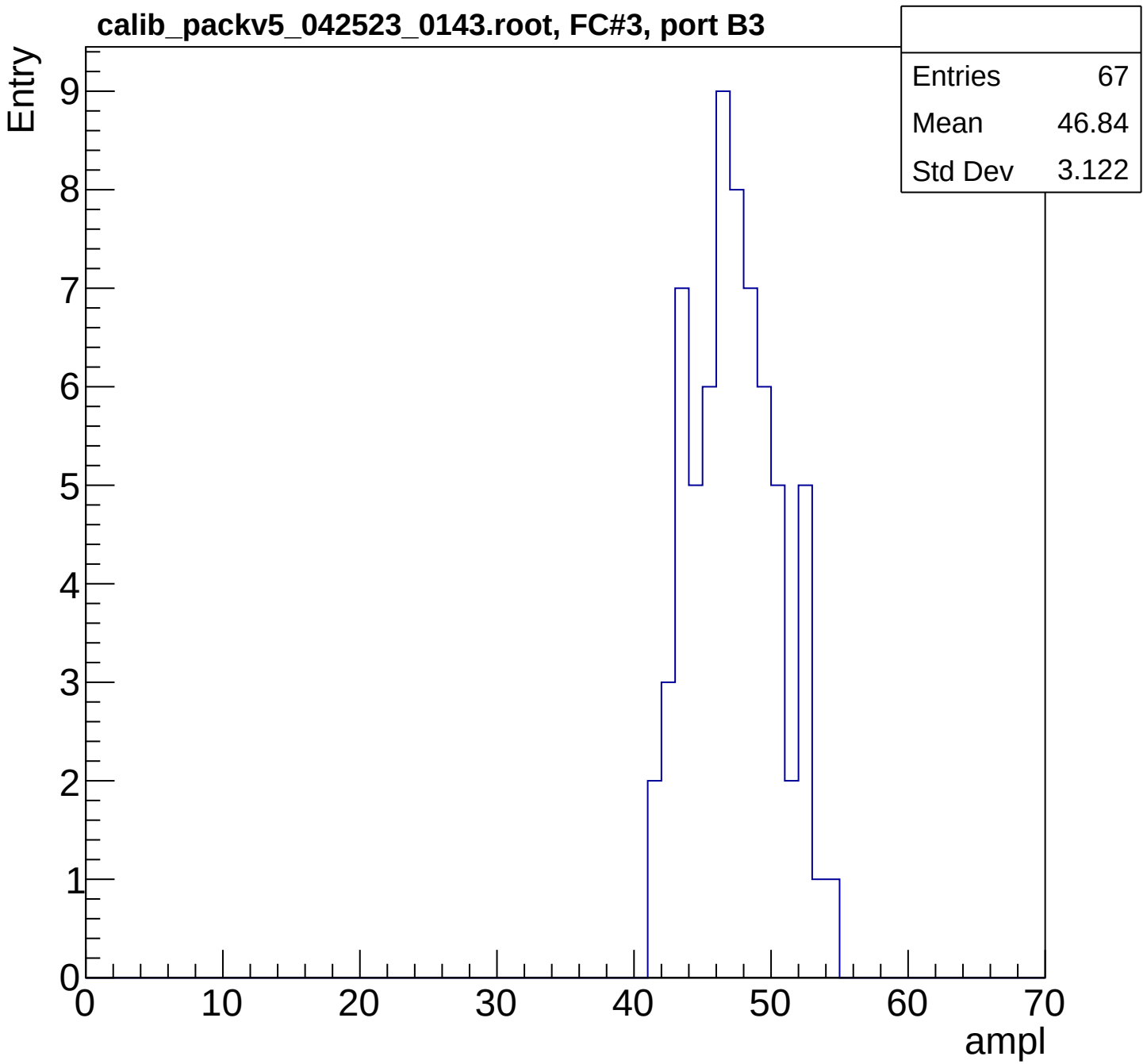
B1L000S, U10-ch104, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 46.84
Std Dev 3.122
Entry
ampl
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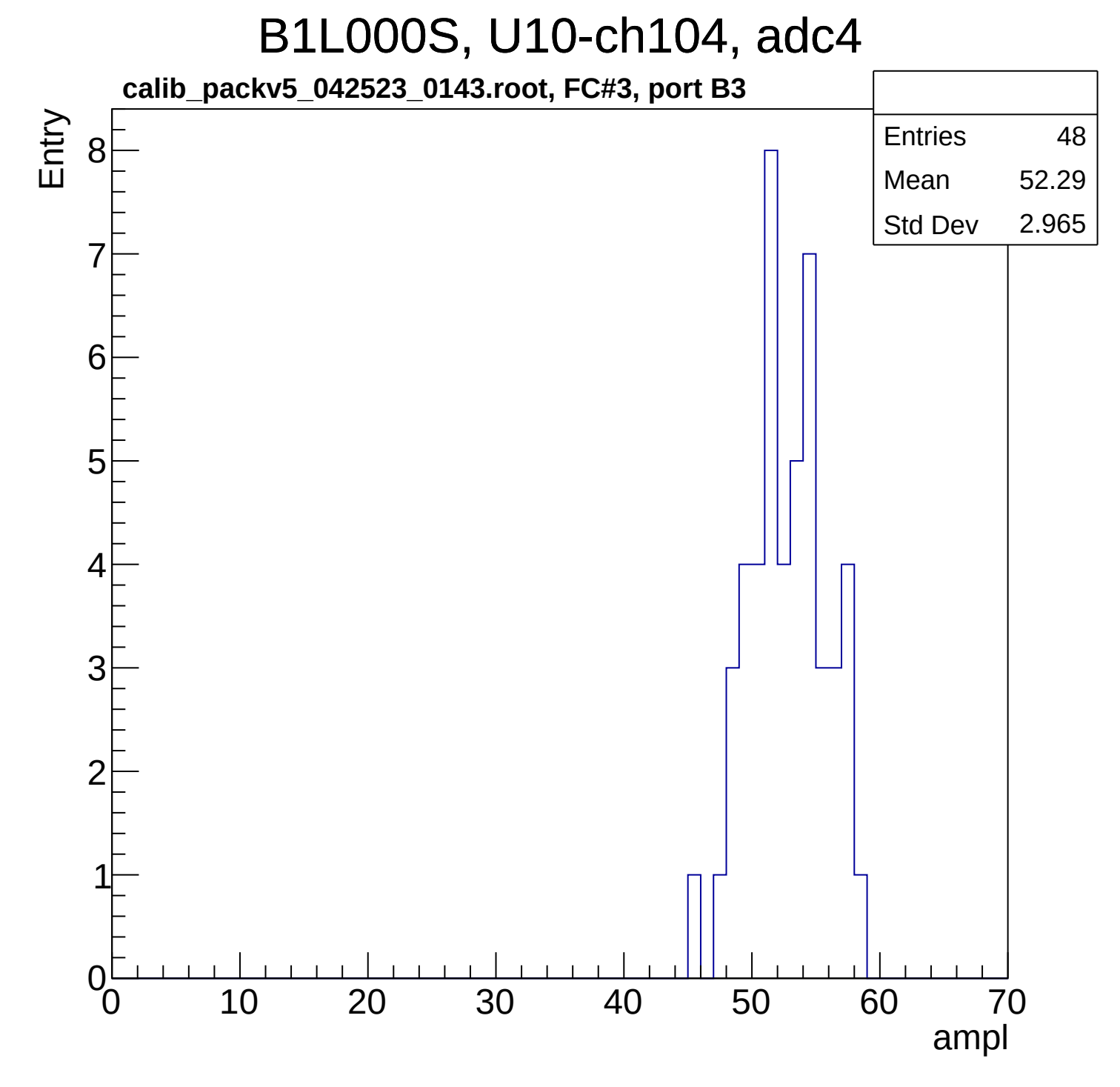
B1L000S, U10-ch104, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 52.29
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
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7
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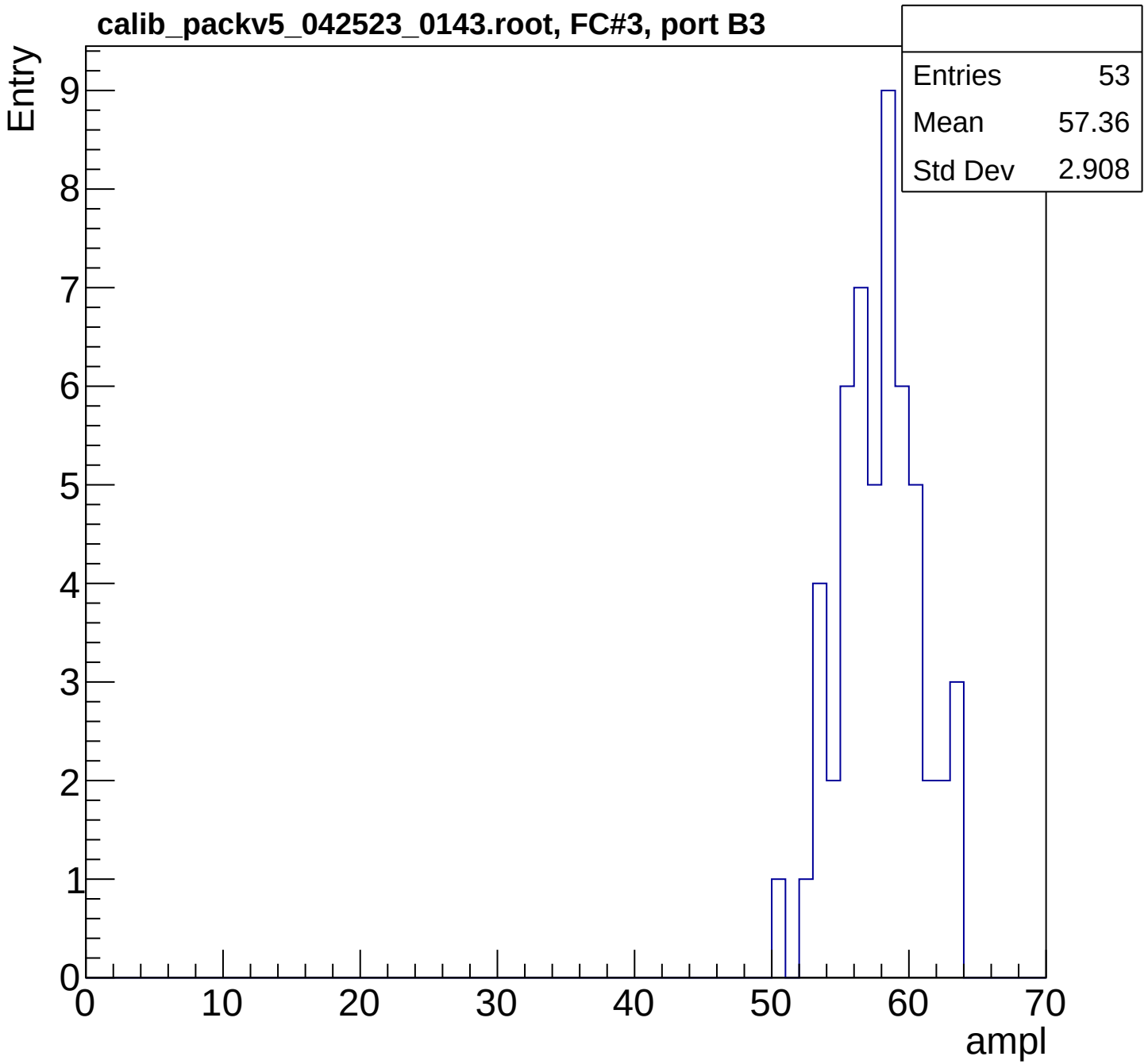
B1L000S, U10-ch104, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 57.36
Std Dev 2.908
Entry
ampl
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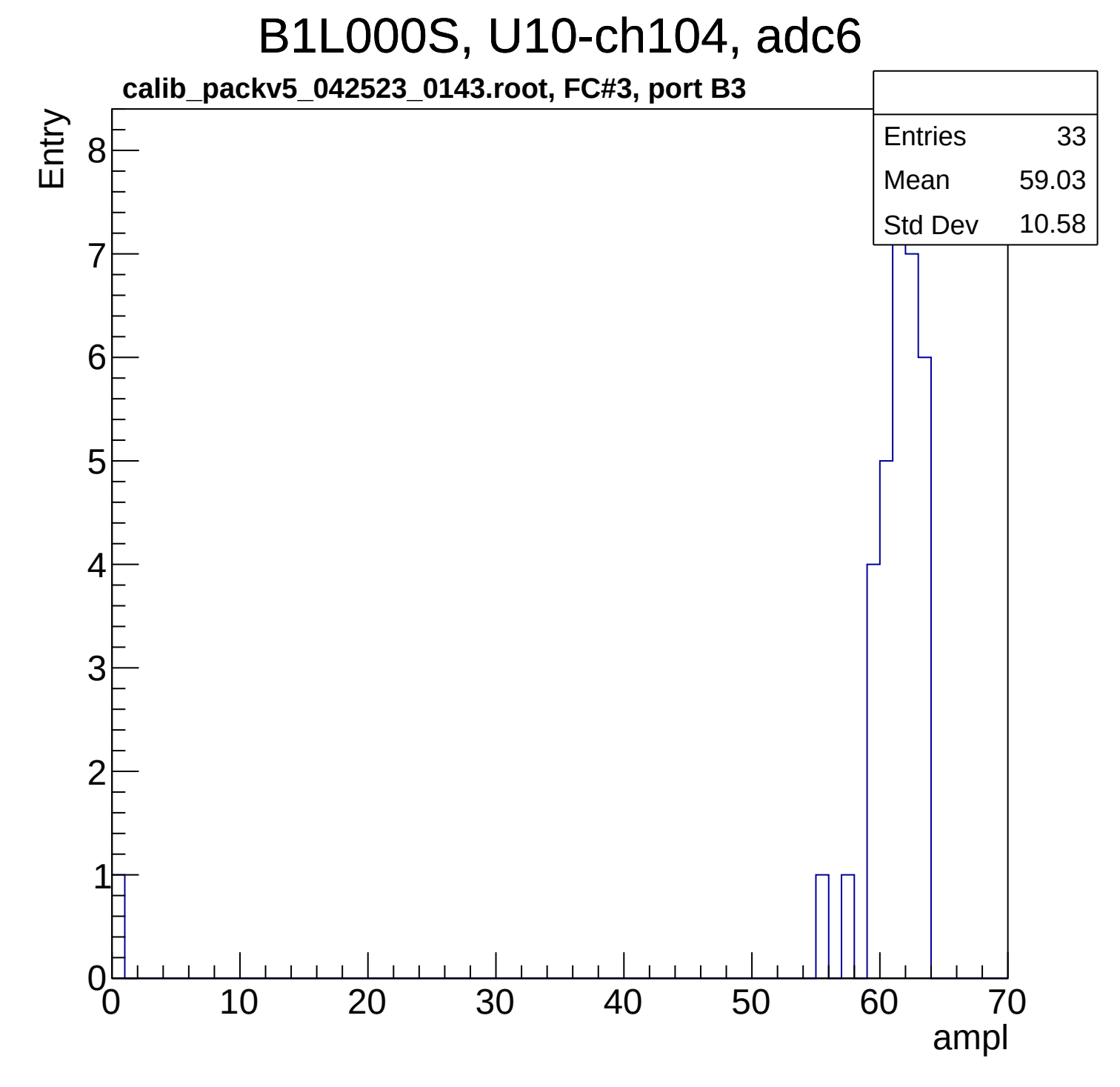
B1L000S, U10-ch104, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 33
Mean 59.03
Std Dev 10.58
Entry
ampl
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2
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0
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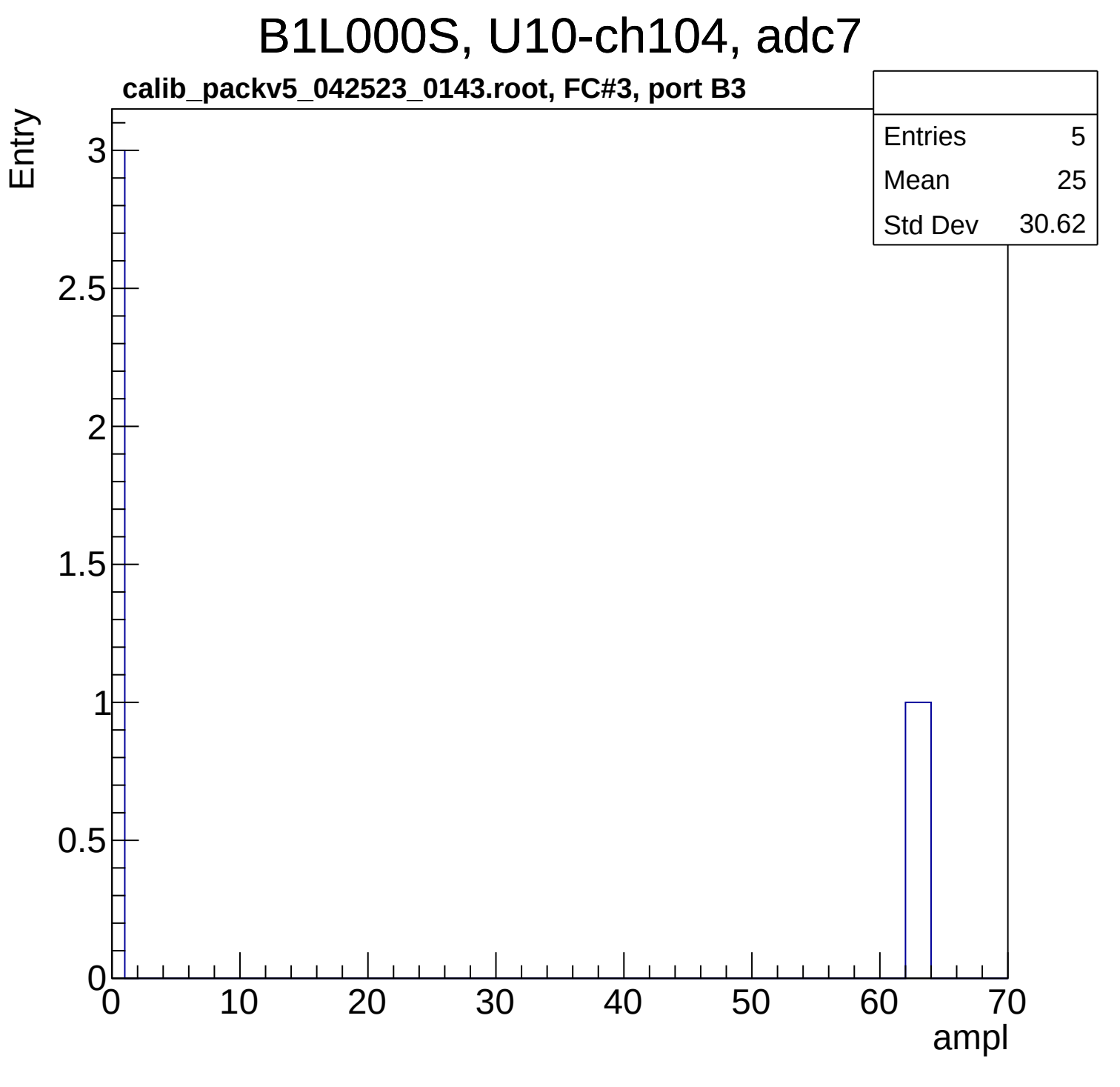
B1L000S, U10-ch104, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 25
Std Dev 30.62
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
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60
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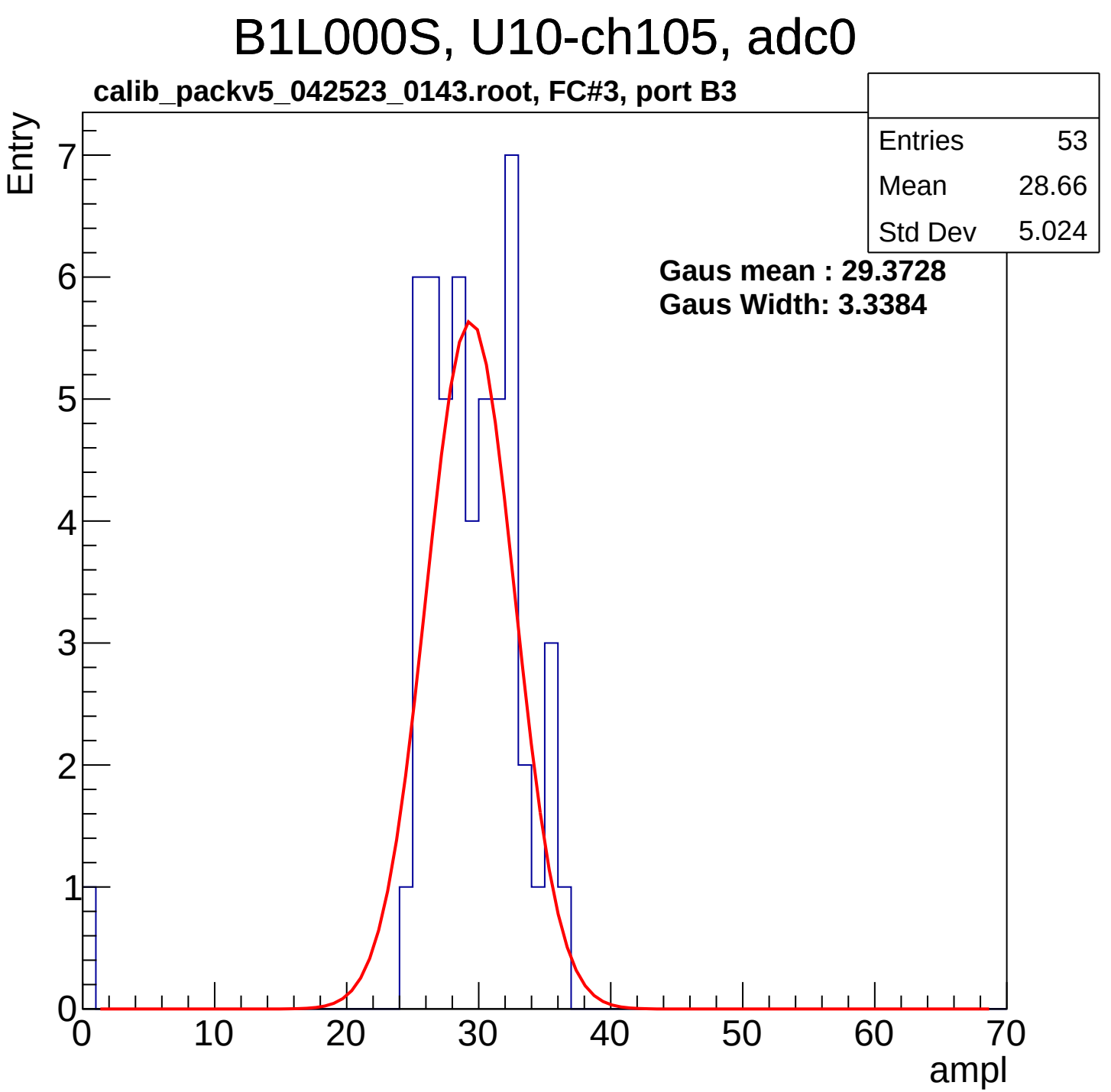
B1L000S, U10-ch105, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 28.66
Std Dev 5.024
Gaus mean : 29.3728
Gaus Width: 3.3384
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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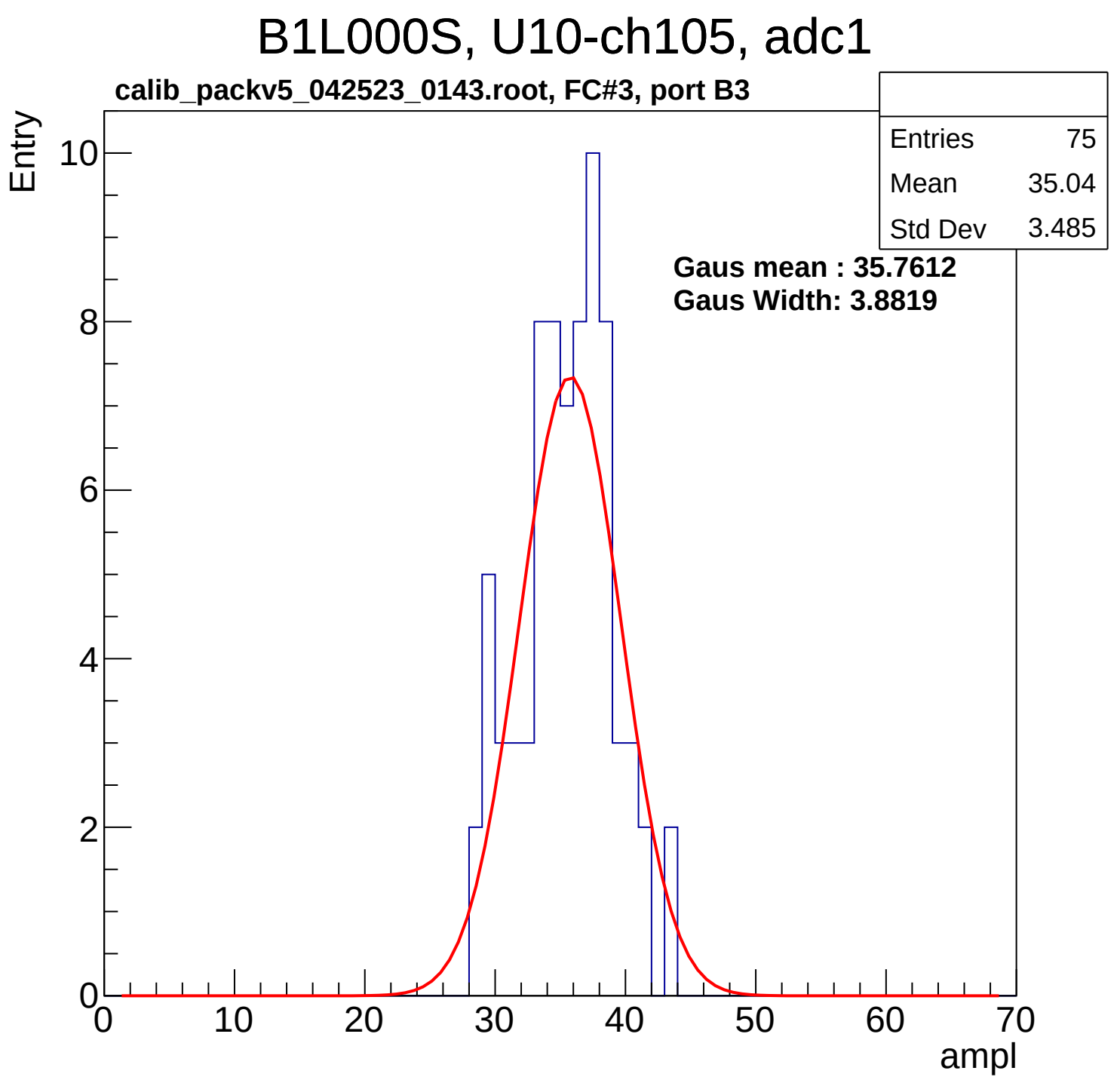
B1L000S, U10-ch105, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 35.04
Std Dev 3.485
Gaus mean : 35.7612
Gaus Width: 3.8819
Entry
ampl
0
2
4
6
8
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0
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60
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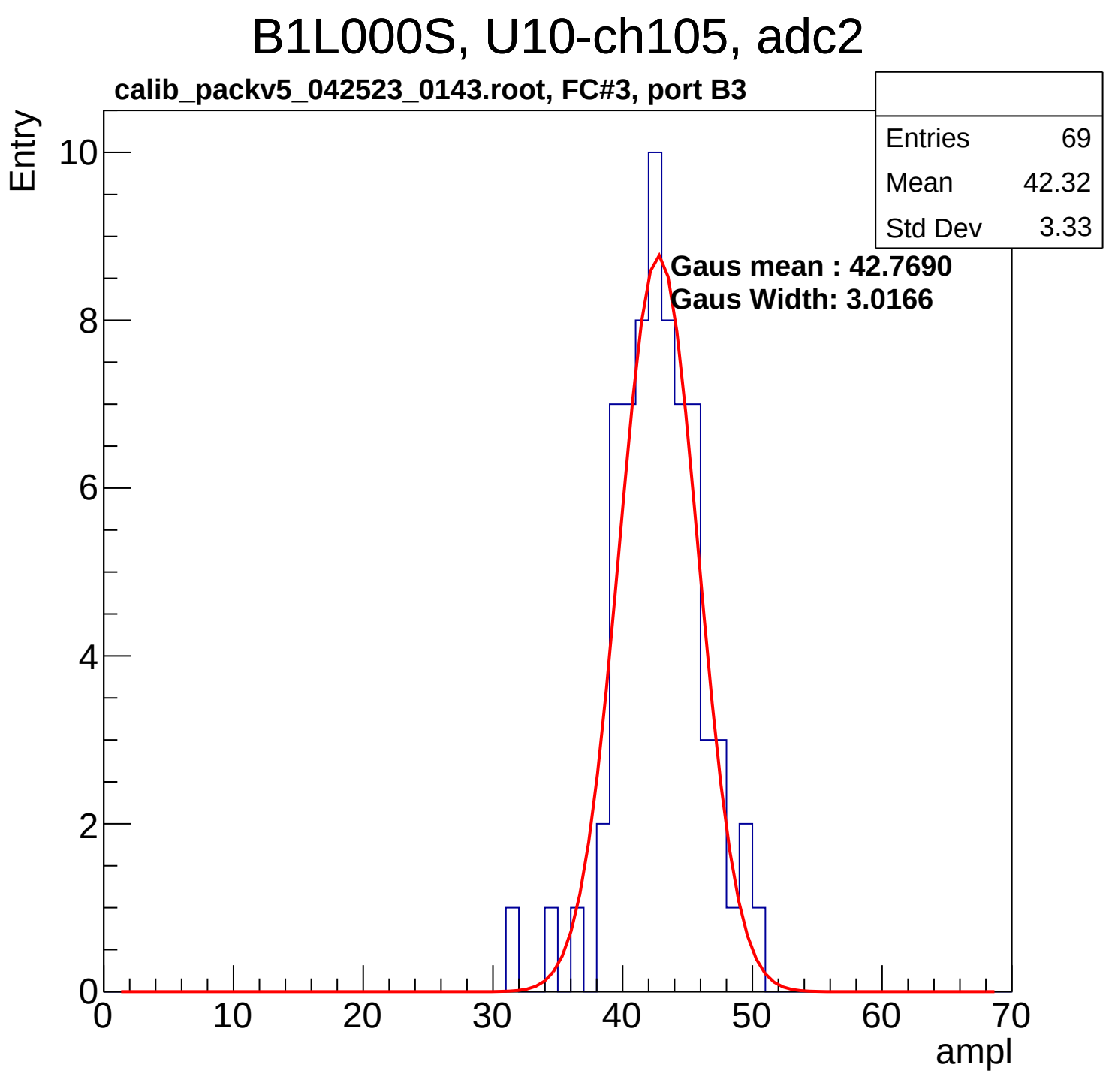
B1L000S, U10-ch105, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 42.32
Std Dev 3.33
Gaus mean : 42.7690
Gaus Width: 3.0166
Entry
ampl
0
2
4
6
8
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0
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60
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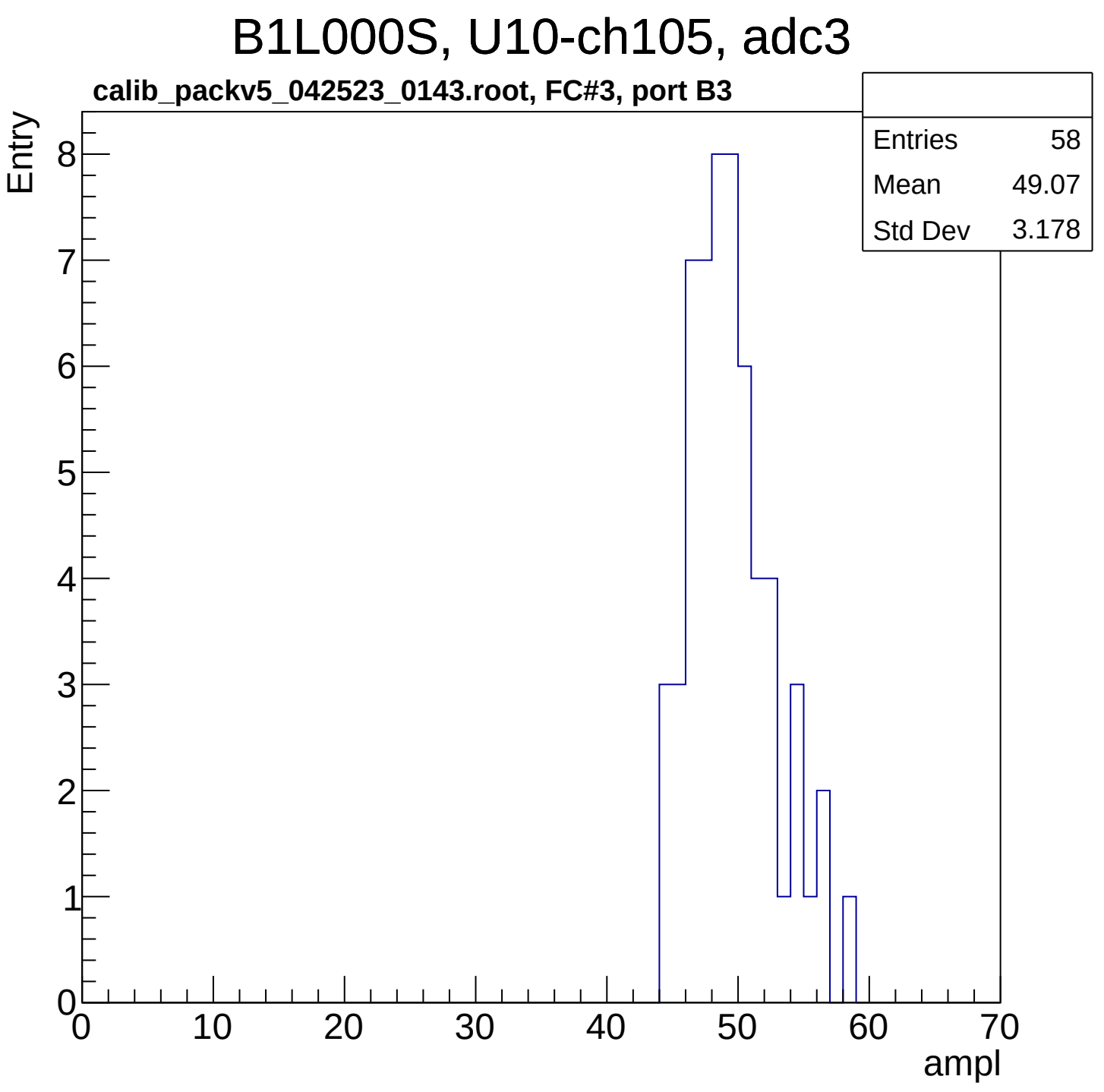
B1L000S, U10-ch105, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 49.07
Std Dev 3.178
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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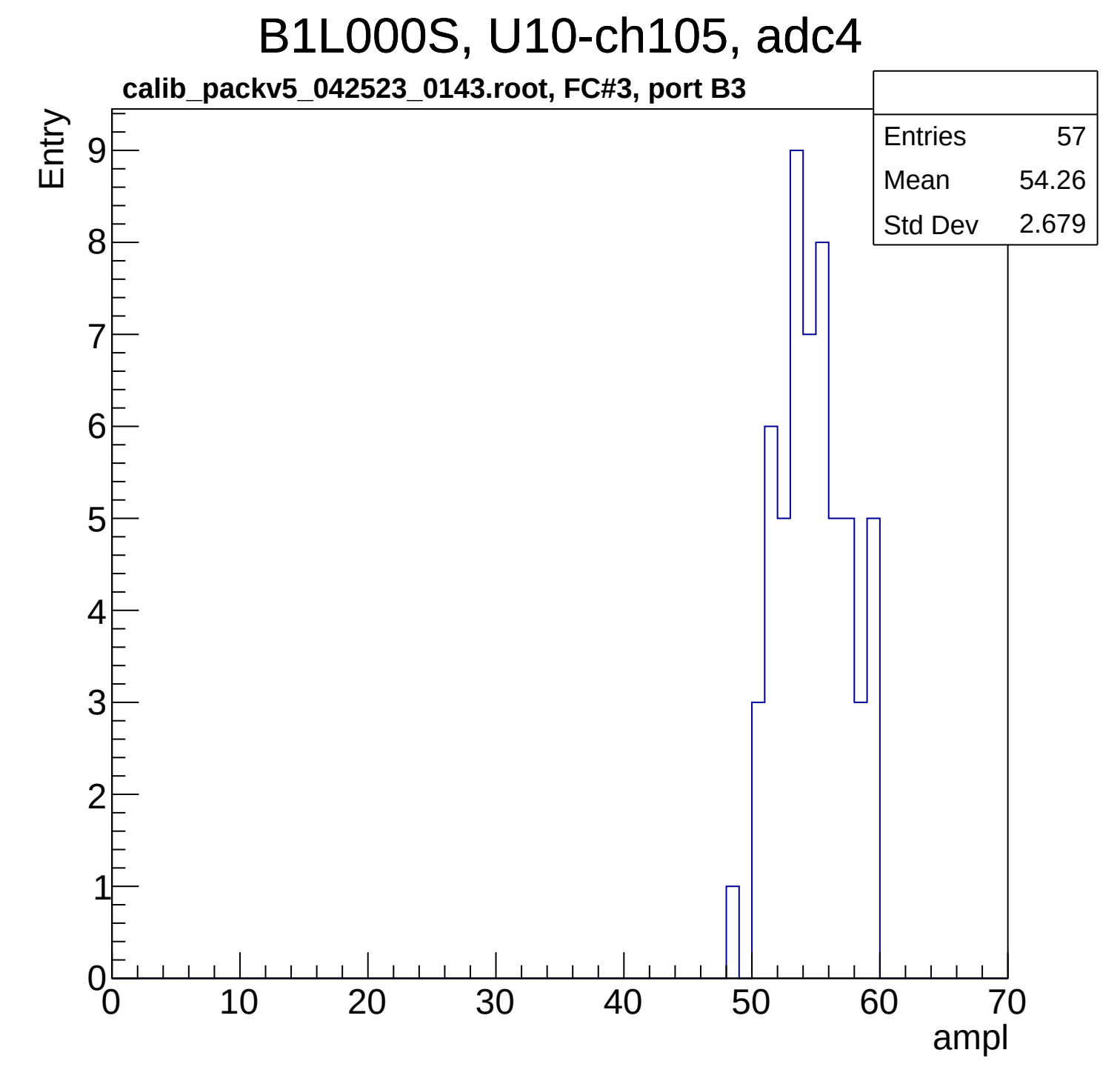
B1L000S, U10-ch105, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 54.26
Std Dev 2.679
Entry
ampl
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2
3
4
5
6
7
8
9
0
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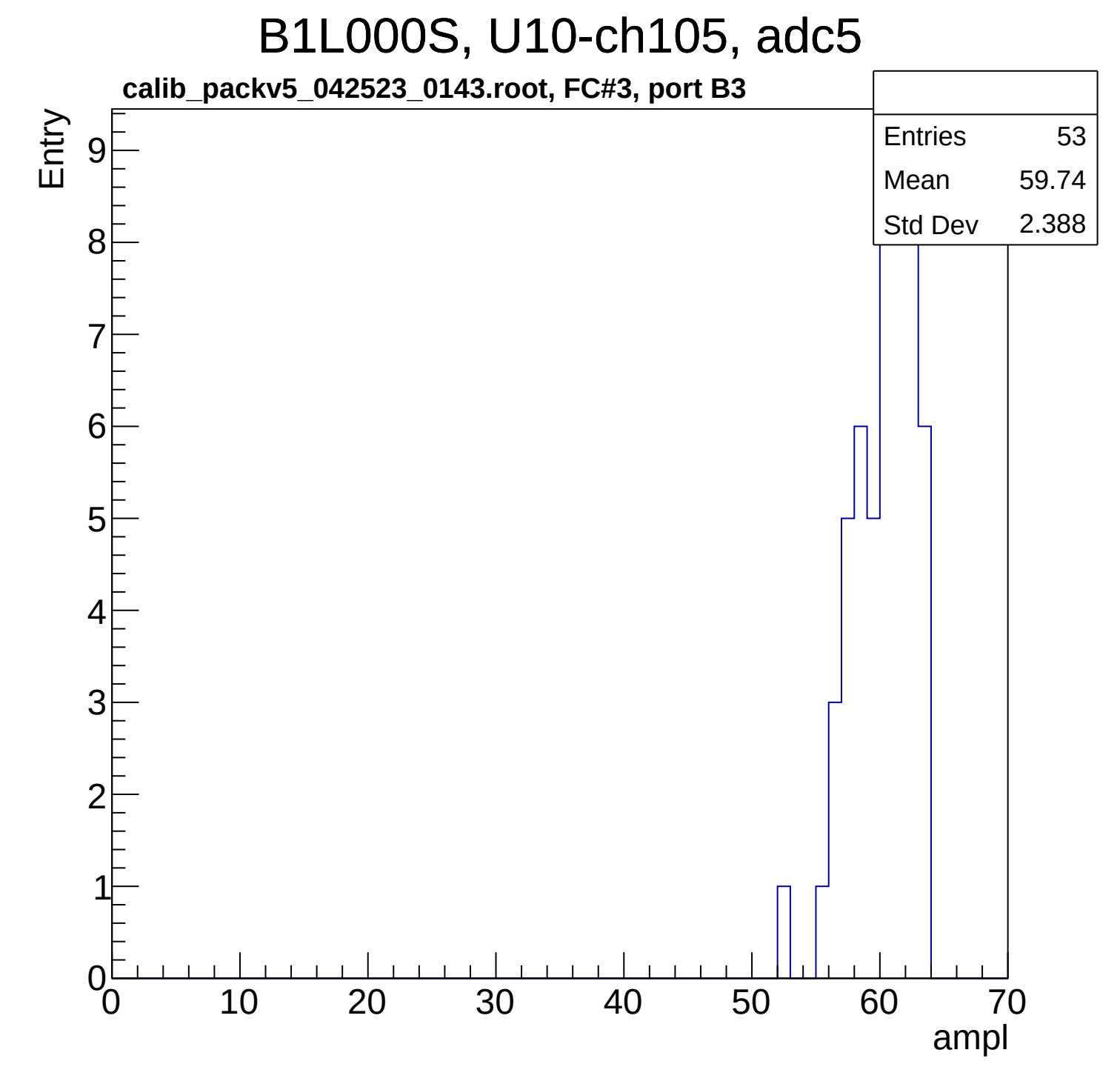
B1L000S, U10-ch105, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 59.74
Std Dev 2.388
Entry
ampl
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2
3
4
5
6
7
8
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0
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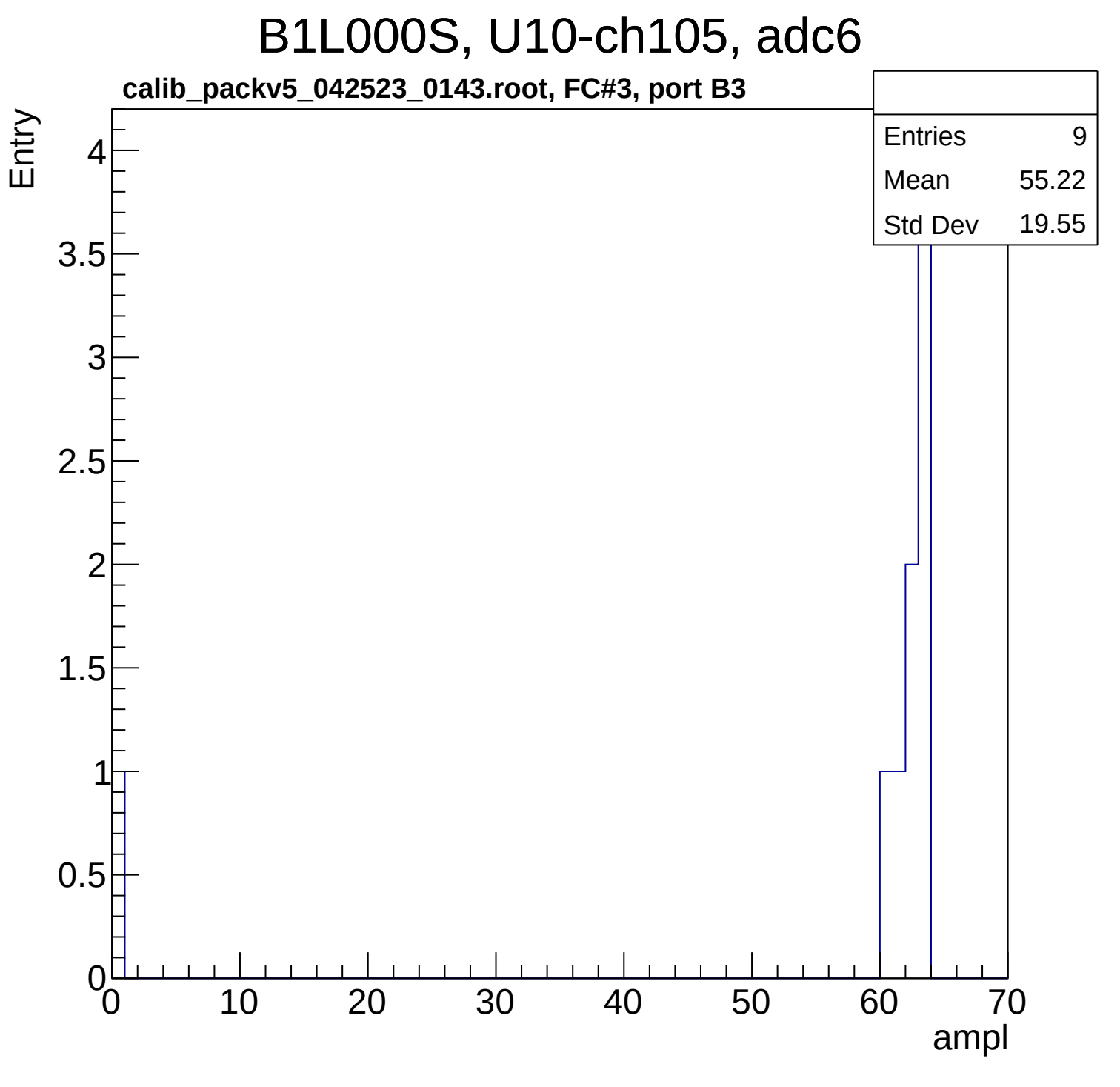
B1L000S, U10-ch105, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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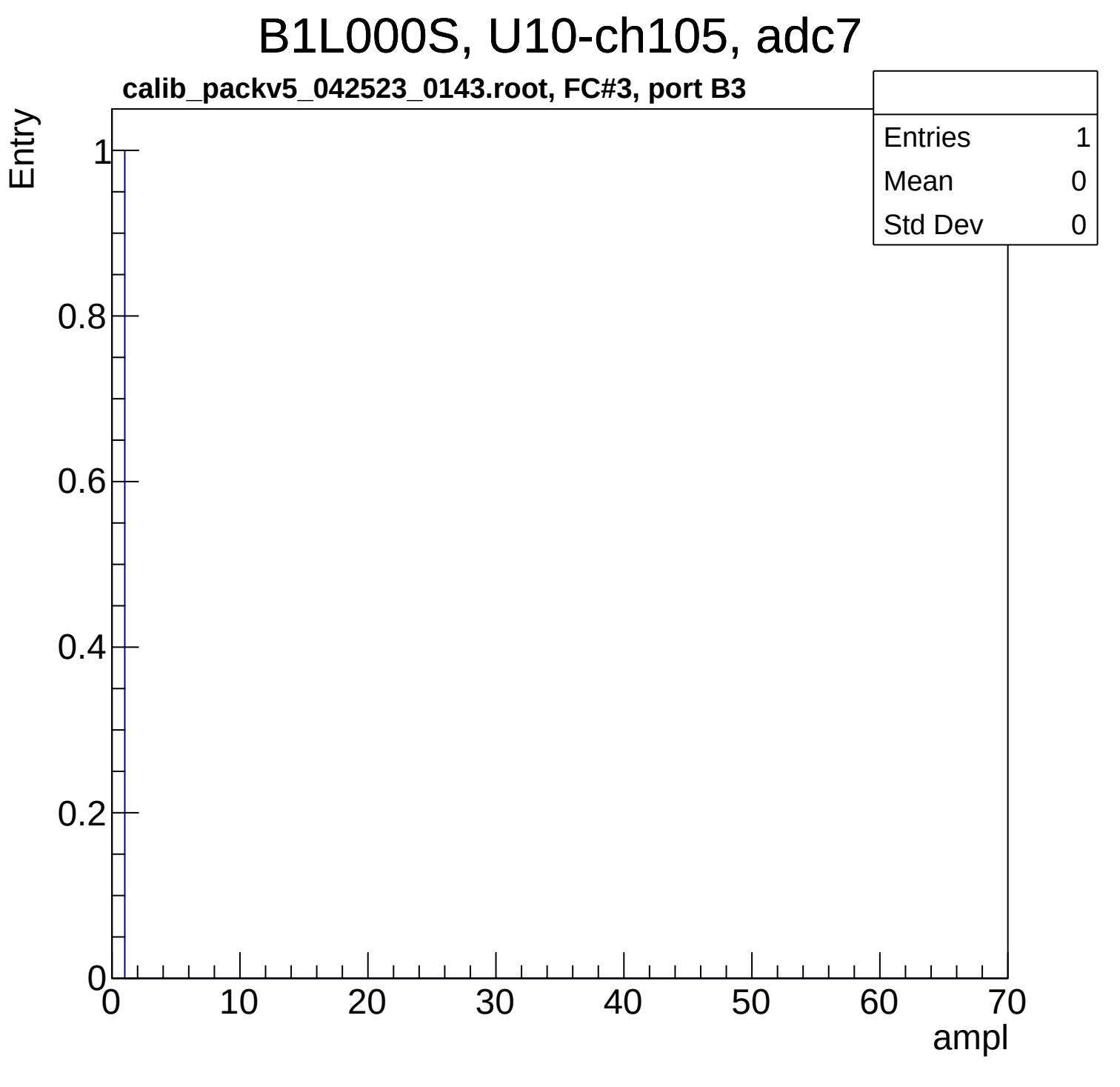
B1L000S, U10-ch105, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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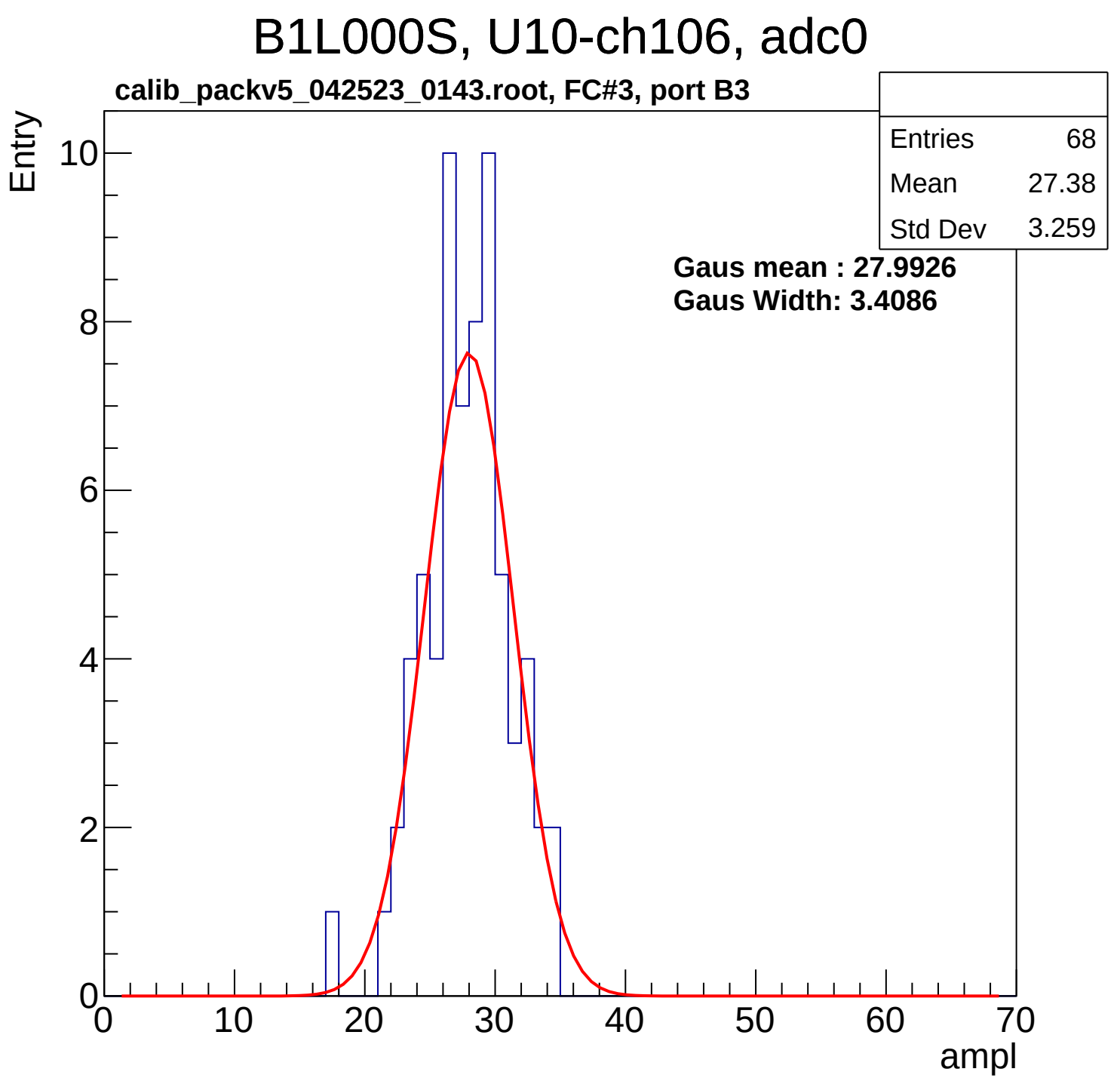
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch106, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 27.38
Std Dev 3.259
Gaus mean : 27.9926
Gaus Width: 3.4086
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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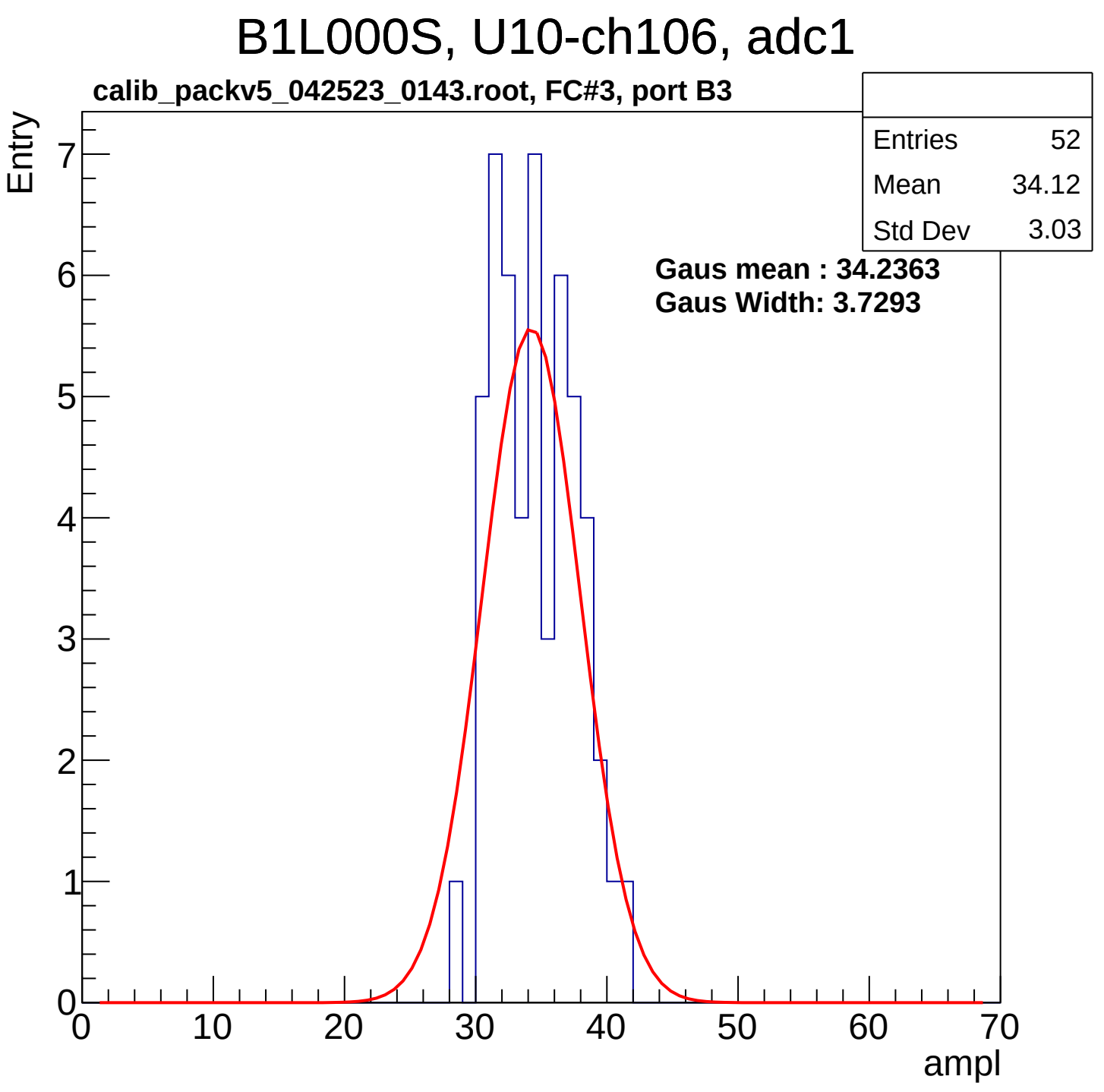
B1L000S, U10-ch106, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 34.12
Std Dev 3.03
Gaus mean : 34.2363
Gaus Width: 3.7293
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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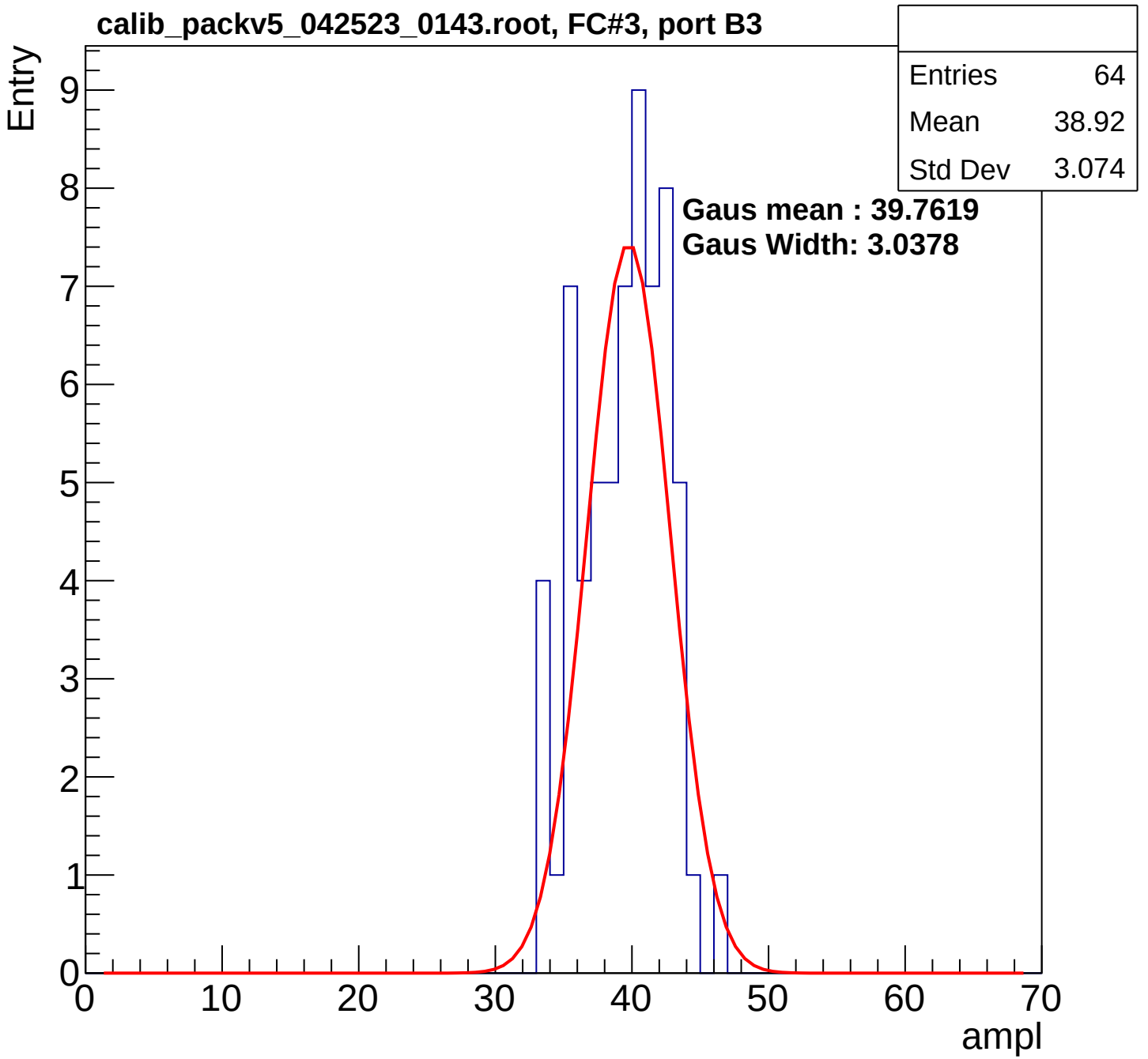
B1L000S, U10-ch106, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 38.92
Std Dev 3.074
Gaus mean : 39.7619
Gaus Width: 3.0378
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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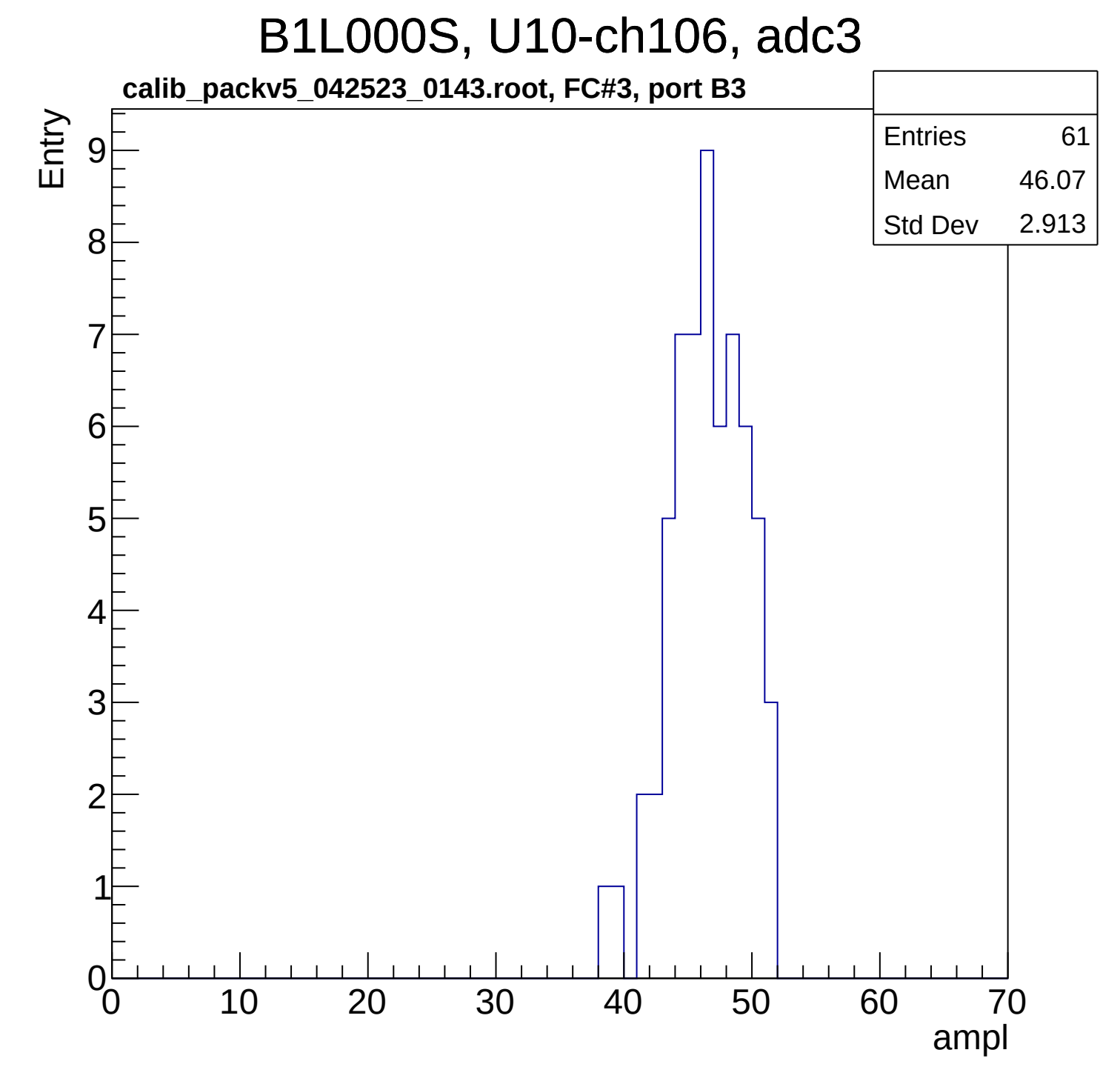
B1L000S, U10-ch106, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 46.07
Std Dev 2.913
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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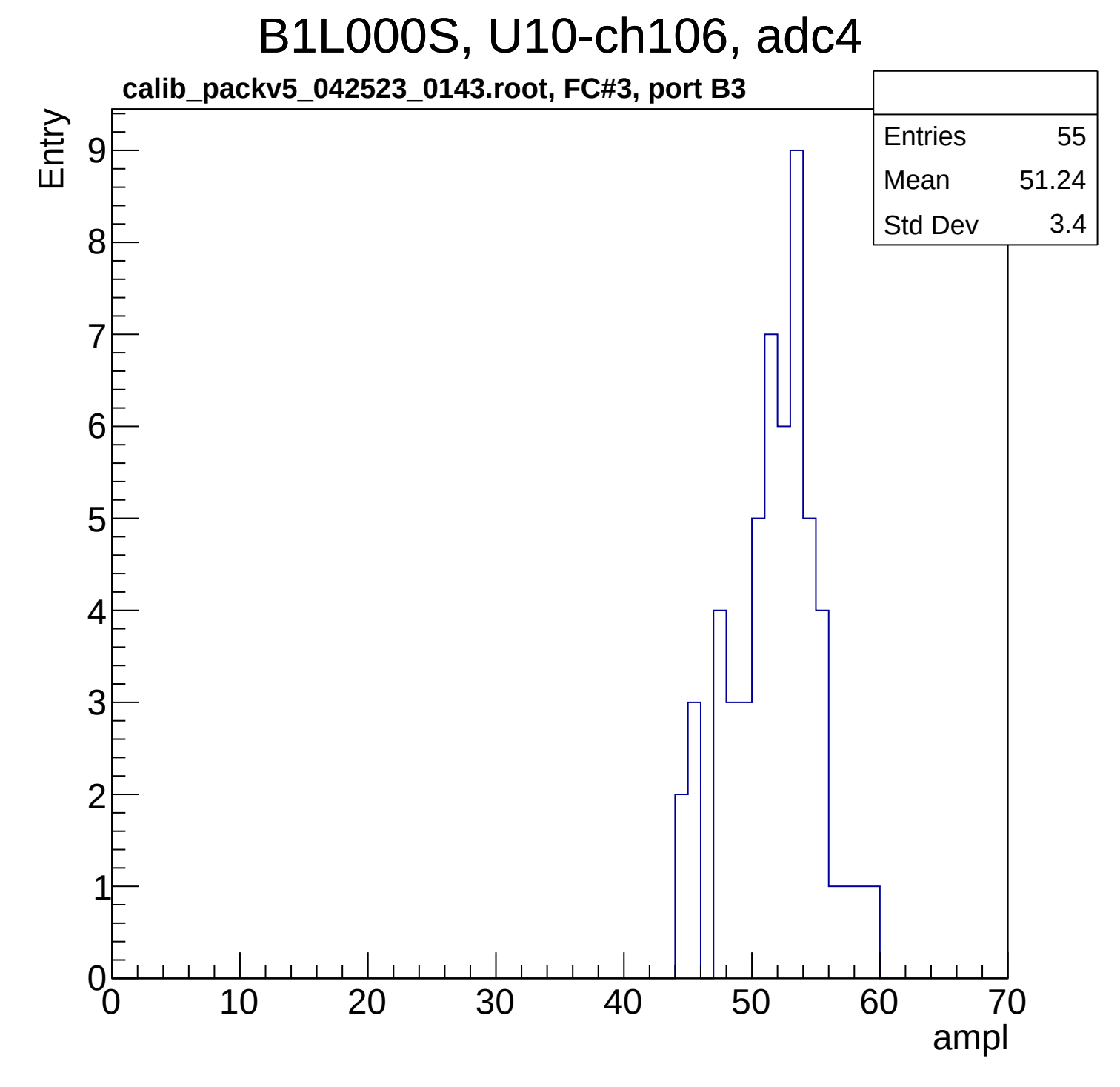
B1L000S, U10-ch106, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 51.24
Std Dev 3.4
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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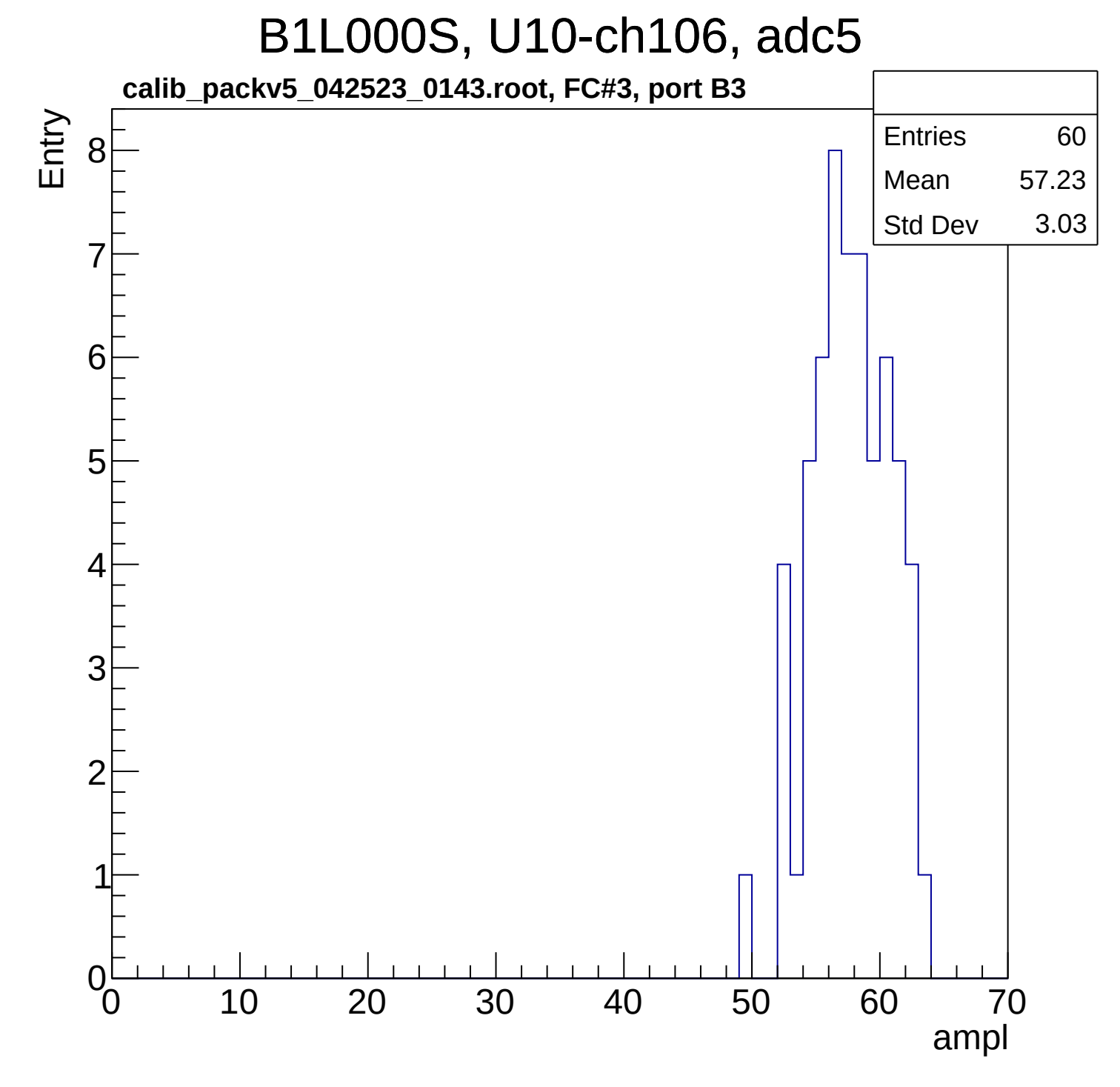
B1L000S, U10-ch106, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 57.23
Std Dev 3.03
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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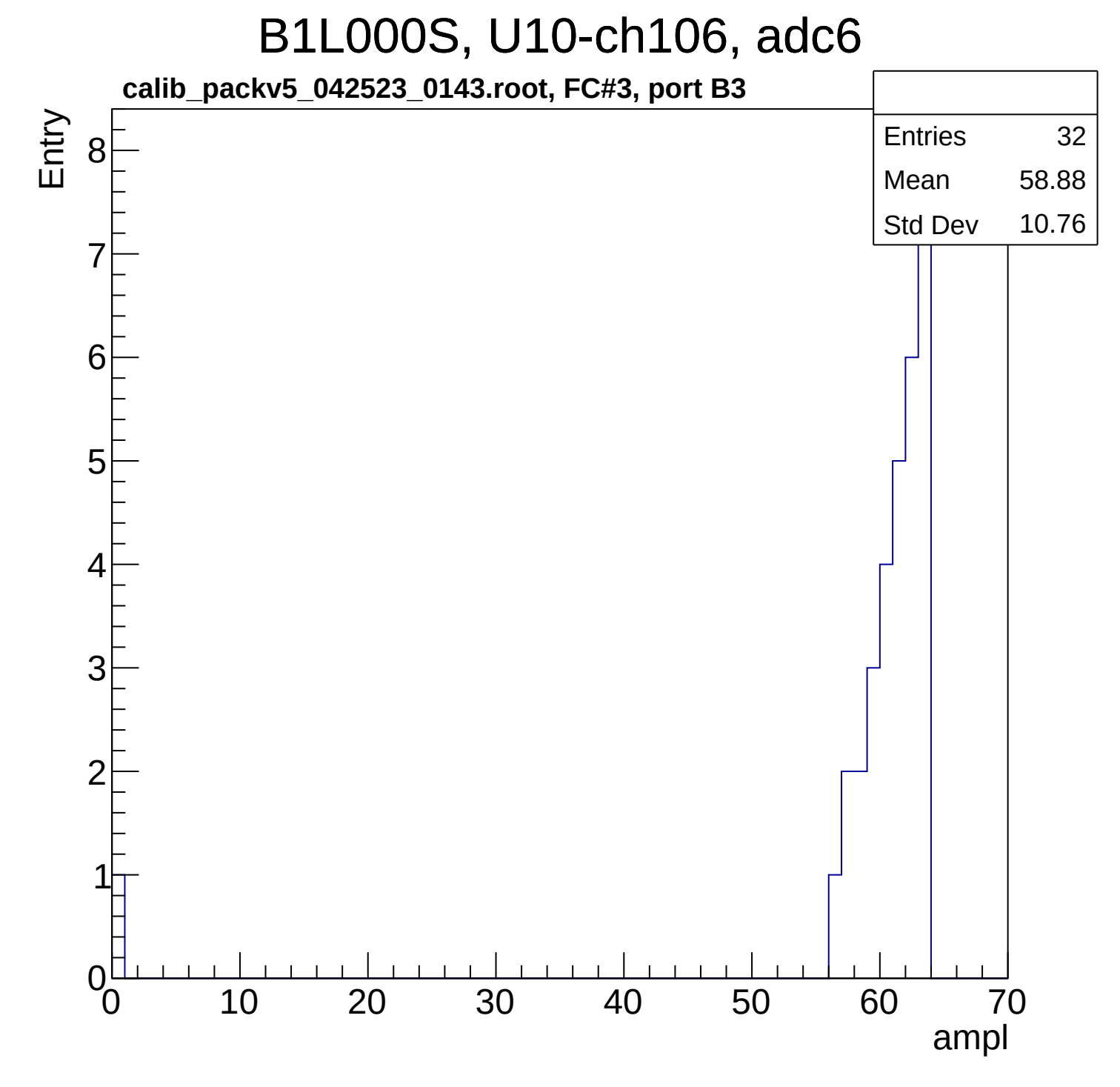
B1L000S, U10-ch106, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 58.88
Std Dev 10.76
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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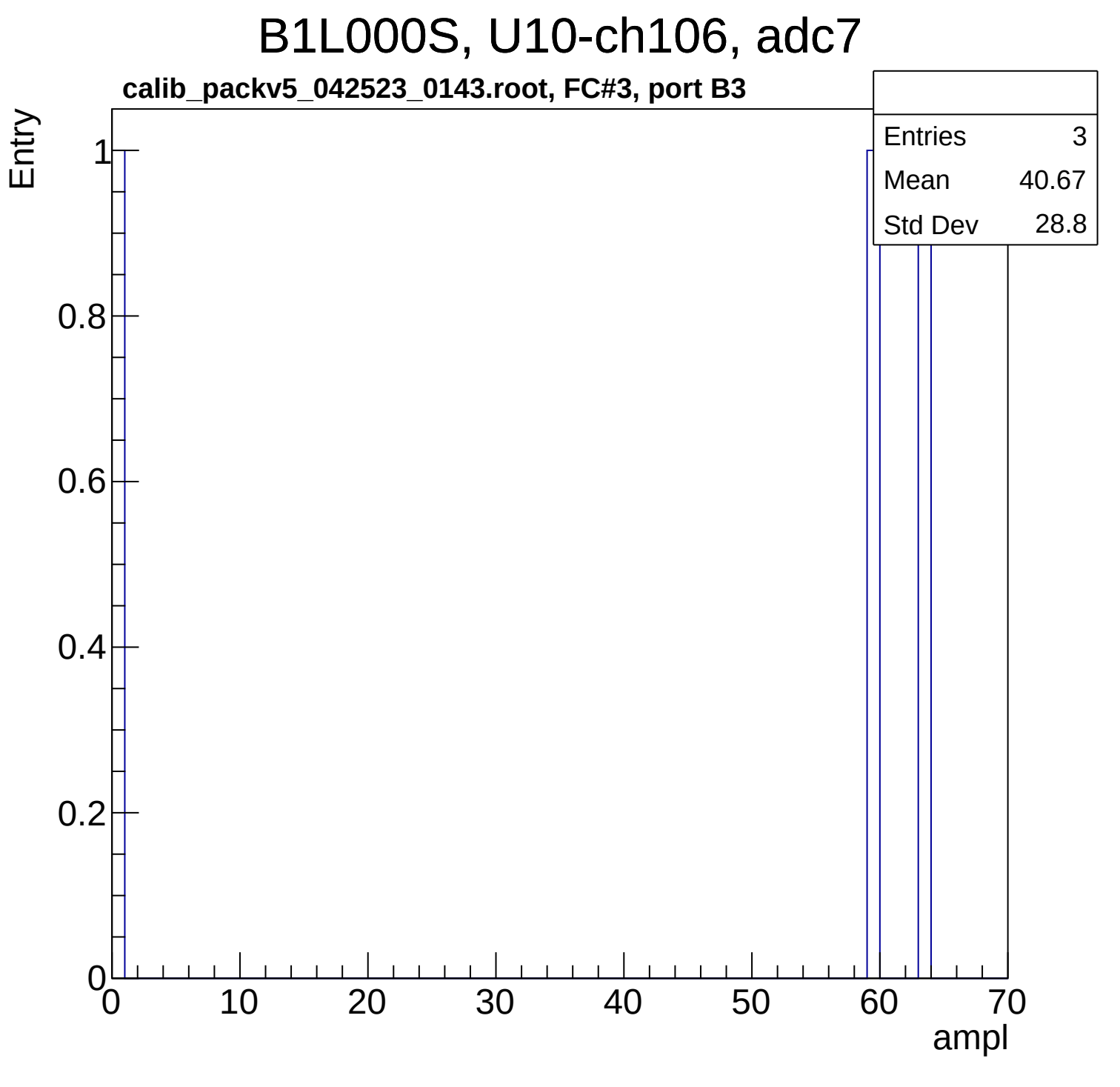
B1L000S, U10-ch106, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 40.67
Std Dev 28.8
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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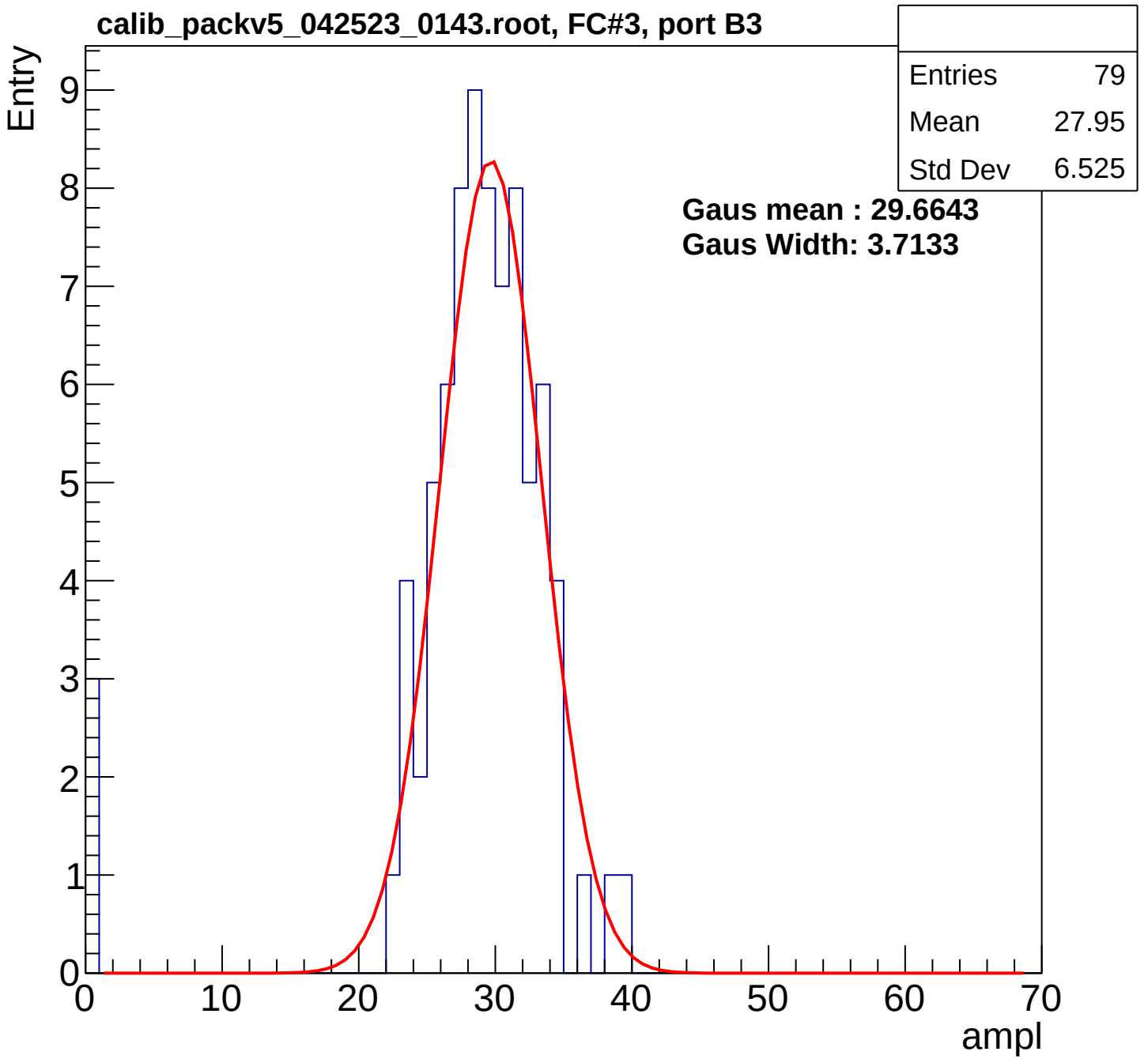
B1L000S, U10-ch107, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 27.95
Std Dev 6.525
Gaus mean : 29.6643
Gaus Width: 3.7133
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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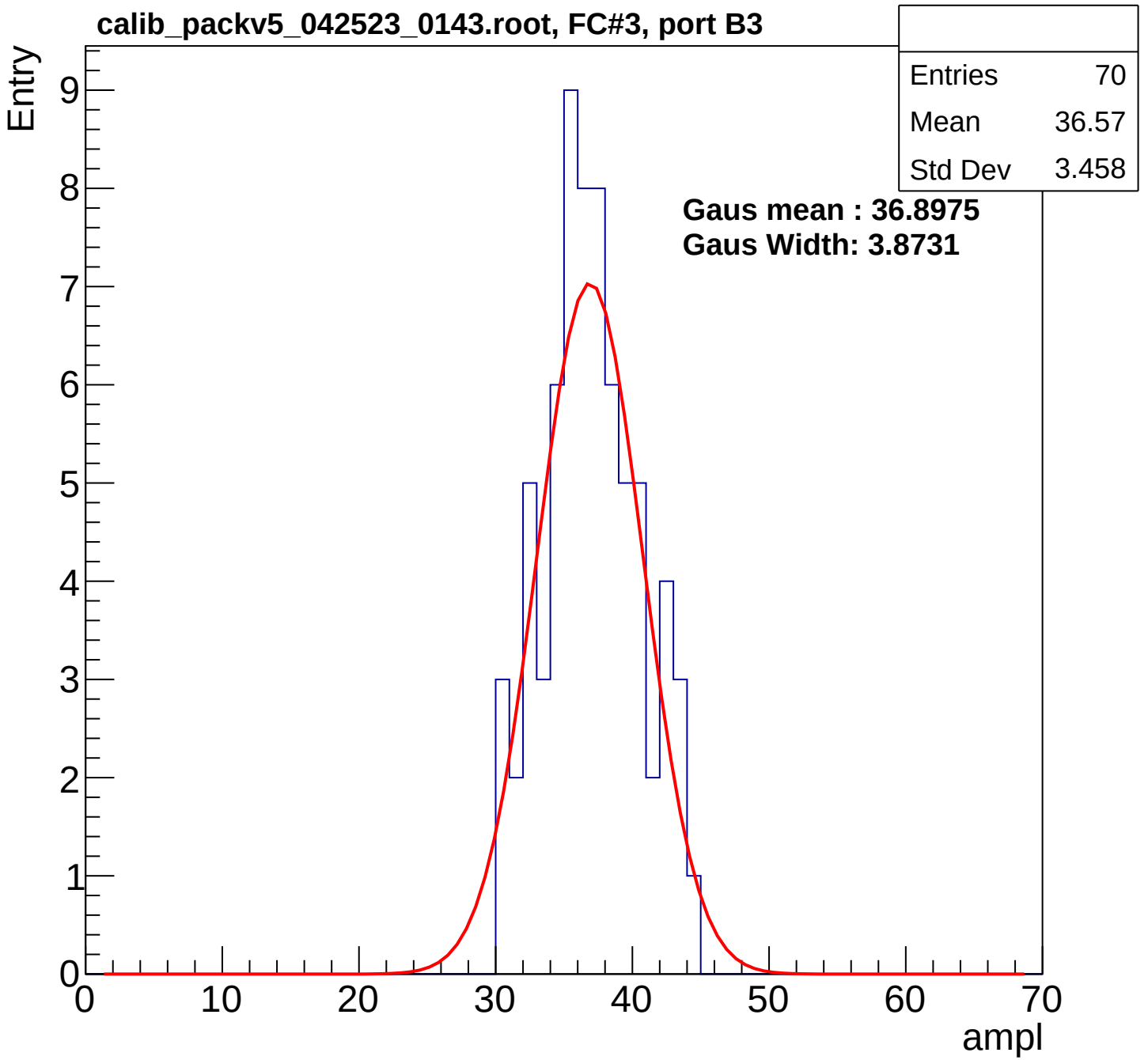
B1L000S, U10-ch107, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 36.57
Std Dev 3.458
Gaus mean : 36.8975
Gaus Width: 3.8731
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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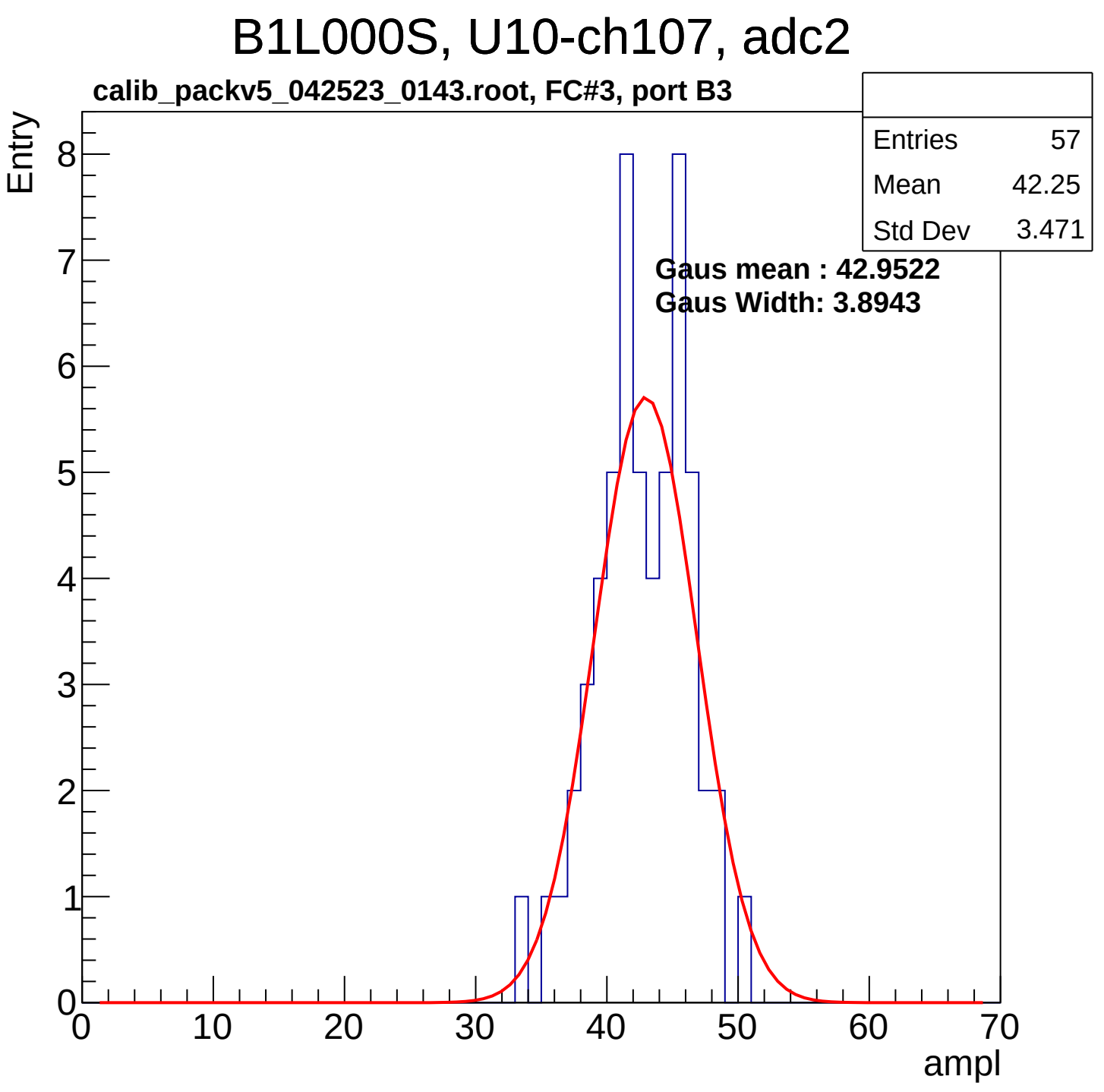
B1L000S, U10-ch107, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 42.25
Std Dev 3.471
Gaus mean : 42.9522
Gaus Width: 3.8943
Entry
ampl
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2
3
4
5
6
7
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0
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60
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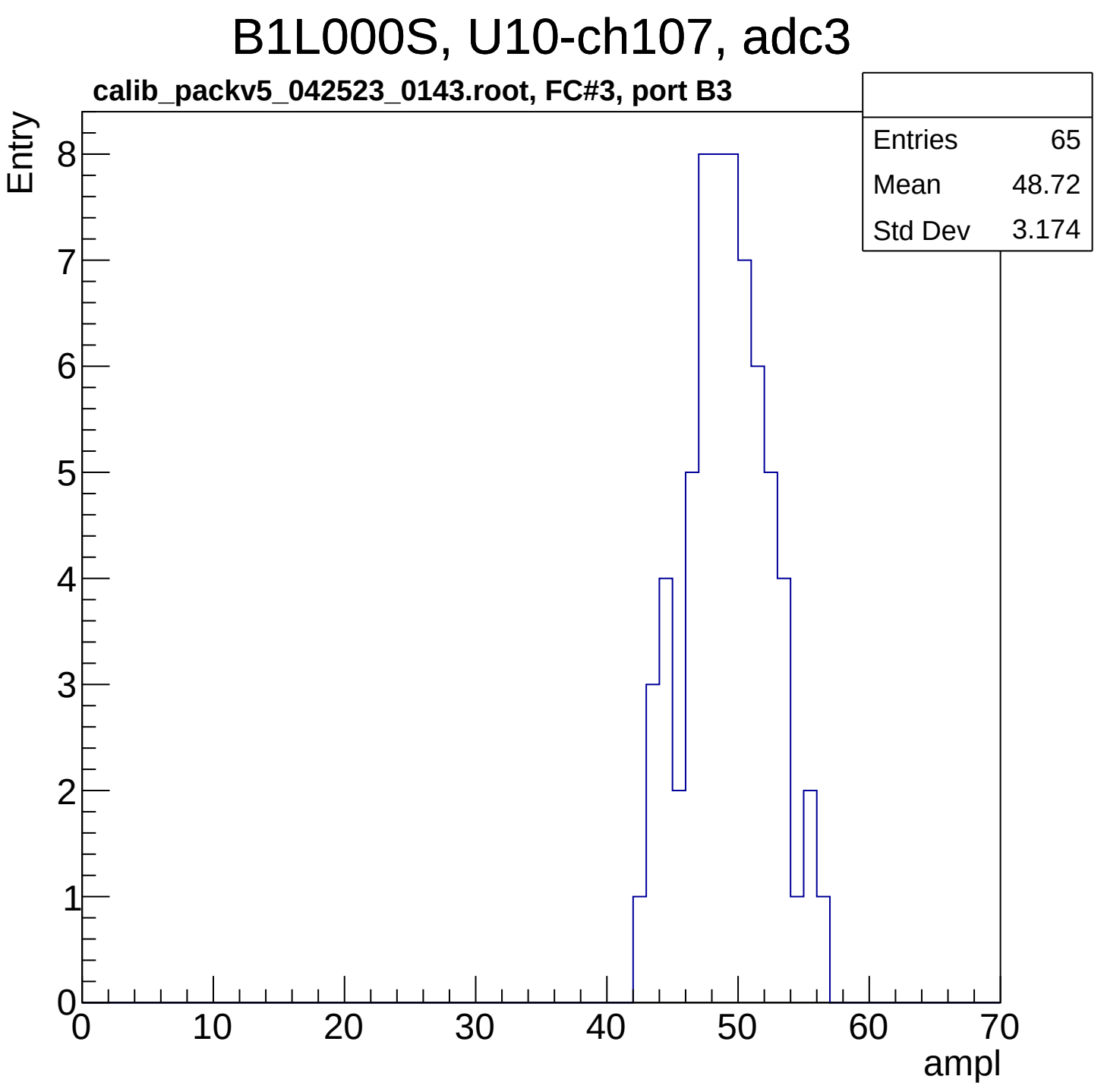
B1L000S, U10-ch107, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.72
Std Dev 3.174
Entry
ampl
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2
3
4
5
6
7
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0
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60
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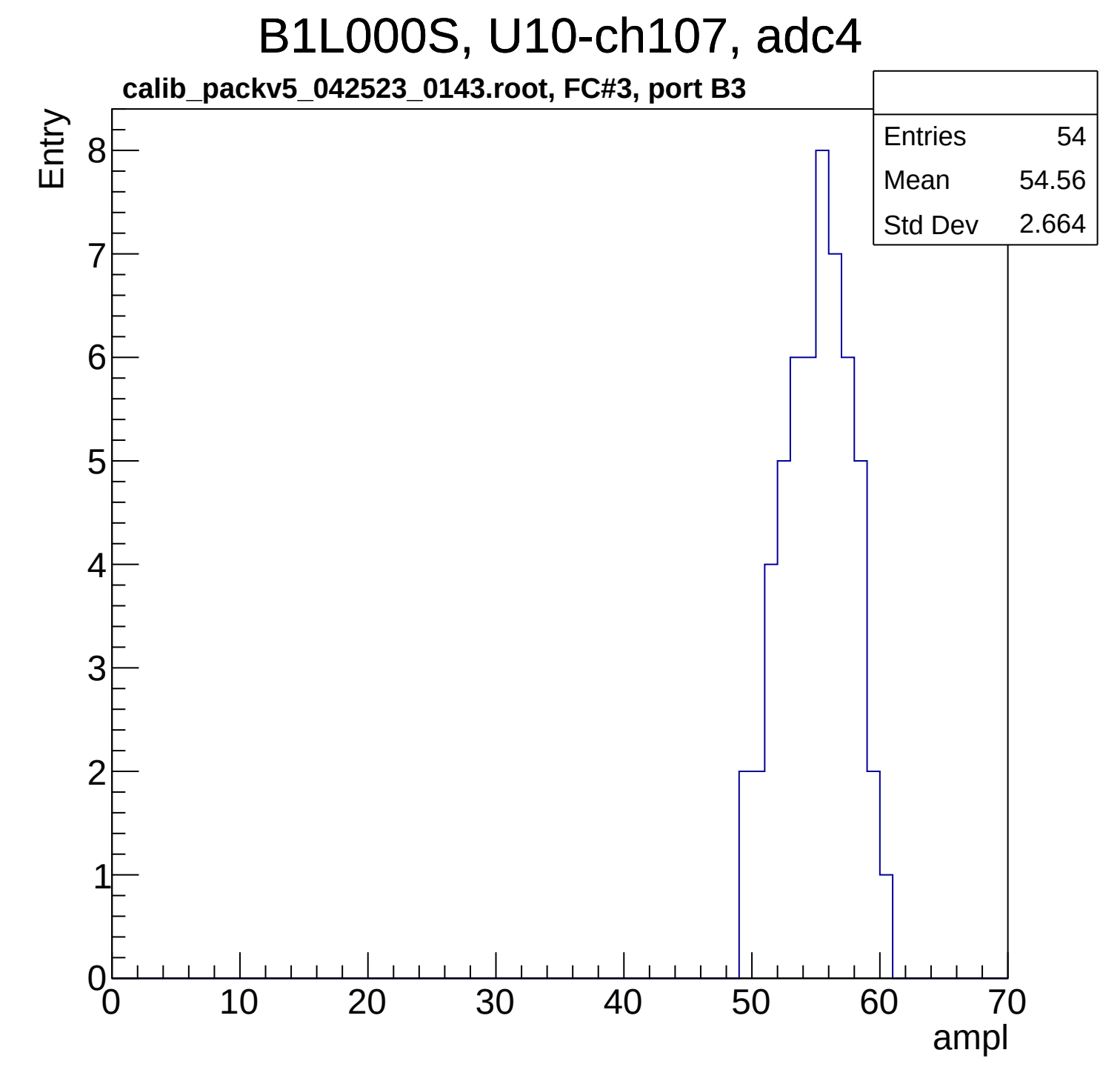
B1L000S, U10-ch107, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.56
Std Dev 2.664
Entry
ampl
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0
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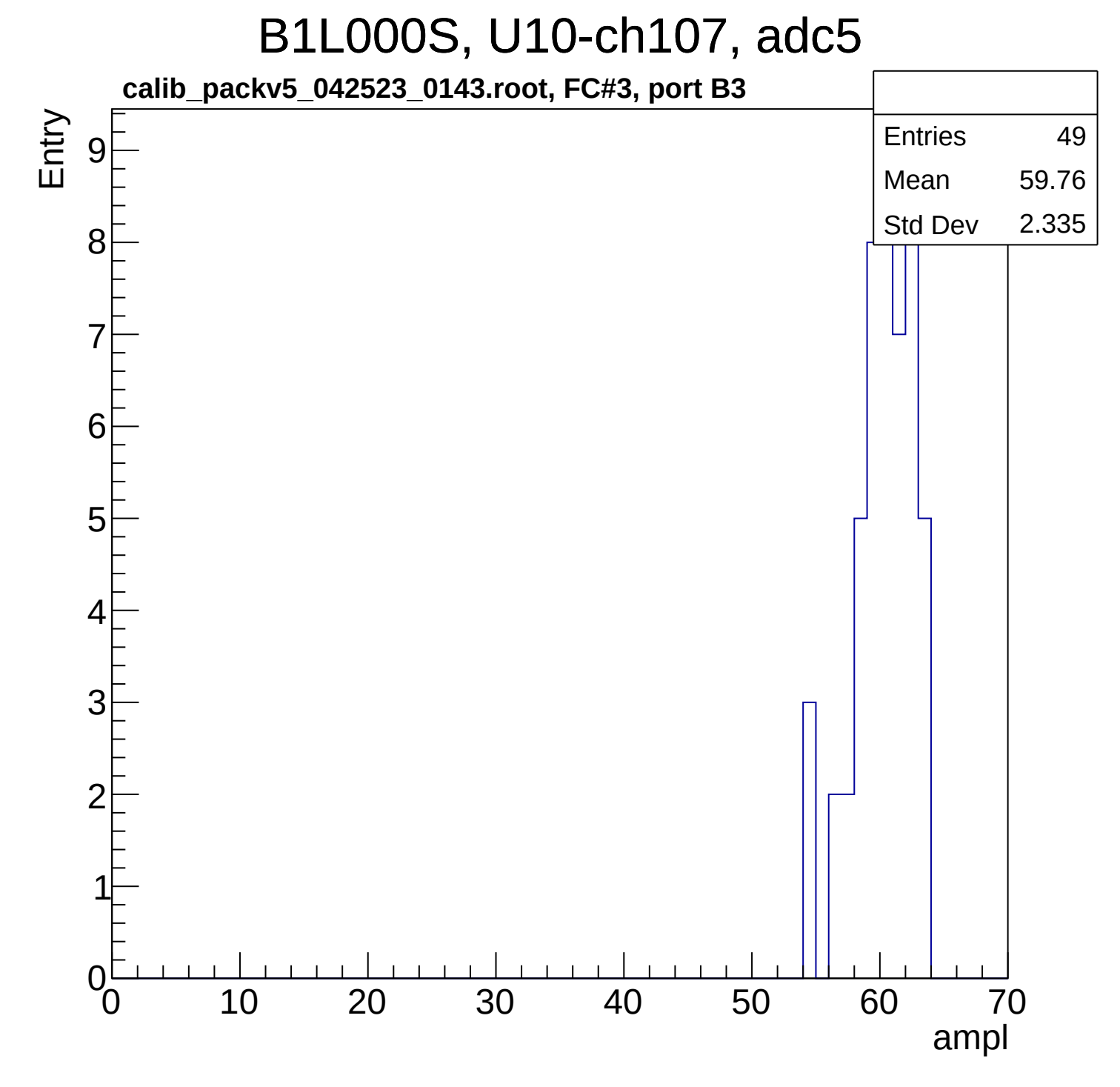
B1L000S, U10-ch107, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 59.76
Std Dev 2.335
Entry
ampl
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4
5
6
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0
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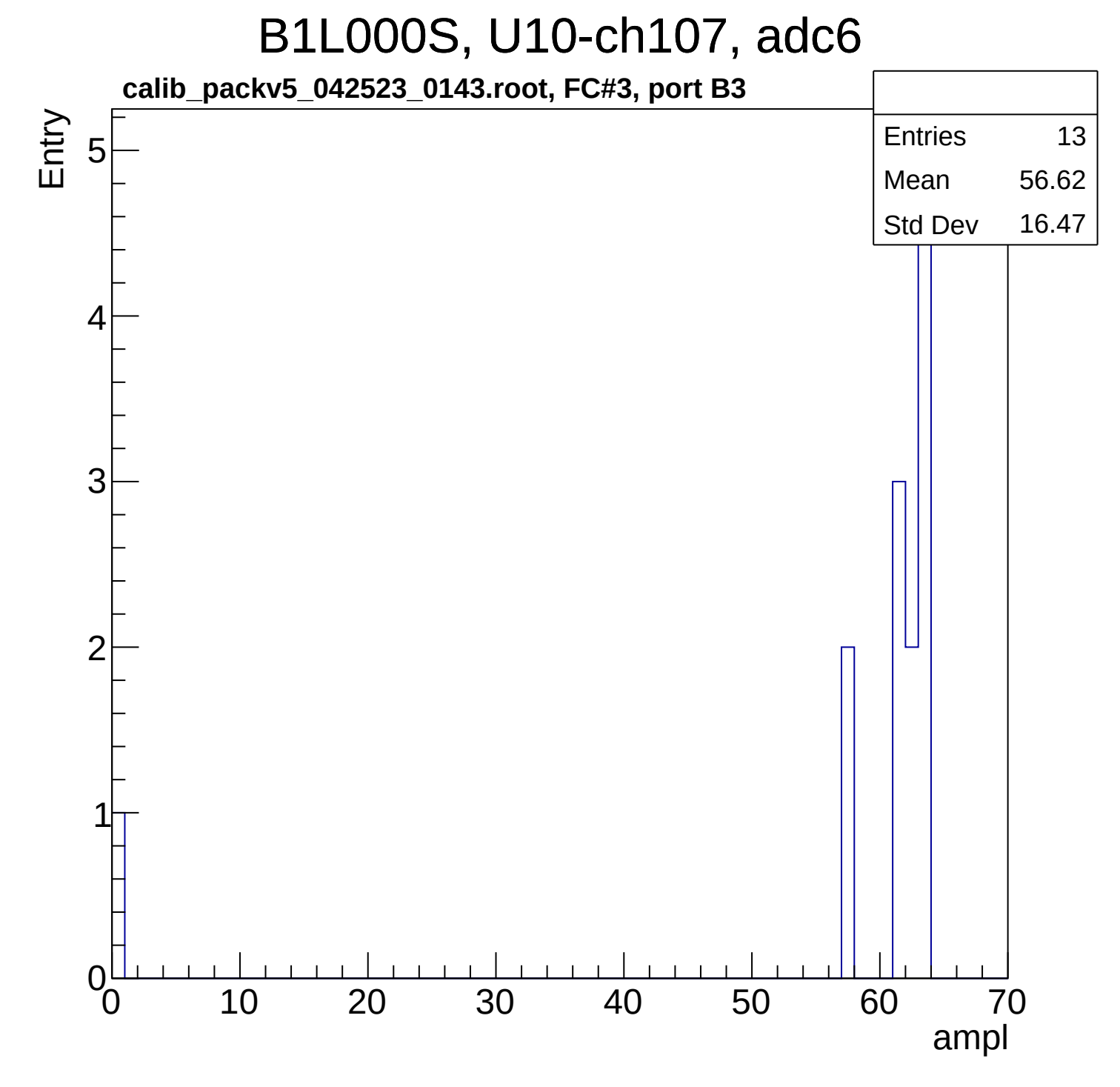
B1L000S, U10-ch107, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.62
Std Dev 16.47
Entry
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2
3
4
5
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10
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30
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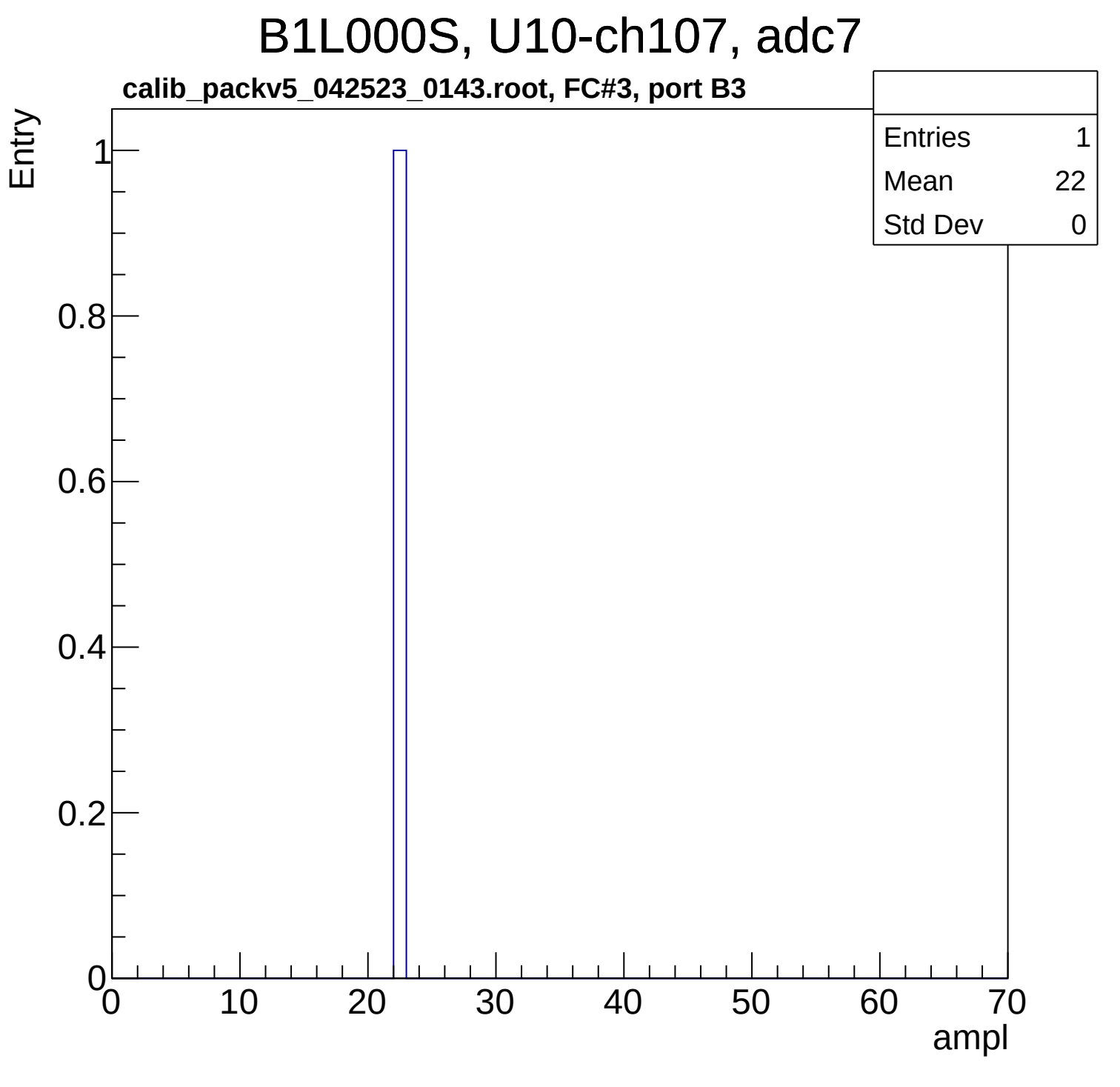
50
60
70
ampl


B1L000S, U10-ch107, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.4
0.2
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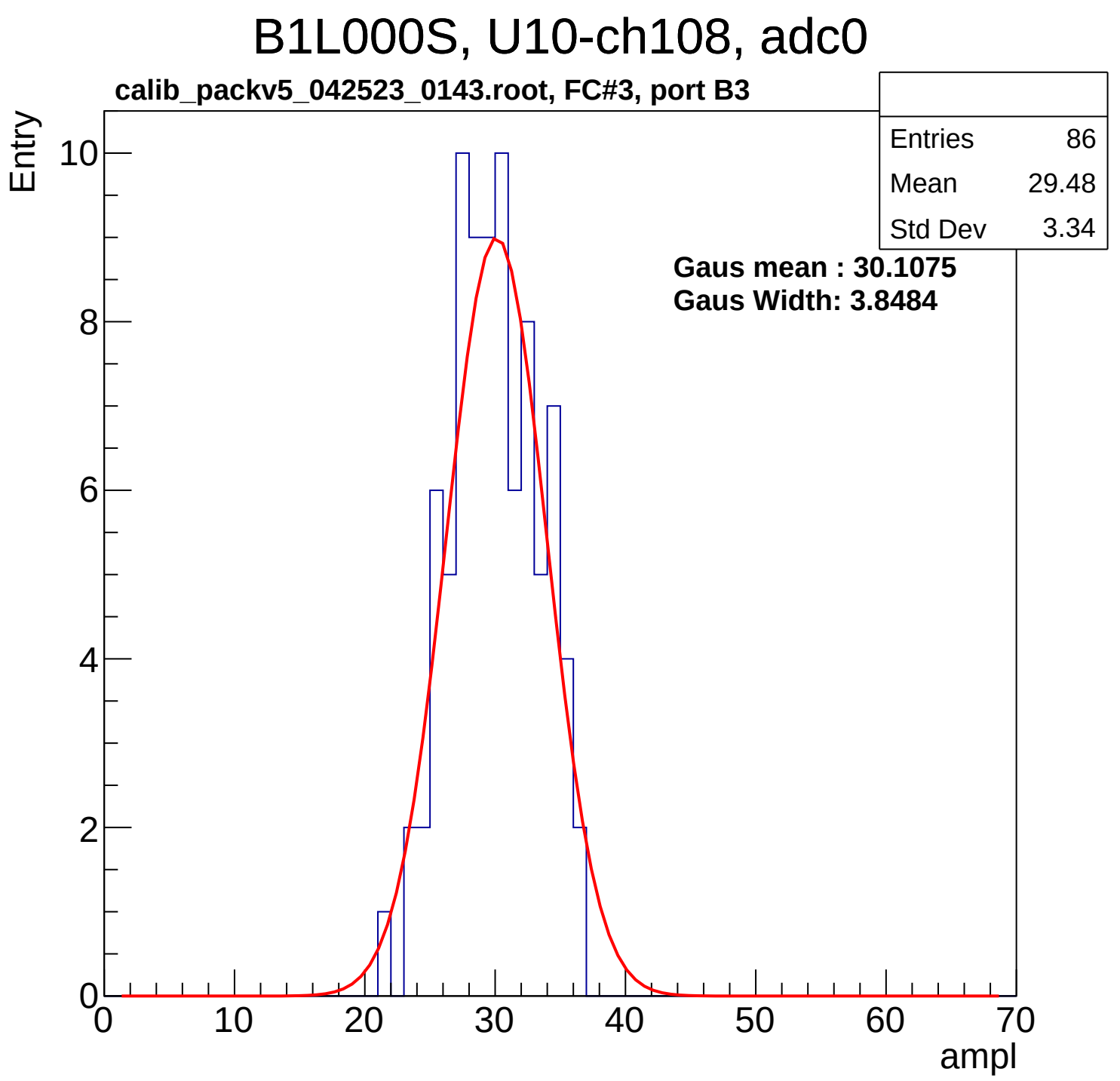
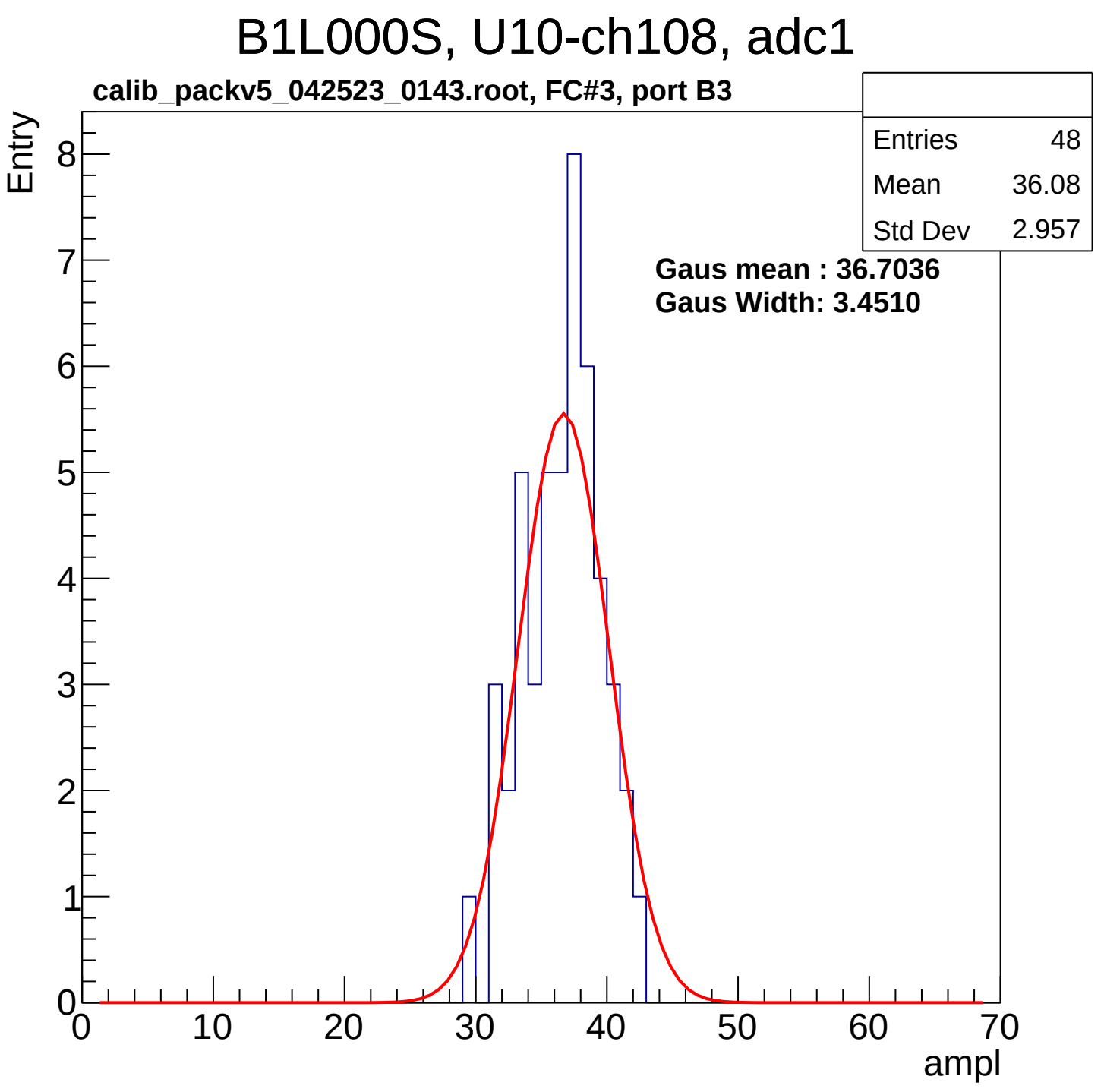
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch108, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 29.48
Std Dev 3.34
Gaus mean : 30.1075
Gaus Width: 3.8484
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch108, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 36.08
Std Dev 2.957
Gaus mean : 36.7036
Gaus Width: 3.4510
Entry
ampl
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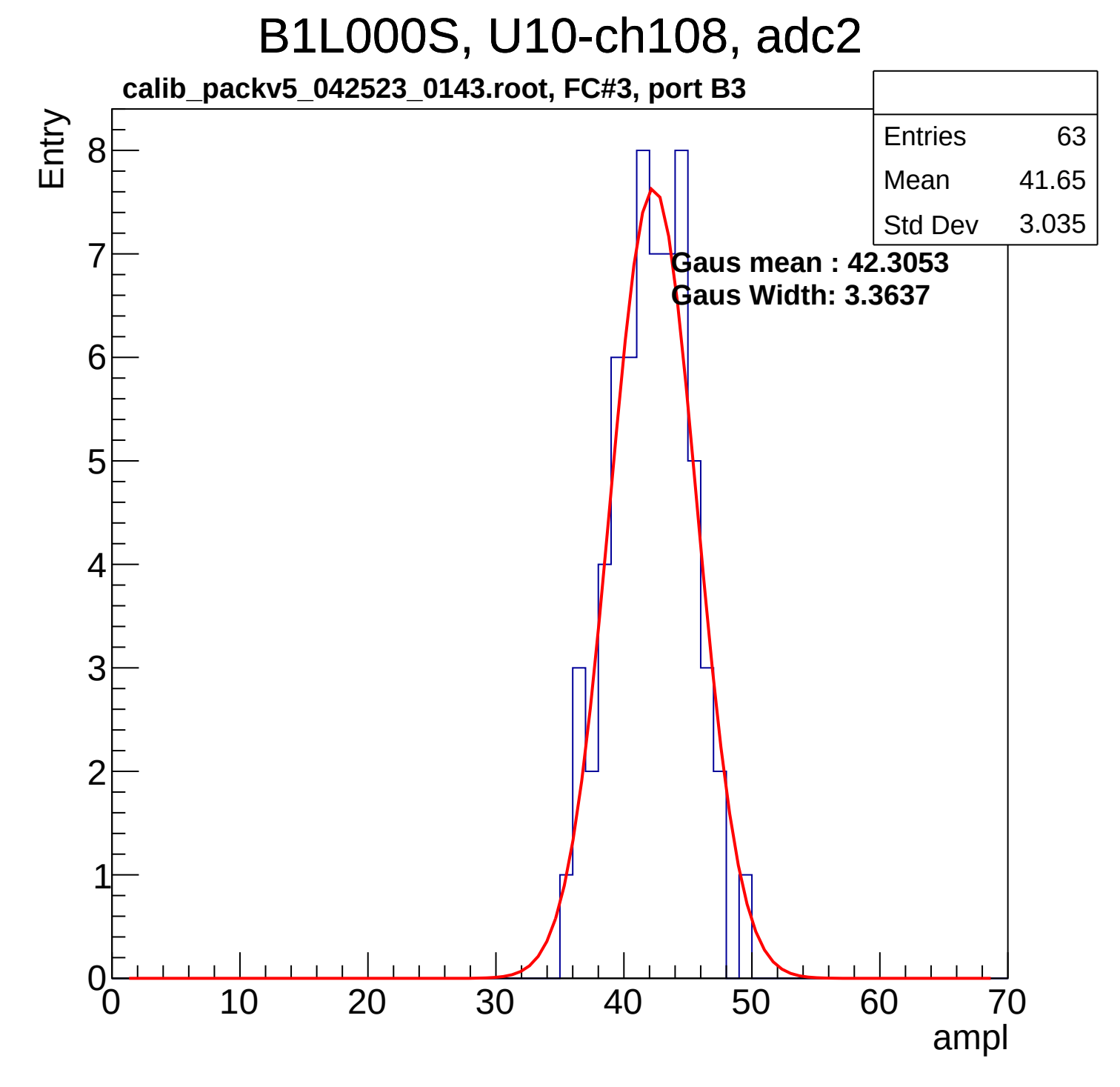
B1L000S, U10-ch108, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 41.65
Std Dev 3.035
Gaus mean : 42.3053
Gaus Width: 3.3637
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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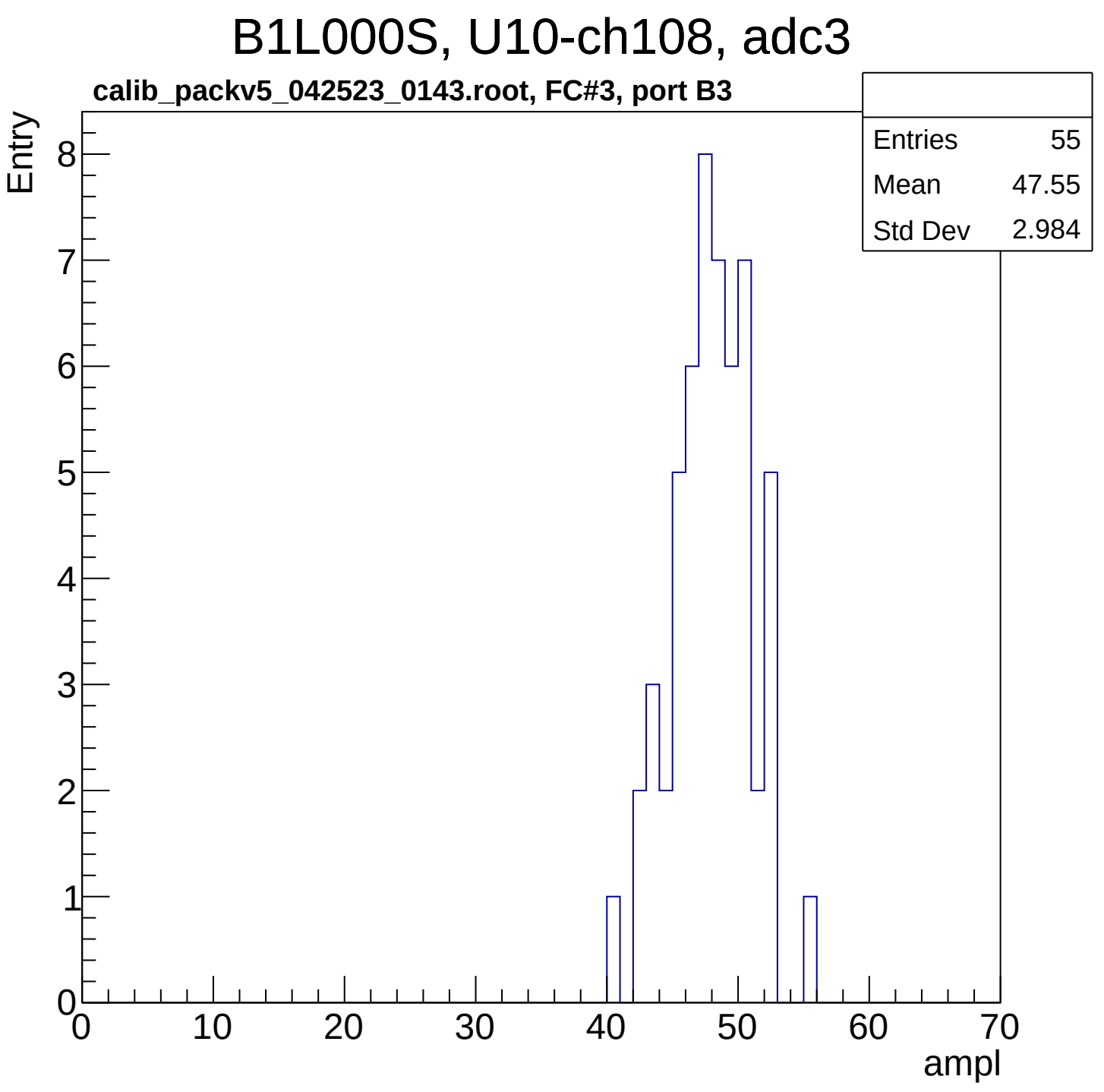
B1L000S, U10-ch108, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 47.55
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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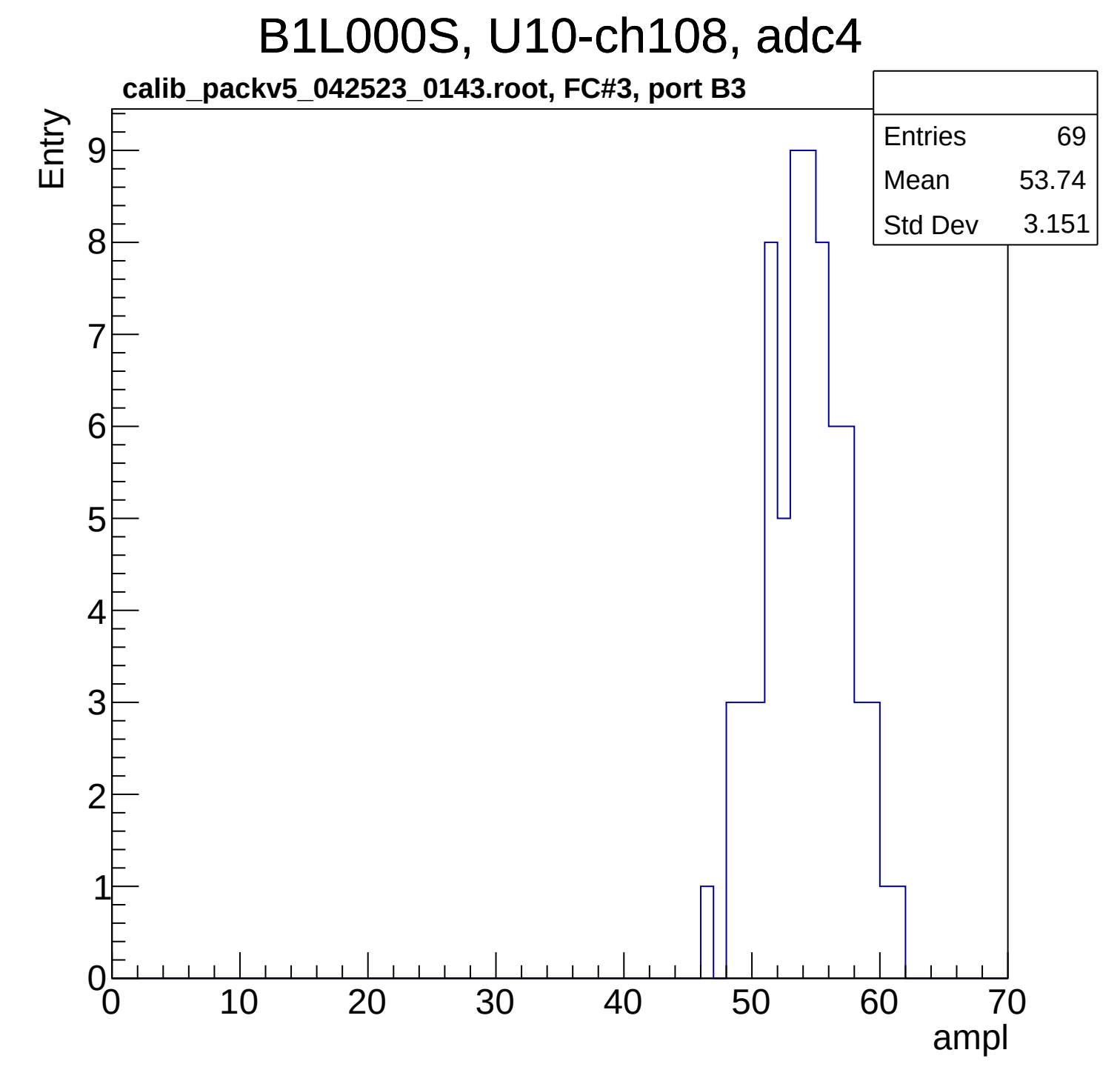
B1L000S, U10-ch108, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 53.74
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
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0
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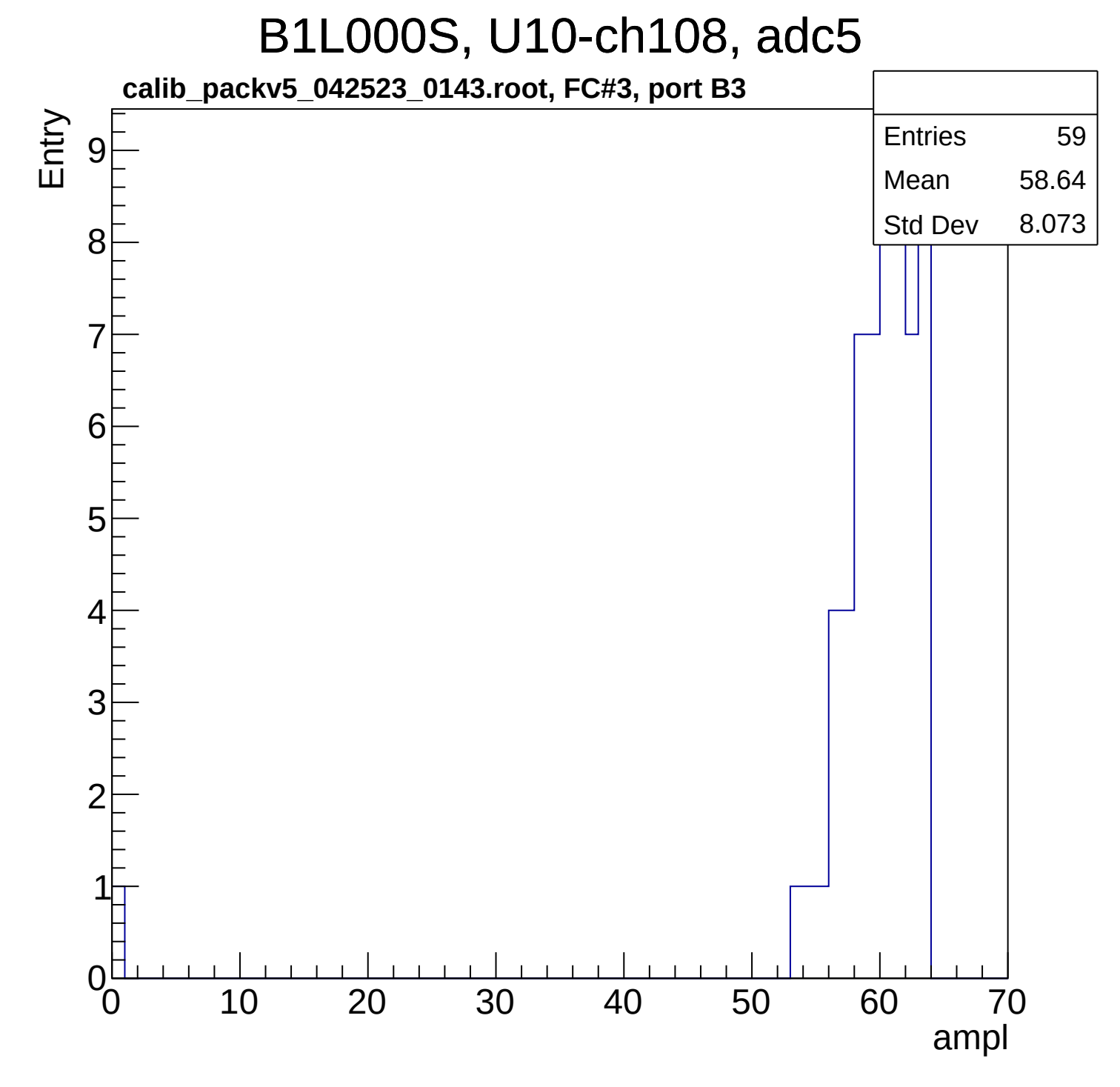
B1L000S, U10-ch108, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.64
Std Dev 8.073
Entry
ampl
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5
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7
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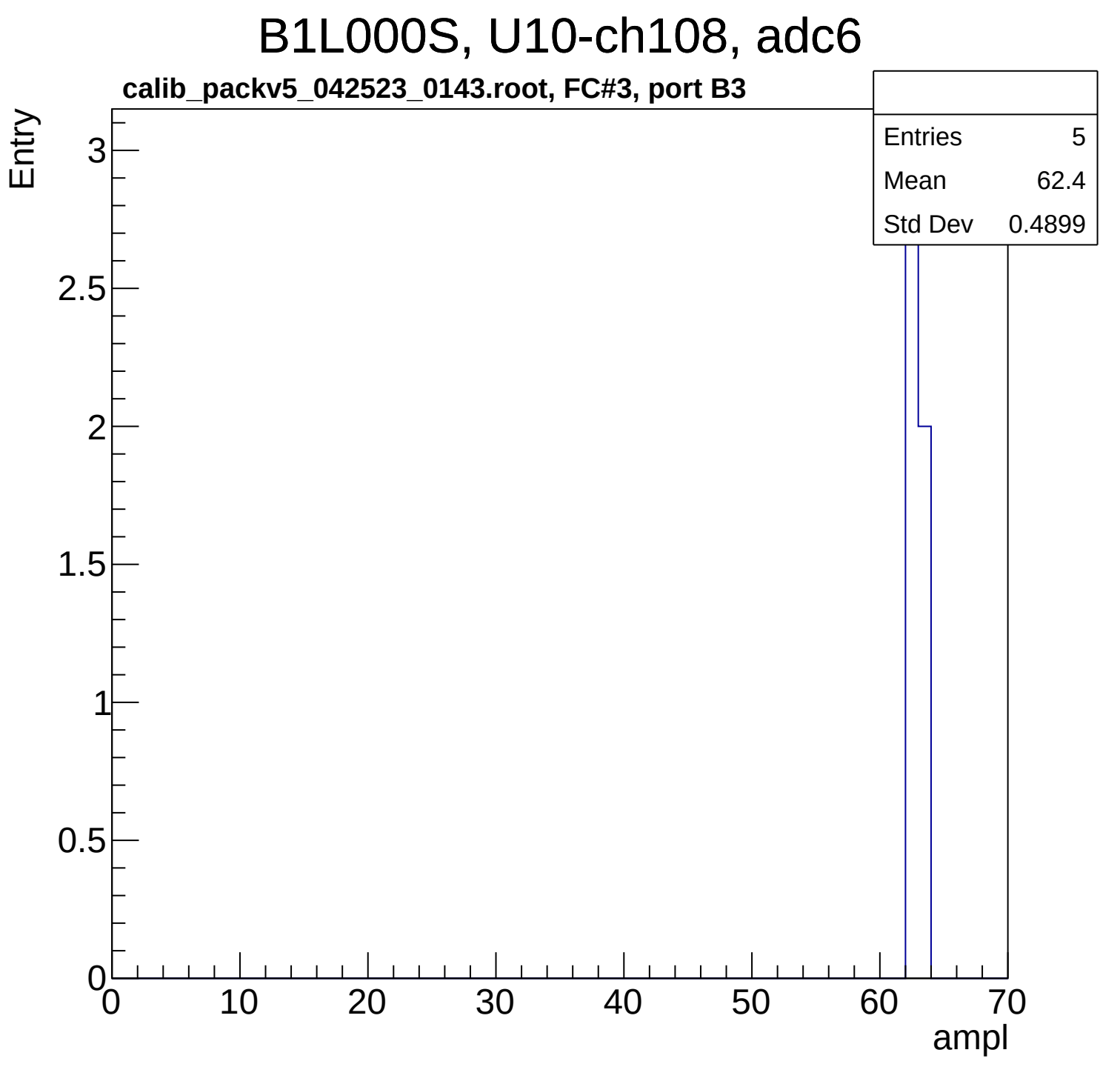
B1L000S, U10-ch108, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62.4
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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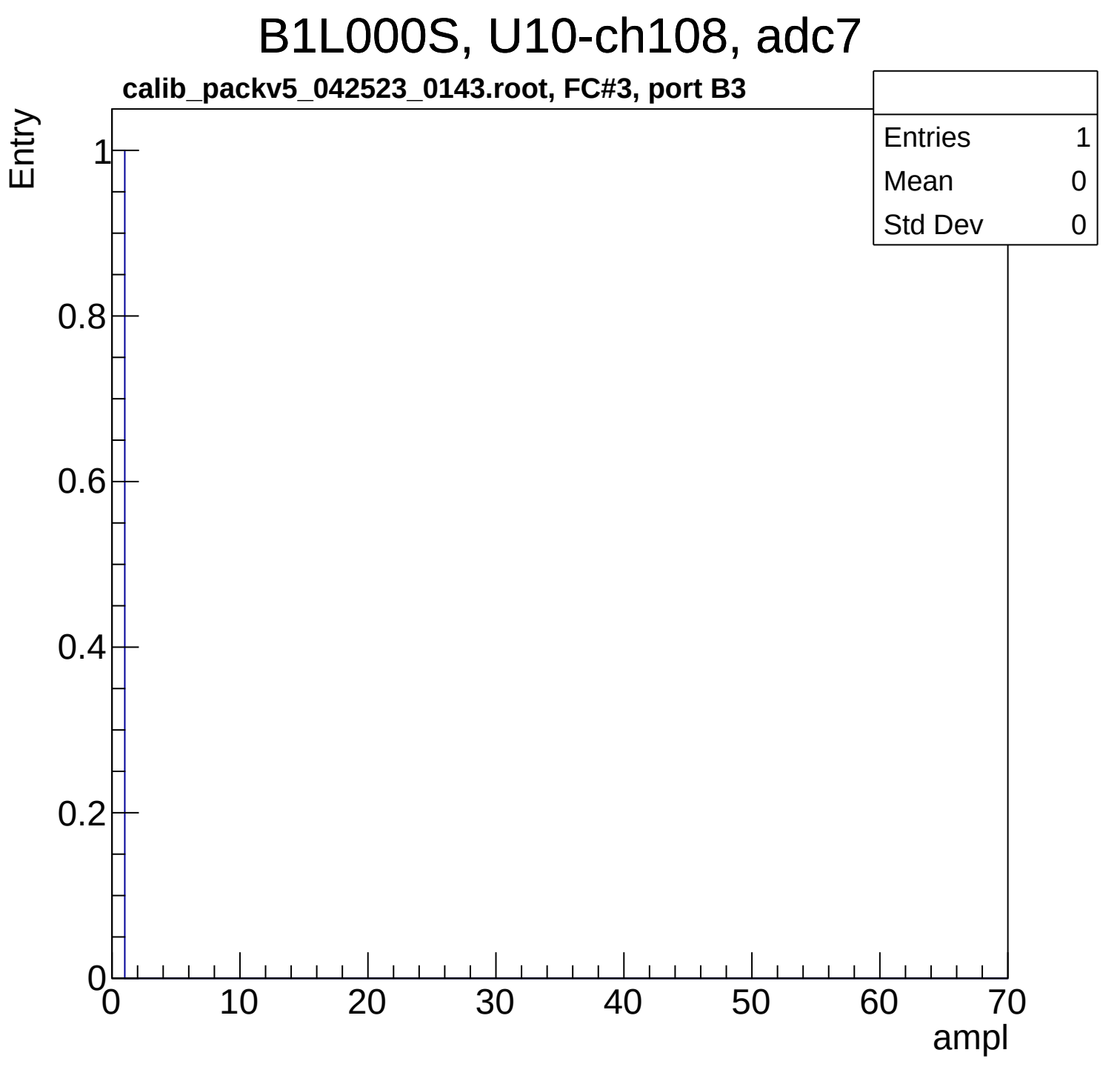
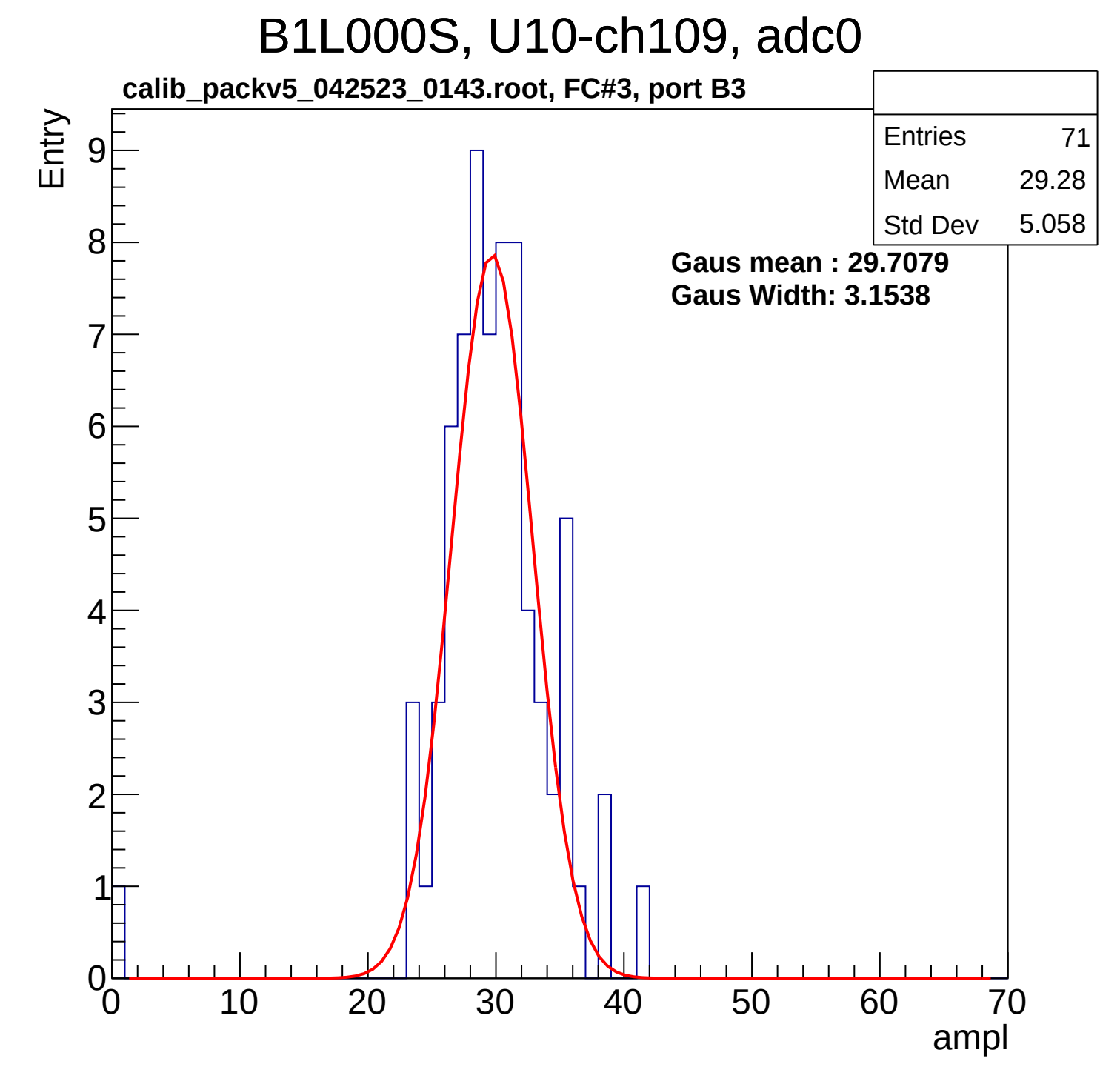

B1L000S, U10-ch108, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch109, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 29.28
Std Dev 5.058
Gaus mean : 29.7079
Gaus Width: 3.1538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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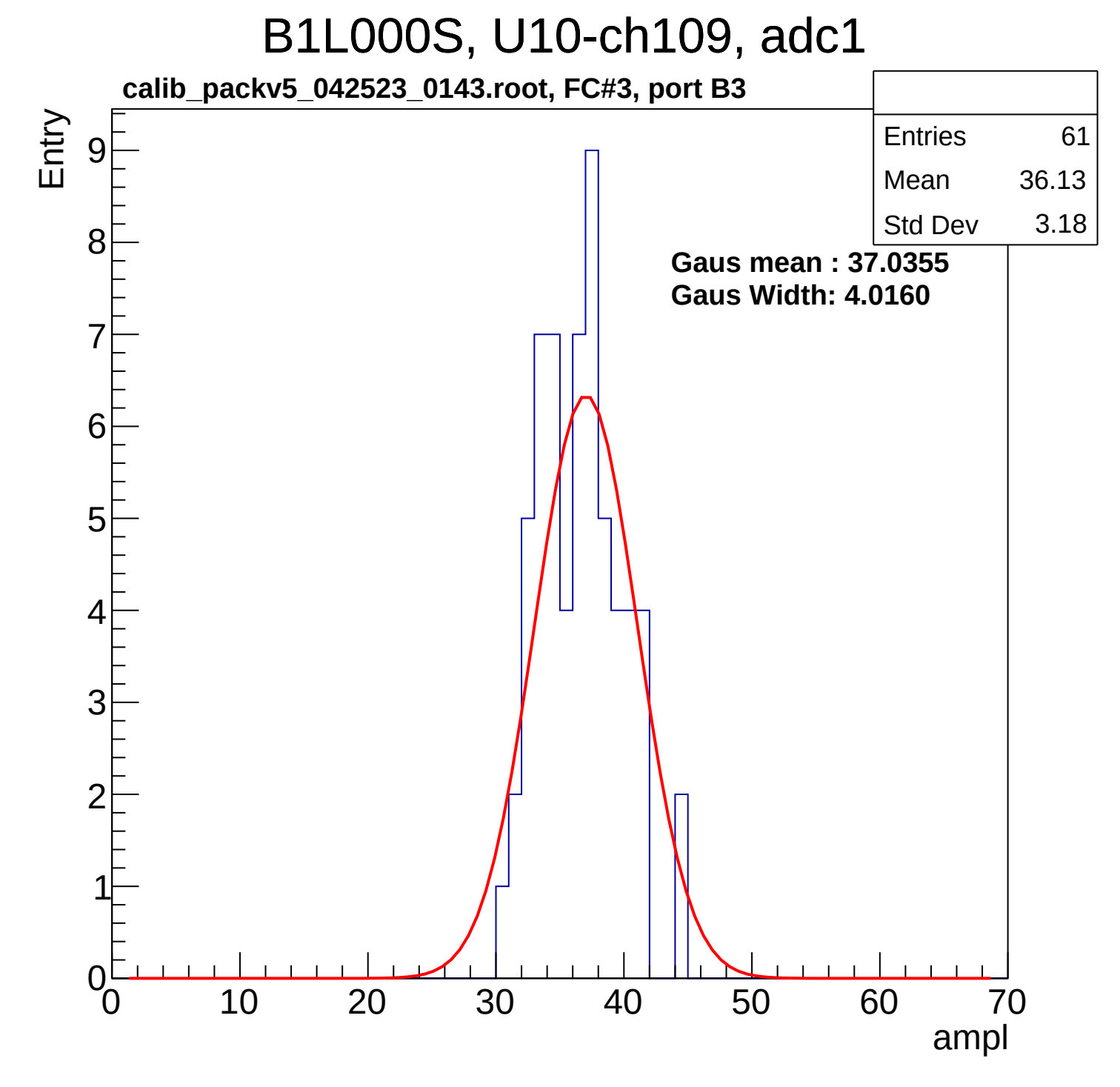
B1L000S, U10-ch109, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 36.13
Std Dev 3.18
Gaus mean : 37.0355
Gaus Width: 4.0160
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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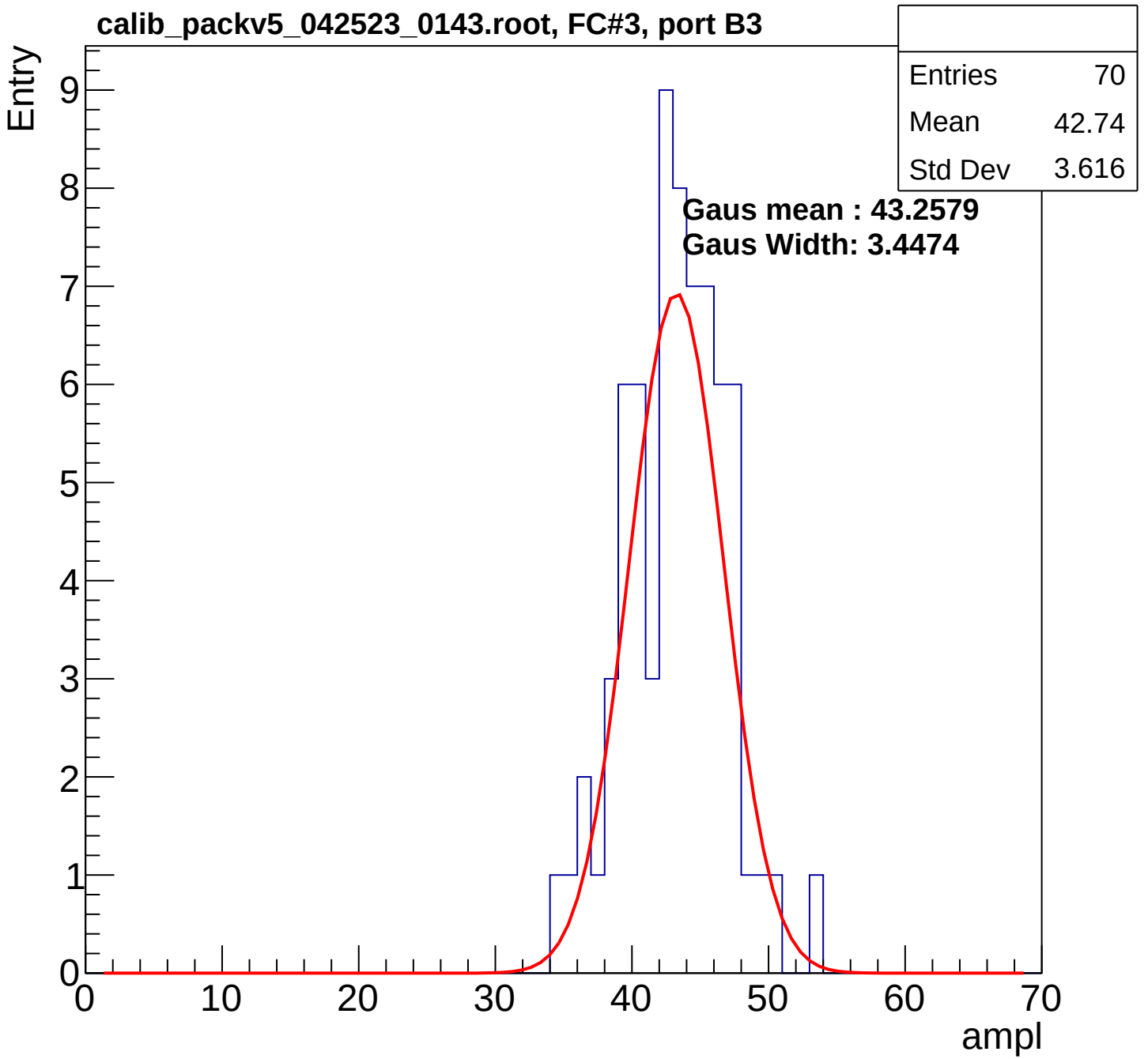
B1L000S, U10-ch109, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.74
Std Dev 3.616
Gaus mean : 43.2579
Gaus Width: 3.4474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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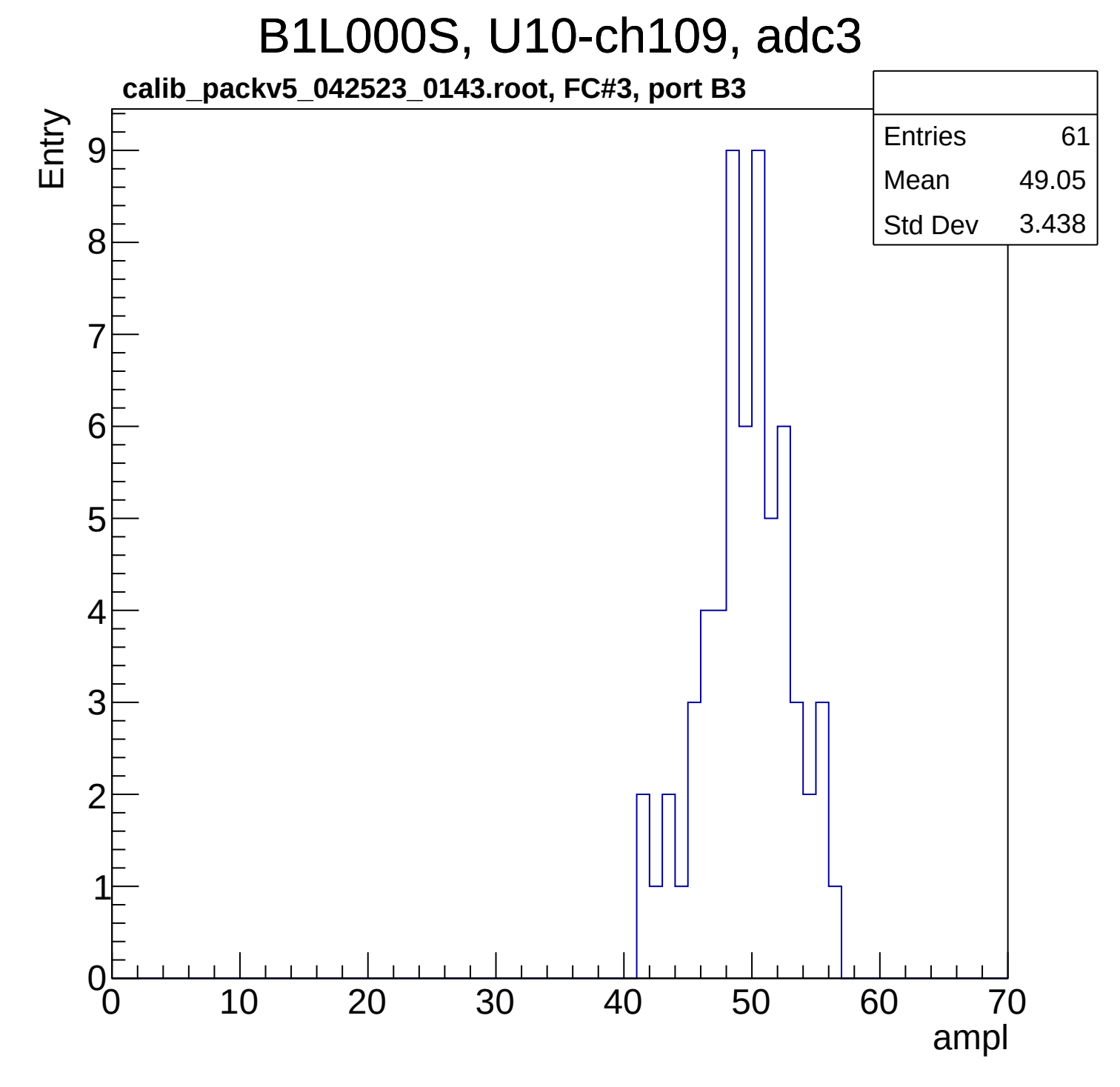
B1L000S, U10-ch109, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 49.05
Std Dev 3.438
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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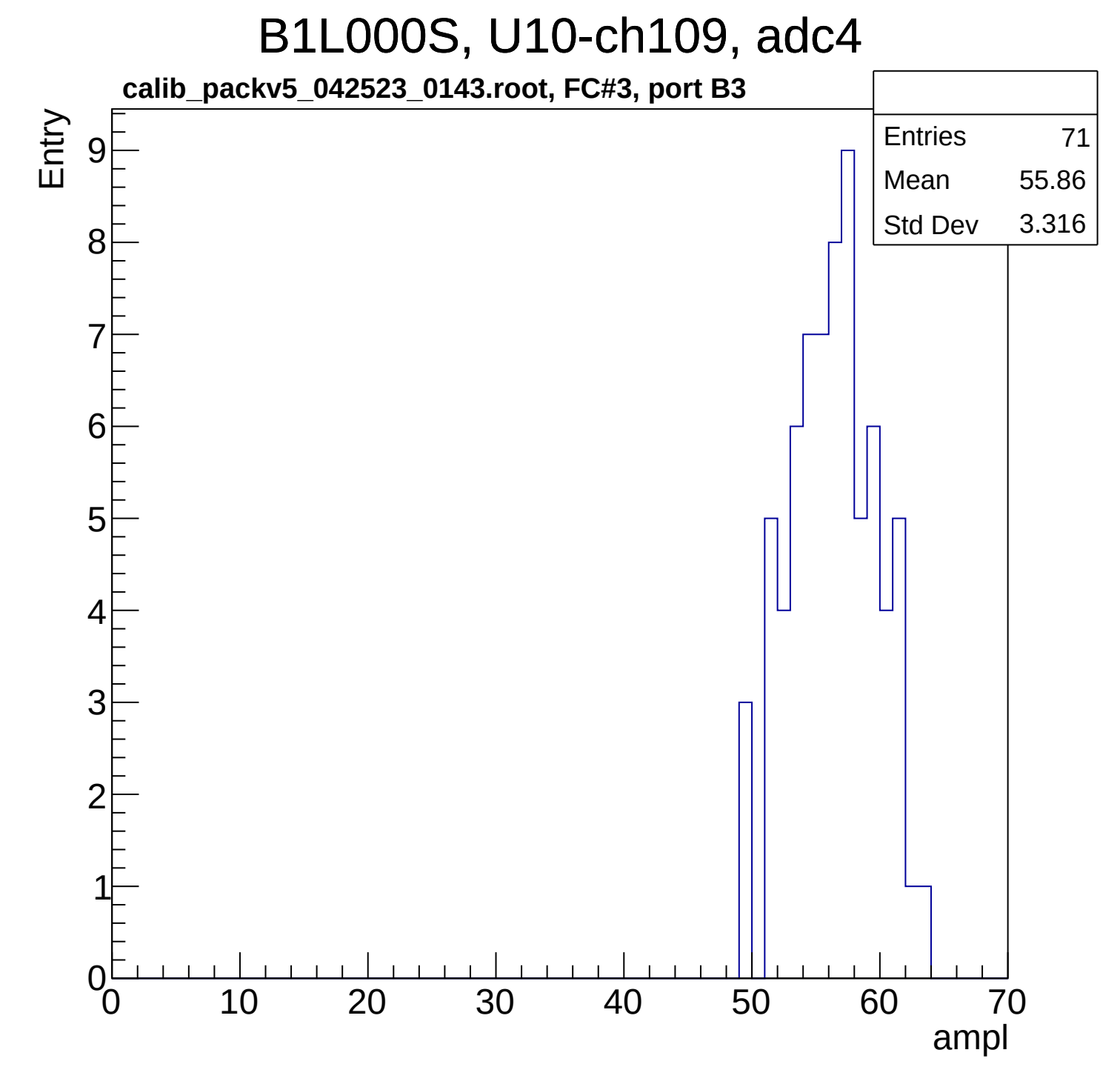
B1L000S, U10-ch109, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 55.86
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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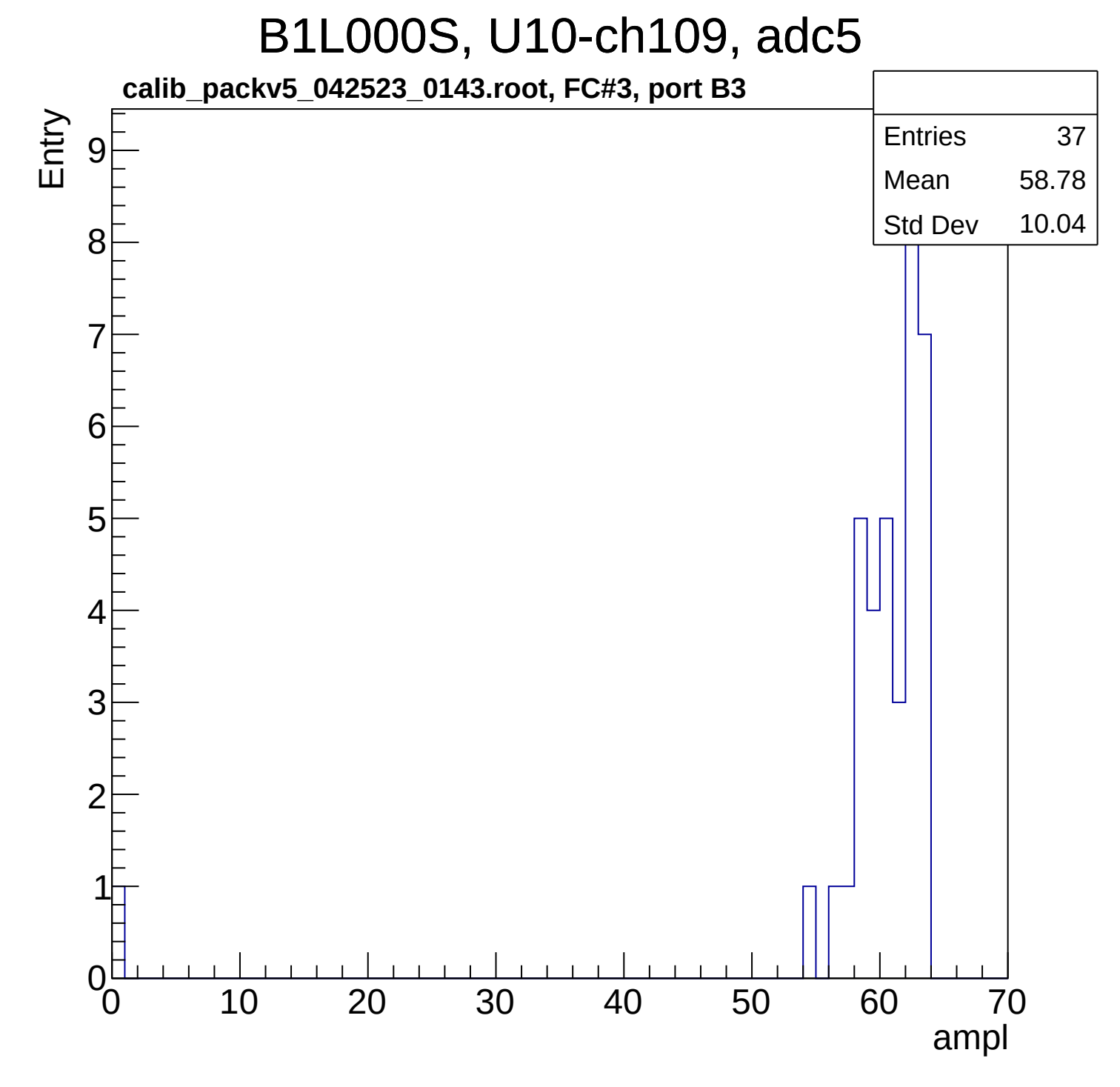
B1L000S, U10-ch109, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 58.78
Std Dev 10.04
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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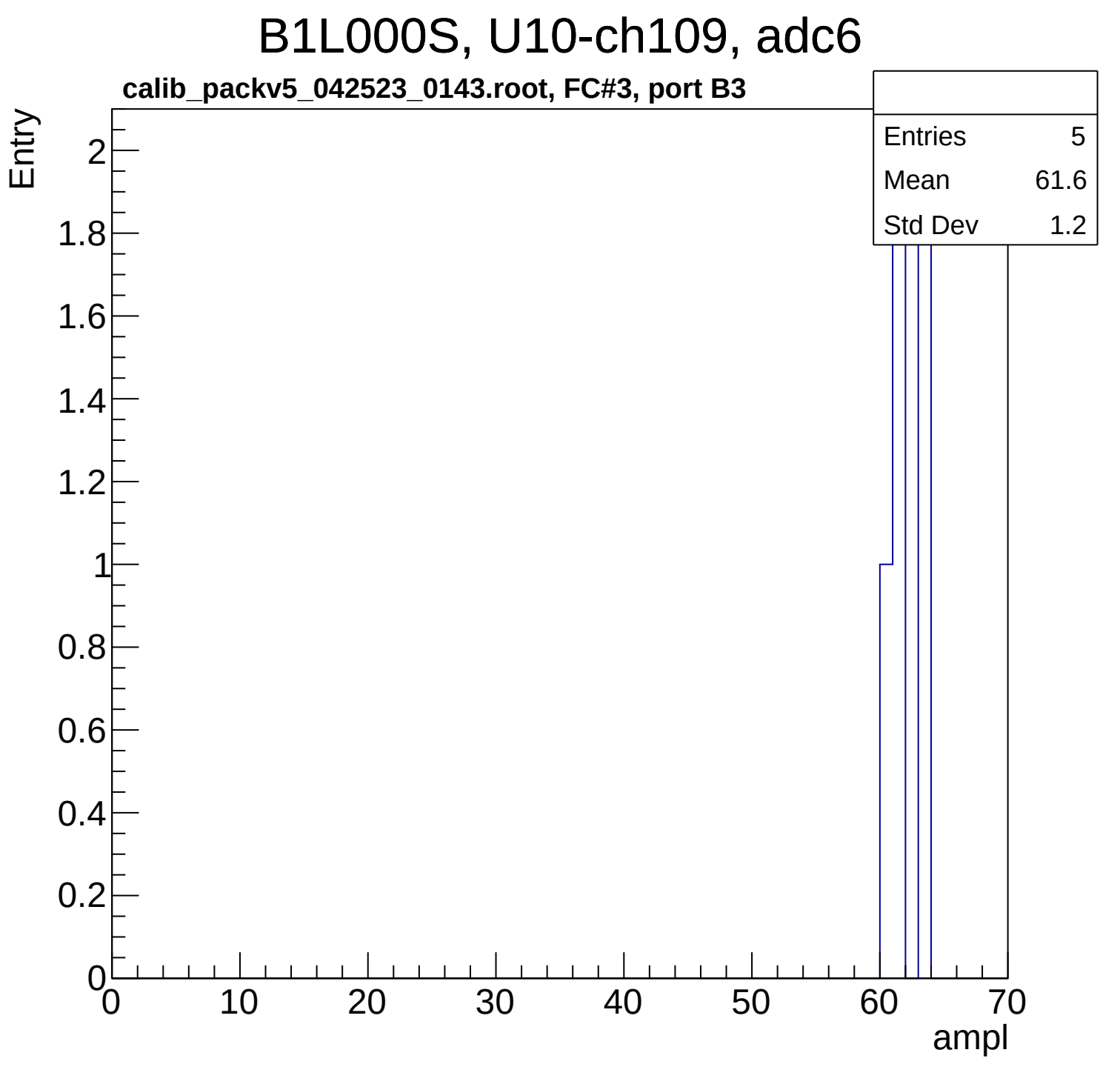
B1L000S, U10-ch109, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 61.6
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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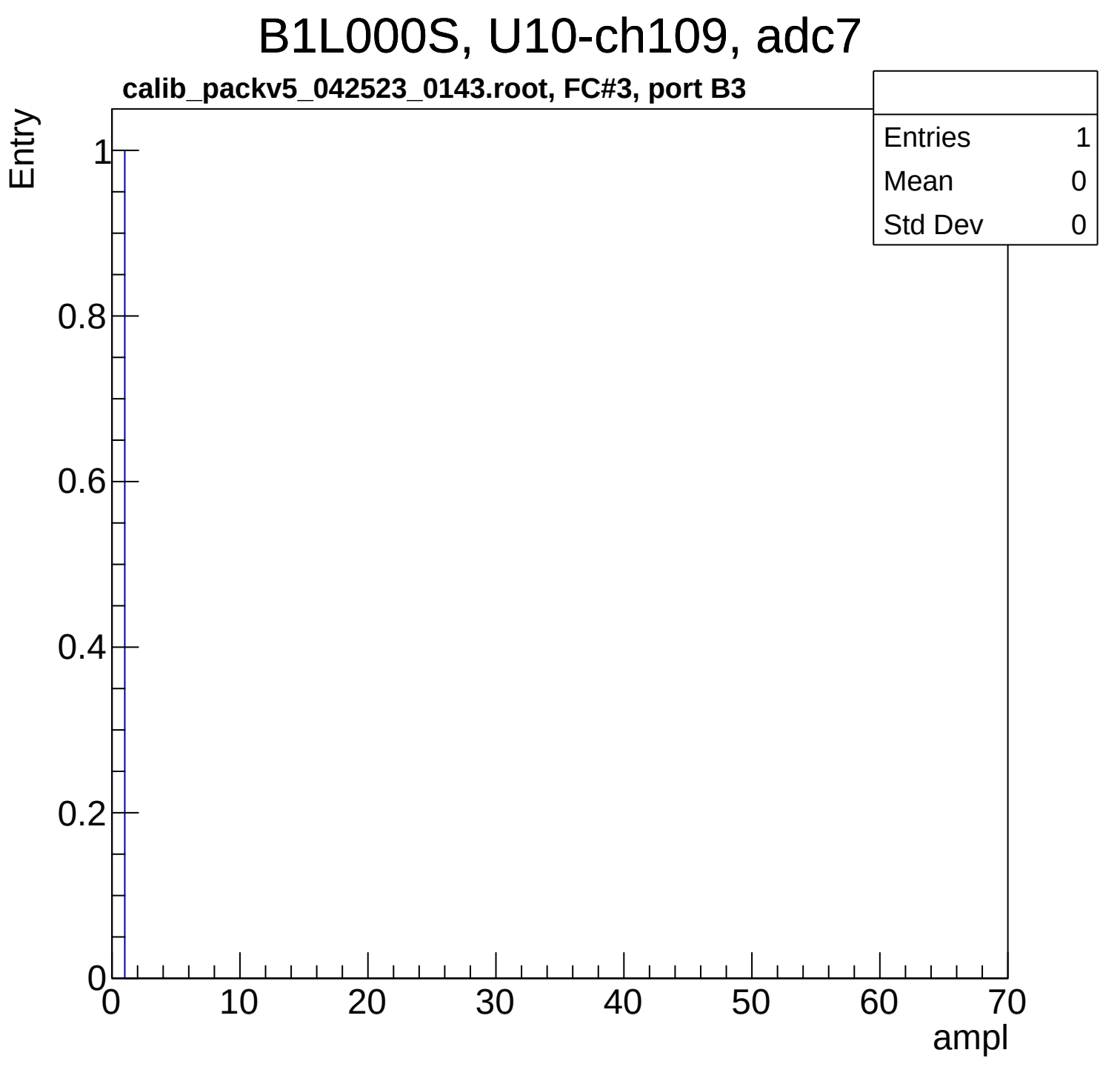
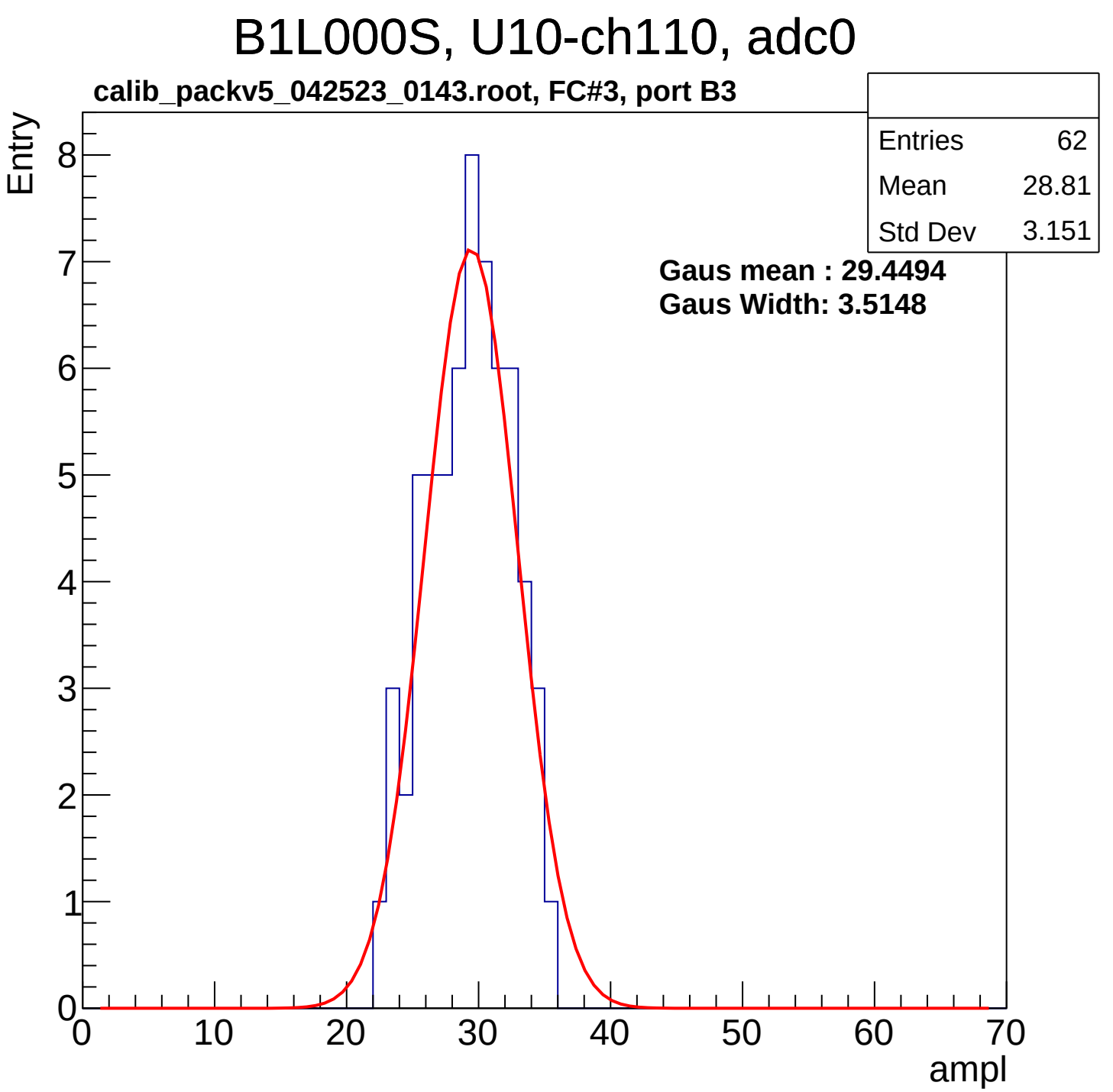

B1L000S, U10-ch109, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch110, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.81
Std Dev 3.151
Gaus mean : 29.4494
Gaus Width: 3.5148
Entry
ampl
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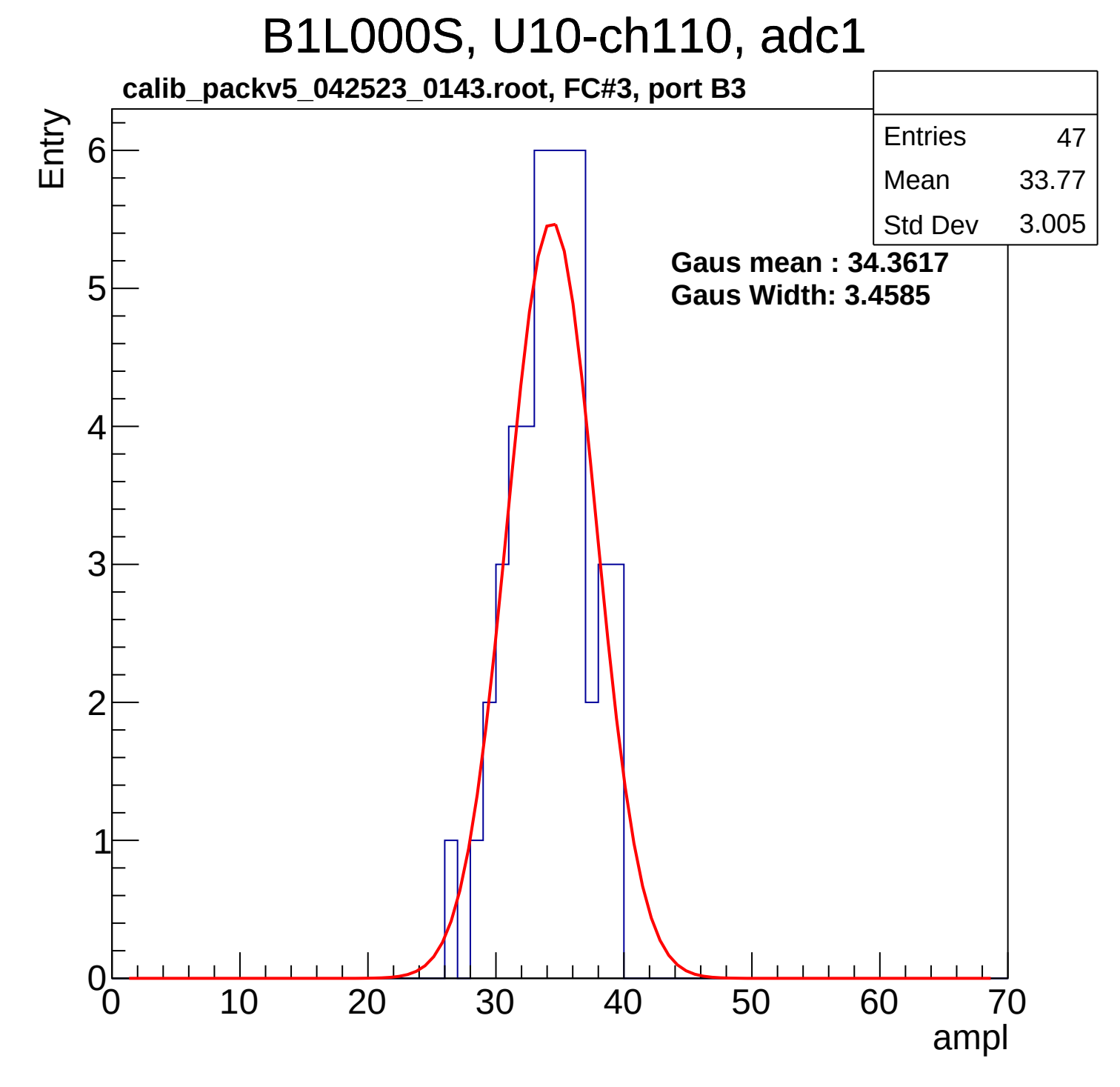
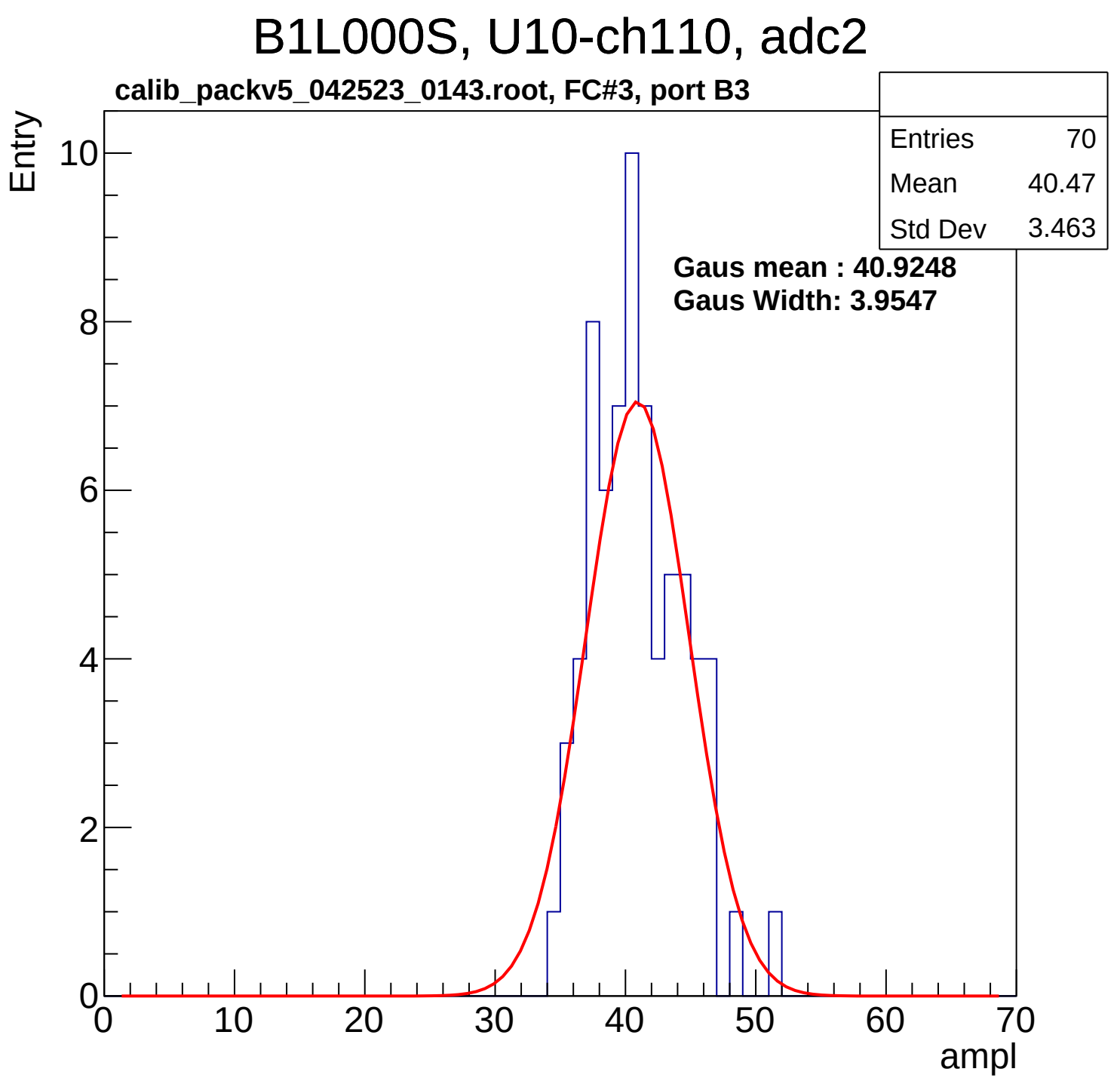
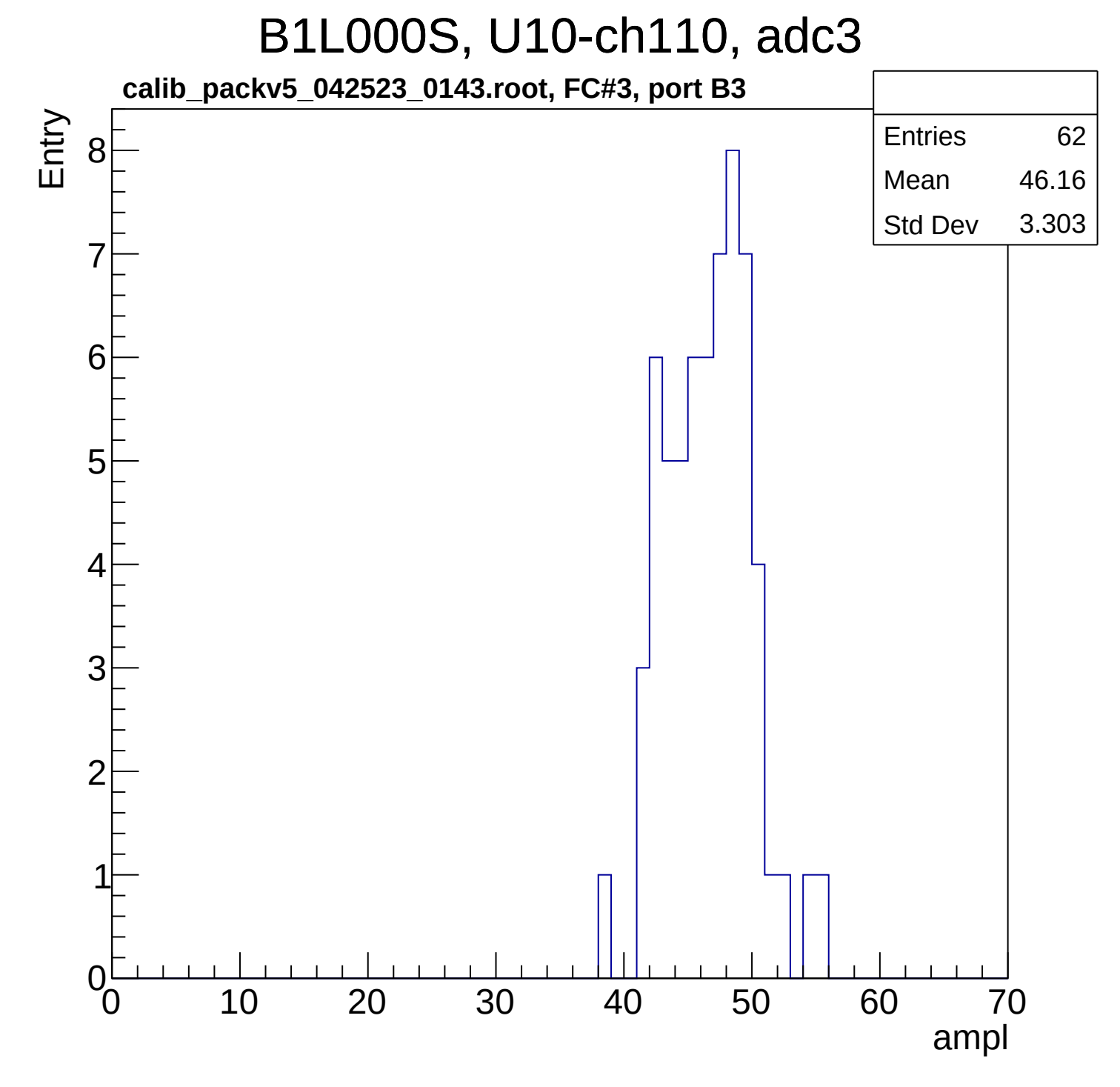
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch110, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 33.77
Std Dev 3.005
Gaus mean : 34.3617
Gaus Width: 3.4585
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch110, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 40.47
Std Dev 3.463
Gaus mean : 40.9248
Gaus Width: 3.9547
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch110, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.16
Std Dev 3.303
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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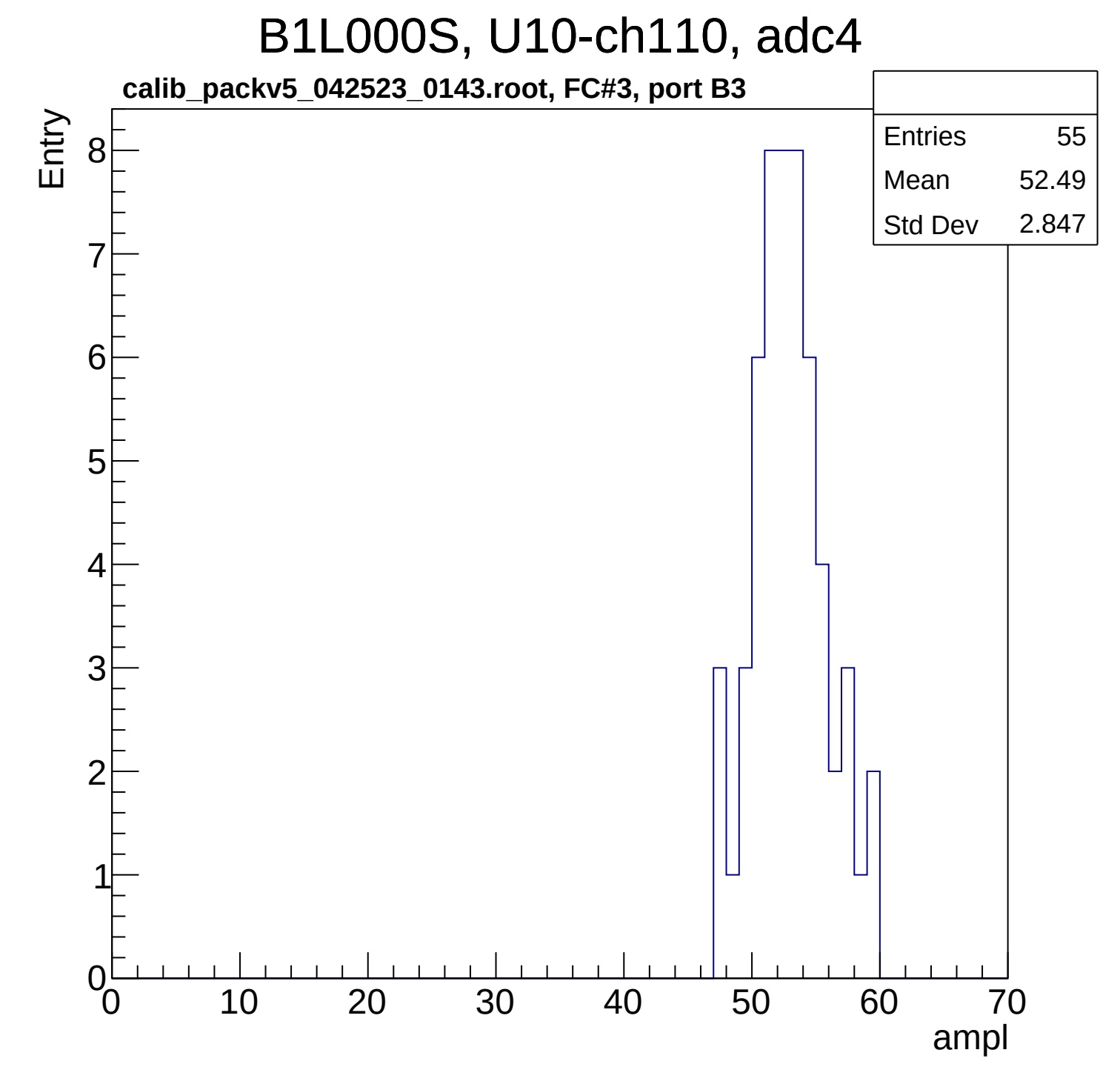
B1L000S, U10-ch110, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.49
Std Dev 2.847
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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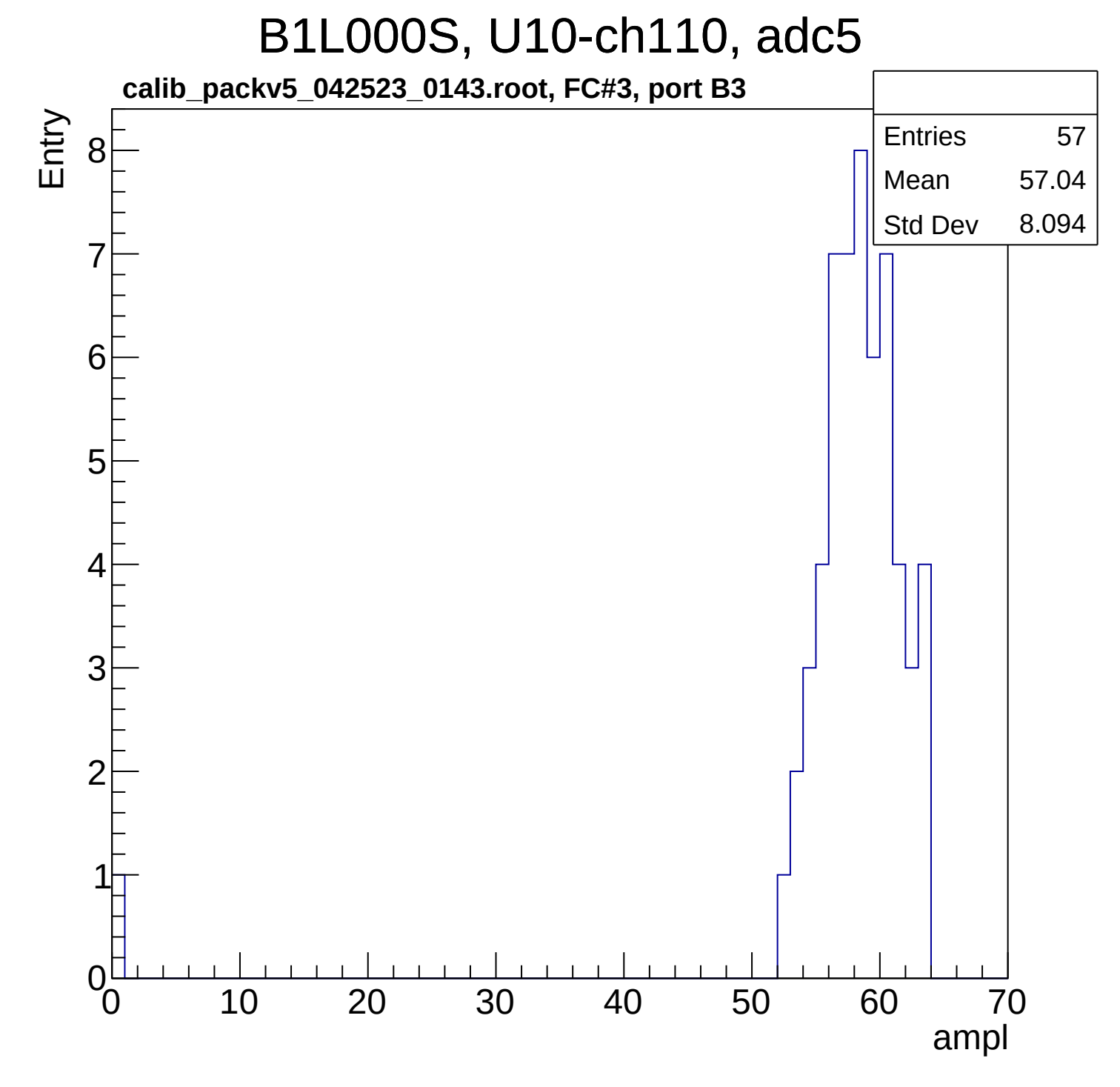
B1L000S, U10-ch110, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.04
Std Dev 8.094
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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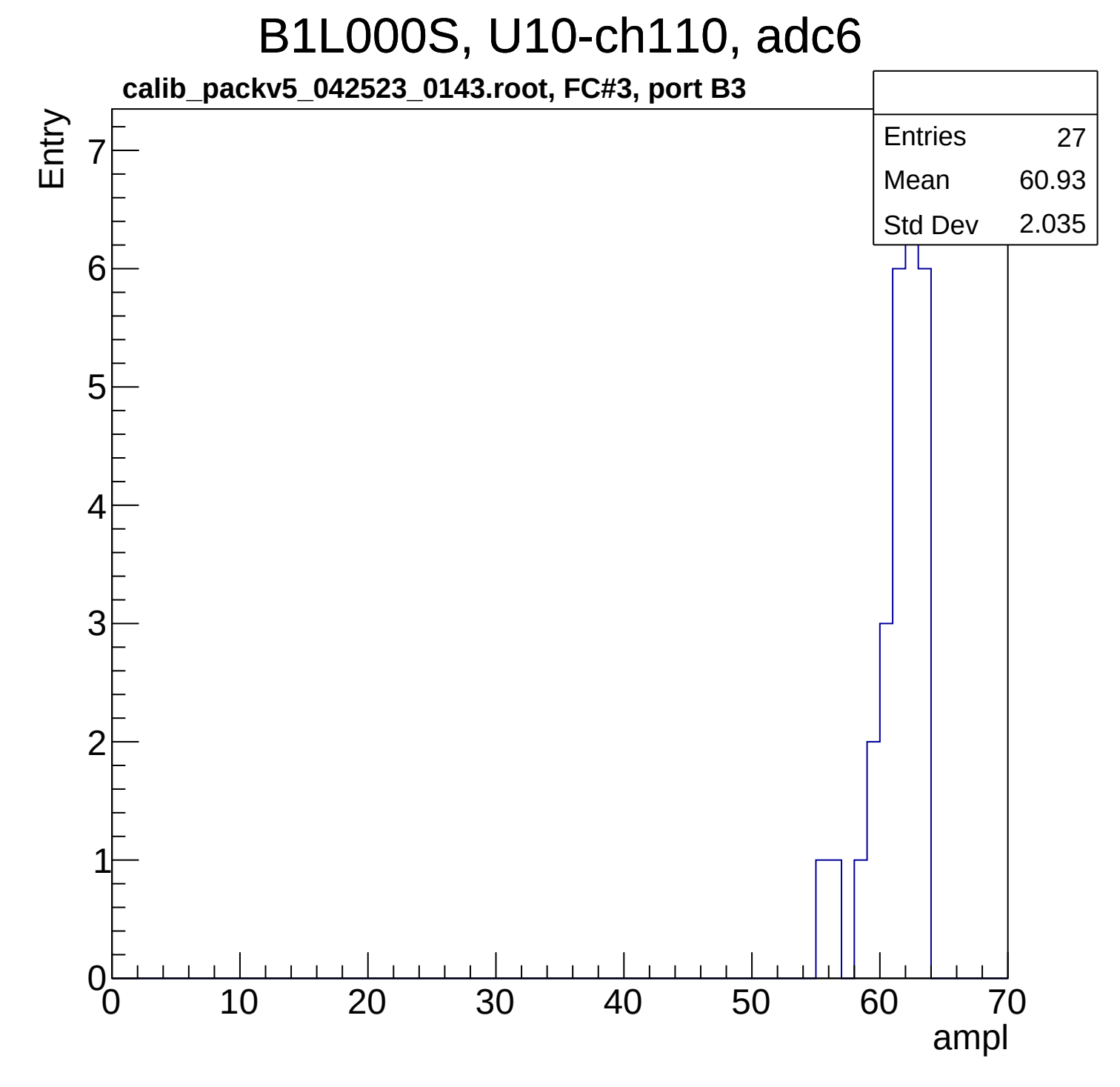
40
50
60
70


B1L000S, U10-ch110, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 27
Mean 60.93
Std Dev 2.035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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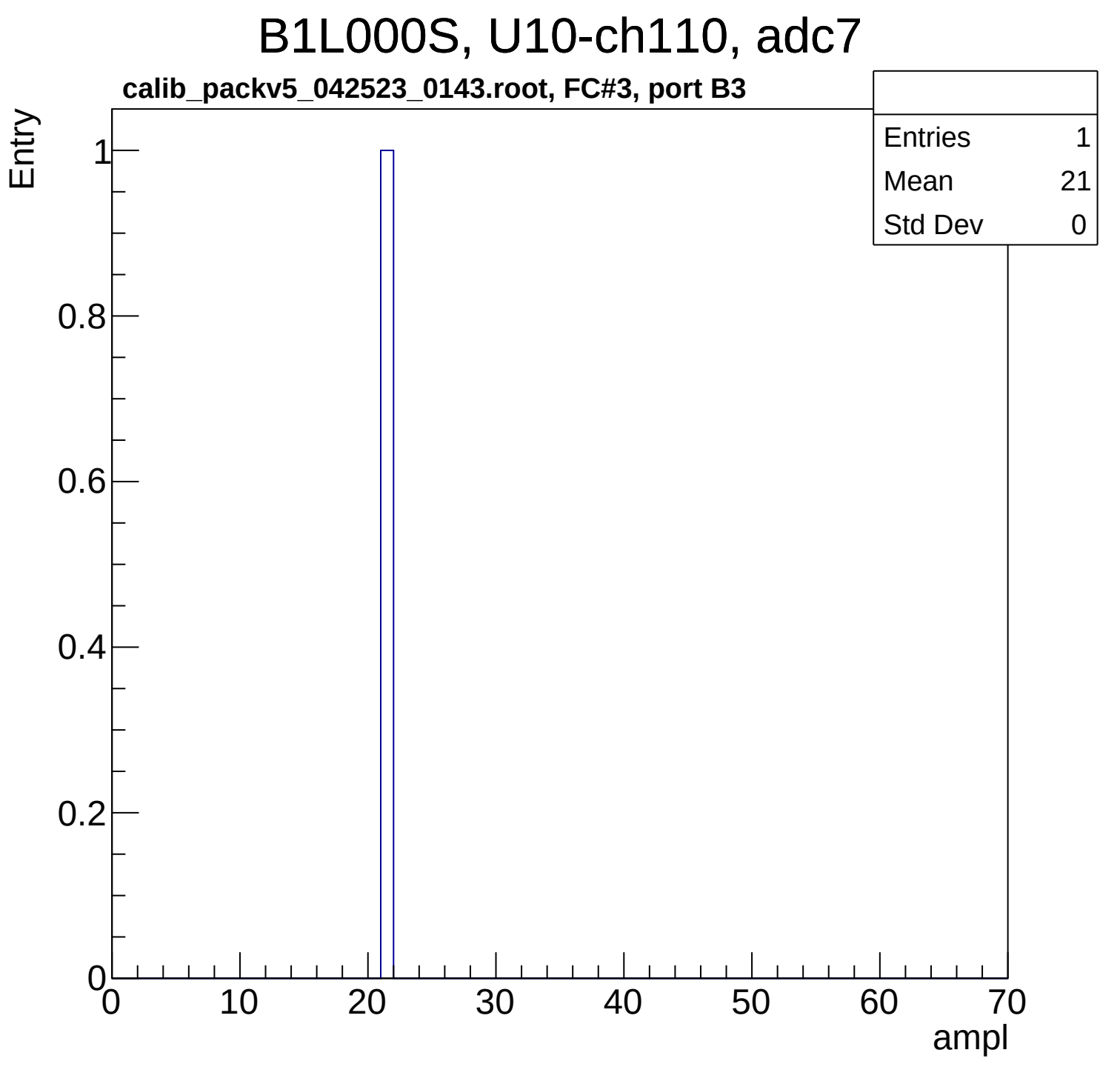
40
50
60
70


B1L000S, U10-ch110, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
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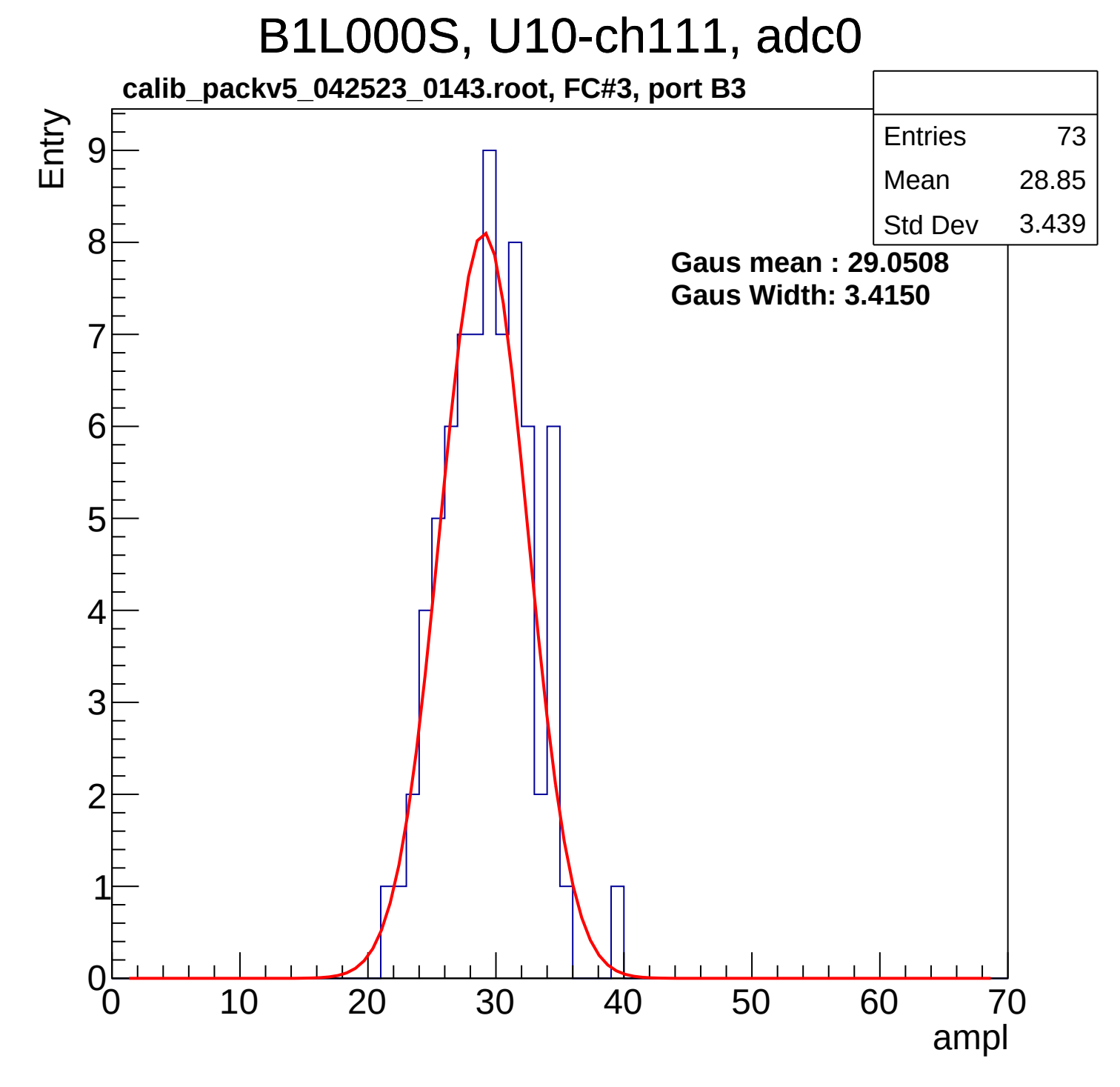
50
60
70
ampl


B1L000S, U10-ch111, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.85
Std Dev 3.439
Gaus mean : 29.0508
Gaus Width: 3.4150
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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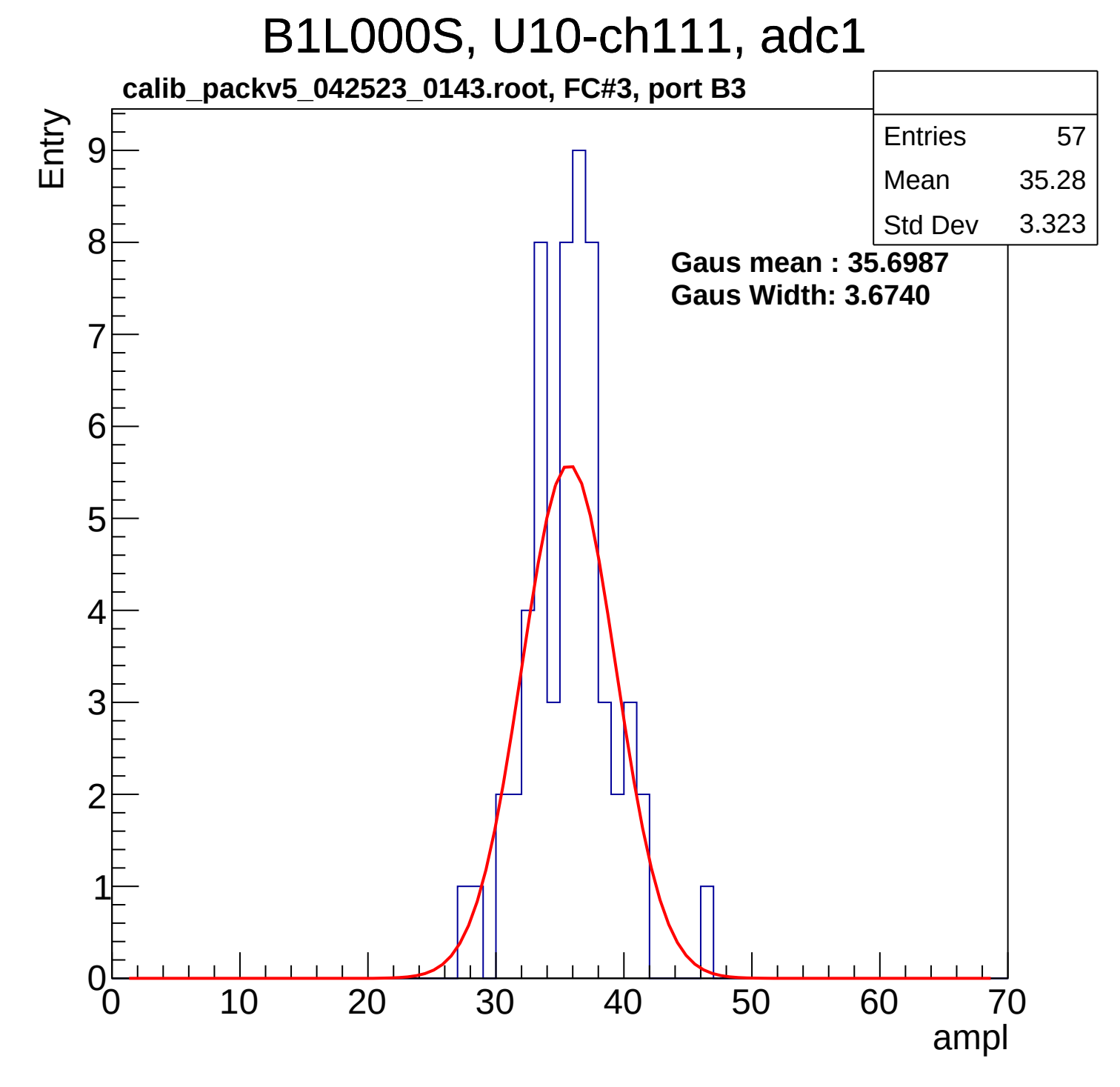
B1L000S, U10-ch111, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 35.28
Std Dev 3.323
Gaus mean : 35.6987
Gaus Width: 3.6740
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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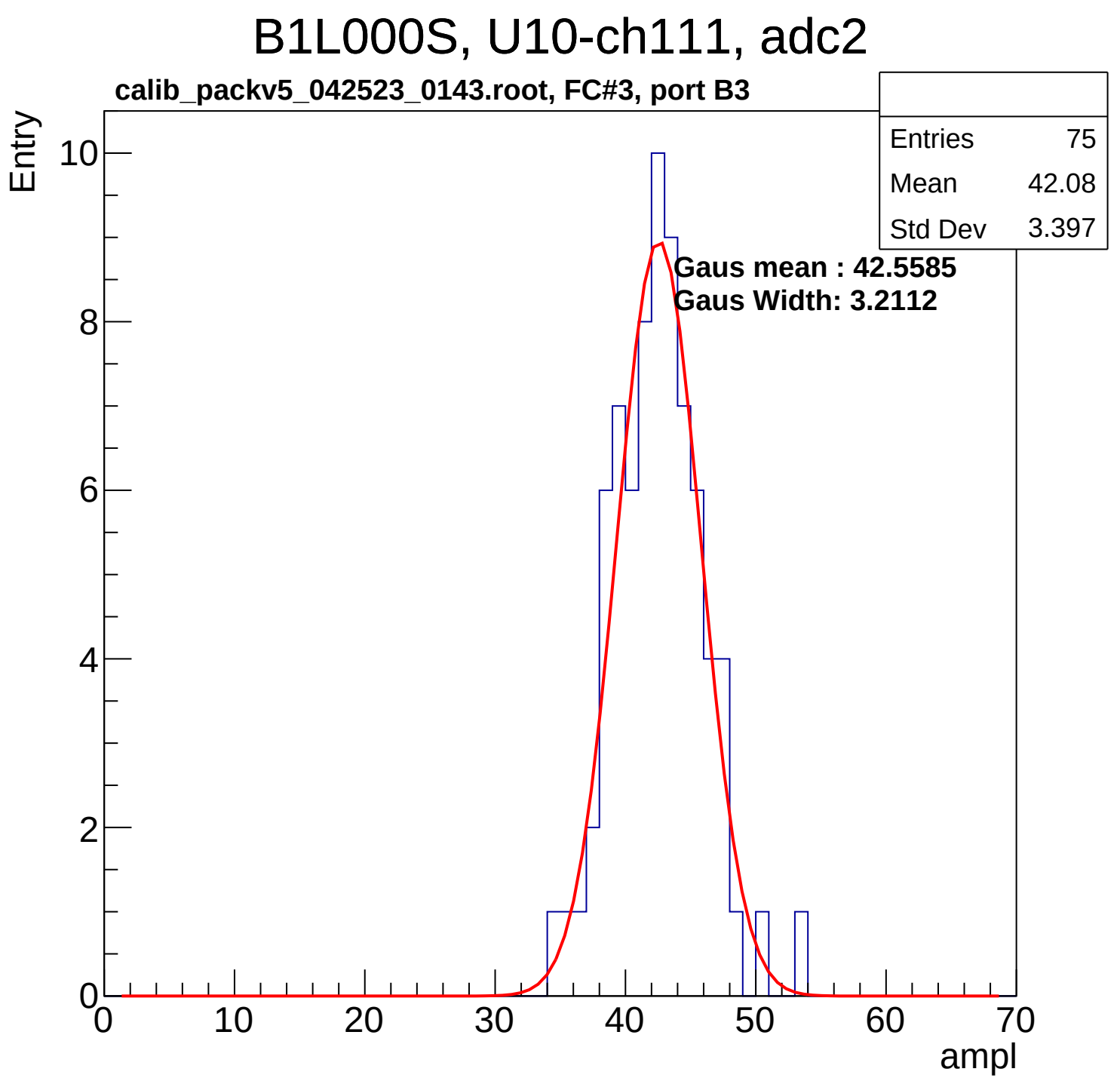
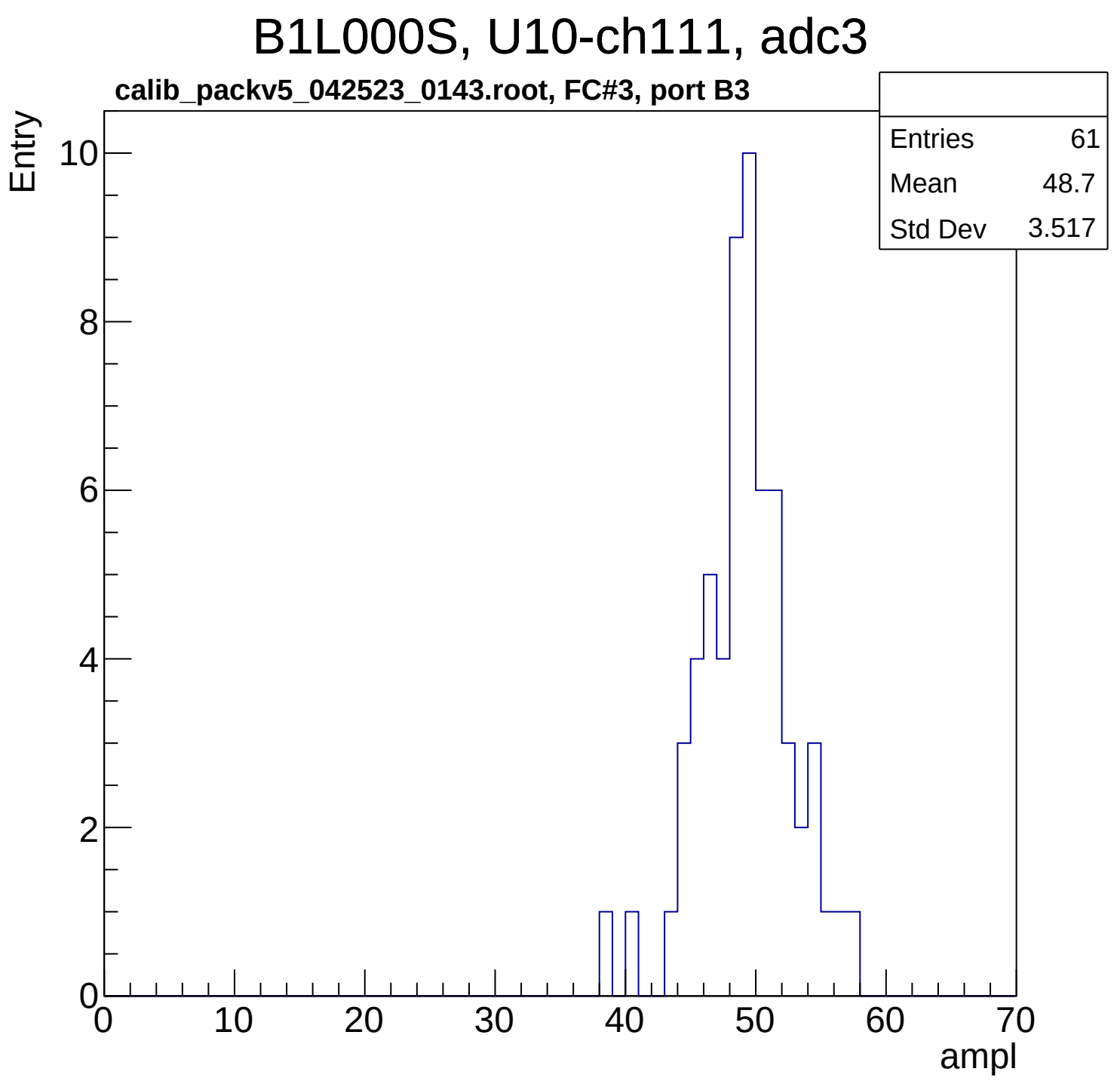

B1L000S, U10-ch111, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 42.08
Std Dev 3.397
Gaus mean : 42.5585
Gaus Width: 3.2112
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch111, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 48.7
Std Dev 3.517
Entry
ampl
0
2
4
6
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0
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40
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60
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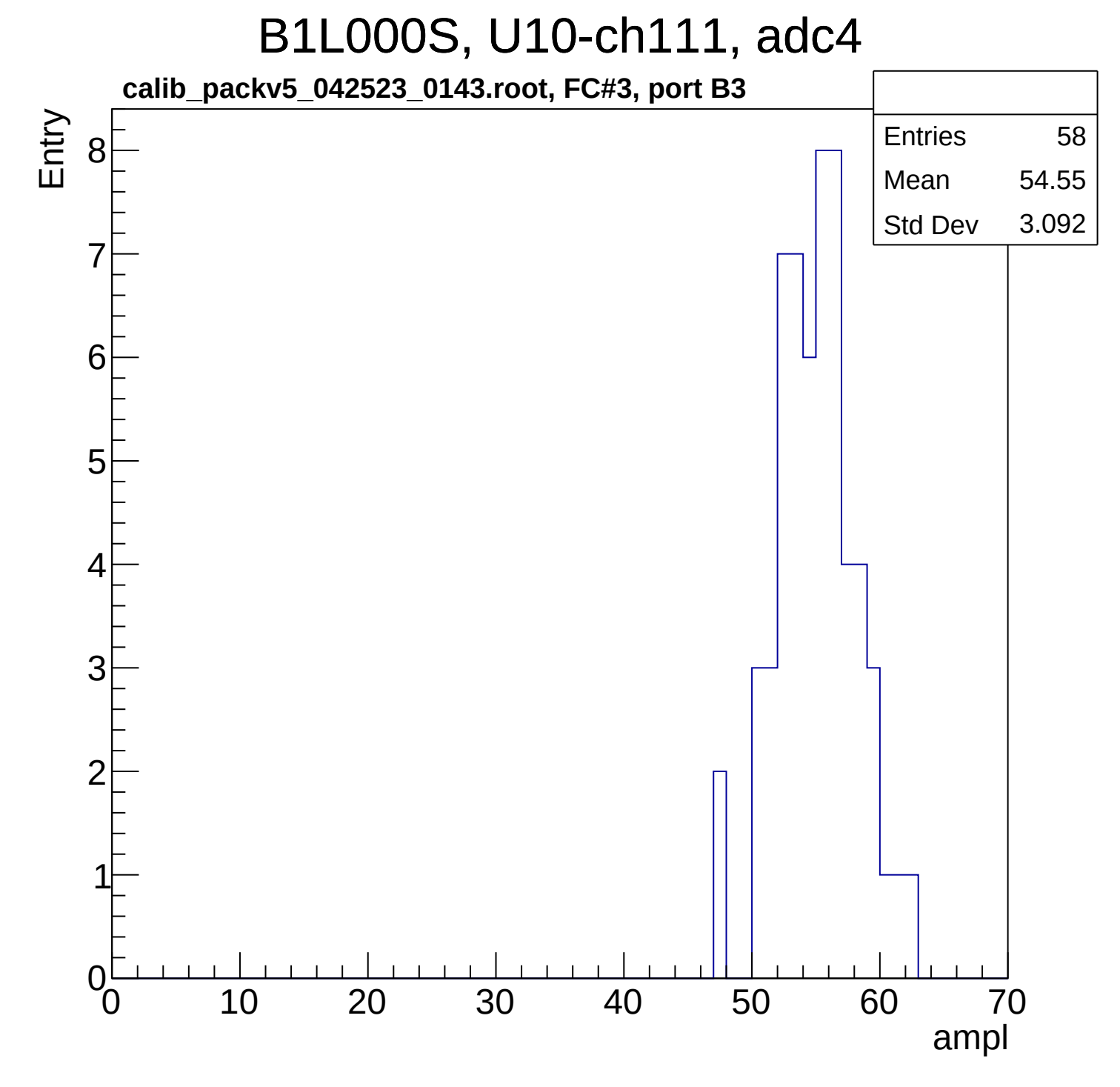
B1L000S, U10-ch111, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.55
Std Dev 3.092
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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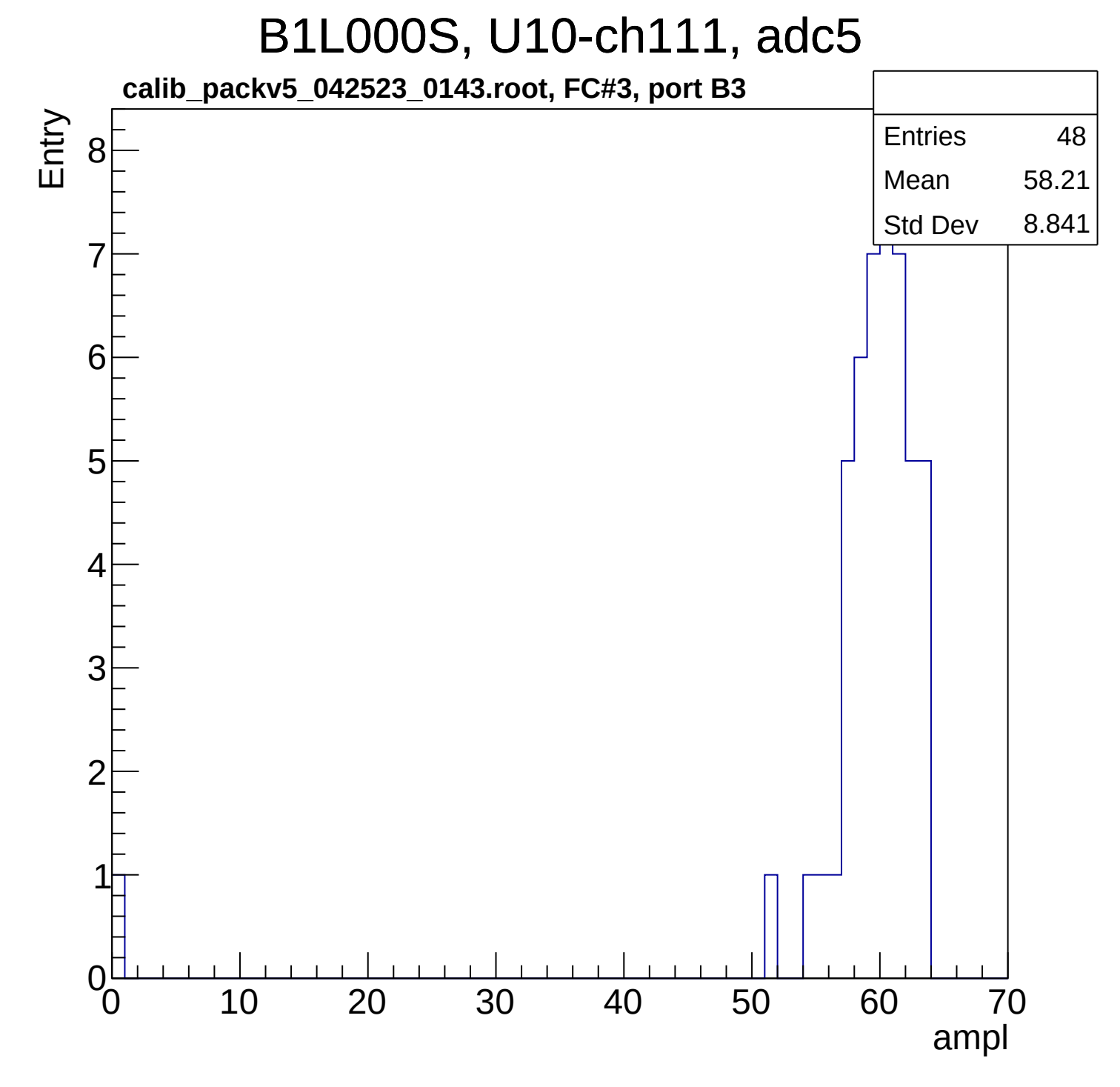
B1L000S, U10-ch111, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.21
Std Dev 8.841
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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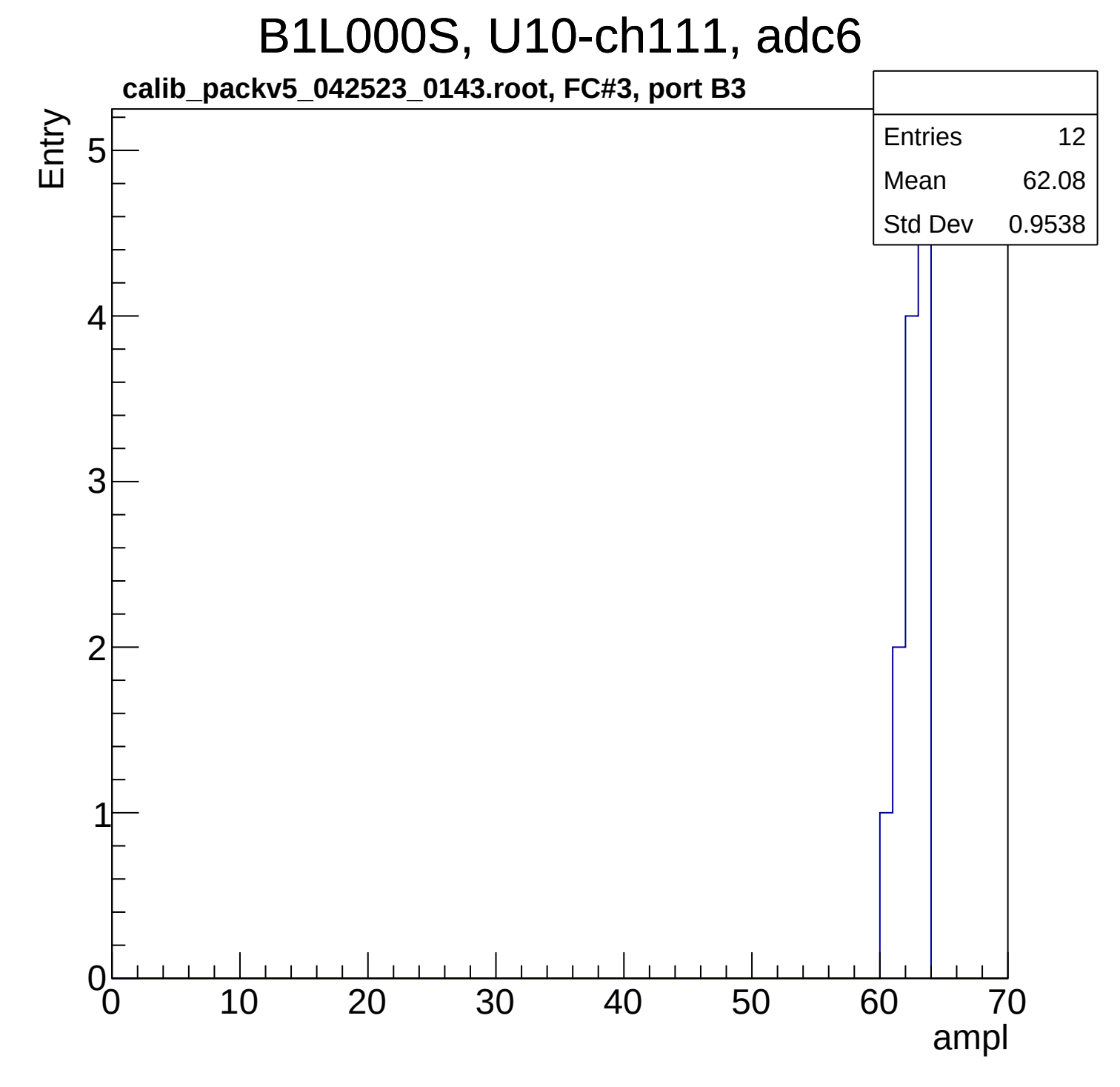
B1L000S, U10-ch111, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 62.08
Std Dev 0.9538
Entry
ampl
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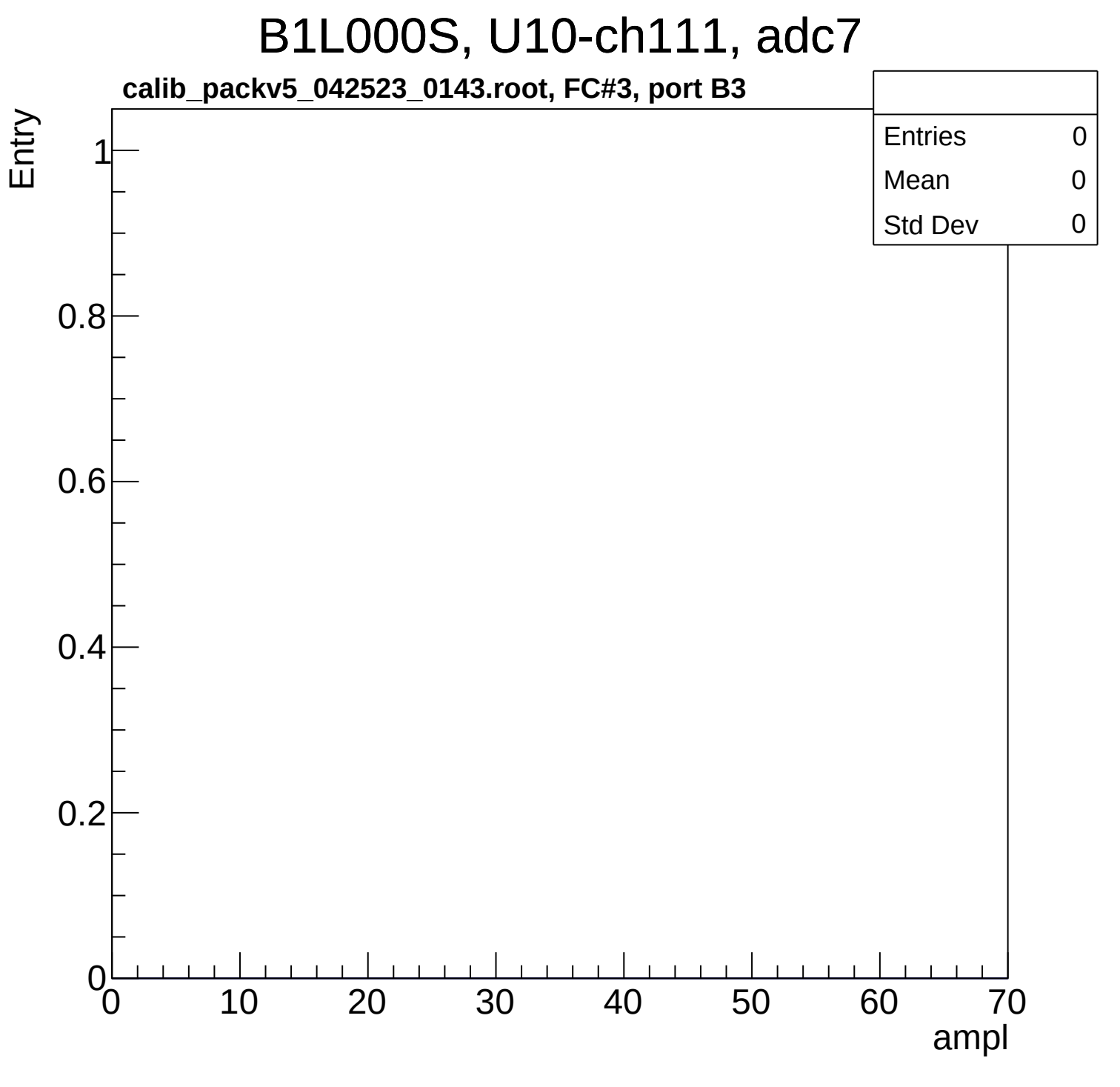
2
3
4
5
0
10
20
30
40
50
60
70


B1L000S, U10-ch111, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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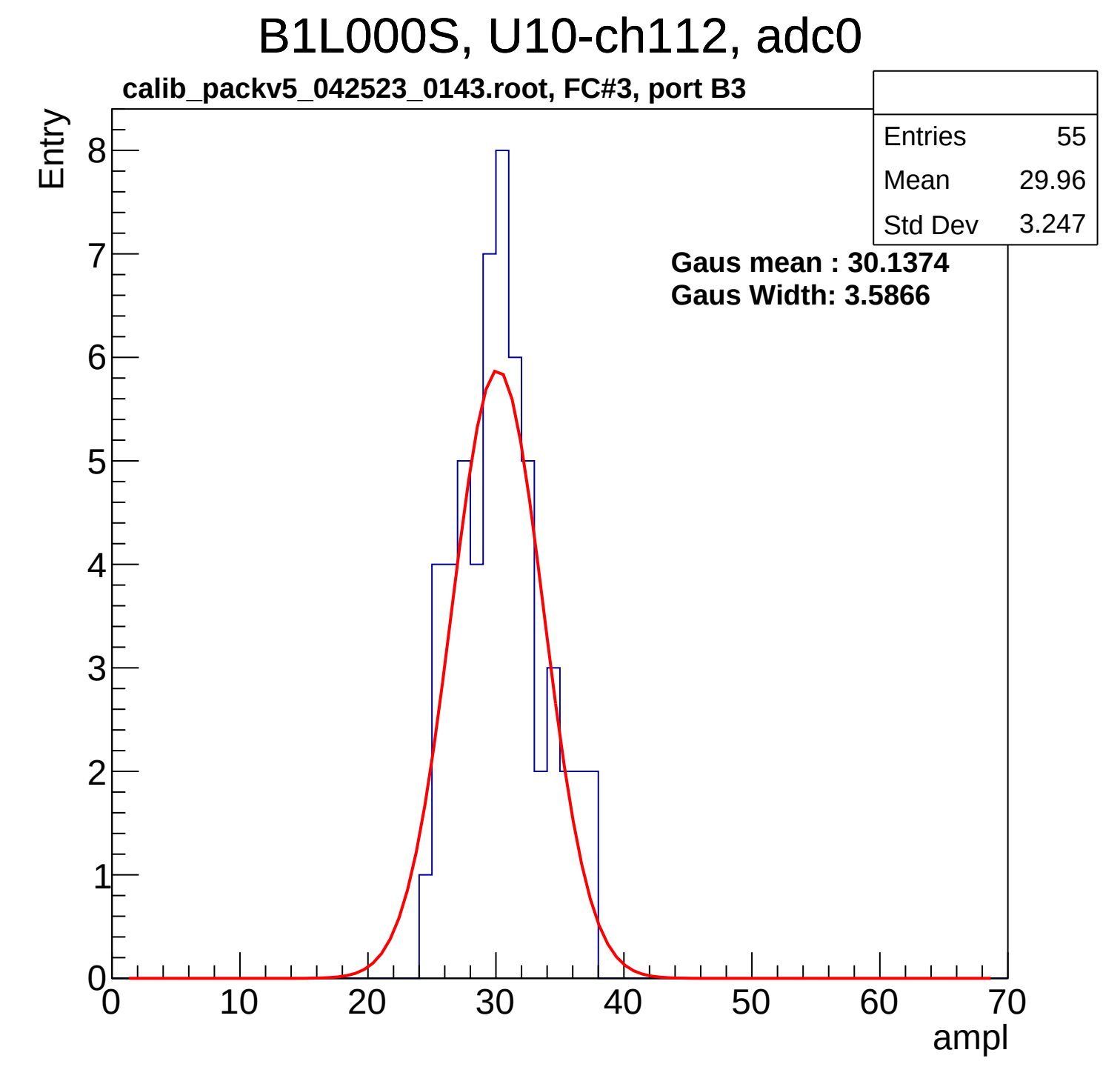
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch112, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 29.96
Std Dev 3.247
Gaus mean : 30.1374
Gaus Width: 3.5866
Entry
ampl
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3
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5
6
7
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0
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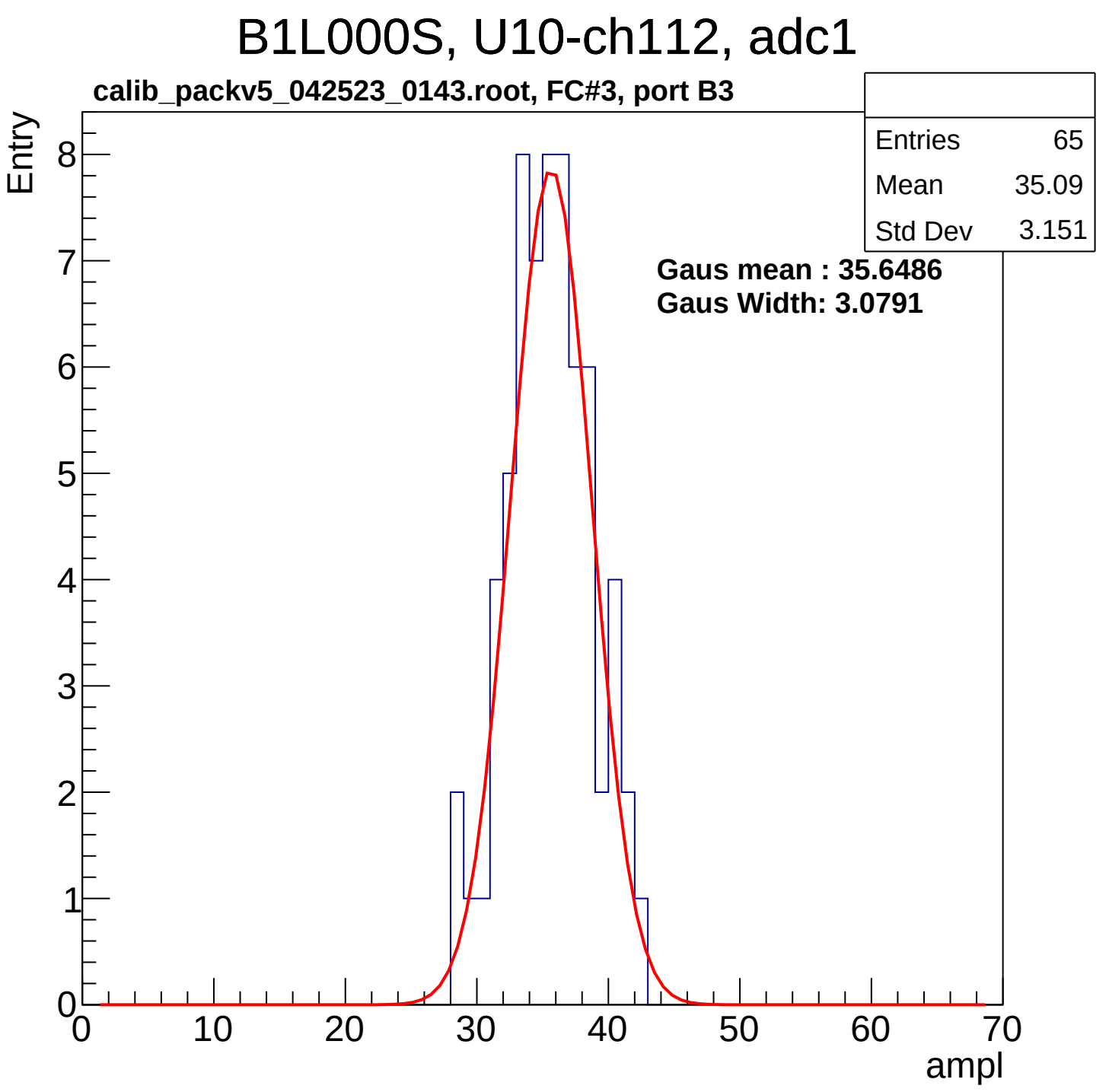
B1L000S, U10-ch112, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.09
Std Dev 3.151
Gaus mean : 35.6486
Gaus Width: 3.0791
Entry
ampl
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3
4
5
6
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0
10
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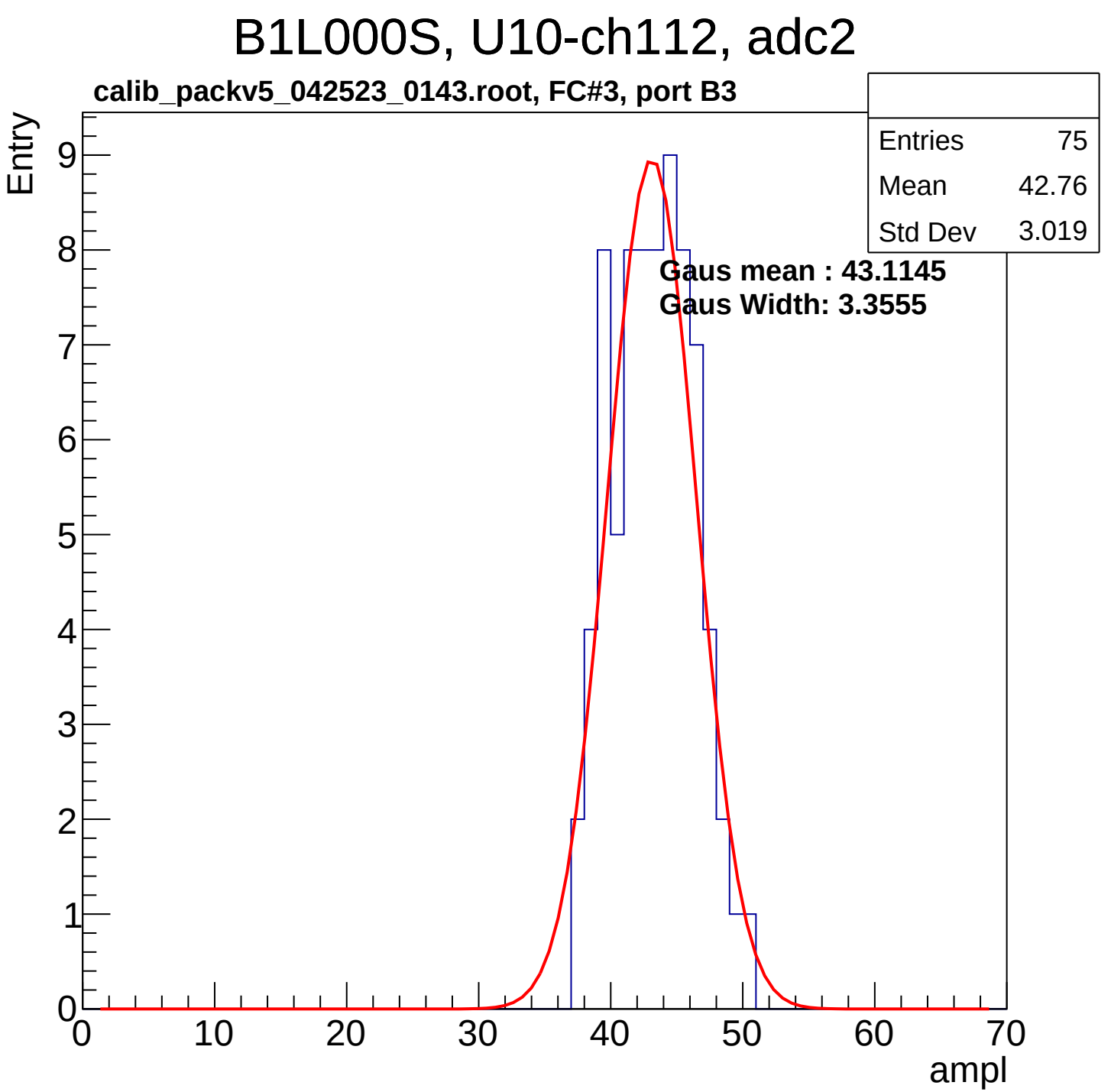
B1L000S, U10-ch112, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 42.76
Std Dev 3.019
Gaus mean : 43.1145
Gaus Width: 3.3555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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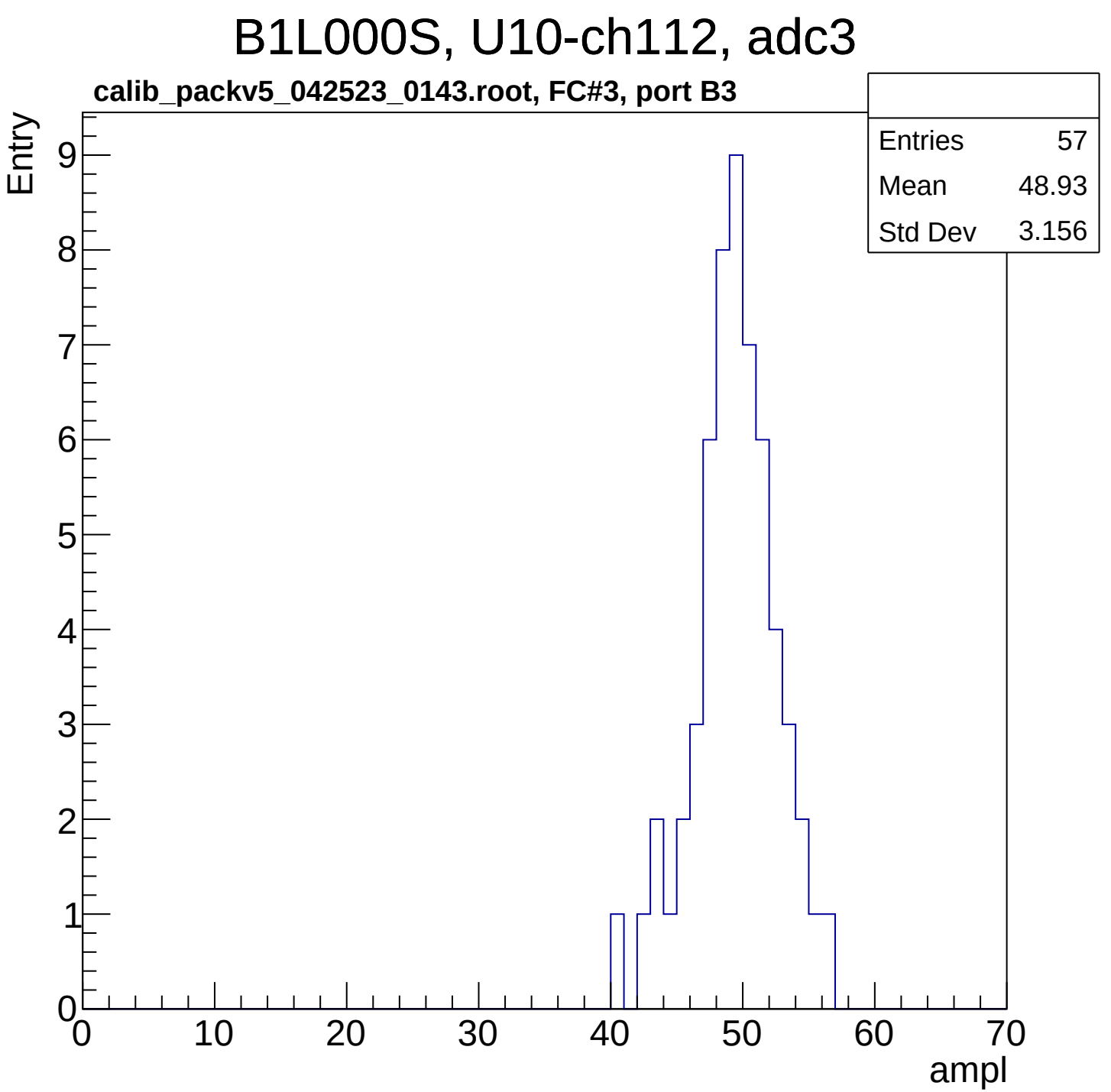
B1L000S, U10-ch112, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 48.93
Std Dev 3.156
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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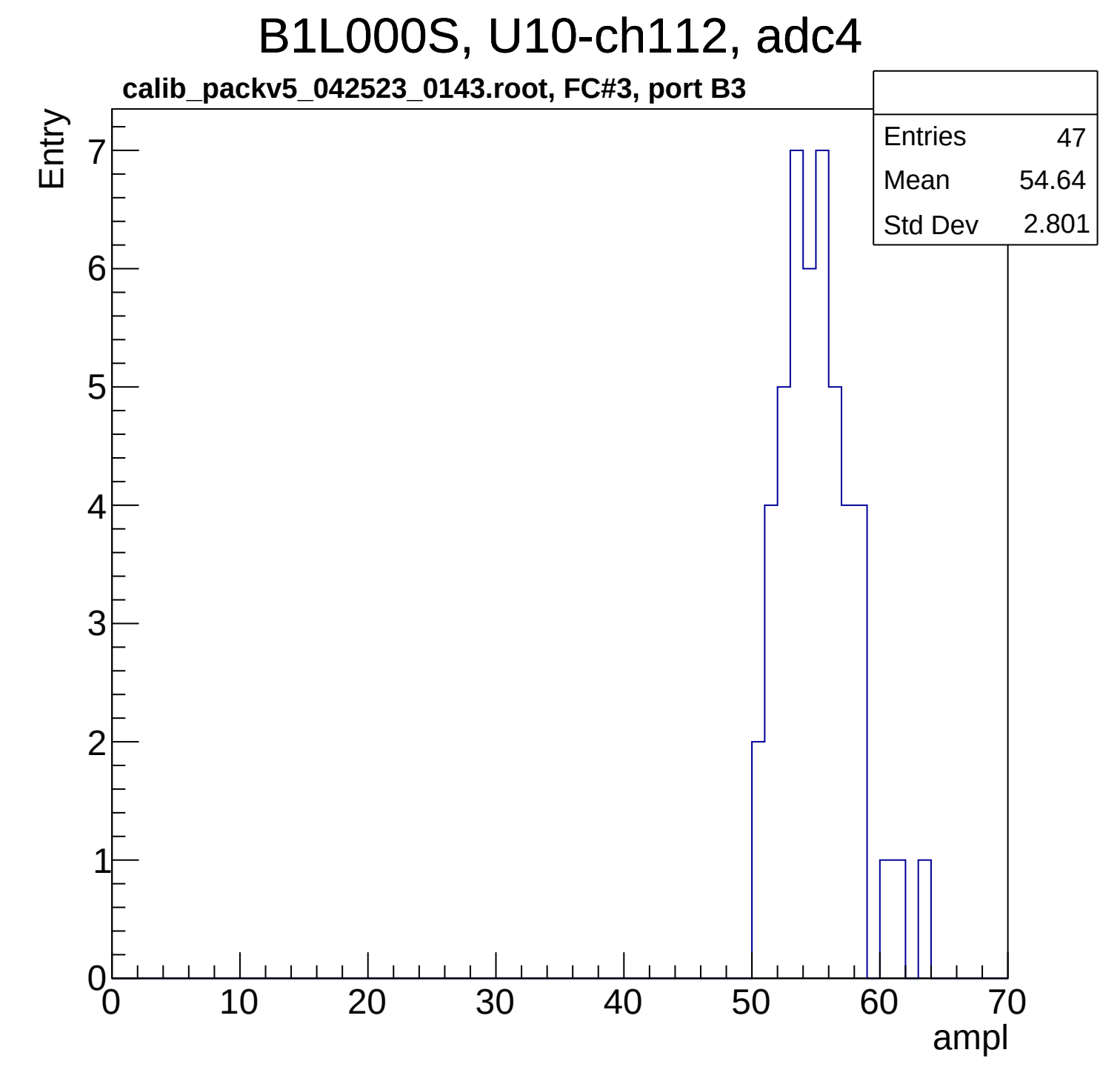
B1L000S, U10-ch112, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 54.64
Std Dev 2.801
Entry
ampl
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1
2
3
4
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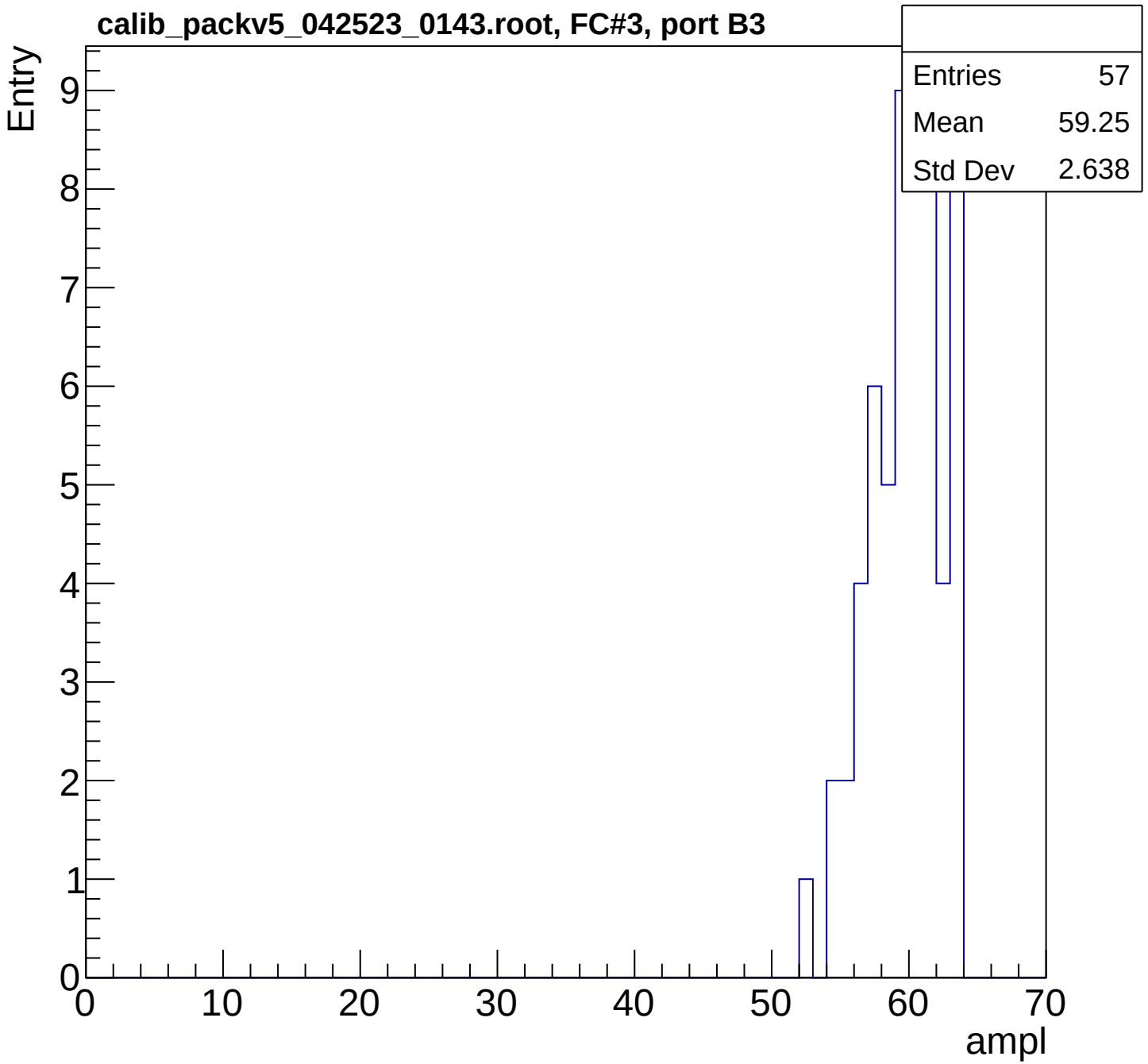
B1L000S, U10-ch112, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 59.25
Std Dev 2.638
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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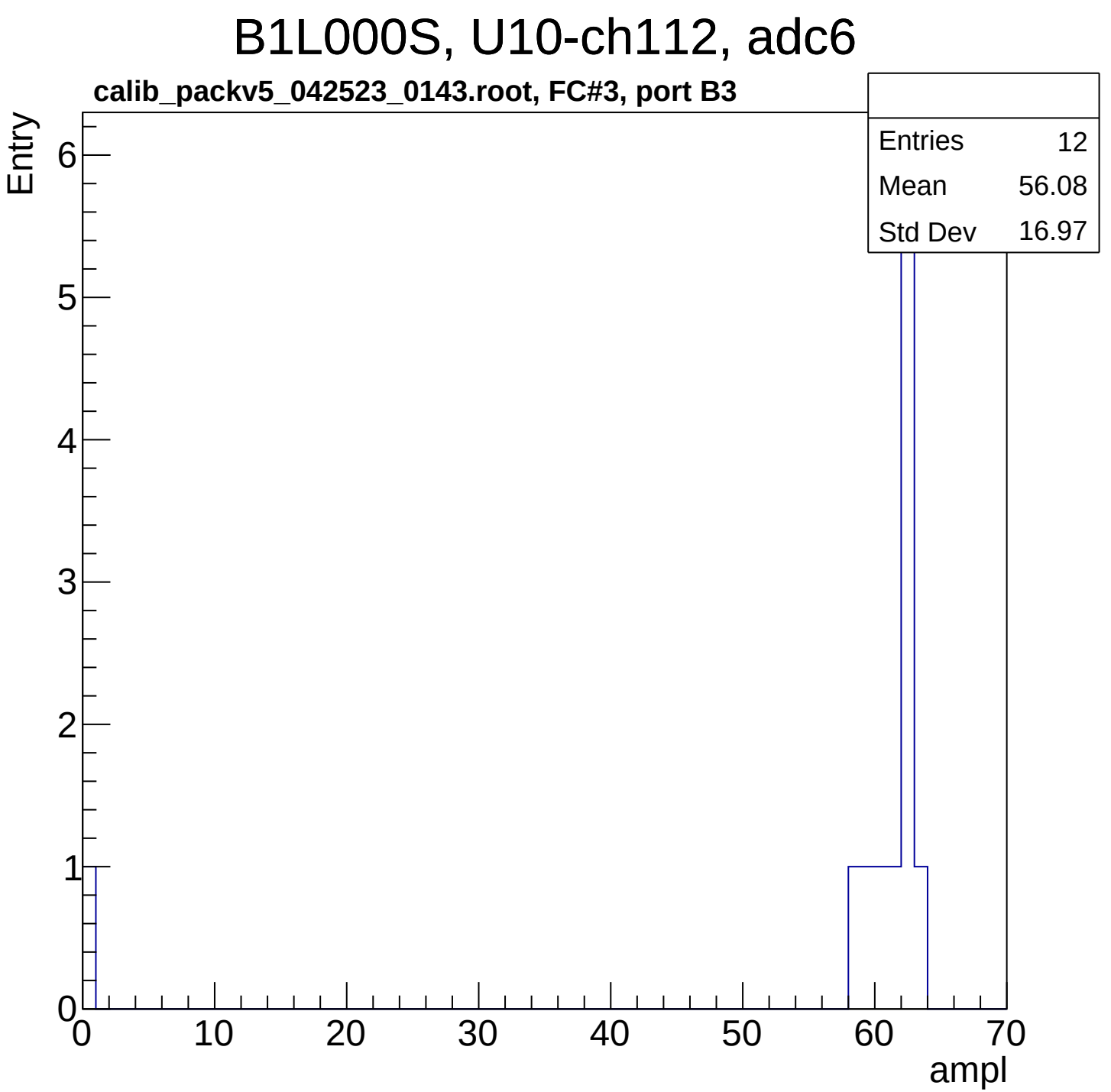
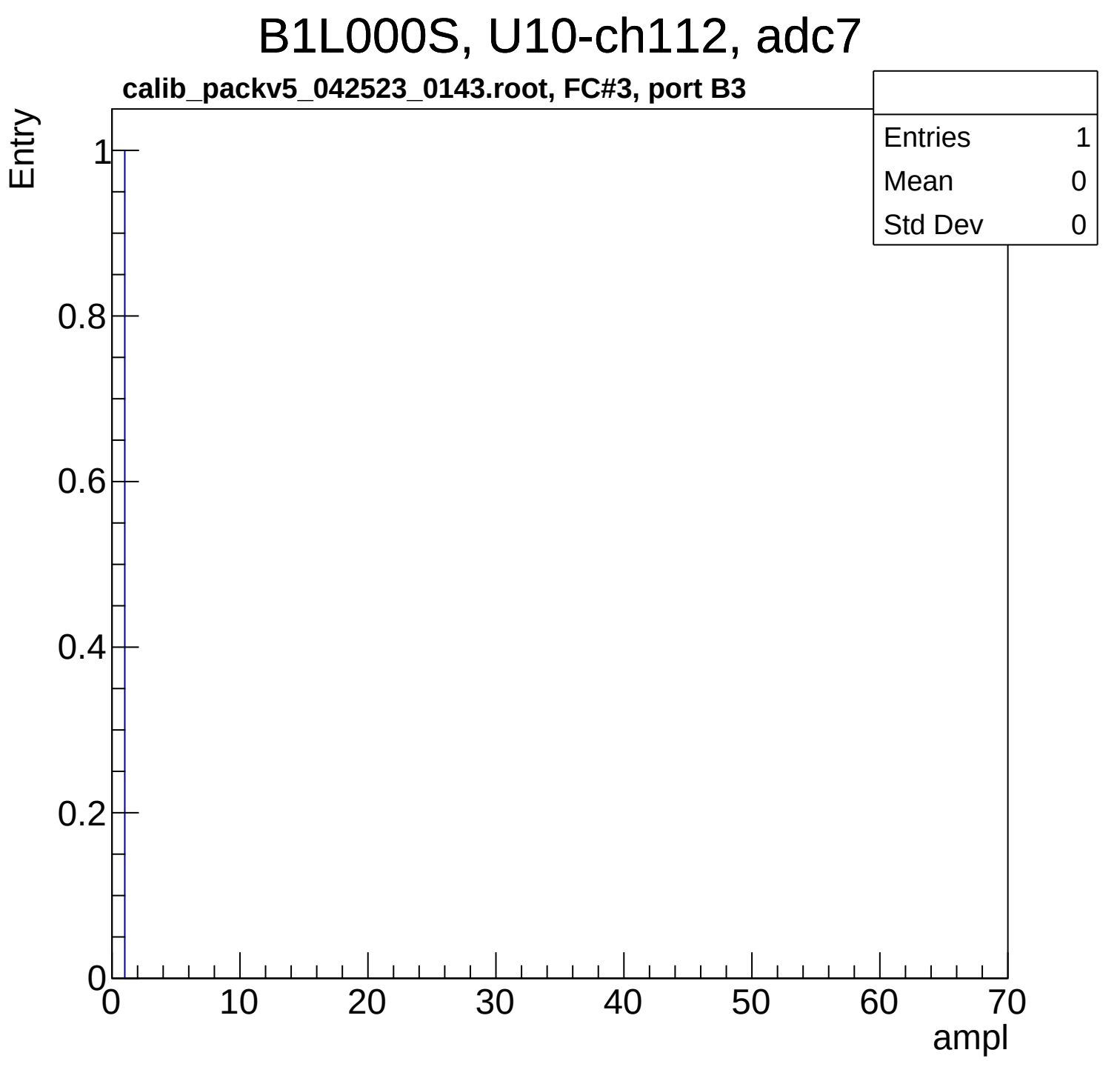
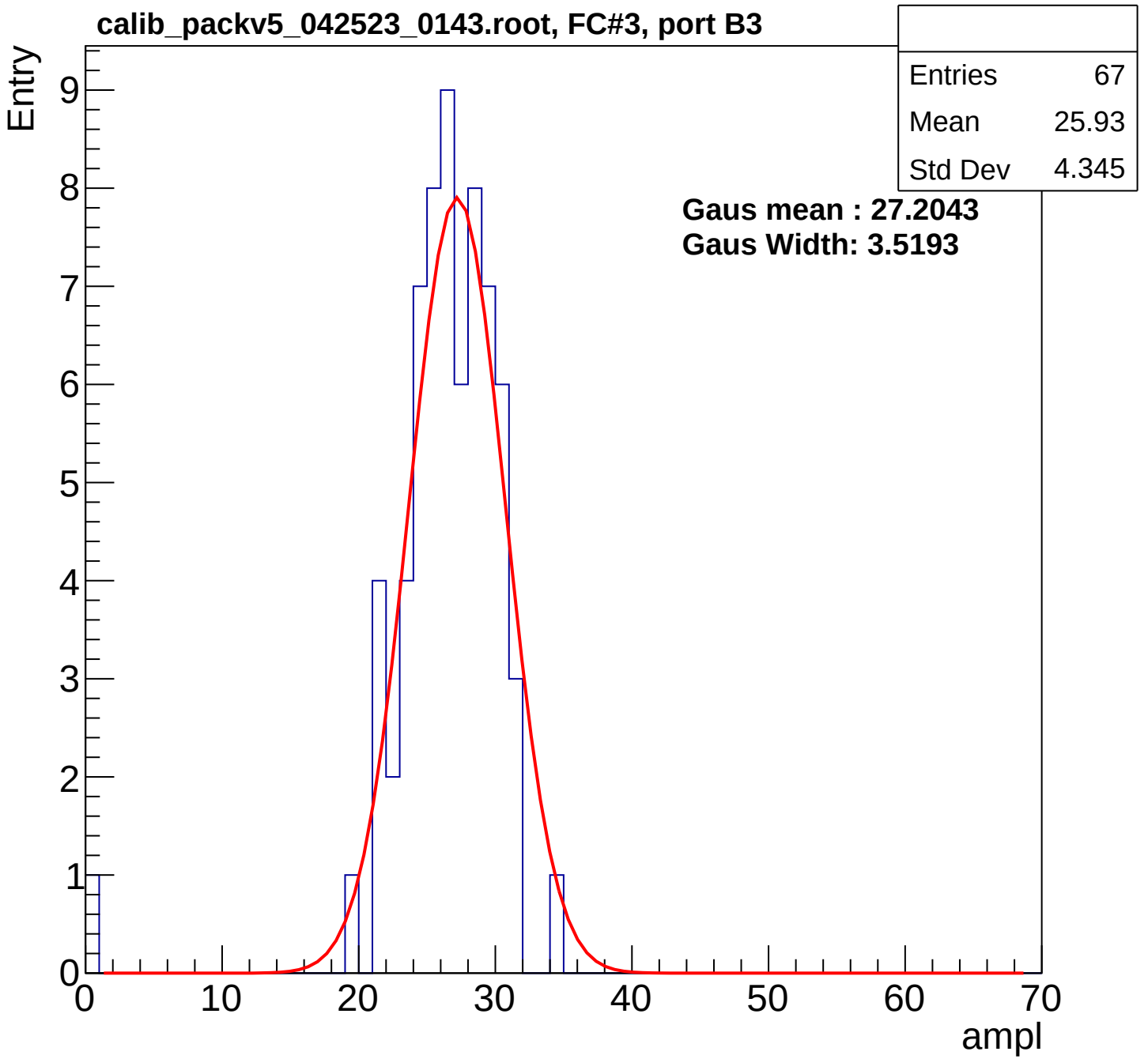

B1L000S, U10-ch112, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.08
Std Dev 16.97
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch112, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch113, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 25.93
Std Dev 4.345
Gaus mean : 27.2043
Gaus Width: 3.5193
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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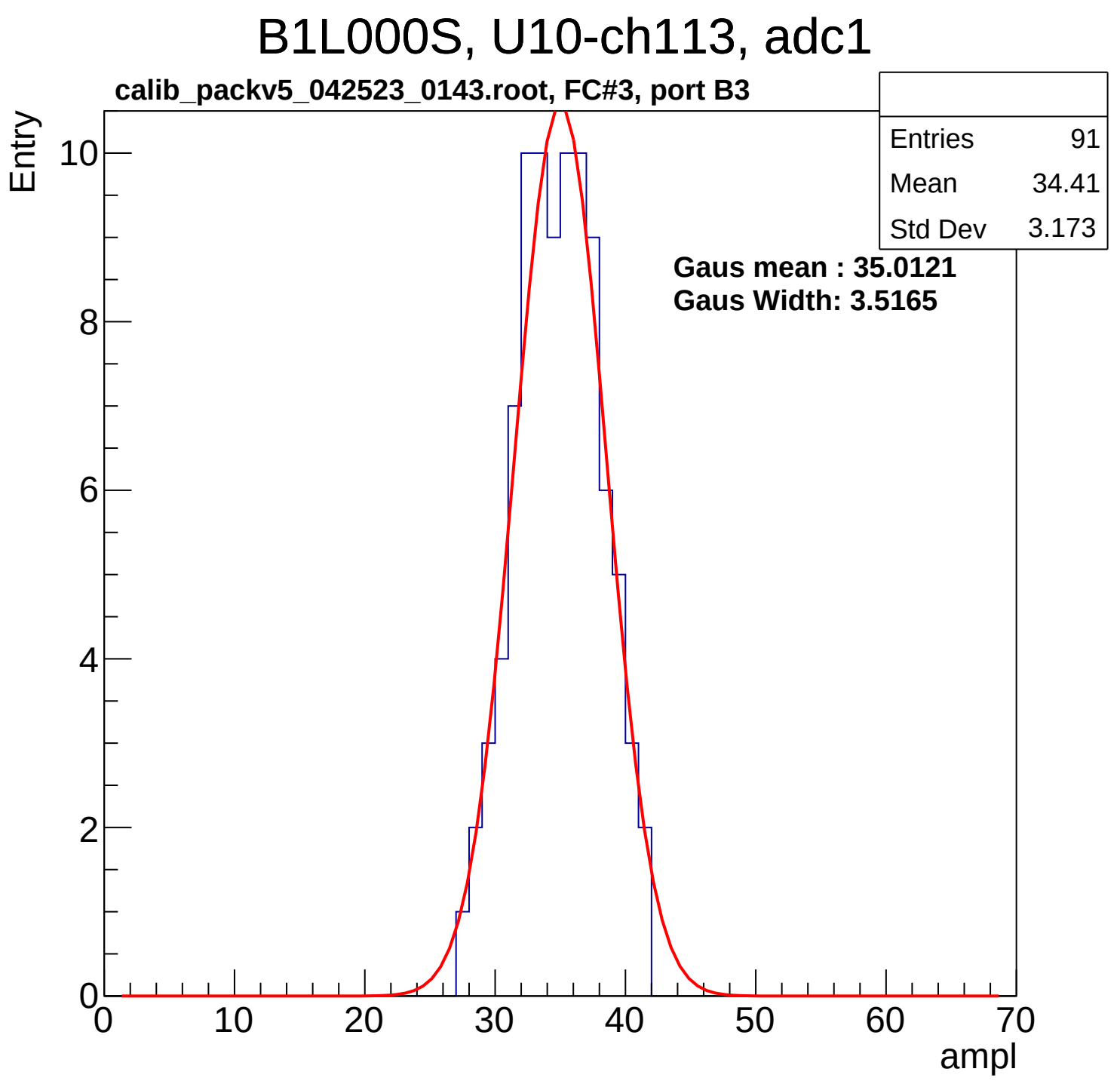
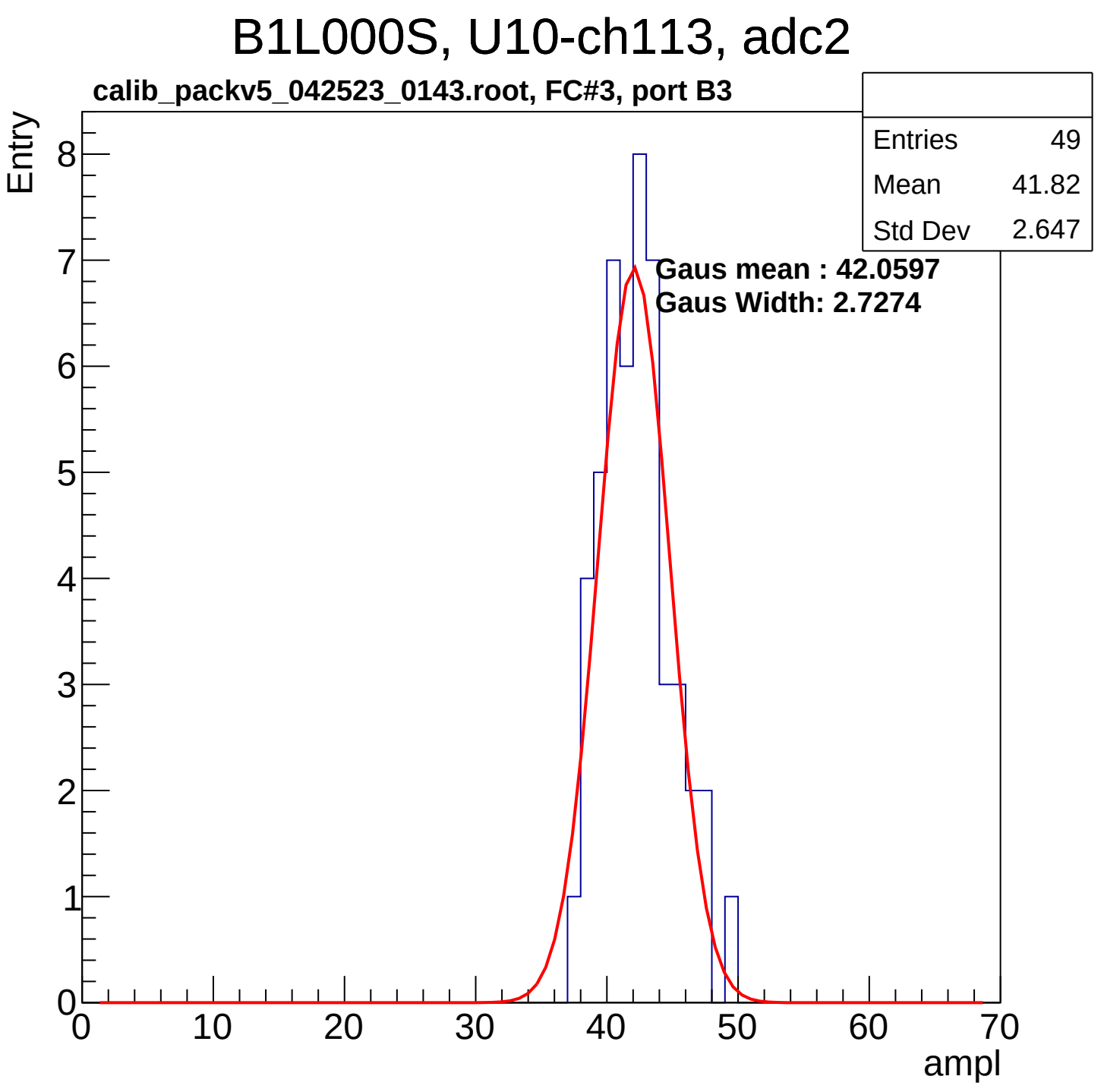

B1L000S, U10-ch113, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 91
Mean 34.41
Std Dev 3.173
Gaus mean : 35.0121
Gaus Width: 3.5165
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch113, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 41.82
Std Dev 2.647
Gaus mean : 42.0597
Gaus Width: 2.7274
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
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60
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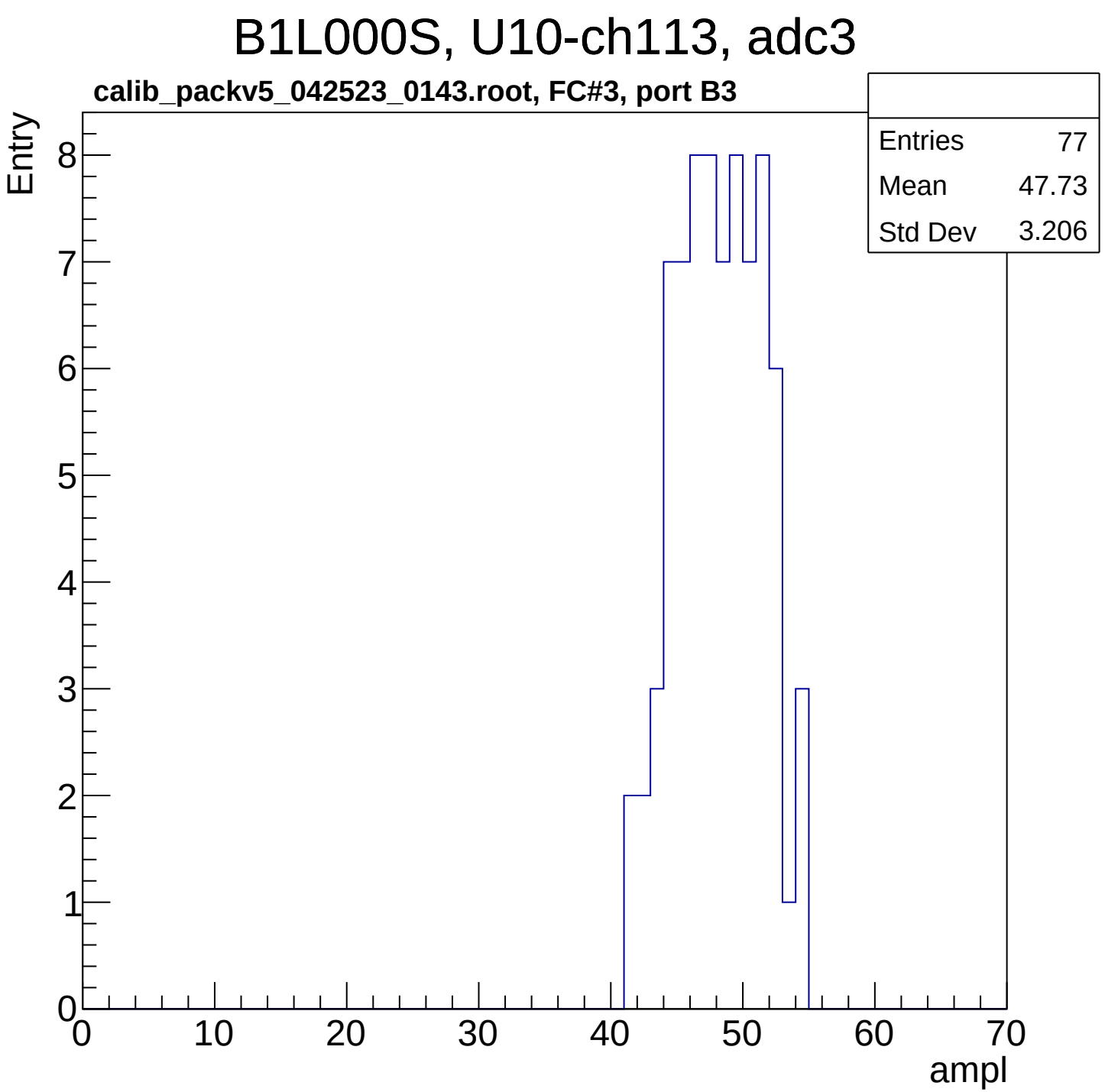
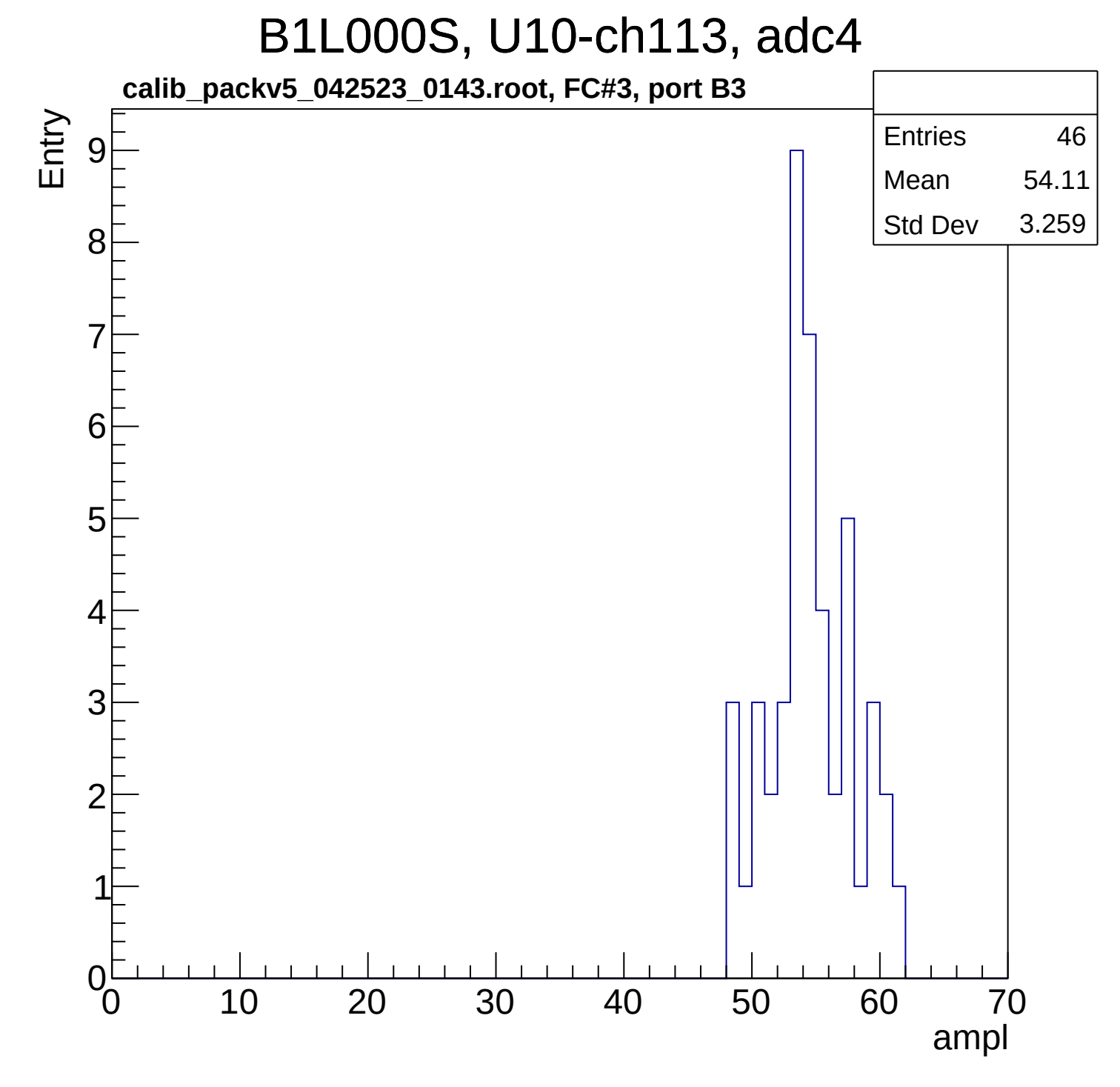

B1L000S, U10-ch113, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 47.73
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch113, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 54.11
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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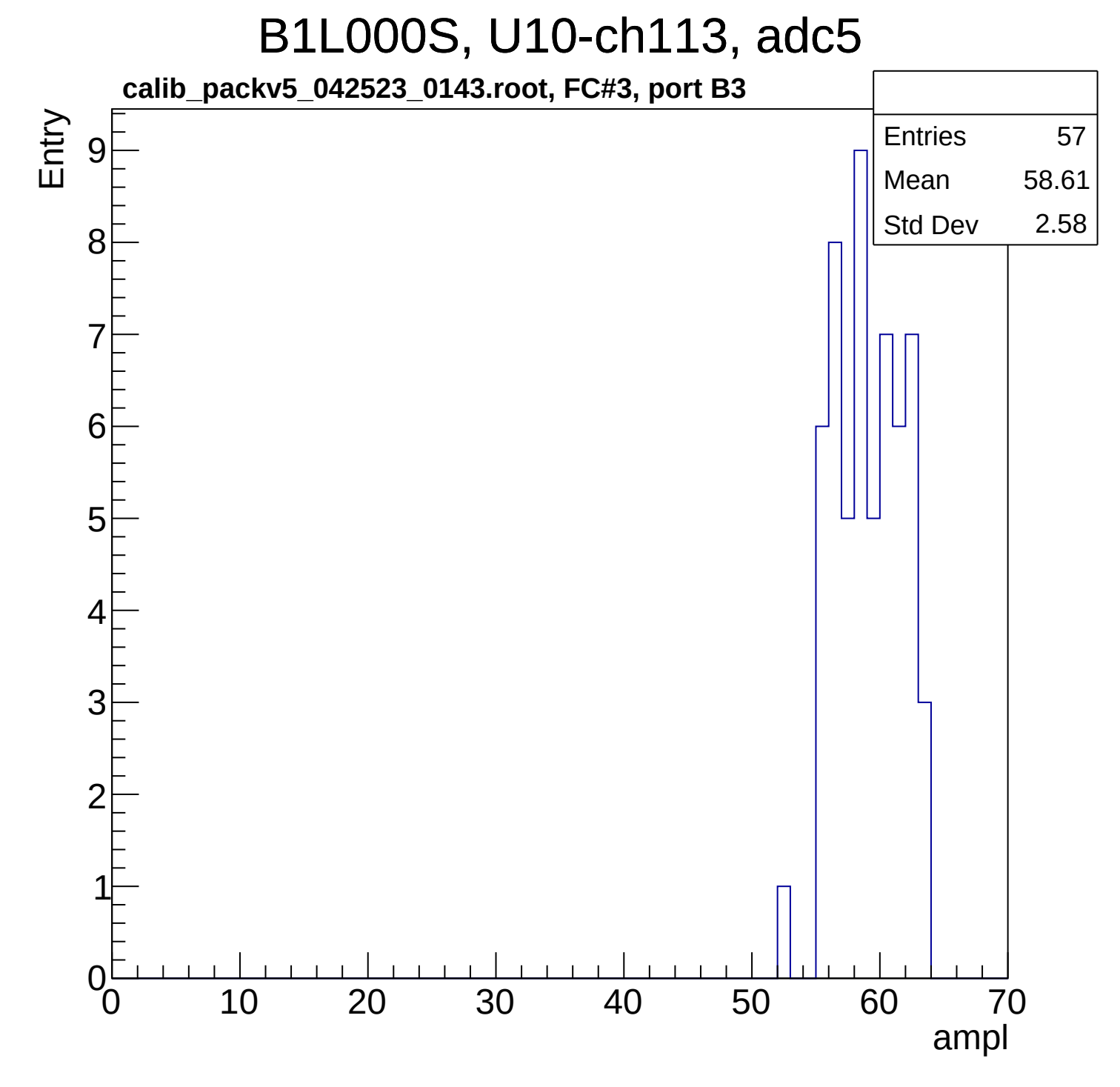
B1L000S, U10-ch113, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.61
Std Dev 2.58
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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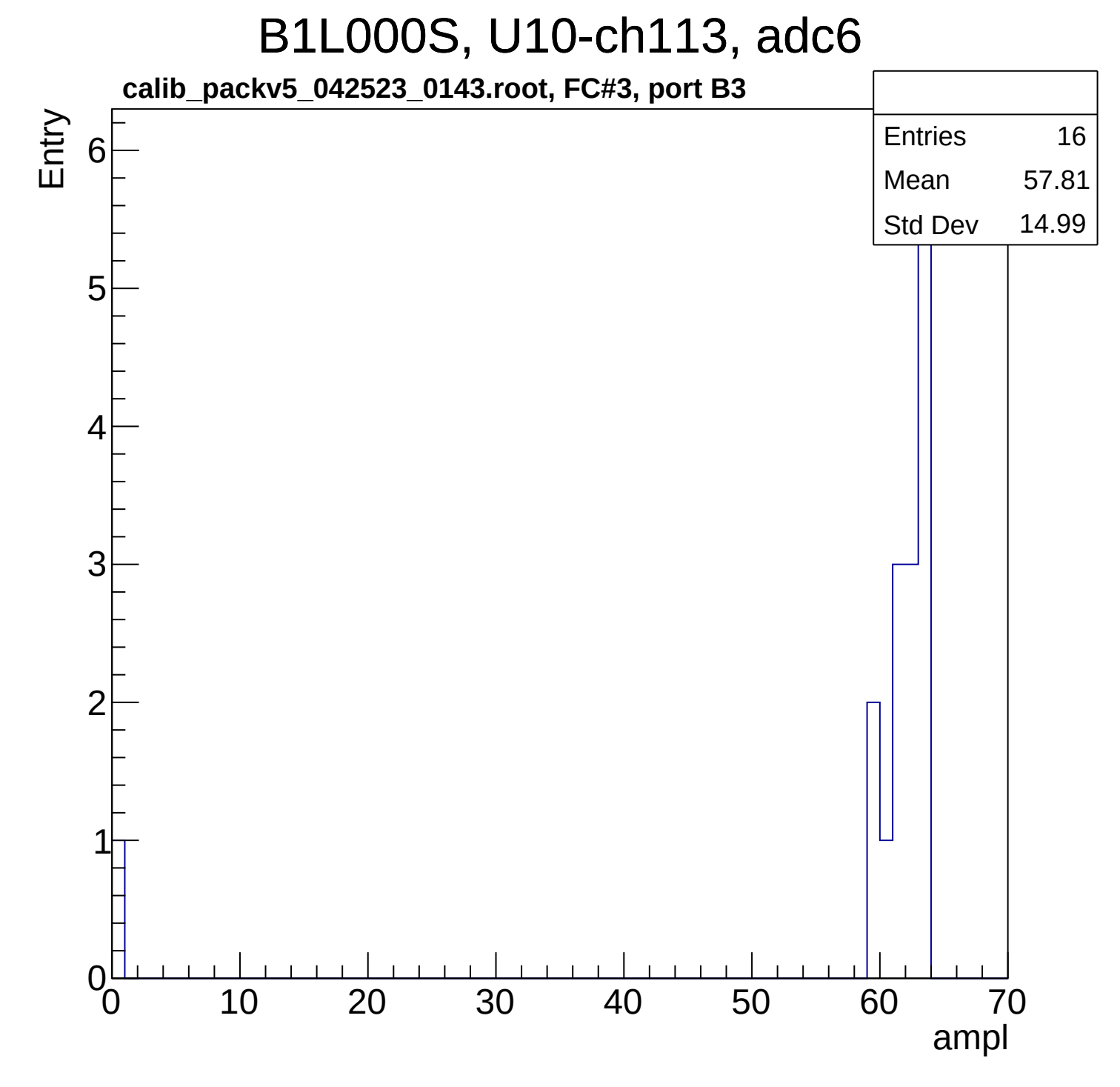
B1L000S, U10-ch113, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.81
Std Dev 14.99
Entry
ampl
0
1
2
3
4
5
6
0
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20
30
40
50
60
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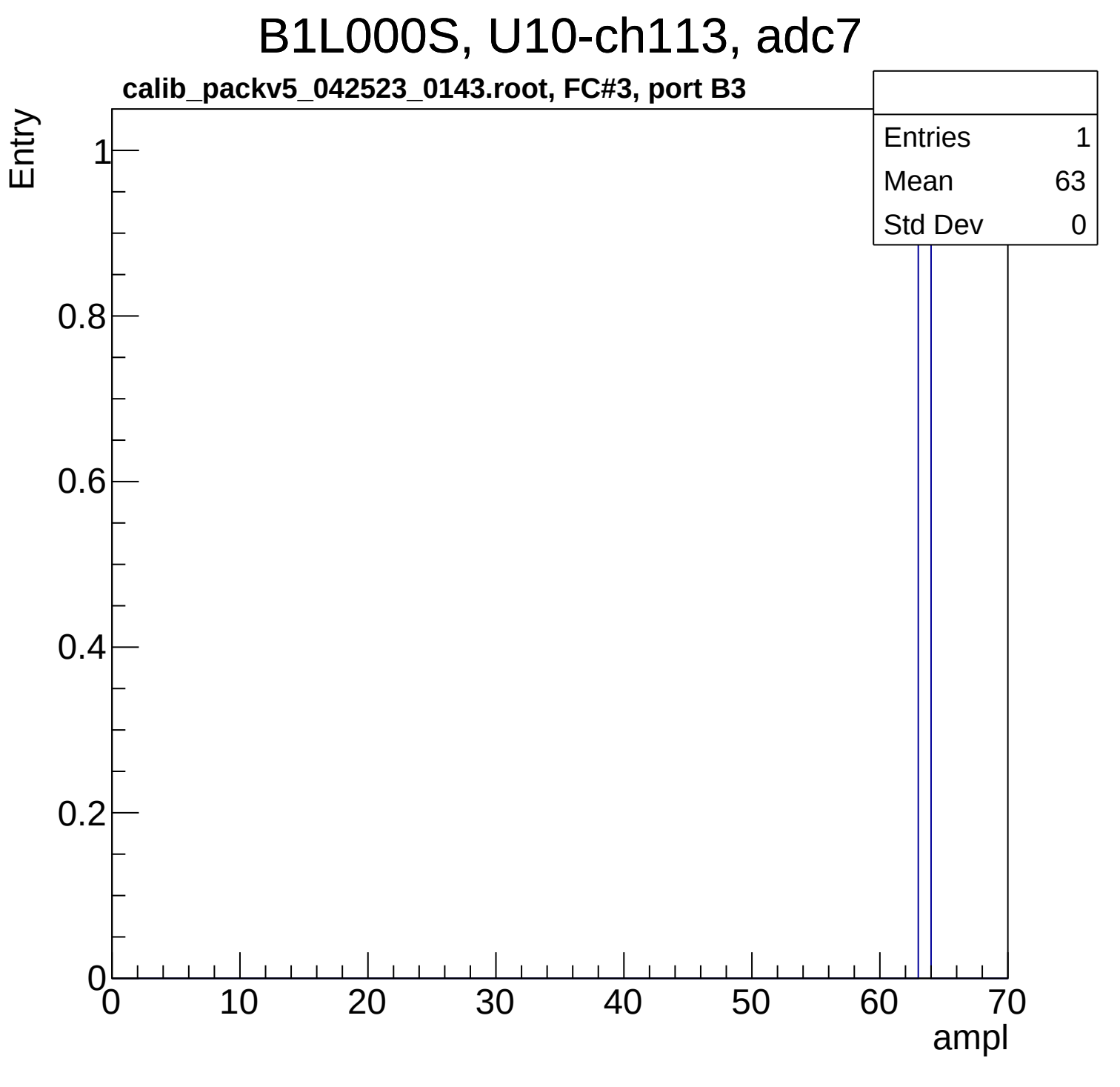
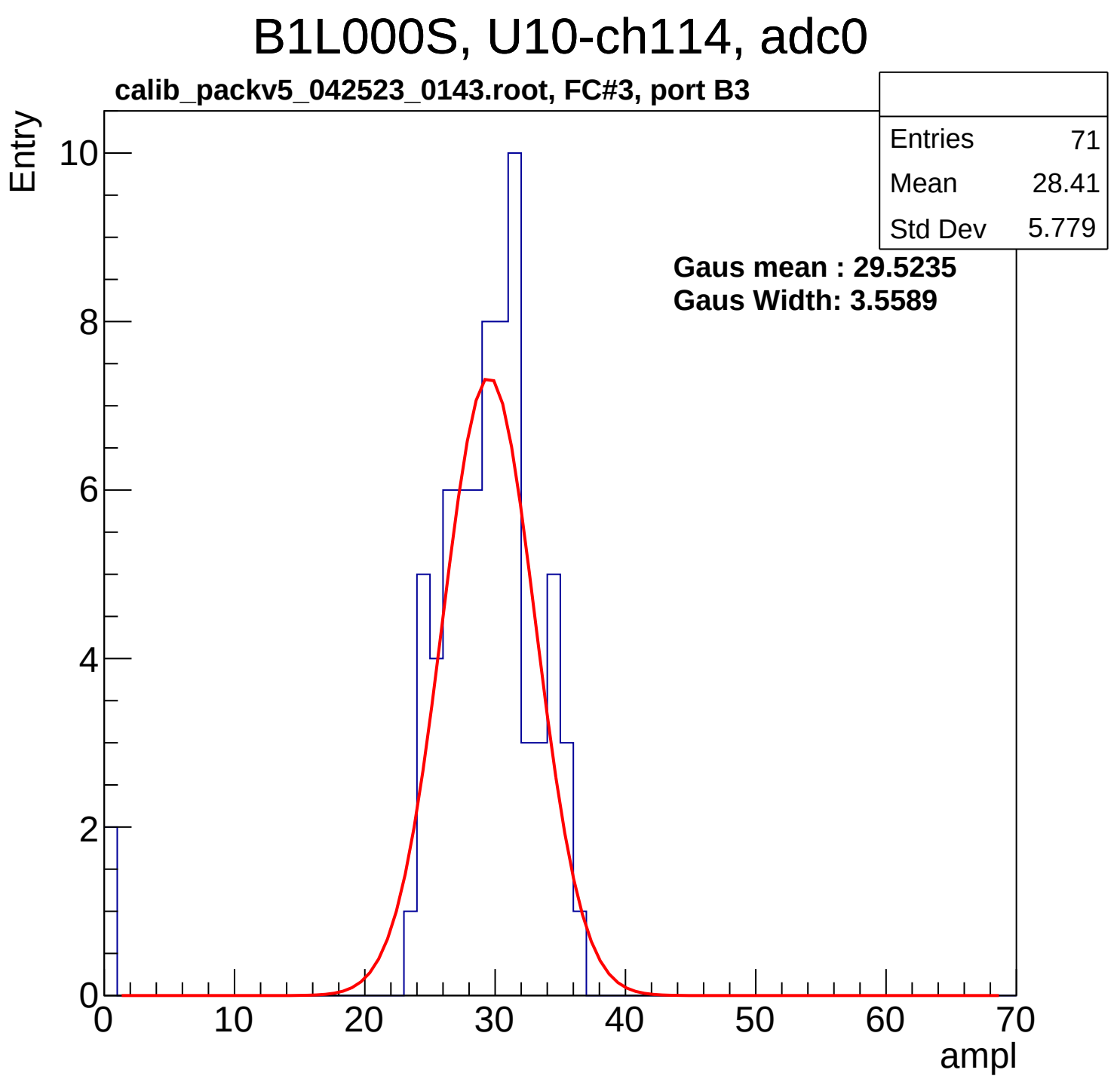
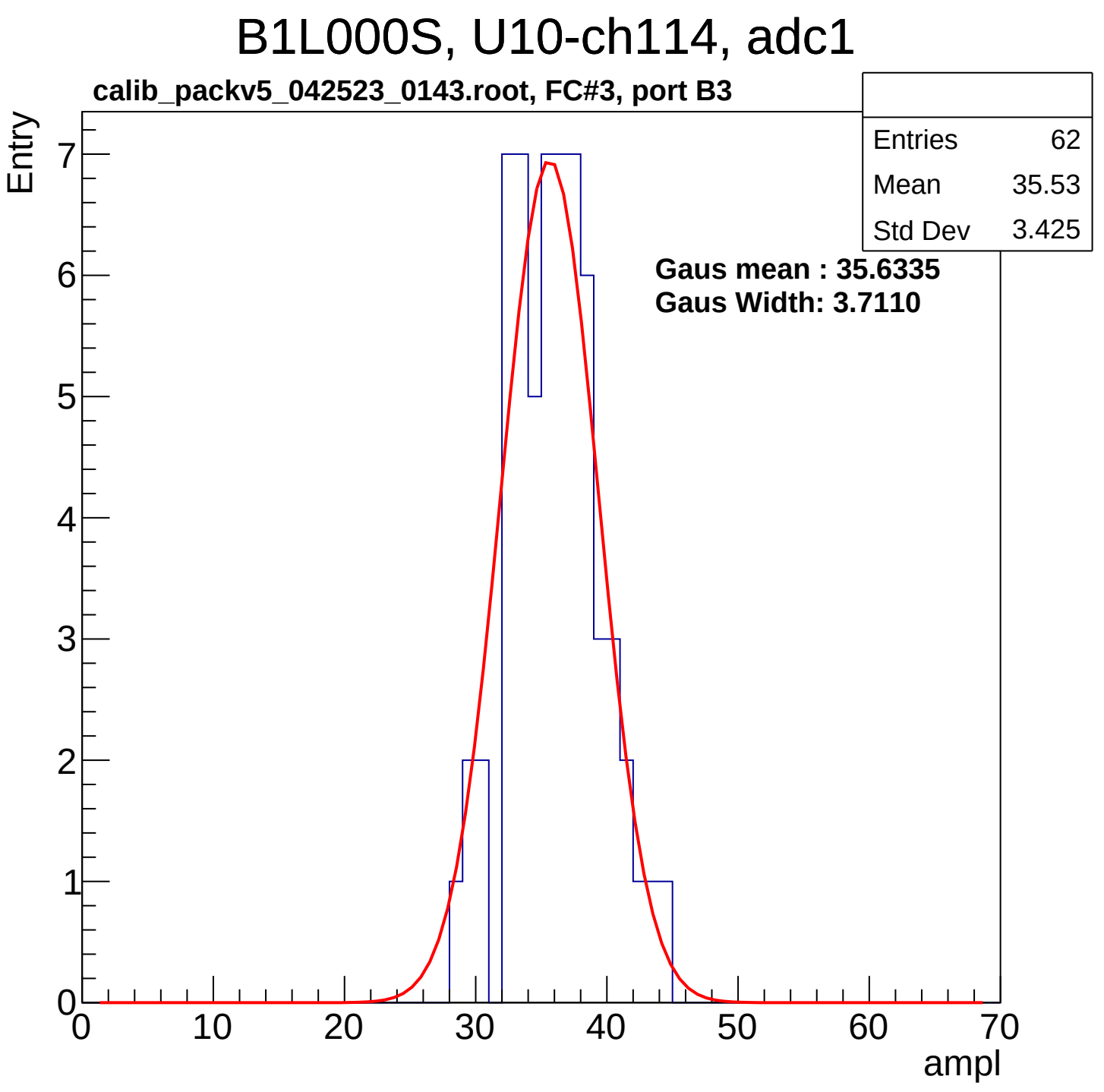
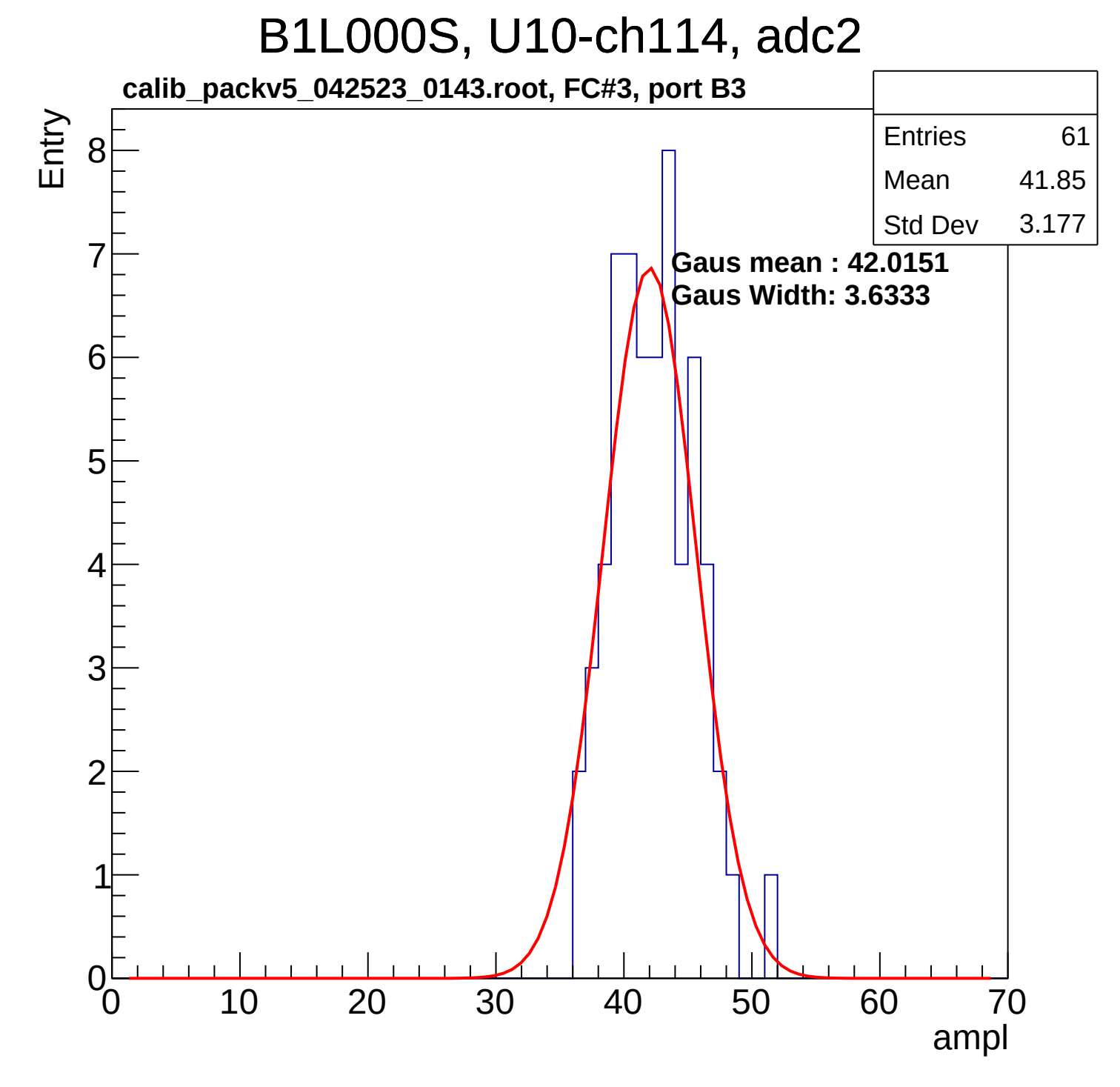

B1L000S, U10-ch113, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch114, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 28.41
Std Dev 5.779
Gaus mean : 29.5235
Gaus Width: 3.5589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch114, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 35.53
Std Dev 3.425
Gaus mean : 35.6335
Gaus Width: 3.7110
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch114, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.85
Std Dev 3.177
Gaus mean : 42.0151
Gaus Width: 3.6333
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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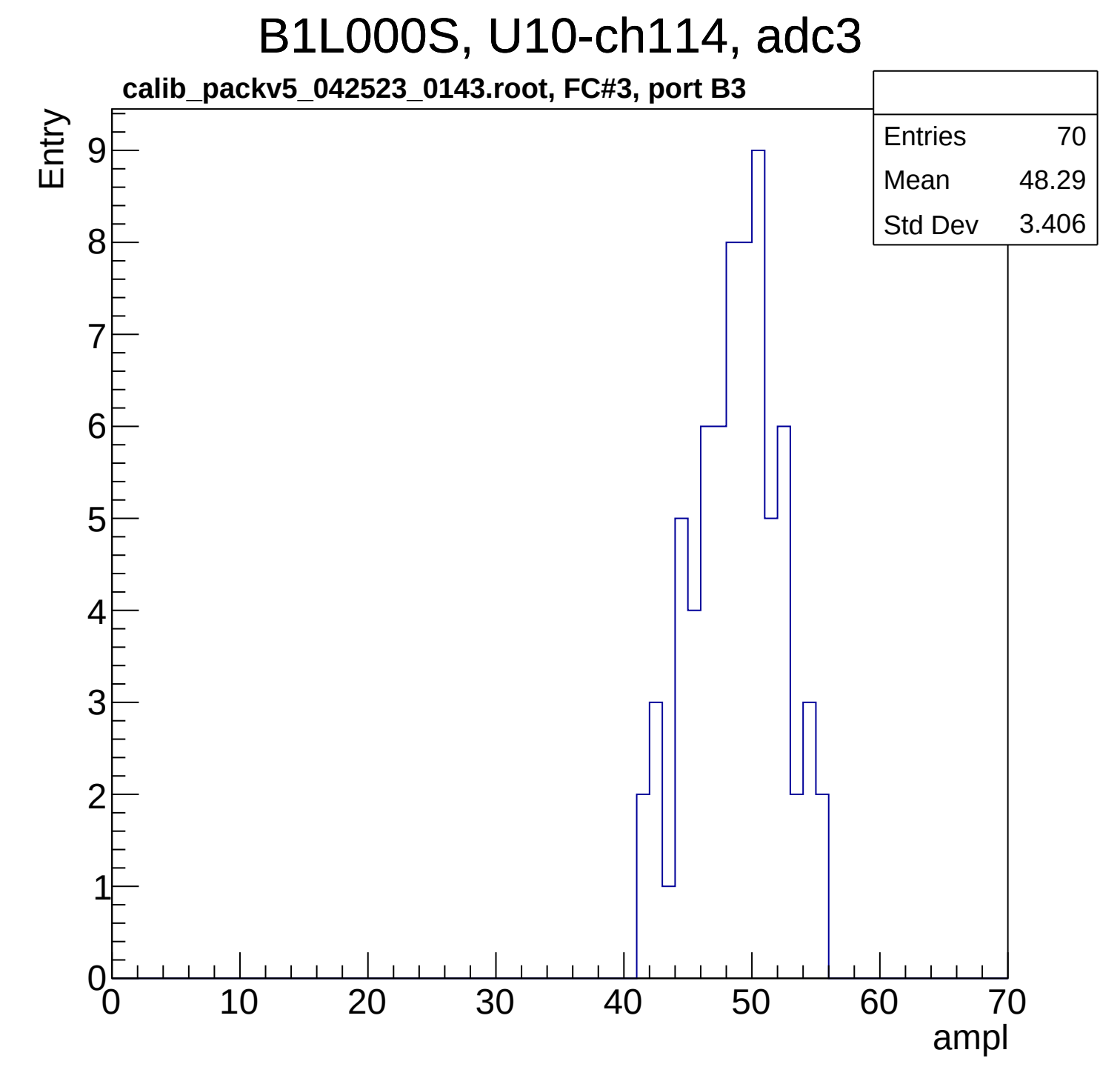
B1L000S, U10-ch114, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 48.29
Std Dev 3.406
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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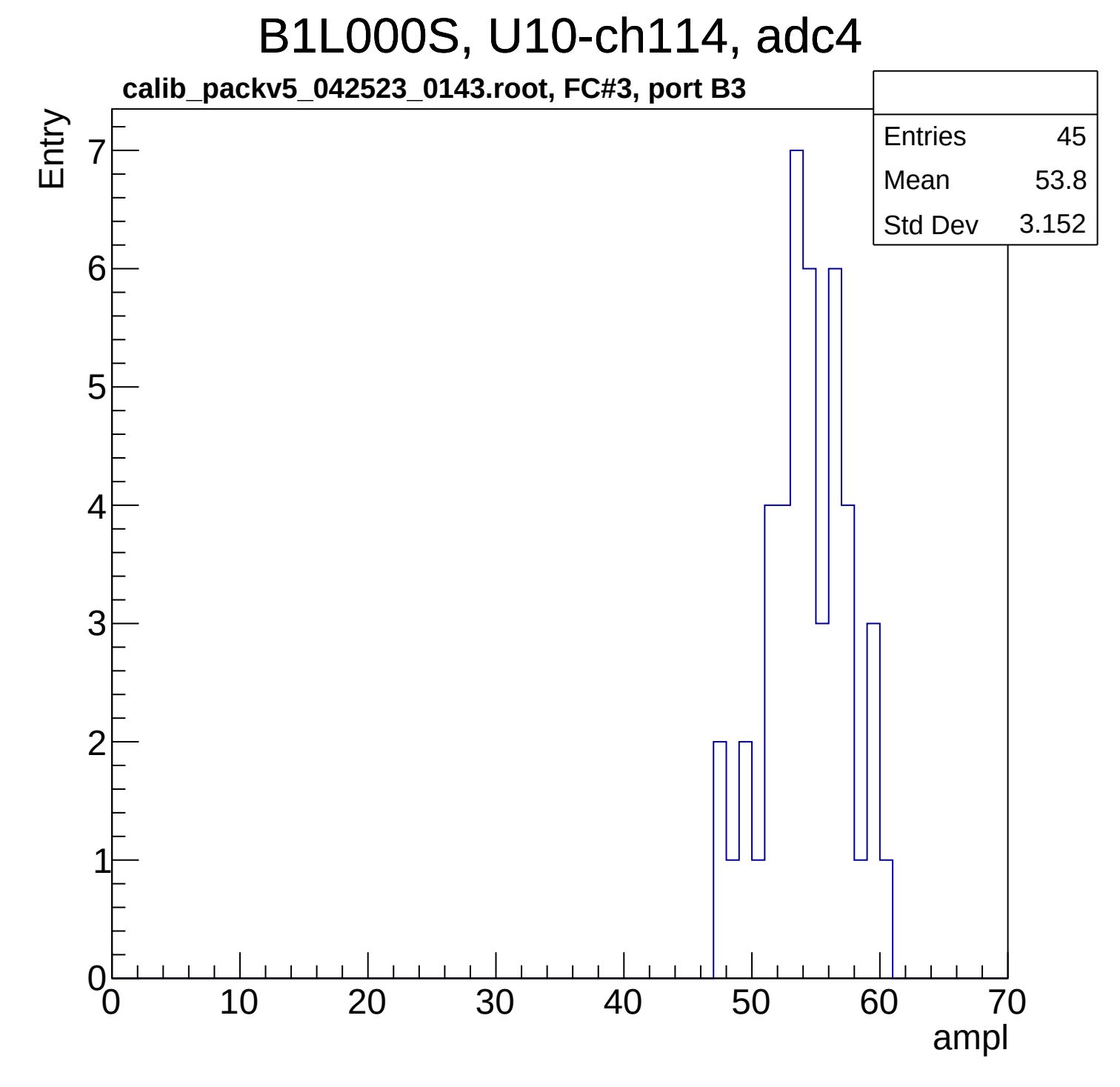
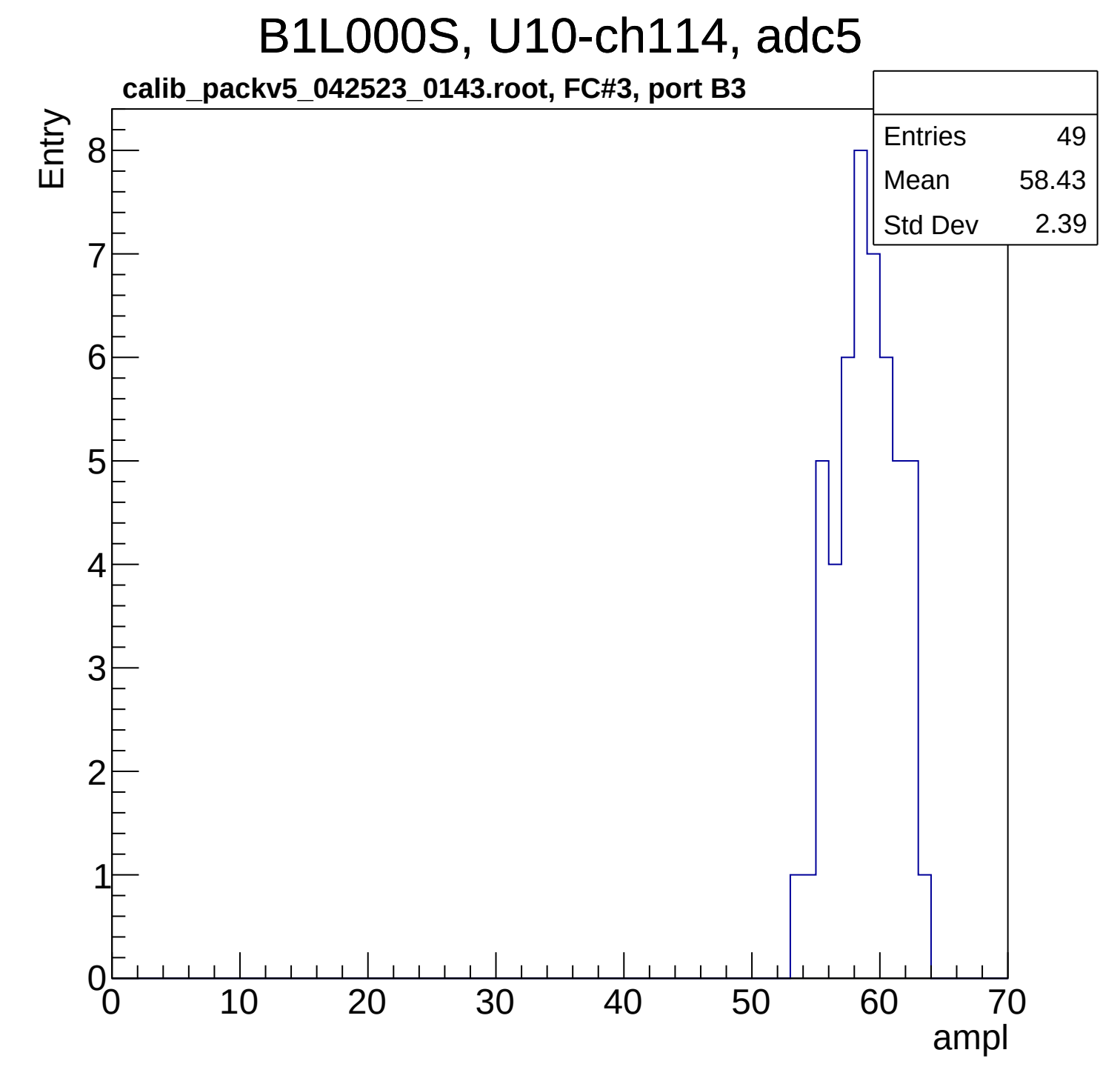
60
70


B1L000S, U10-ch114, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.8
Std Dev 3.152
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch114, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.43
Std Dev 2.39
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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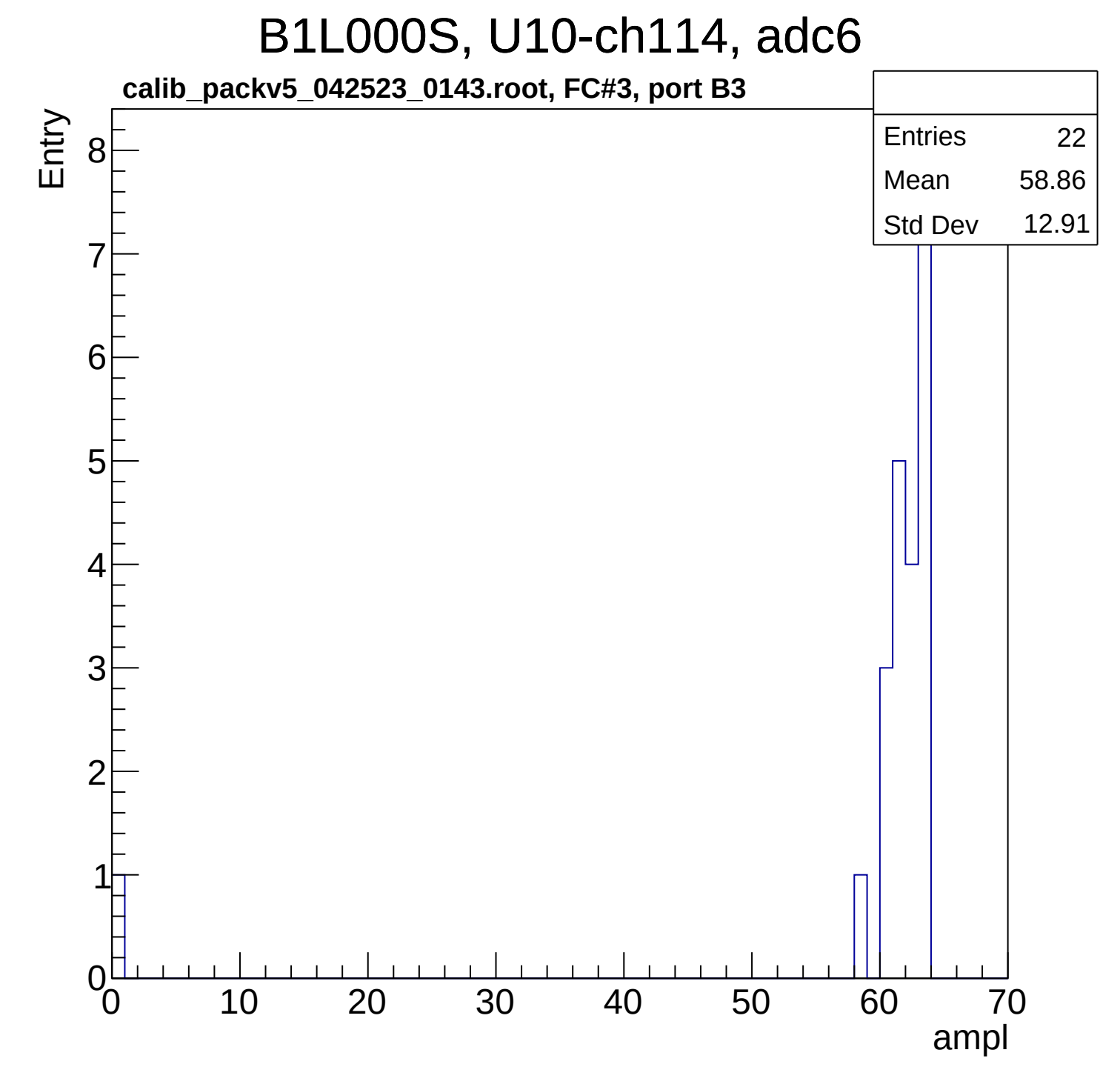
B1L000S, U10-ch114, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.86
Std Dev 12.91
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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50
60
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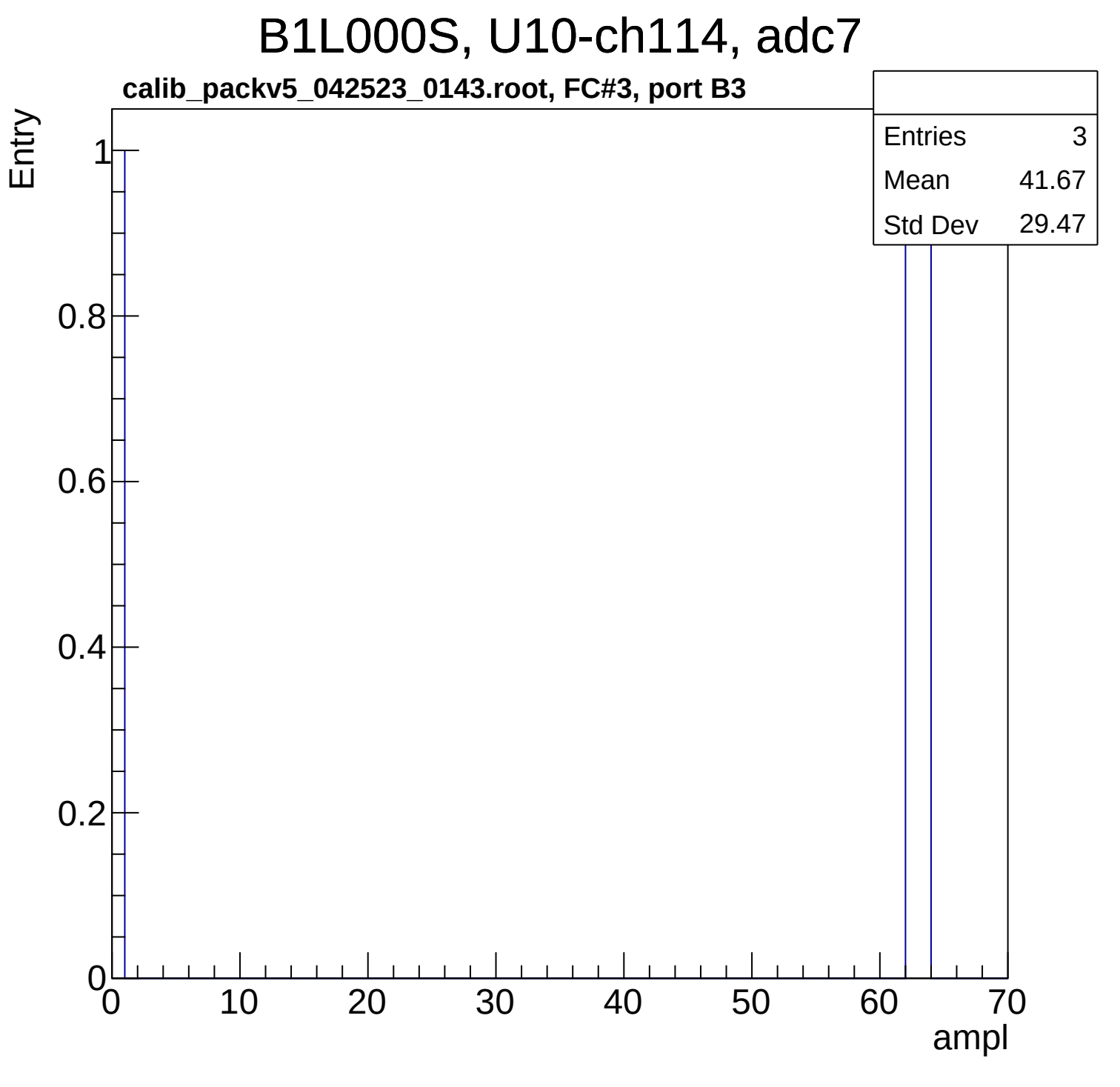
B1L000S, U10-ch114, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
0.2
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10
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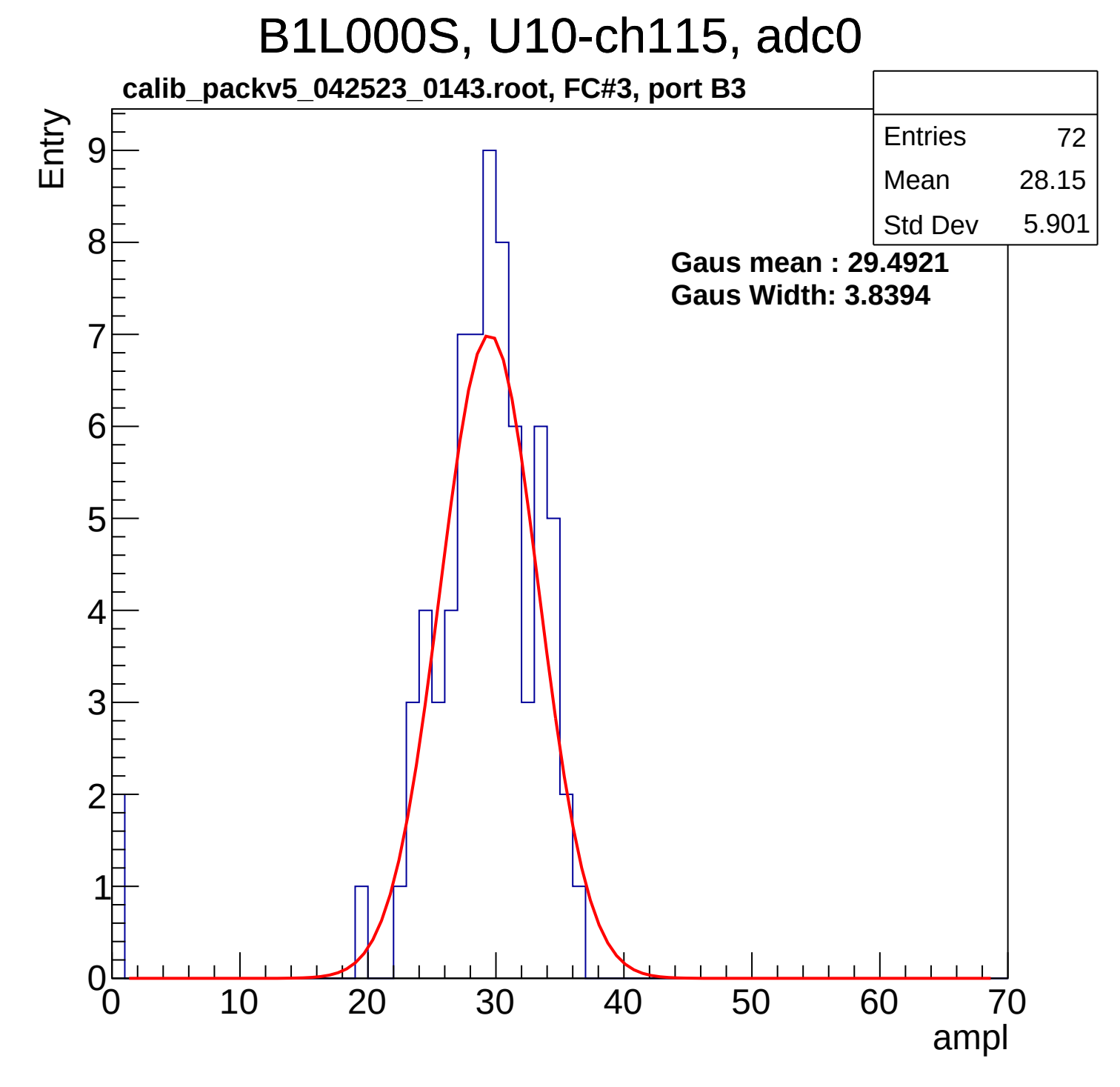
50
60
70
ampl


B1L000S, U10-ch115, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 28.15
Std Dev 5.901
Gaus mean : 29.4921
Gaus Width: 3.8394
Entry
ampl
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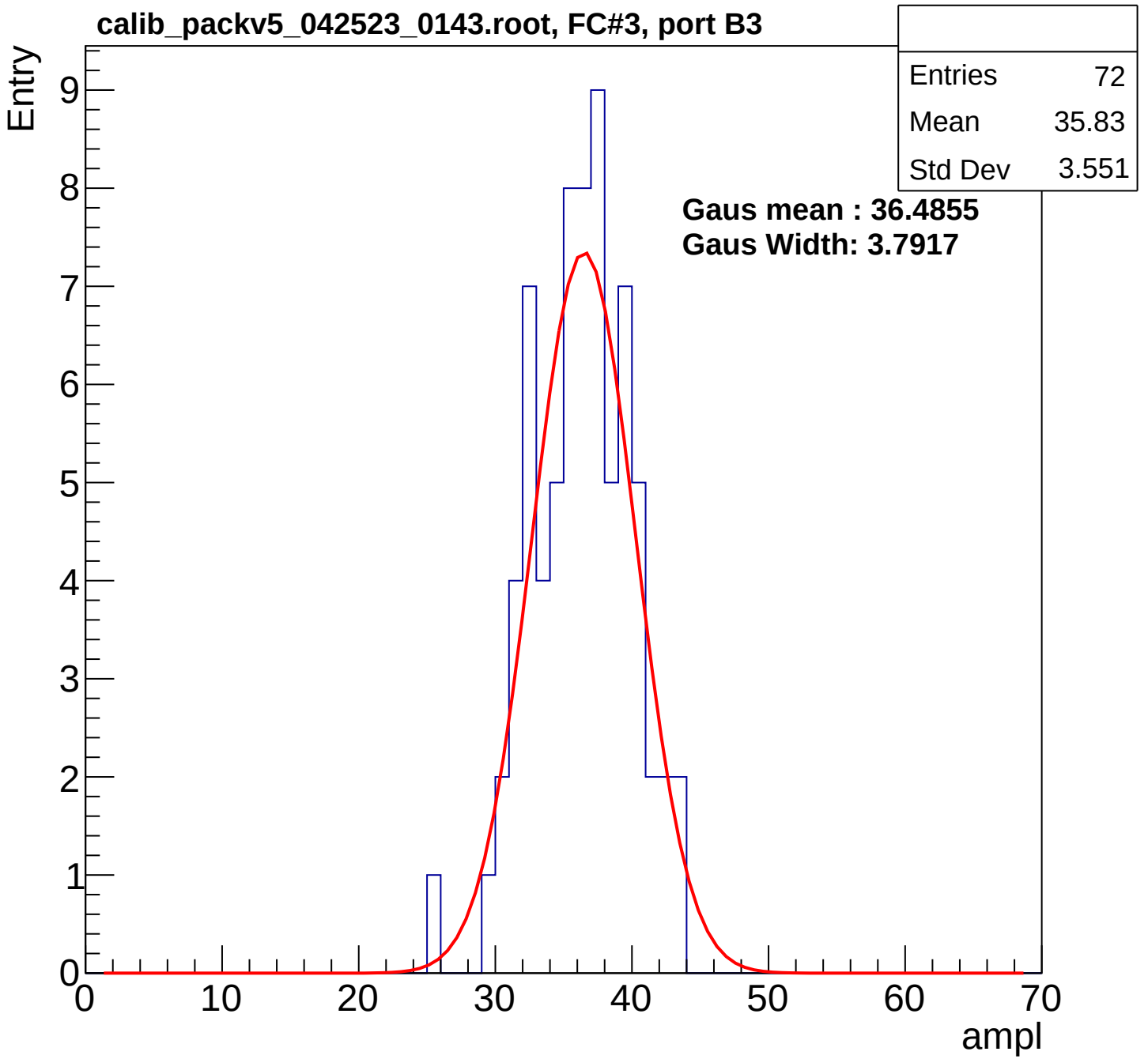
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch115, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.83
Std Dev 3.551
Gaus mean : 36.4855
Gaus Width: 3.7917
Entry
ampl
0
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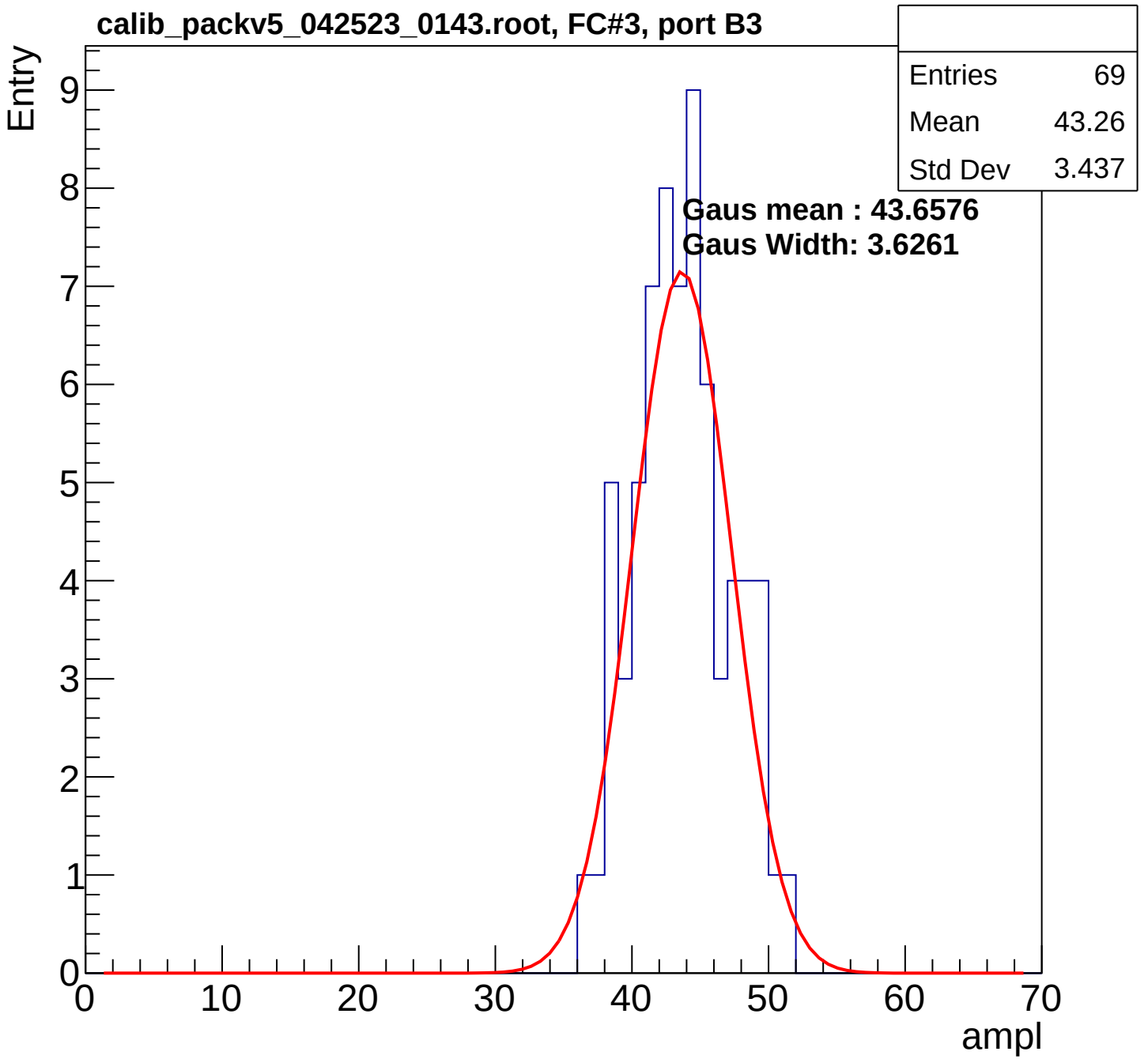
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch115, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 43.26
Std Dev 3.437
Gaus mean : 43.6576
Gaus Width: 3.6261
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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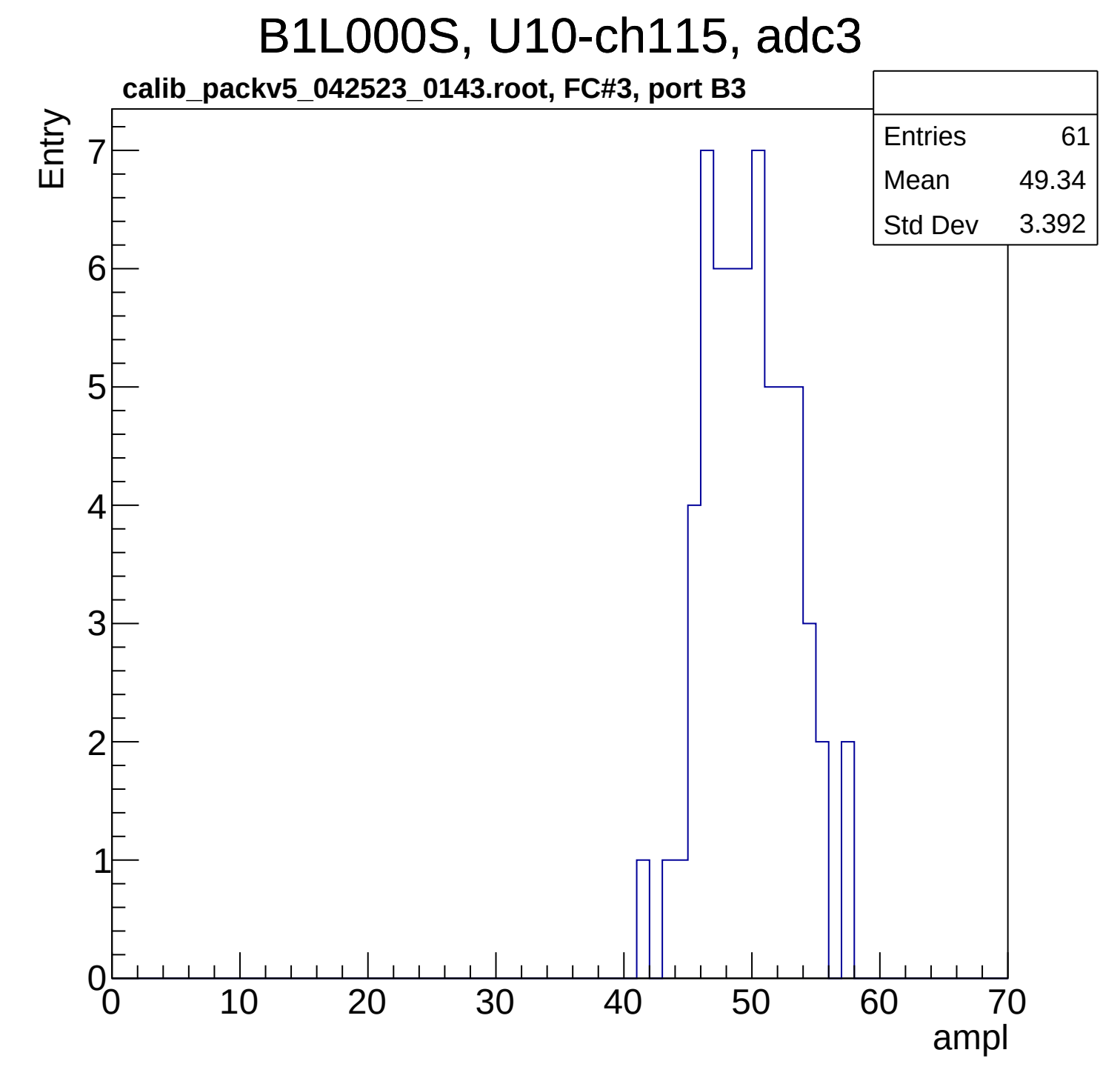
60
70


B1L000S, U10-ch115, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 49.34
Std Dev 3.392
Entry
ampl
0
1
2
3
4
5
6
7
0
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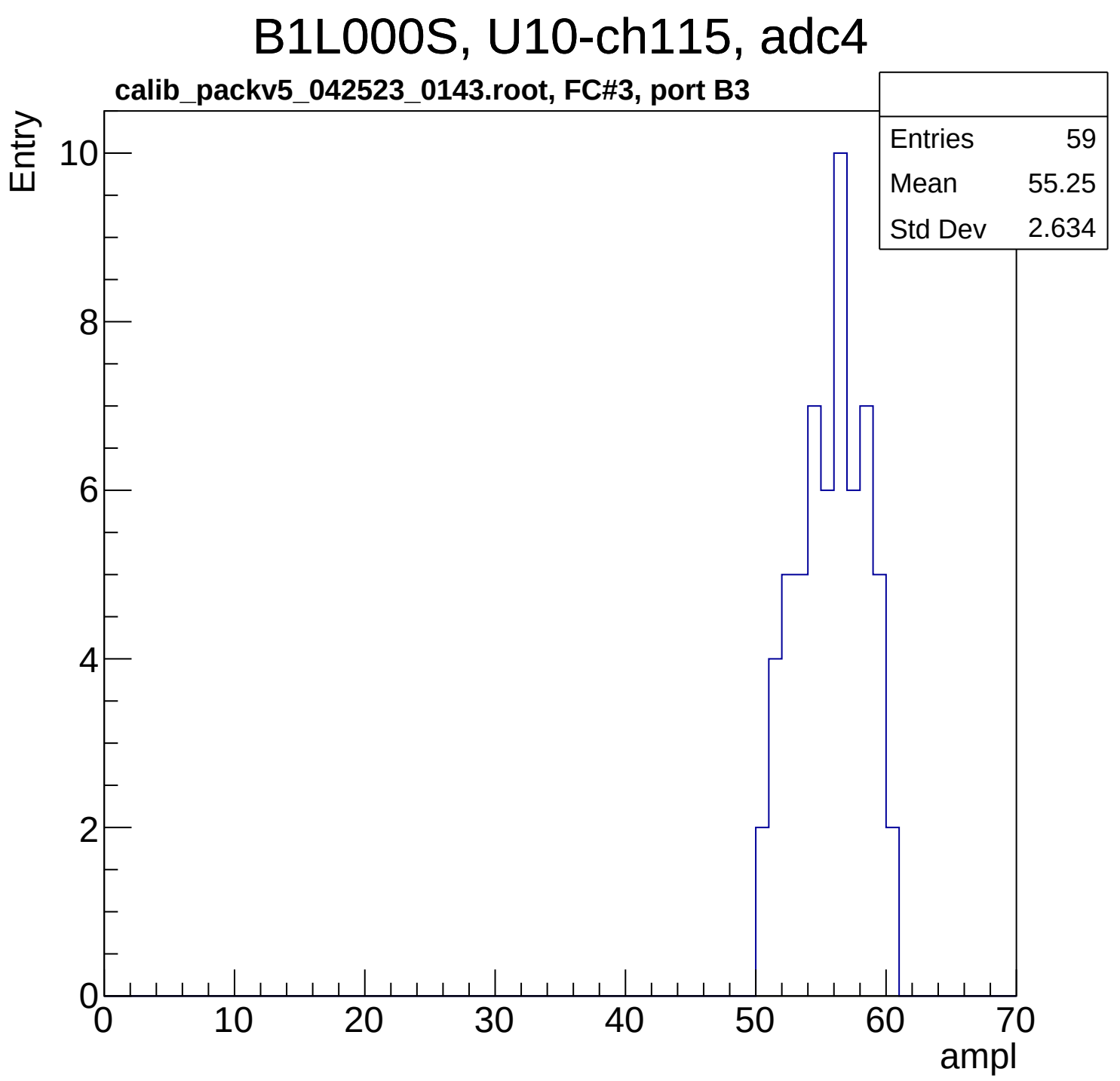
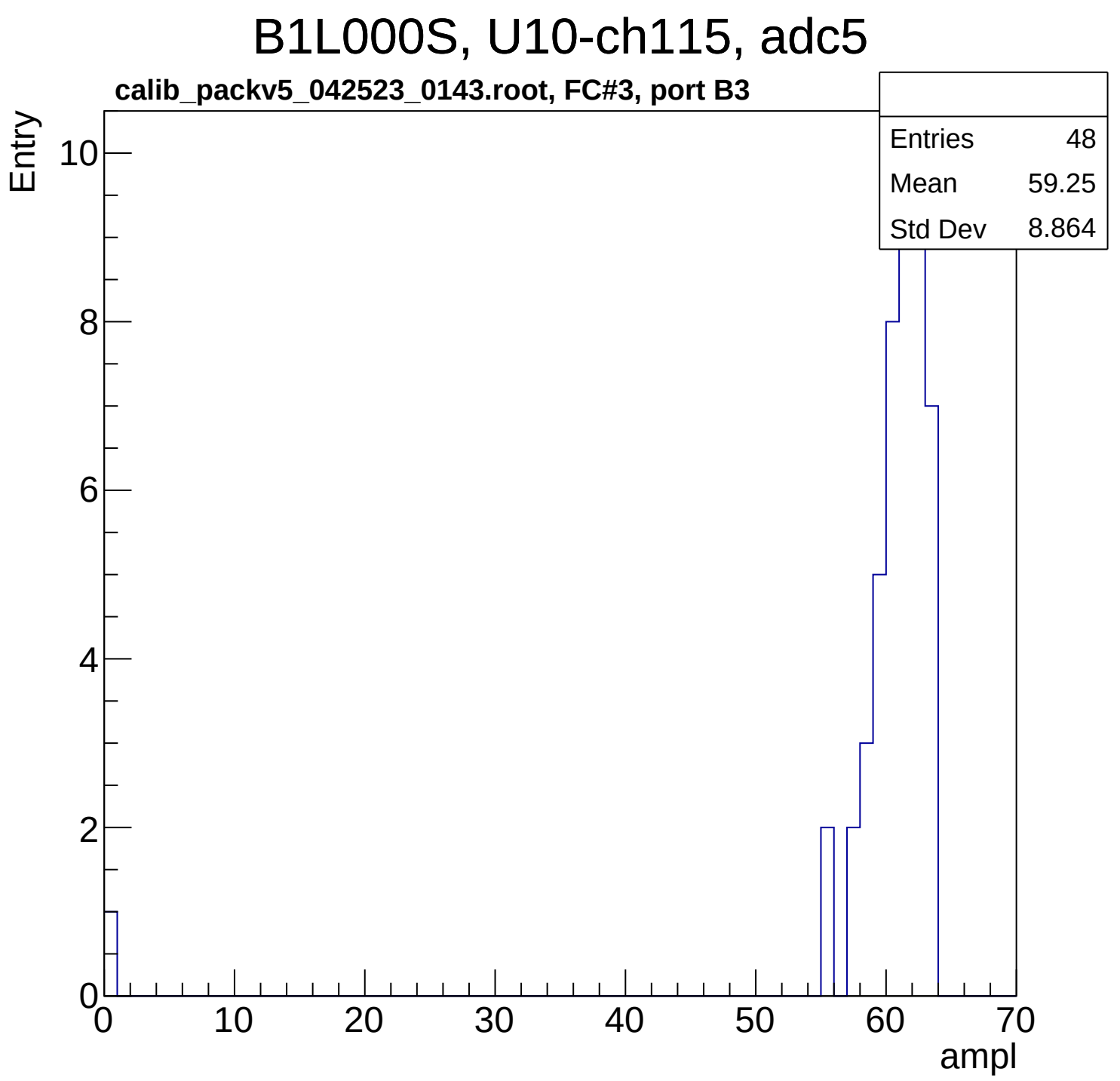
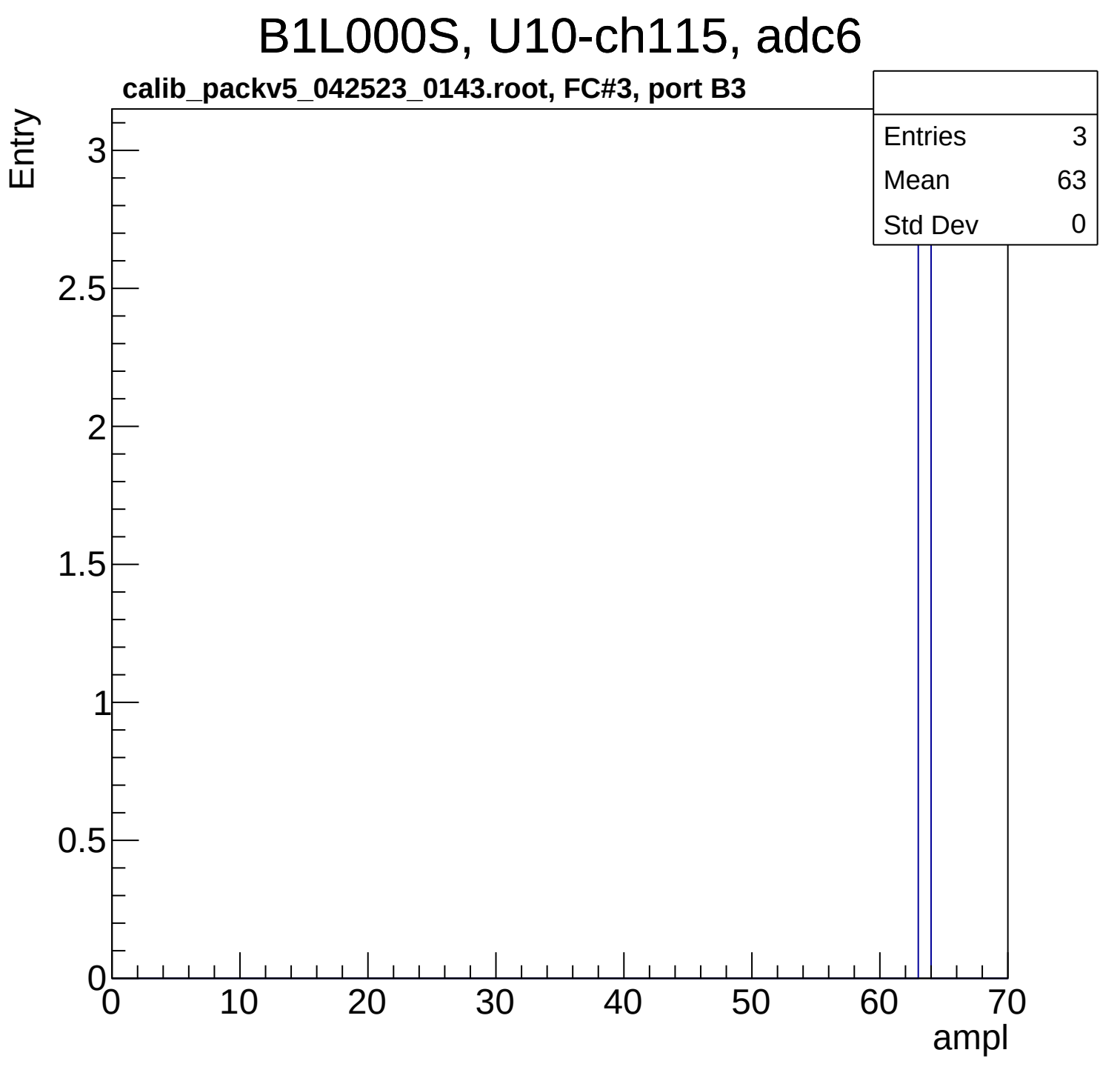
20
30
40
50
60
70


B1L000S, U10-ch115, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 55.25
Std Dev 2.634
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch115, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 59.25
Std Dev 8.864
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch115, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 63
Std Dev 0
Entry
ampl
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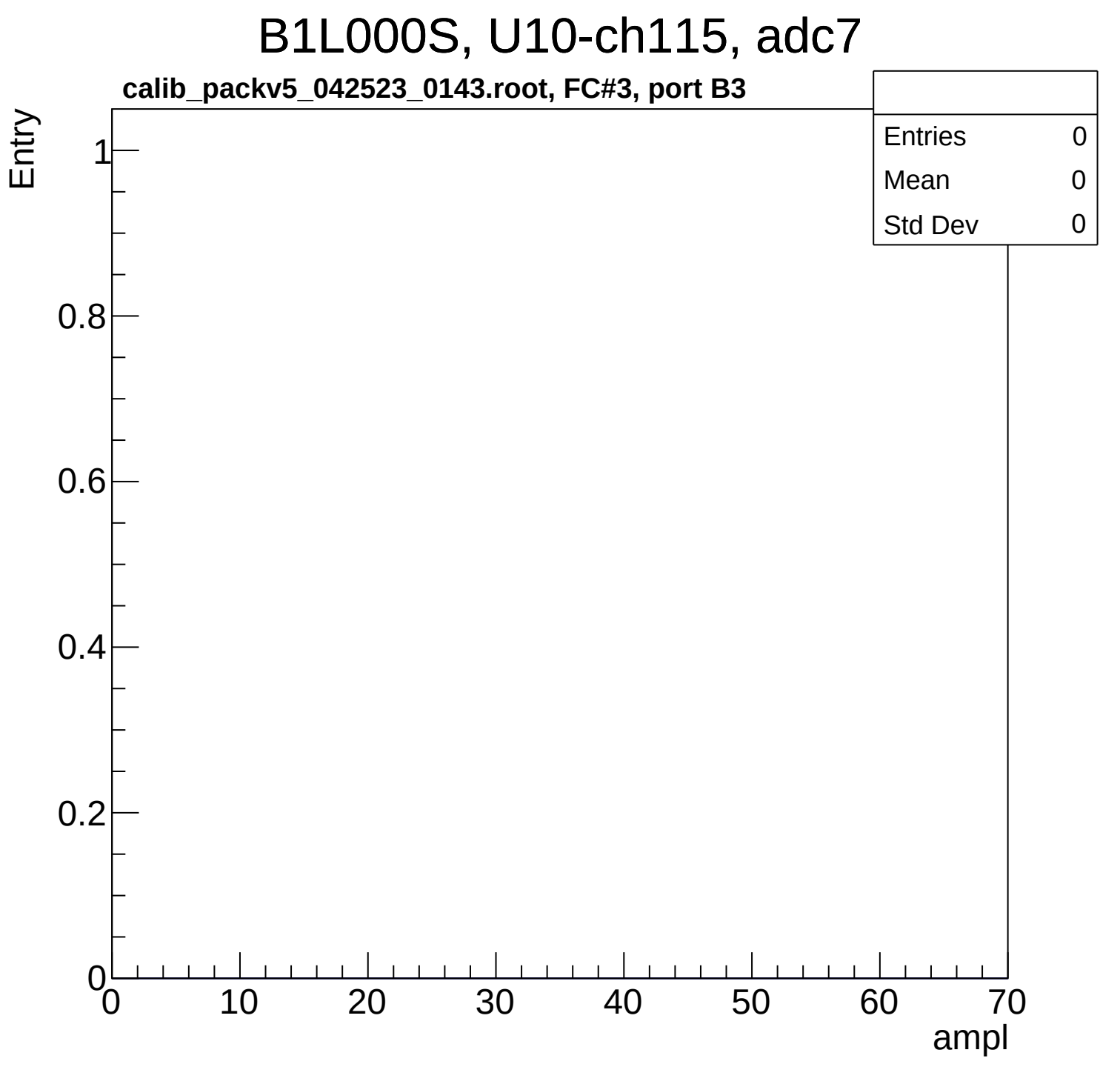
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U10-ch115, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
30
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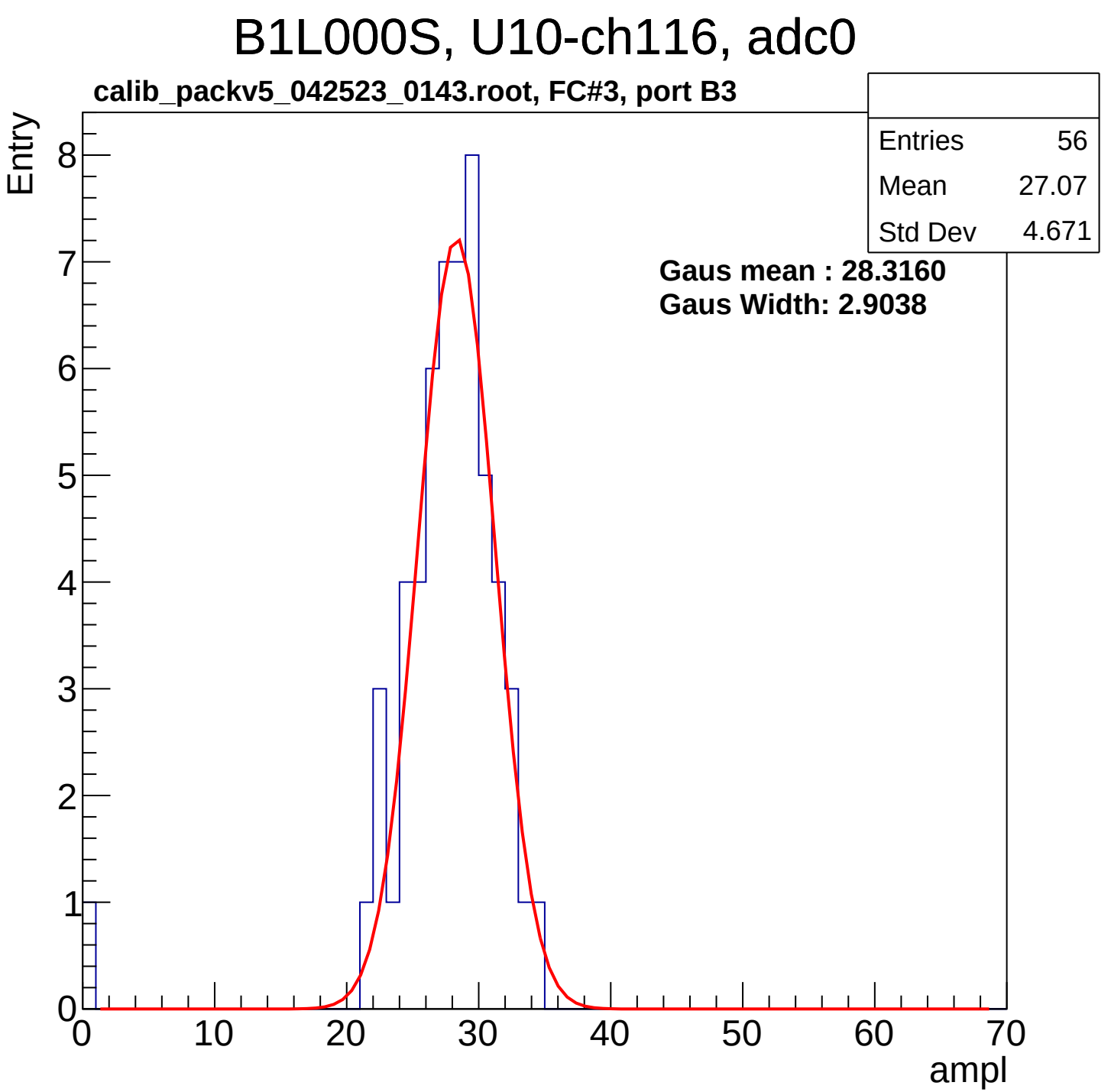
50
60
70
ampl


B1L000S, U10-ch116, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 27.07
Std Dev 4.671
Gaus mean : 28.3160
Gaus Width: 2.9038
Entry
ampl
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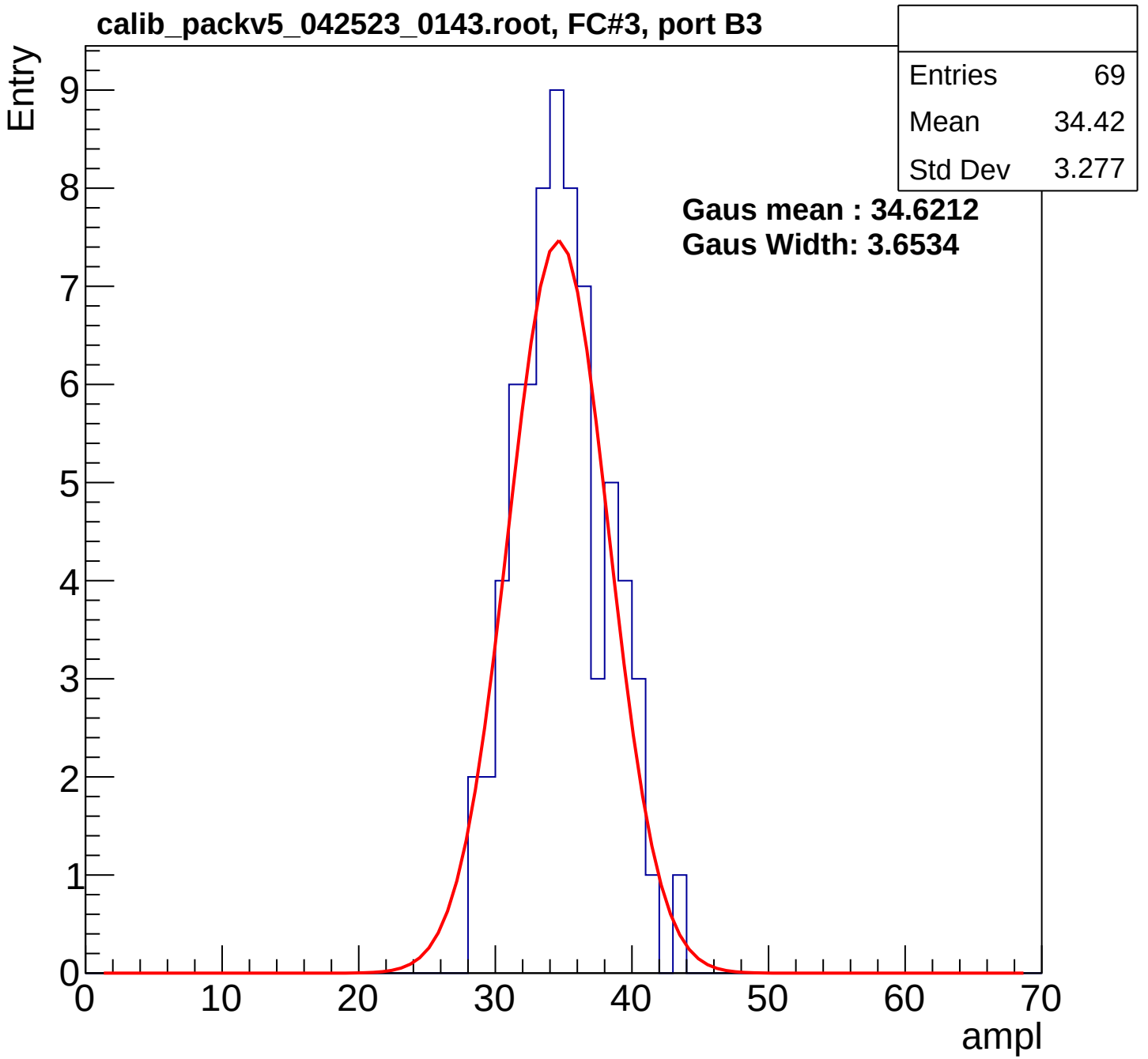
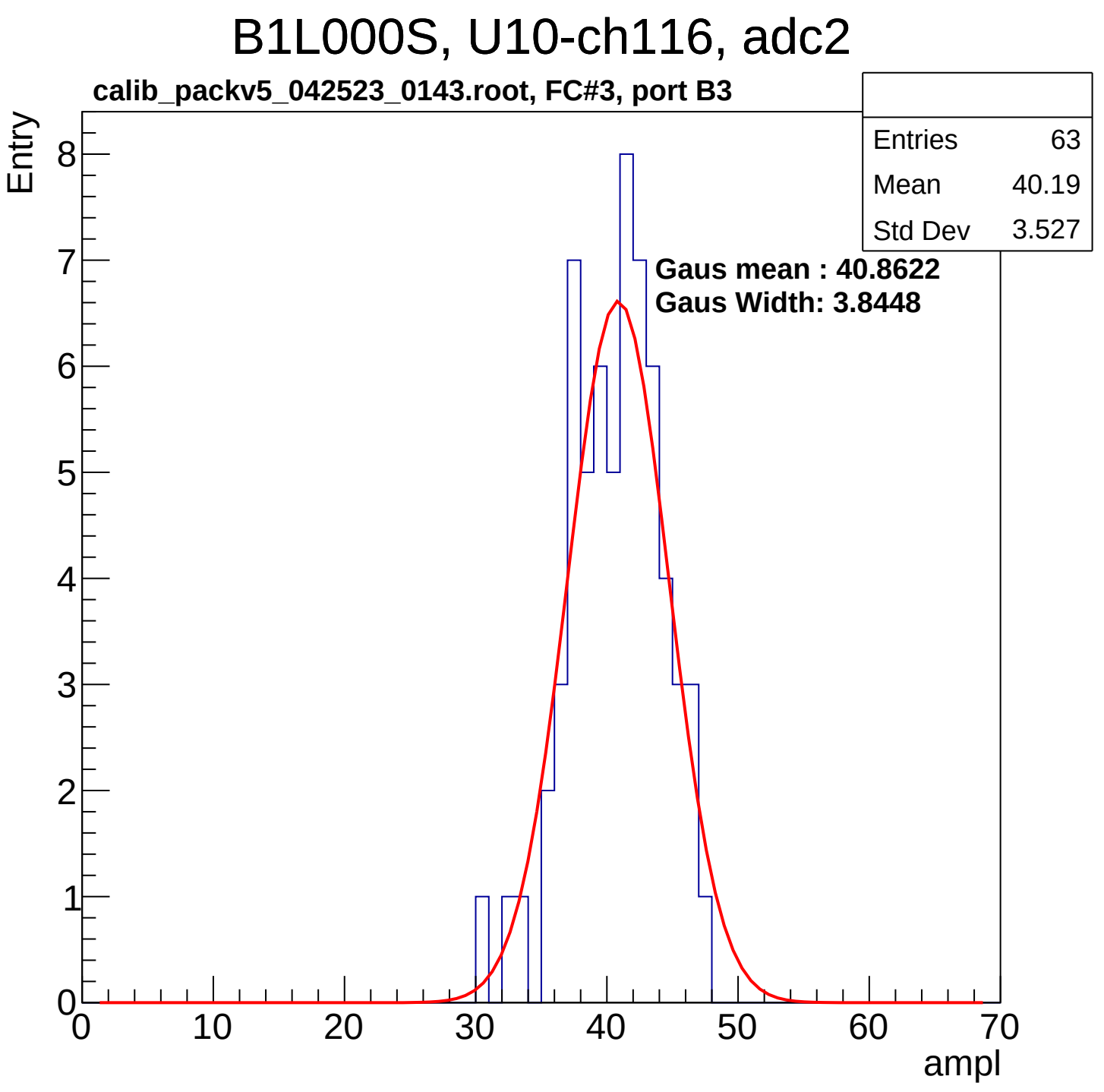
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch116, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.42
Std Dev 3.277
Gaus mean : 34.6212
Gaus Width: 3.6534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch116, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 40.19
Std Dev 3.527
Gaus mean : 40.8622
Gaus Width: 3.8448
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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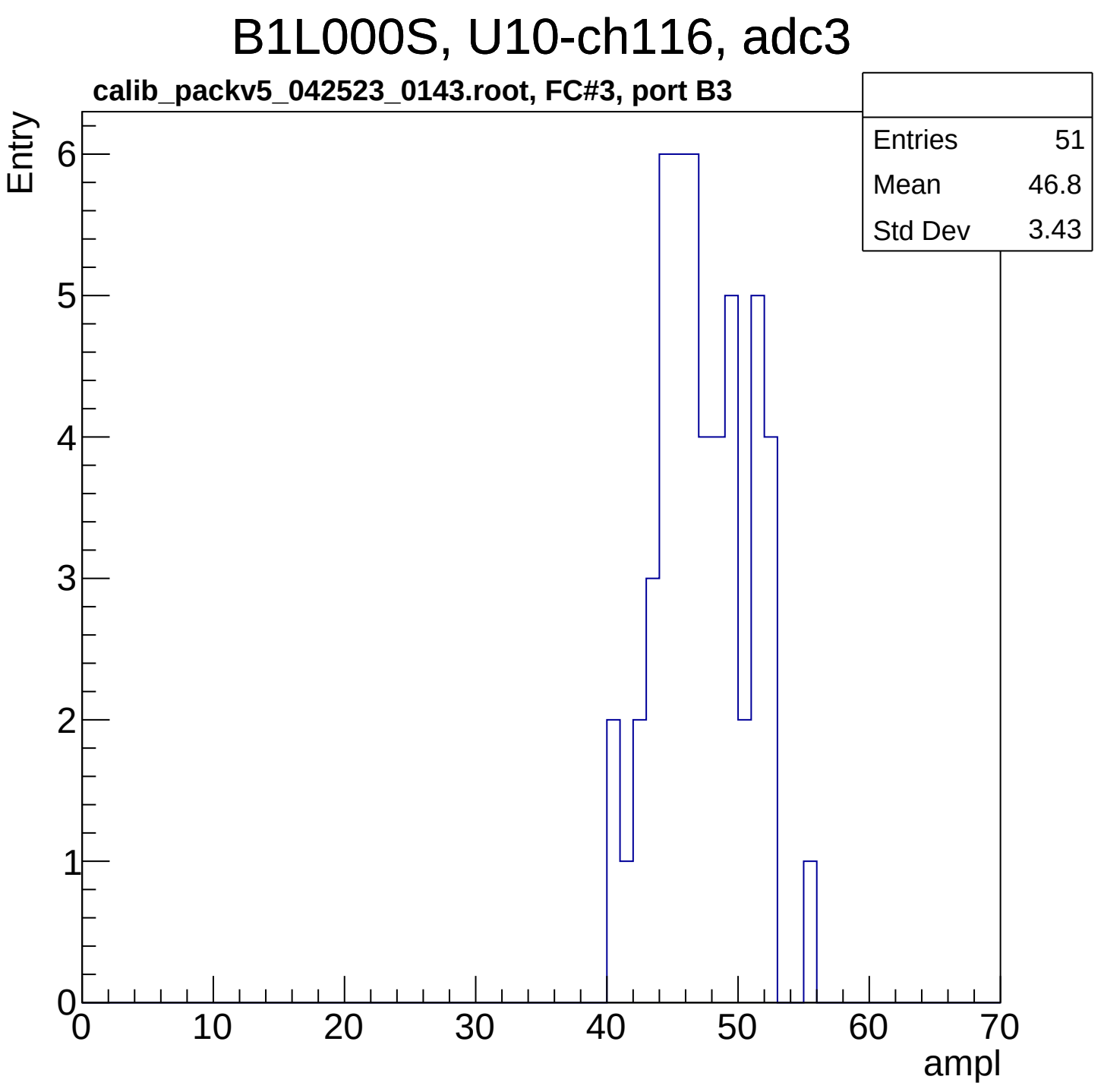
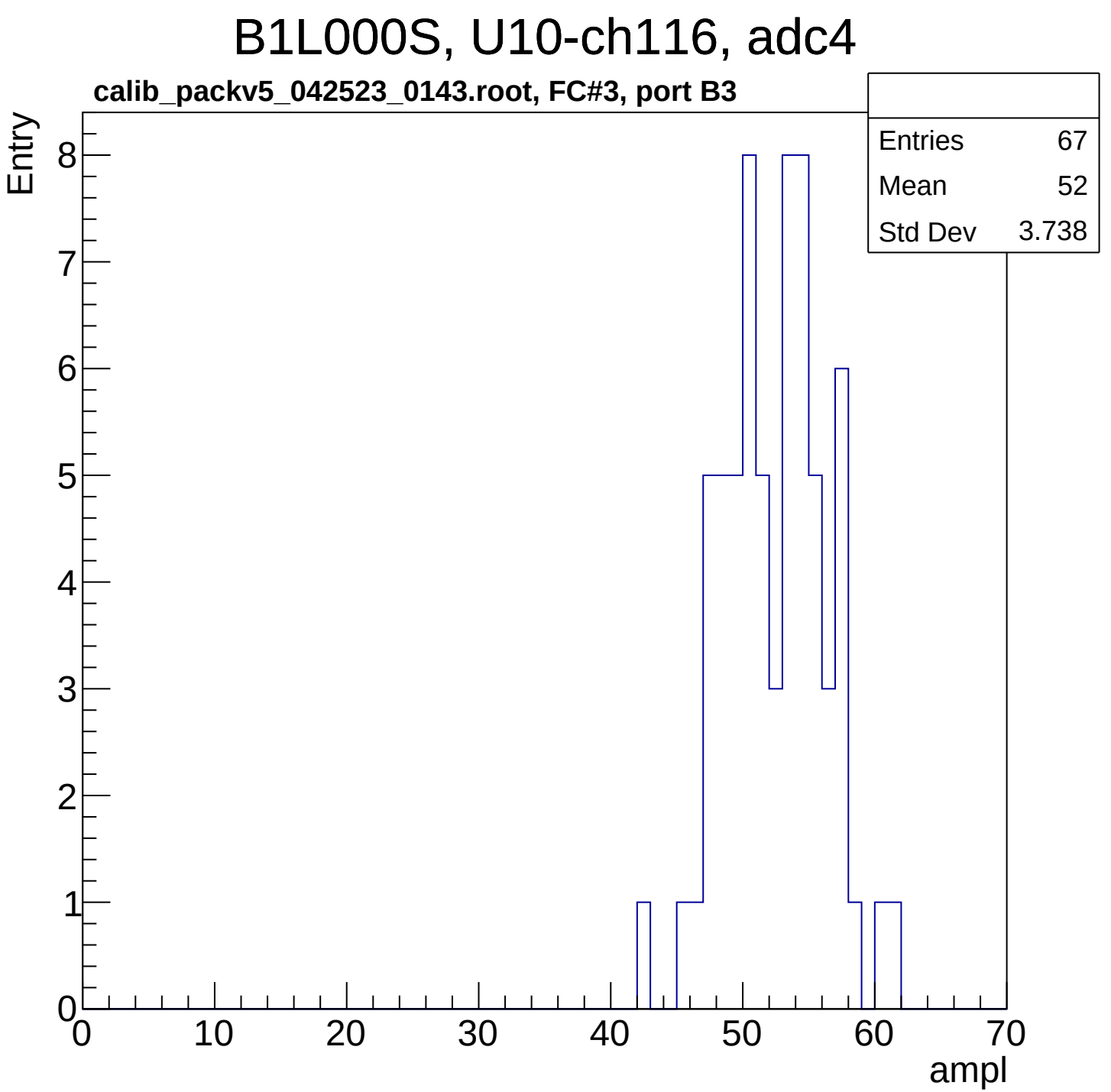

B1L000S, U10-ch116, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 46.8
Std Dev 3.43
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch116, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 52
Std Dev 3.738
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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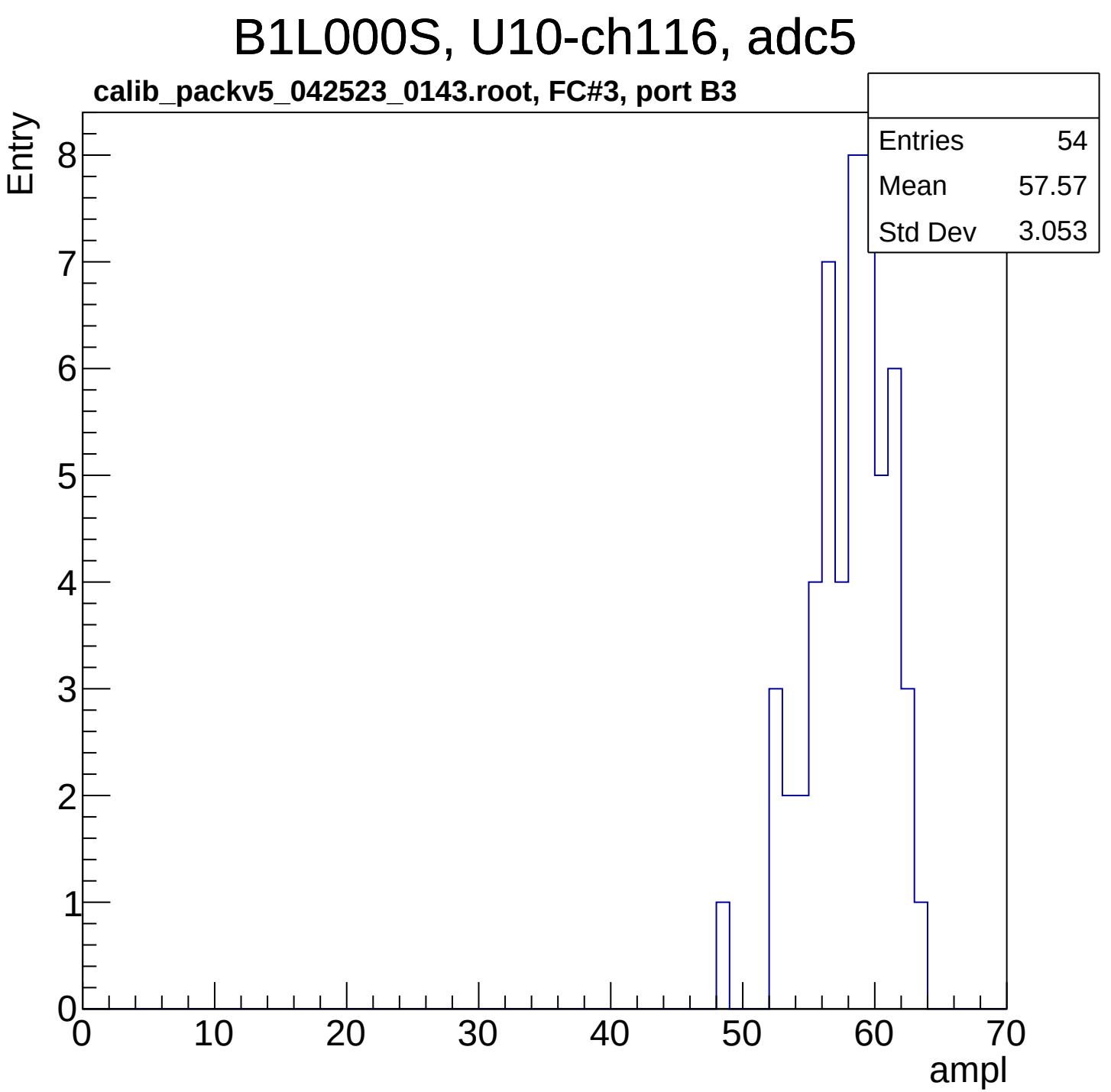
B1L000S, U10-ch116, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 57.57
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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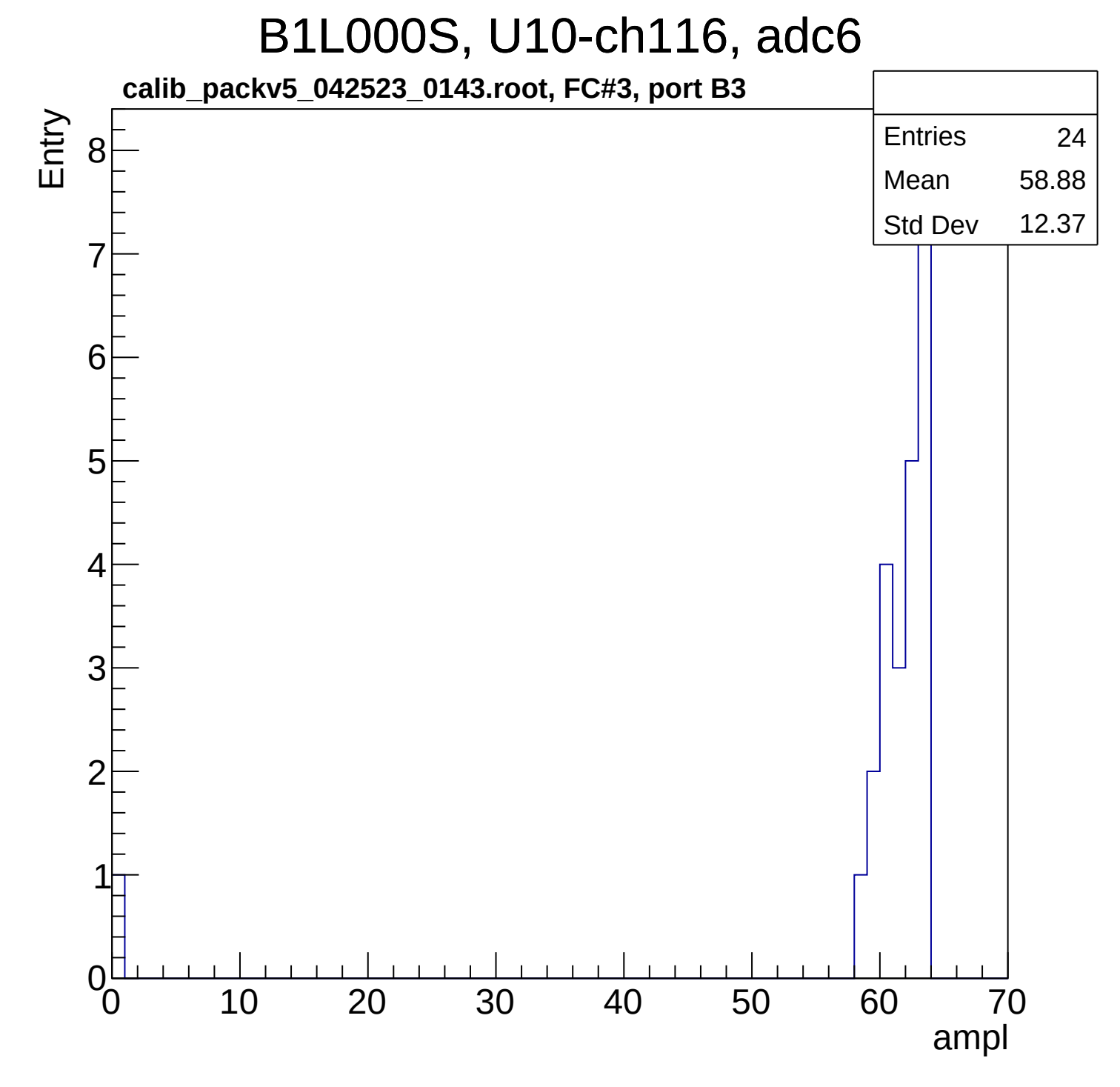
B1L000S, U10-ch116, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 58.88
Std Dev 12.37
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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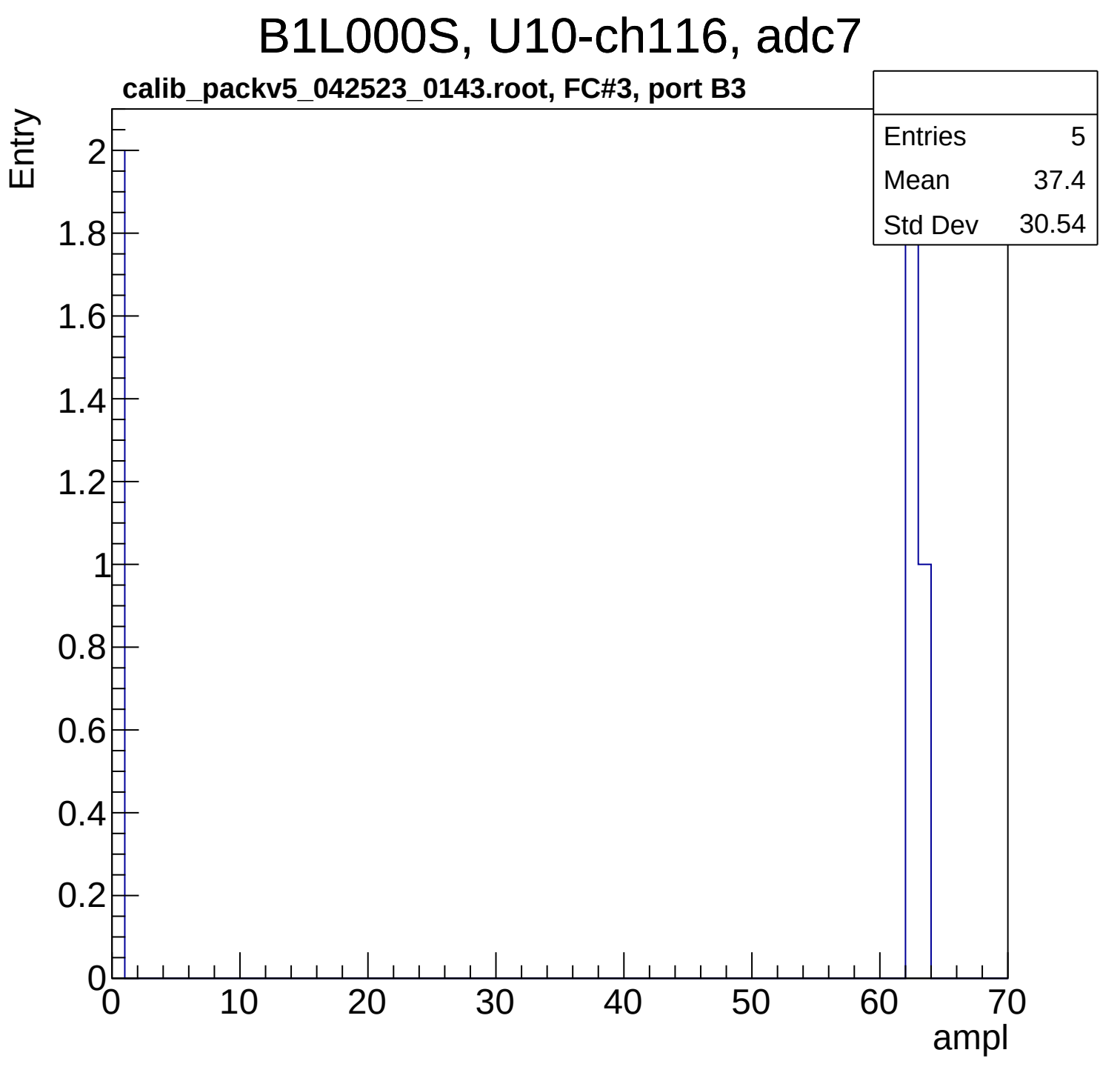
B1L000S, U10-ch116, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 37.4
Std Dev 30.54
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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30
40
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60
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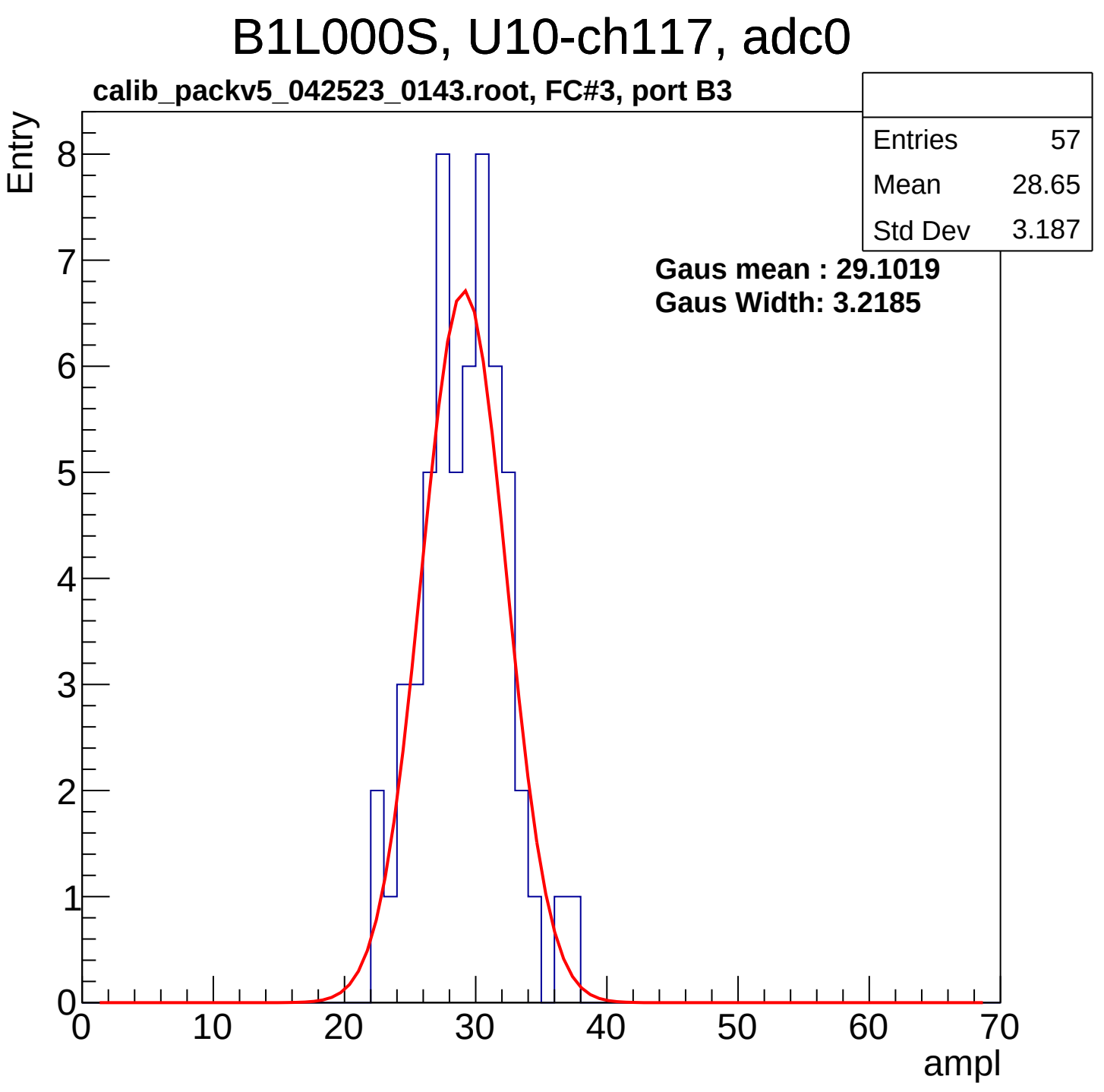
B1L000S, U10-ch117, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.65
Std Dev 3.187
Gaus mean : 29.1019
Gaus Width: 3.2185
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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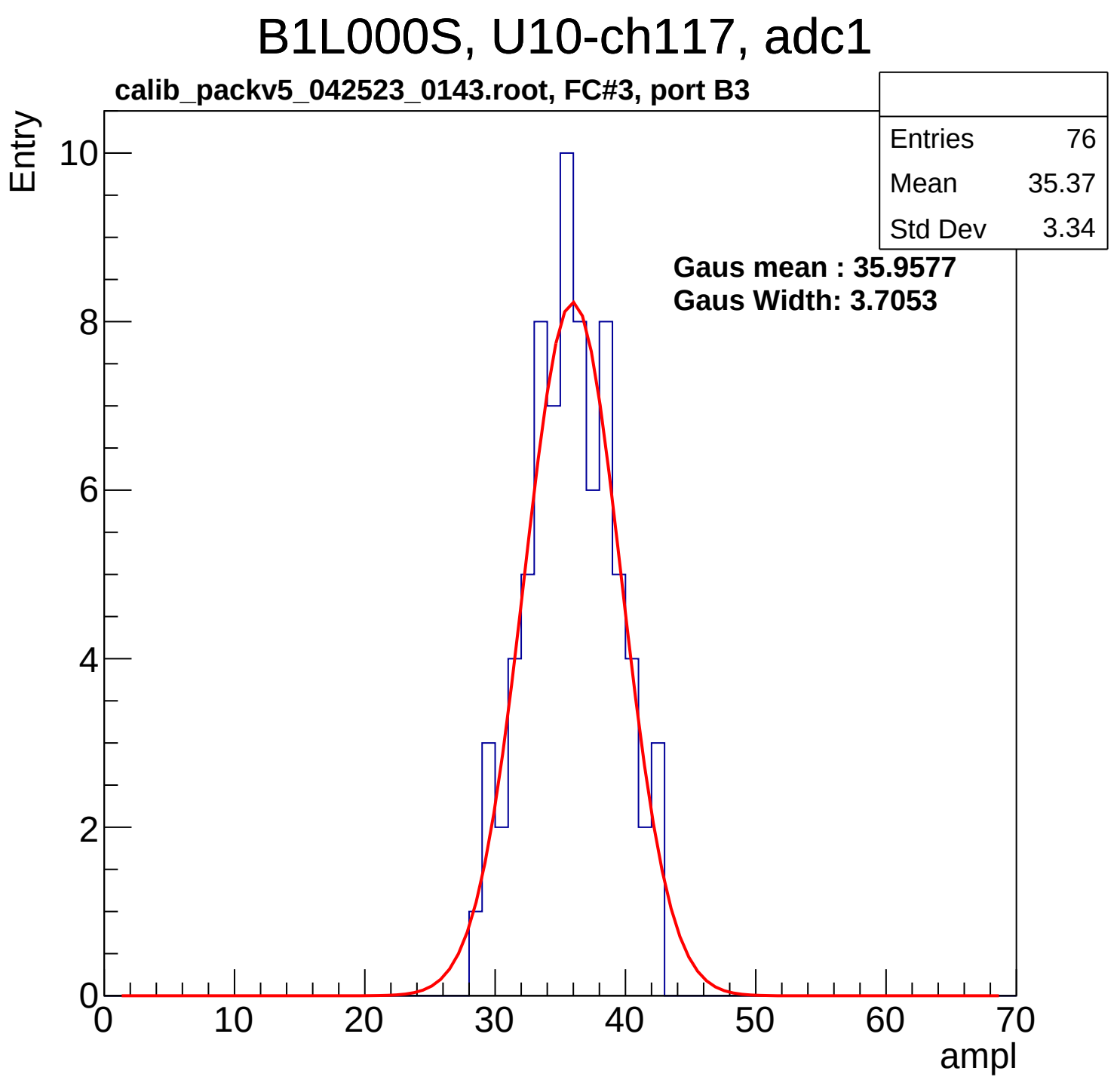
B1L000S, U10-ch117, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 35.37
Std Dev 3.34
Gaus mean : 35.9577
Gaus Width: 3.7053
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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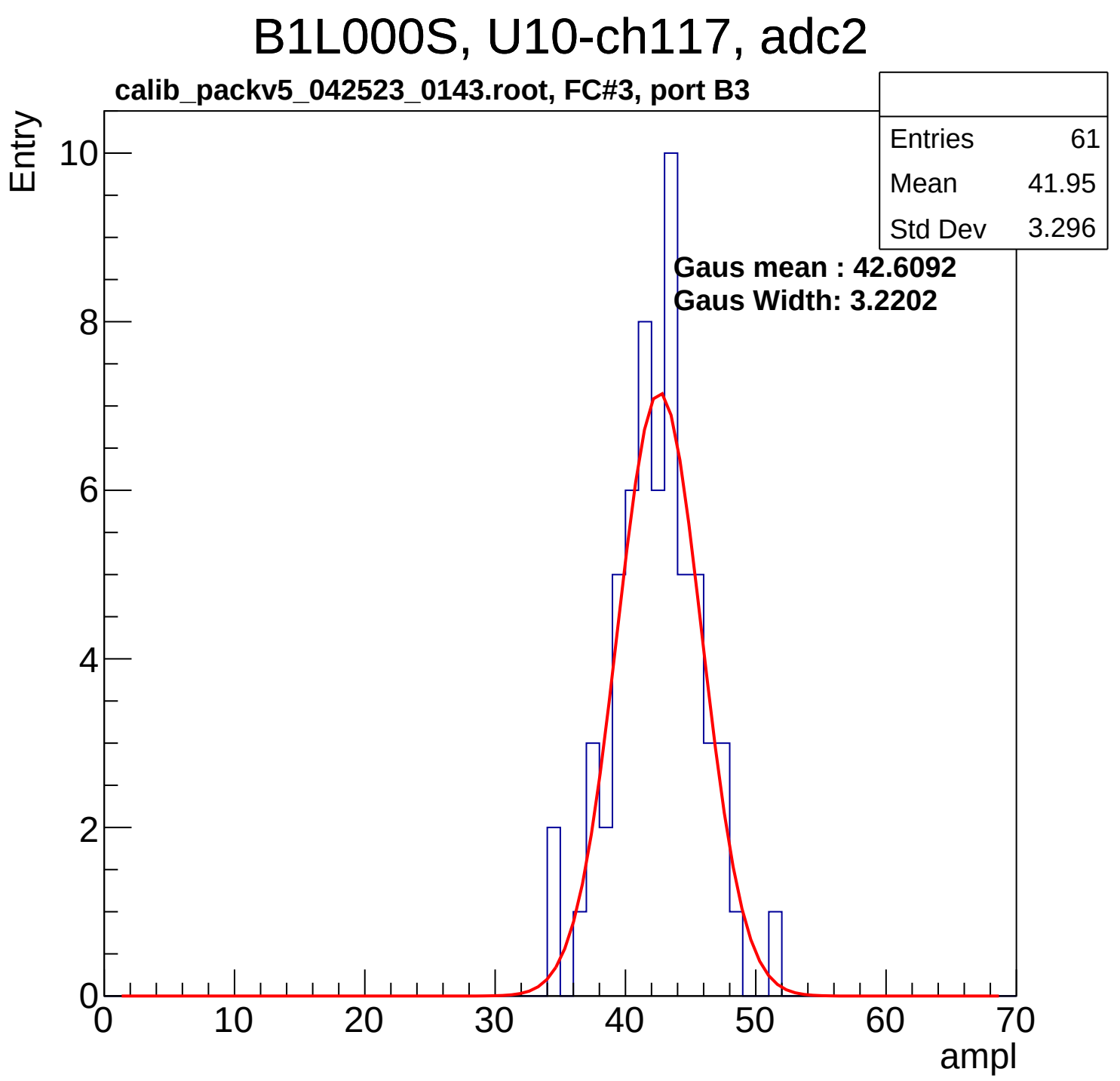
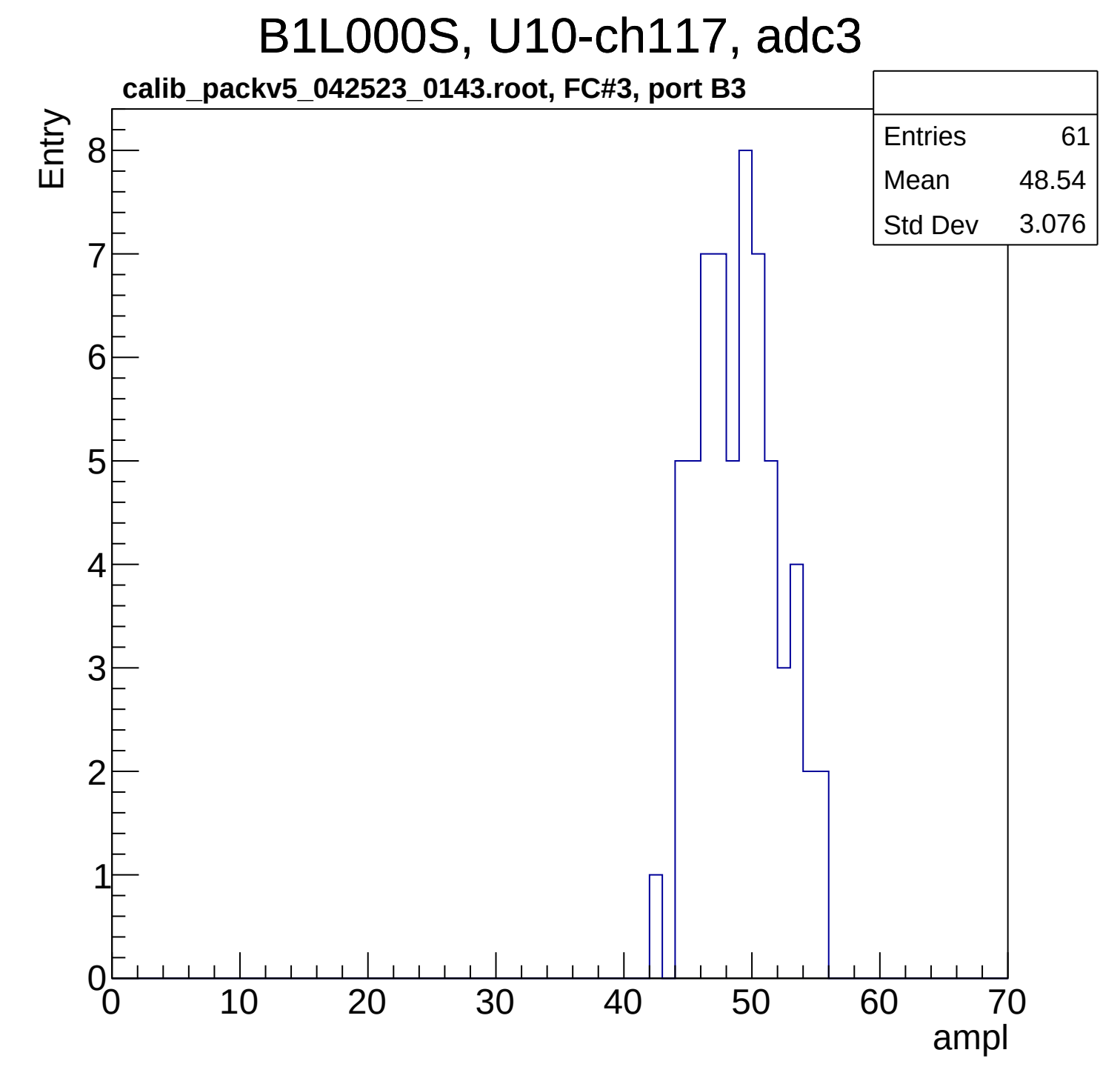
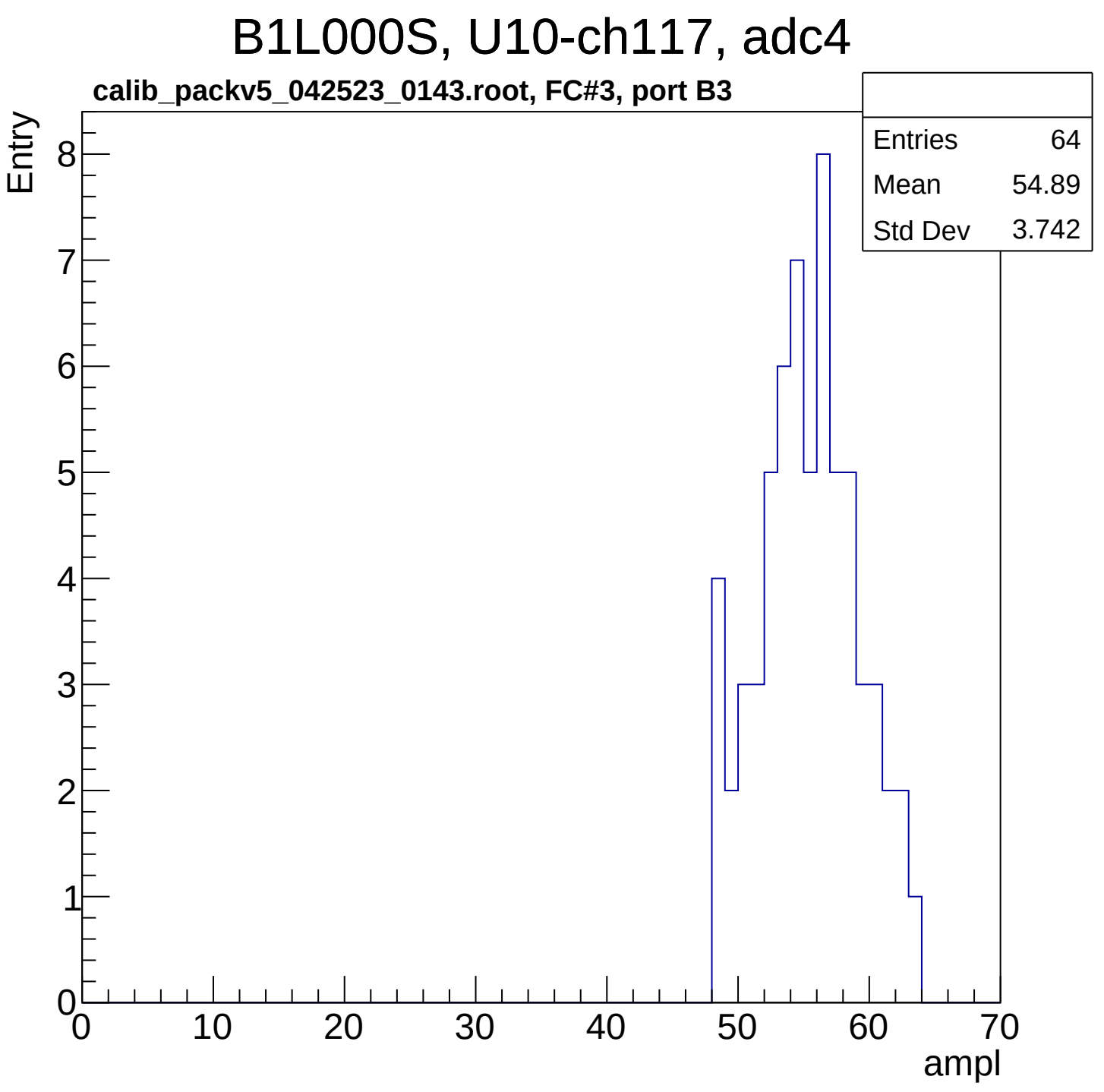
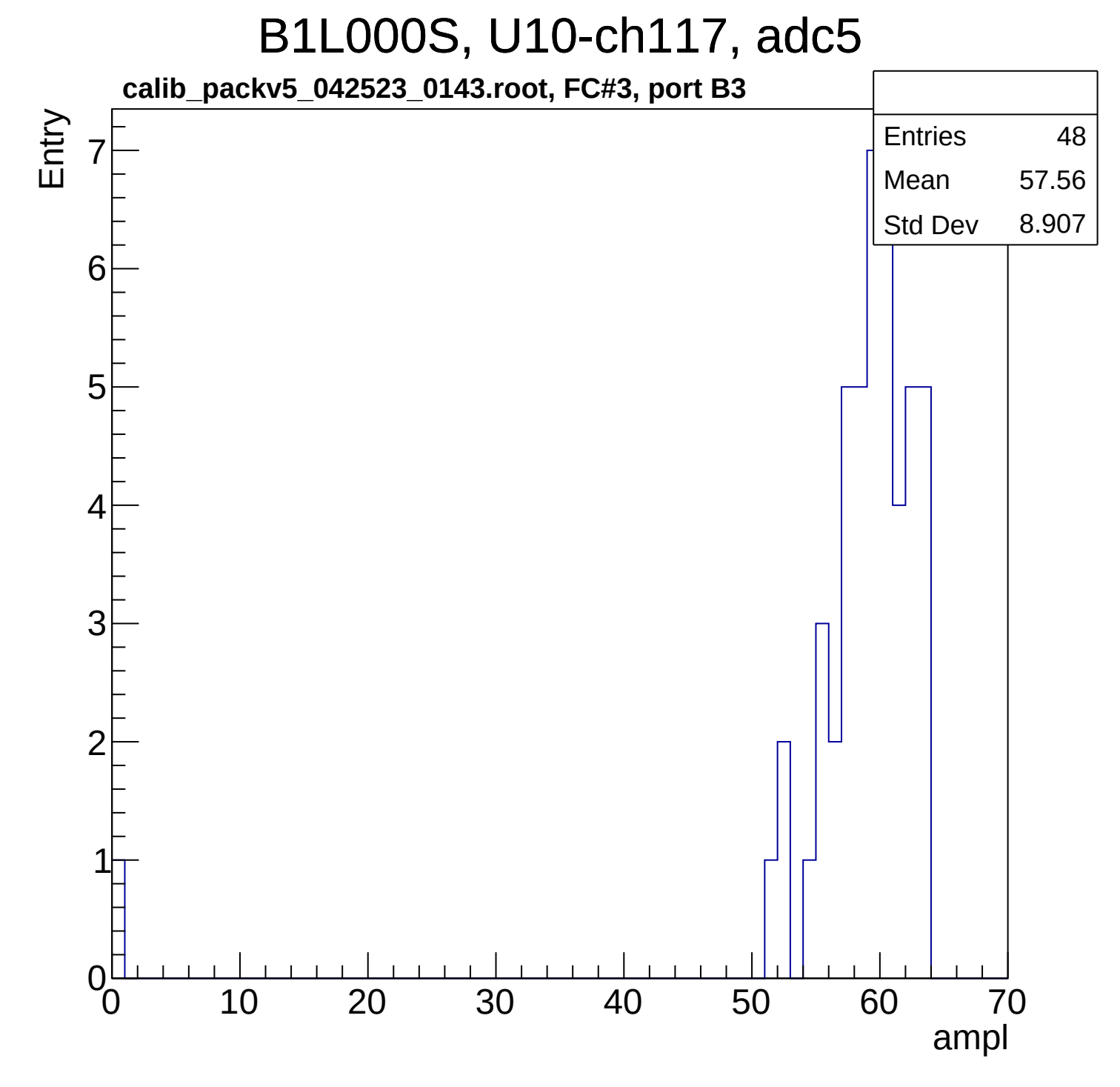
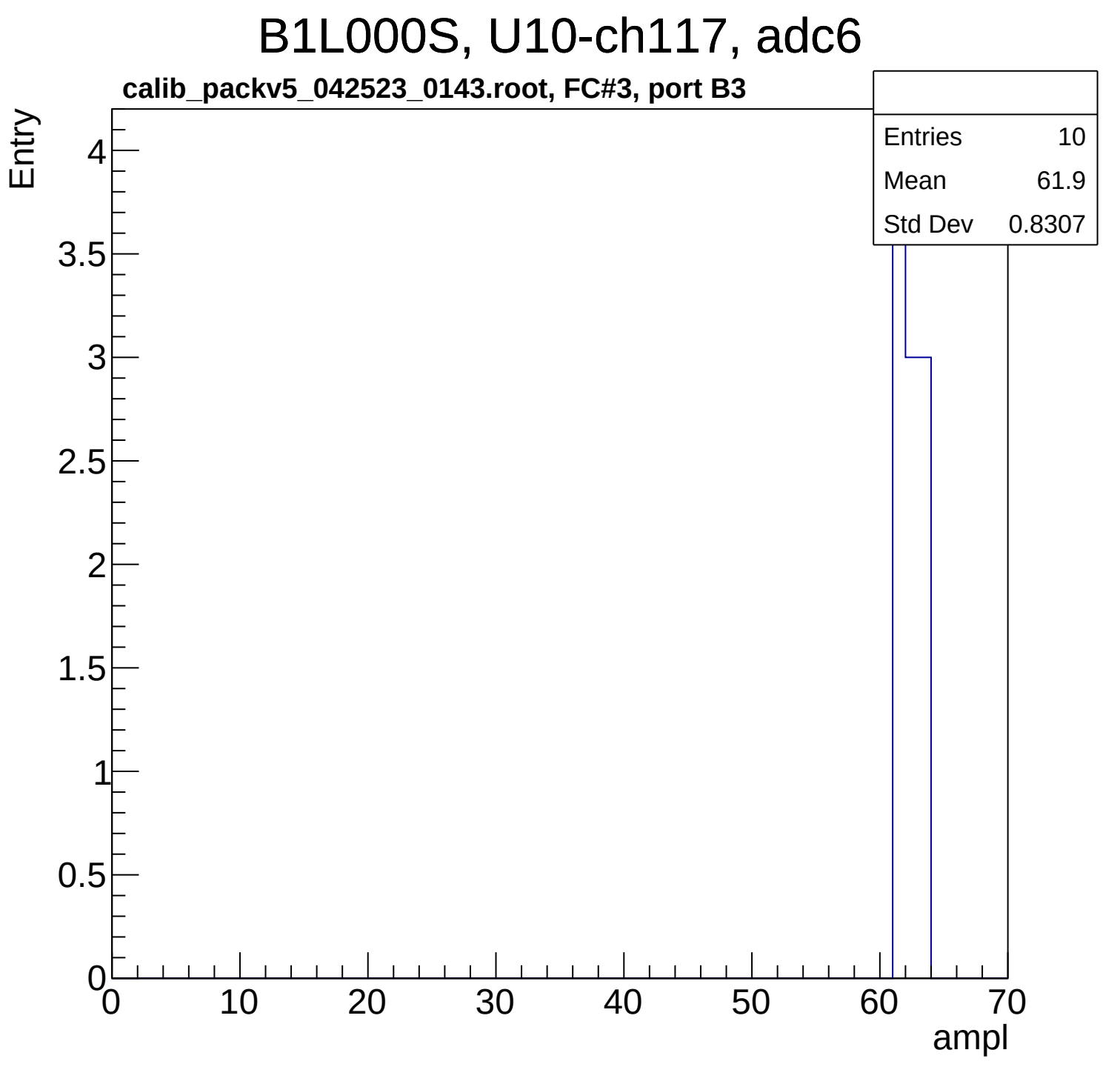
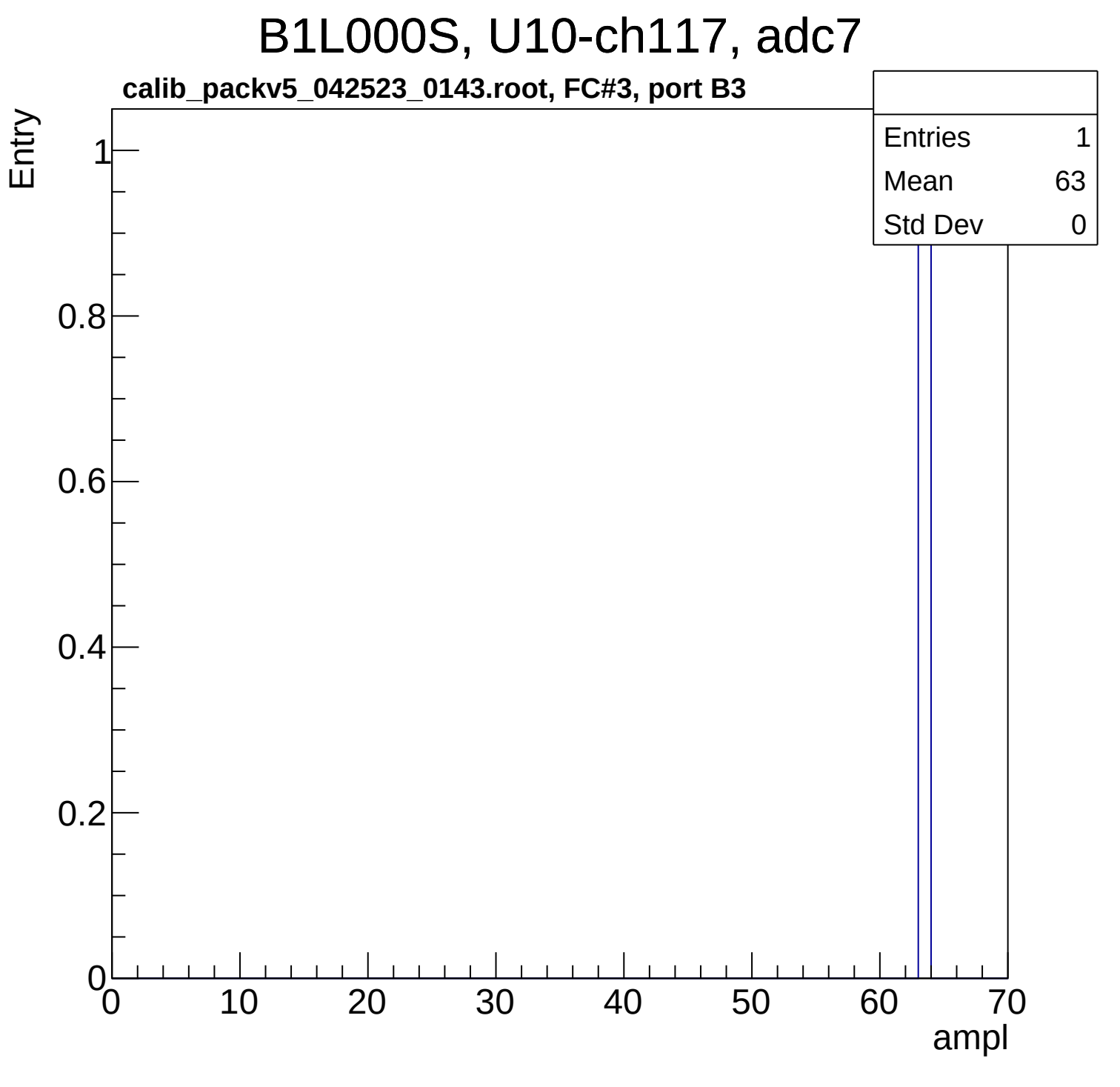

B1L000S, U10-ch117, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 41.95
Std Dev 3.296
Gaus mean : 42.6092
Gaus Width: 3.2202
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

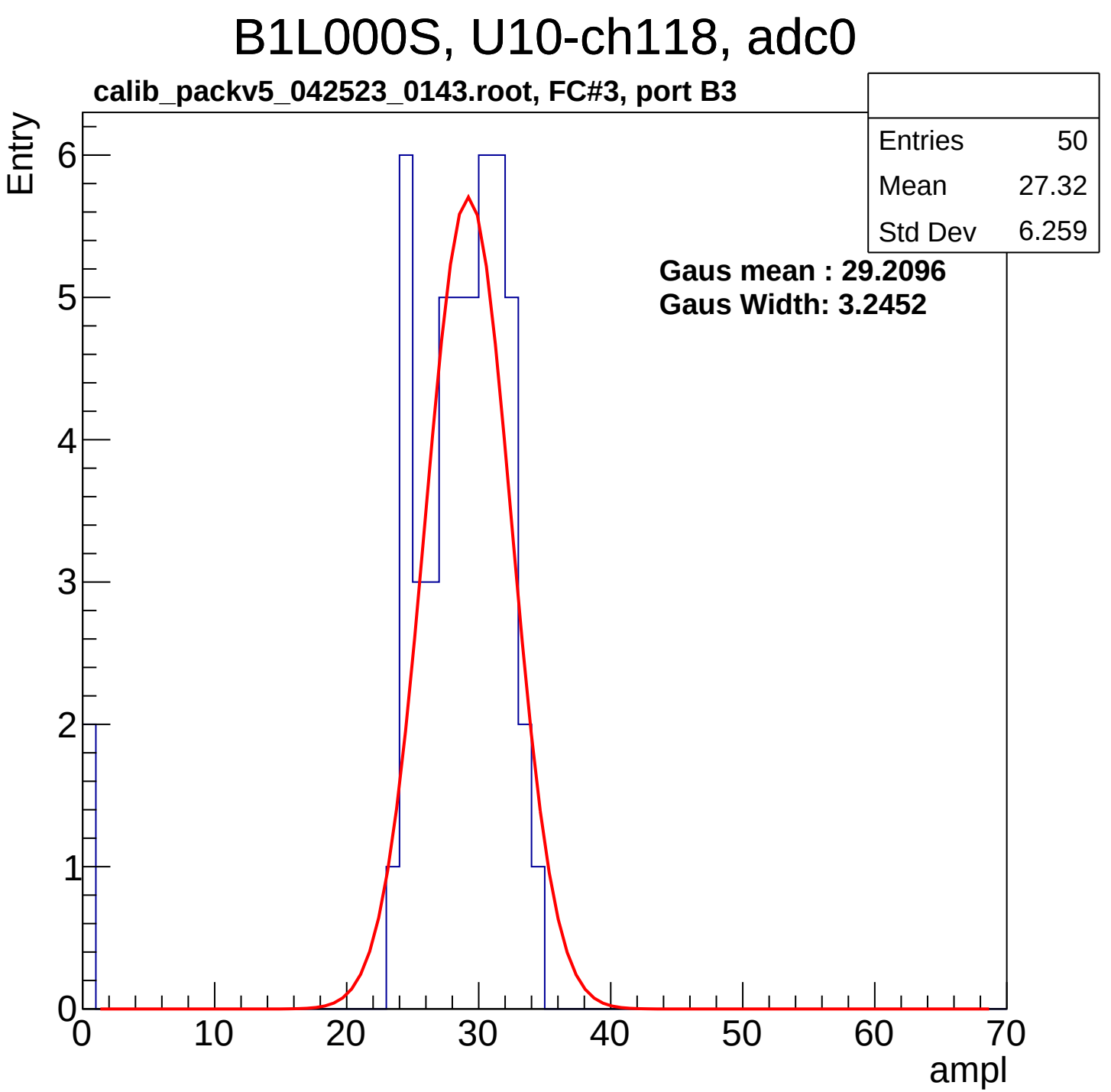

B1L000S, U10-ch117, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 48.54
Std Dev 3.076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

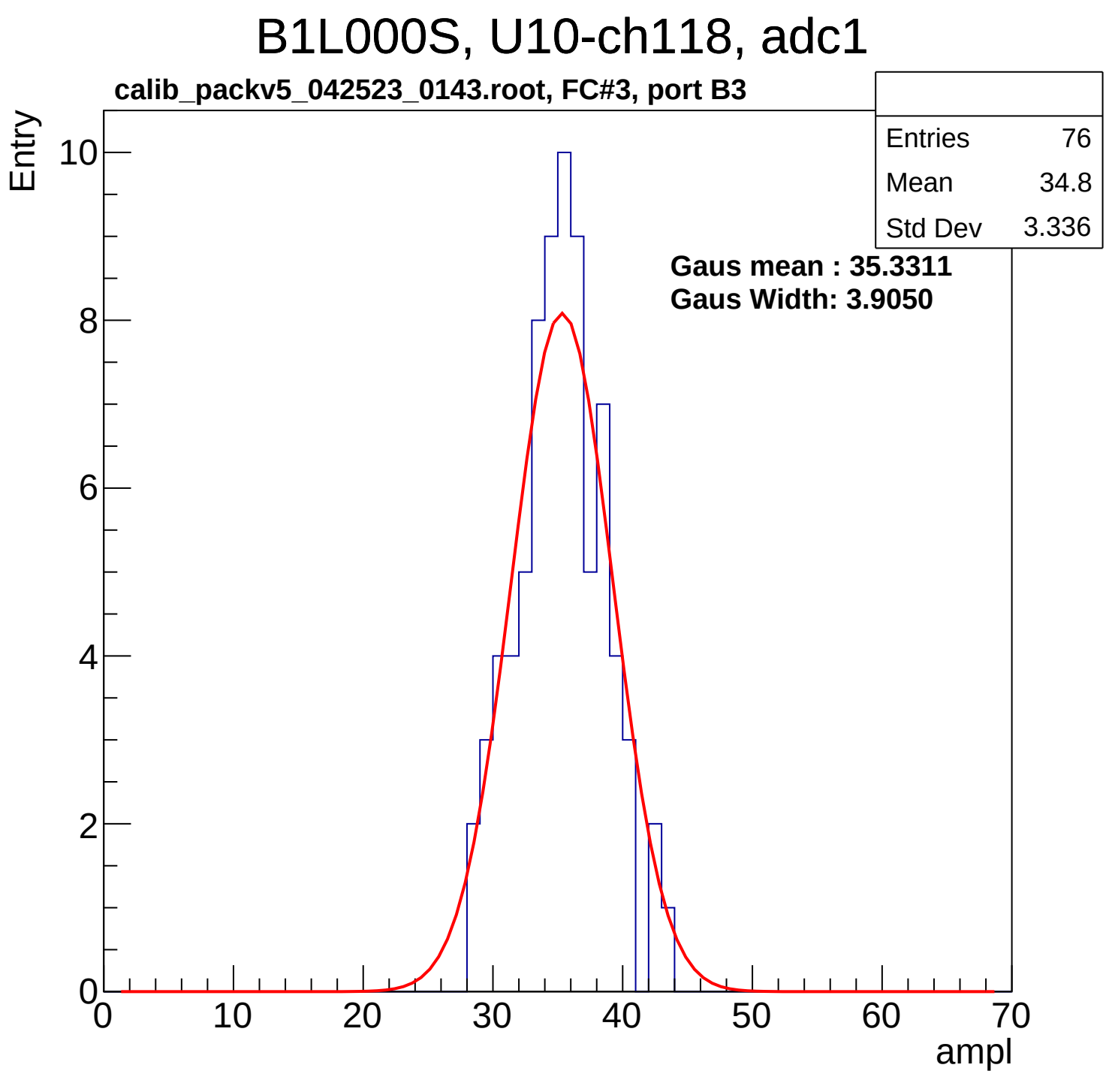

B1L000S, U10-ch117, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 54.89
Std Dev 3.742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

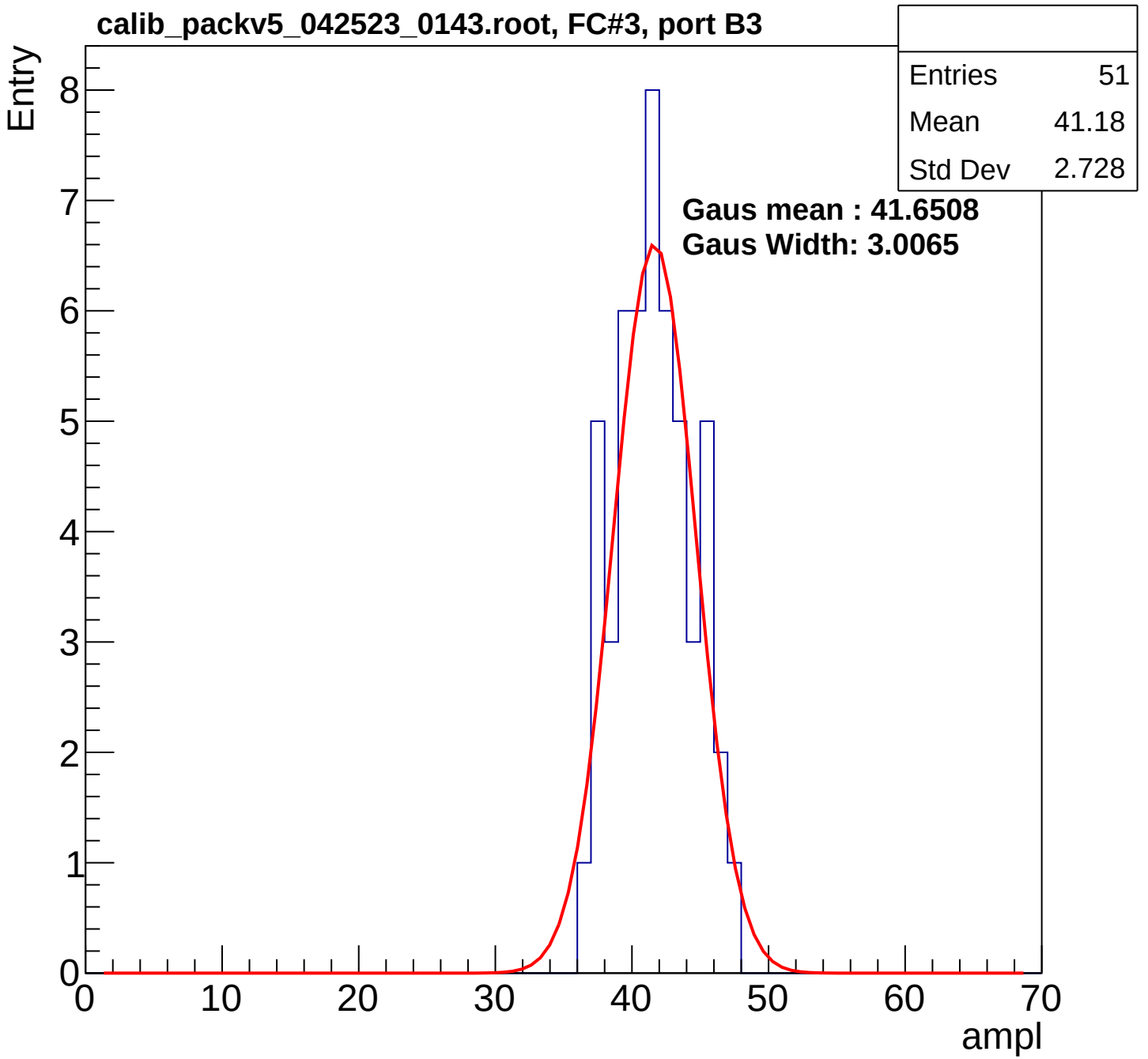

B1L000S, U10-ch117, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 57.56
Std Dev 8.907
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch117, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.9
Std Dev 0.8307
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U10-ch117, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch118, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 27.32
Std Dev 6.259
Gaus mean : 29.2096
Gaus Width: 3.2452
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U10-ch118, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 34.8
Std Dev 3.336
Gaus mean : 35.3311
Gaus Width: 3.9050
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch118, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 41.18
Std Dev 2.728
Gaus mean : 41.6508
Gaus Width: 3.0065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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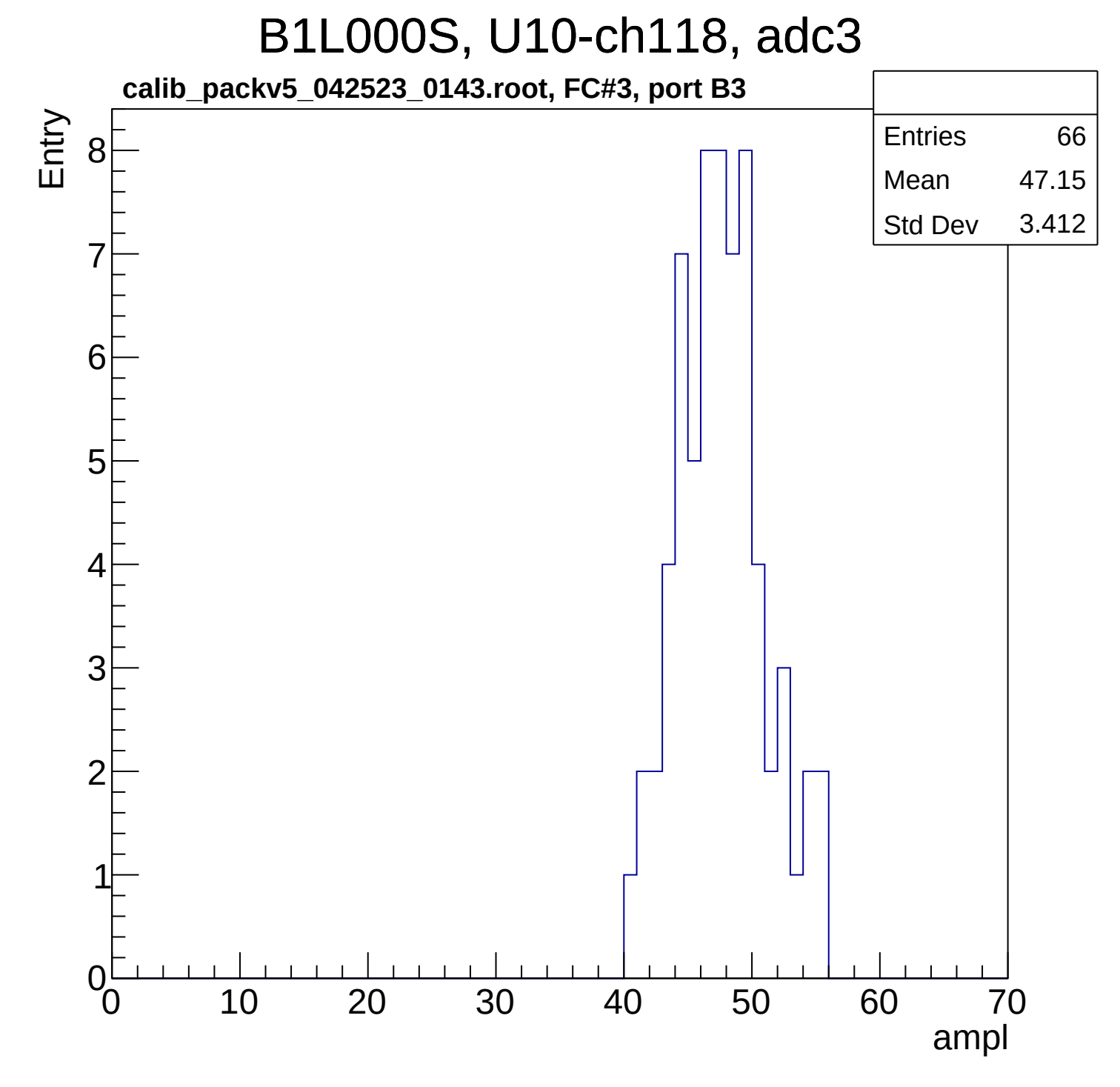
40
50
60
70


B1L000S, U10-ch118, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.15
Std Dev 3.412
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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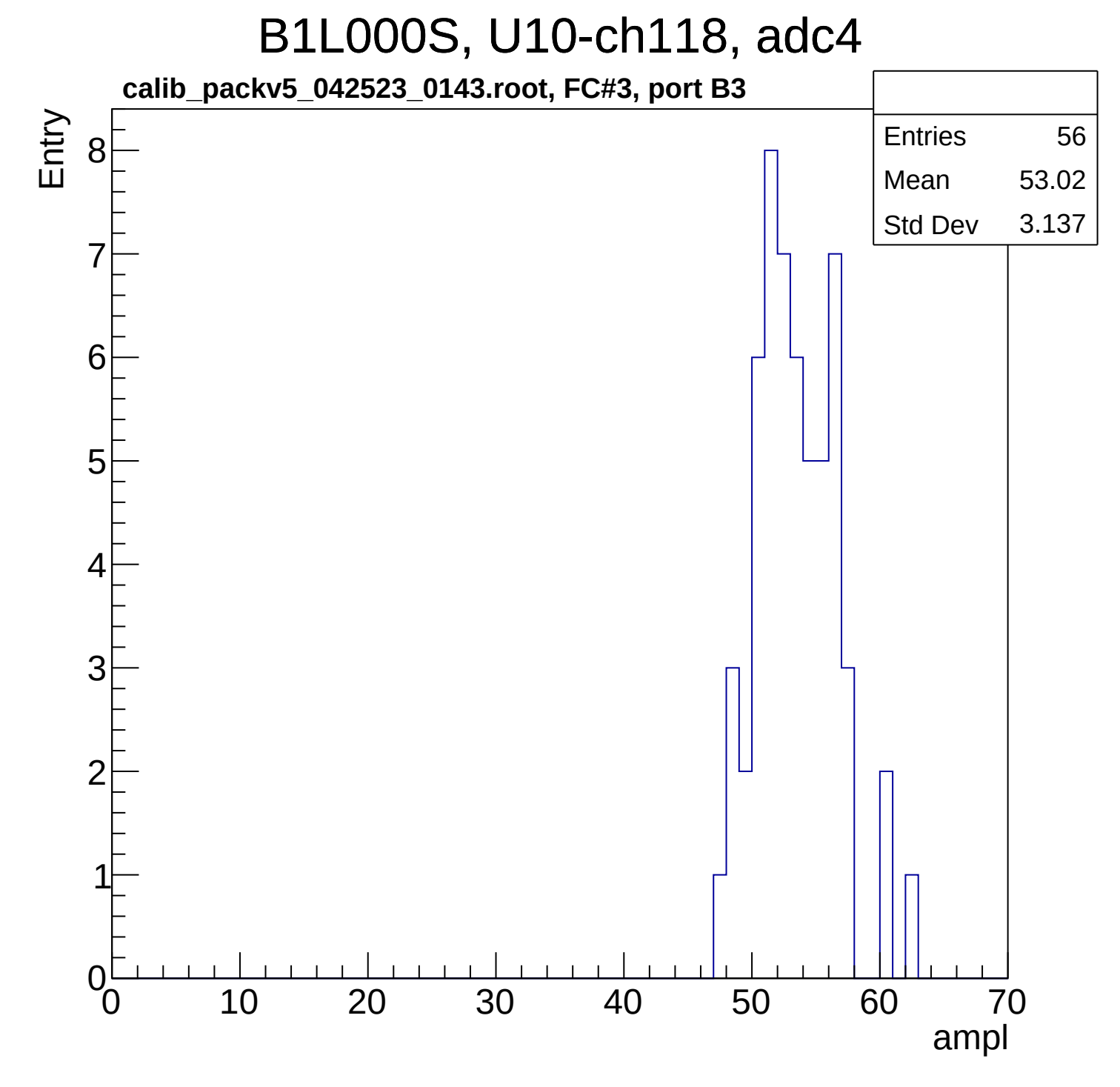
40
50
60
70


B1L000S, U10-ch118, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.02
Std Dev 3.137
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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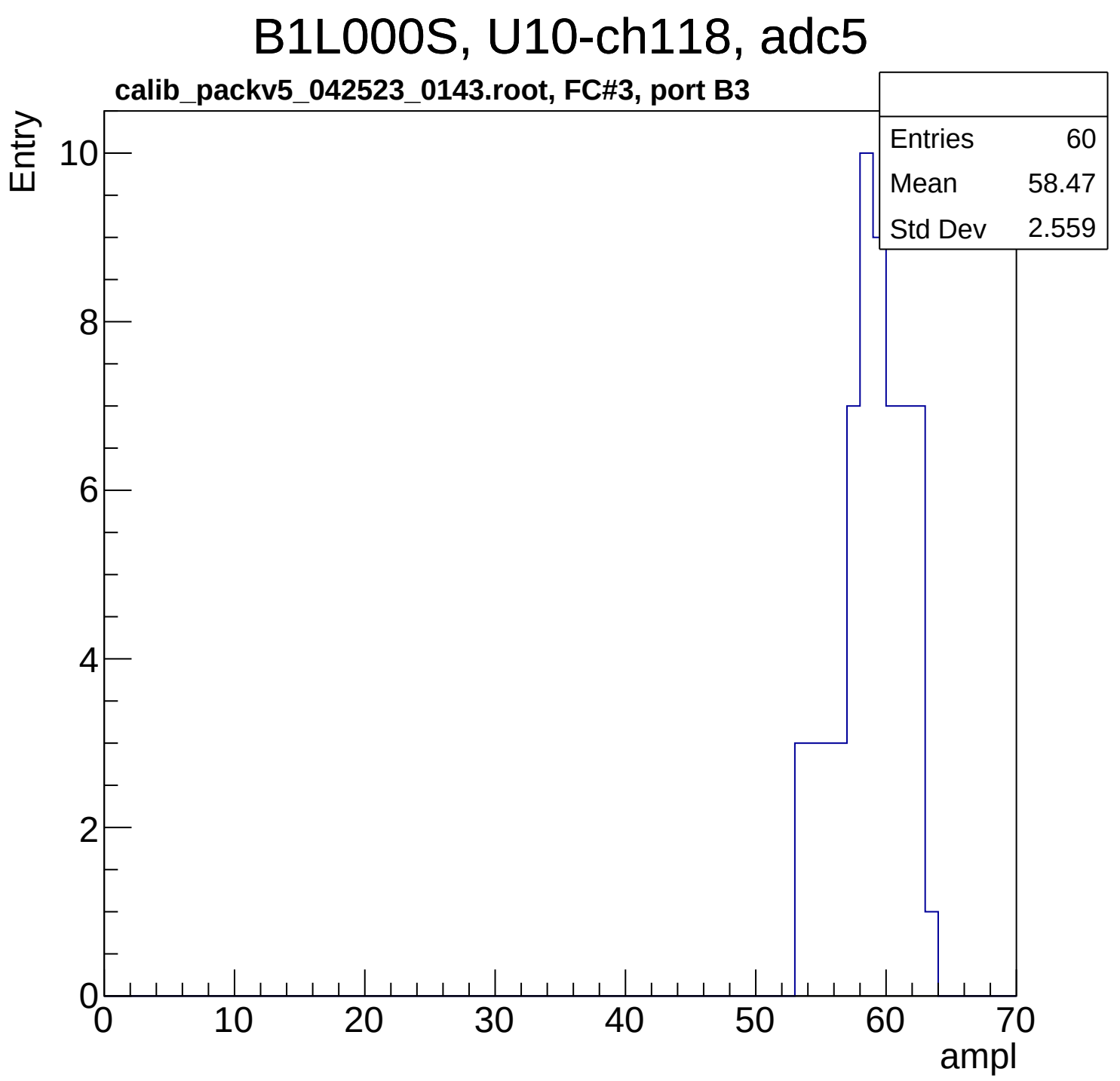
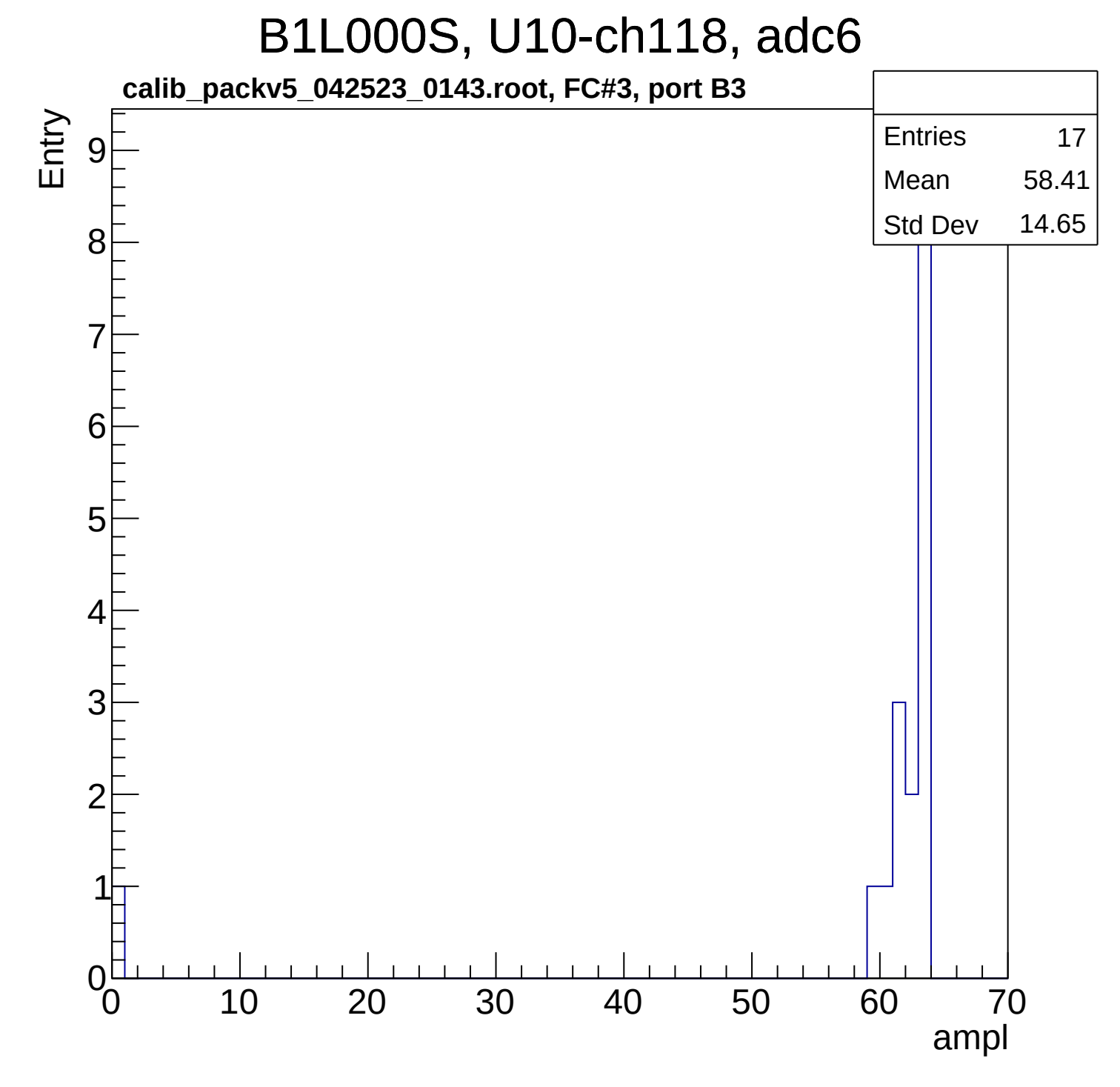

B1L000S, U10-ch118, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.47
Std Dev 2.559
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch118, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 58.41
Std Dev 14.65
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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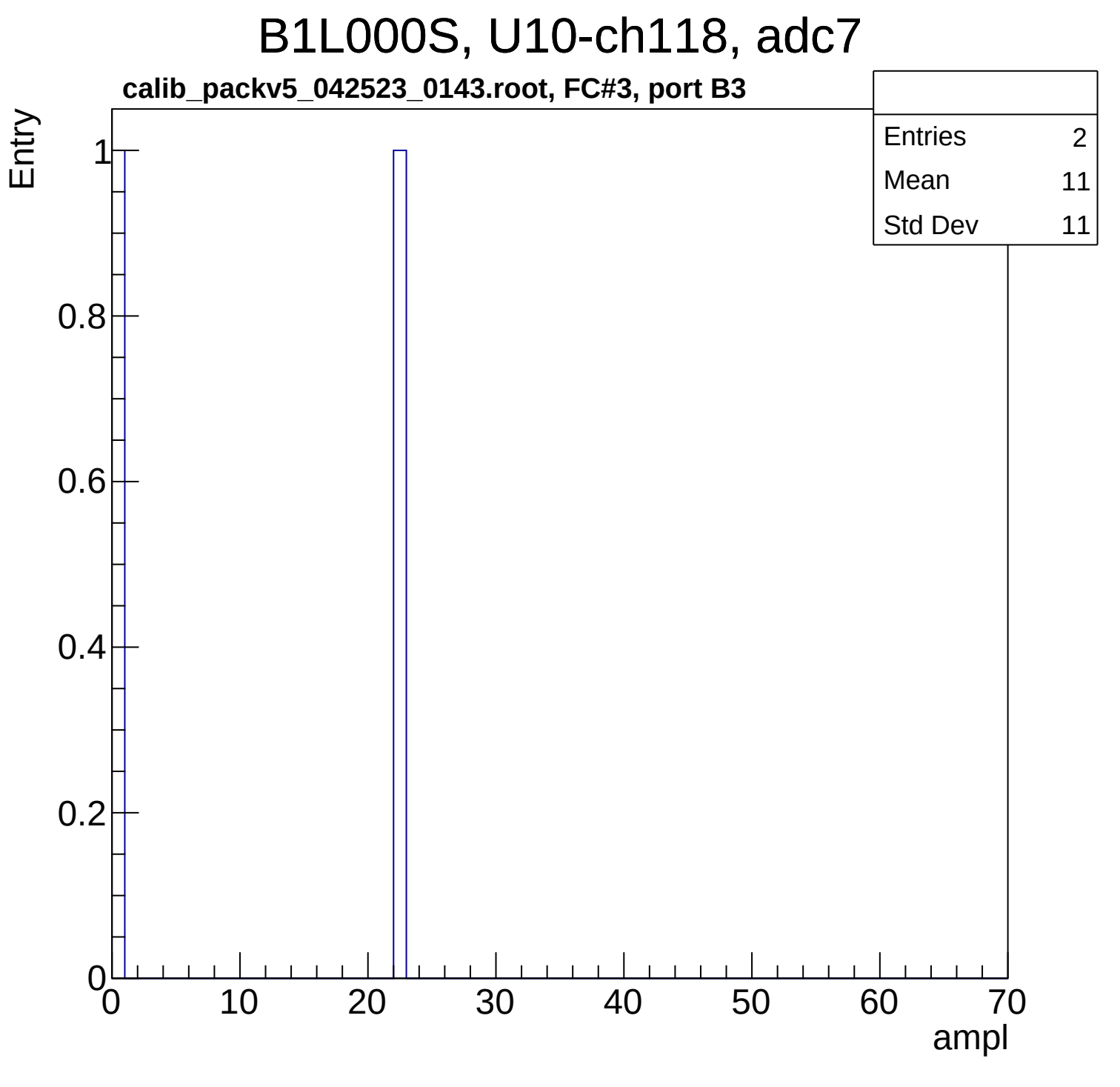
40
50
60
70


B1L000S, U10-ch118, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
0
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10
20
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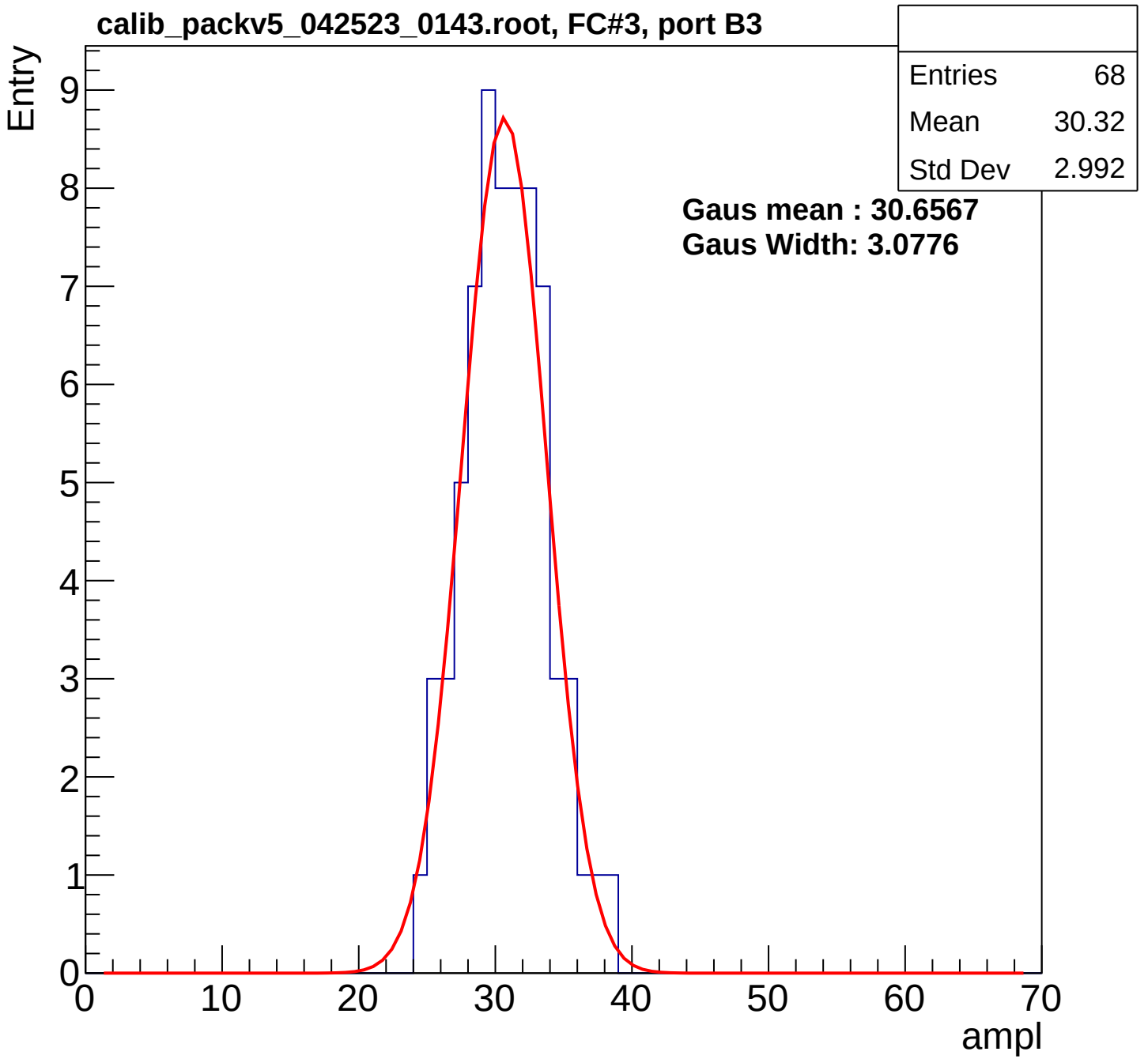
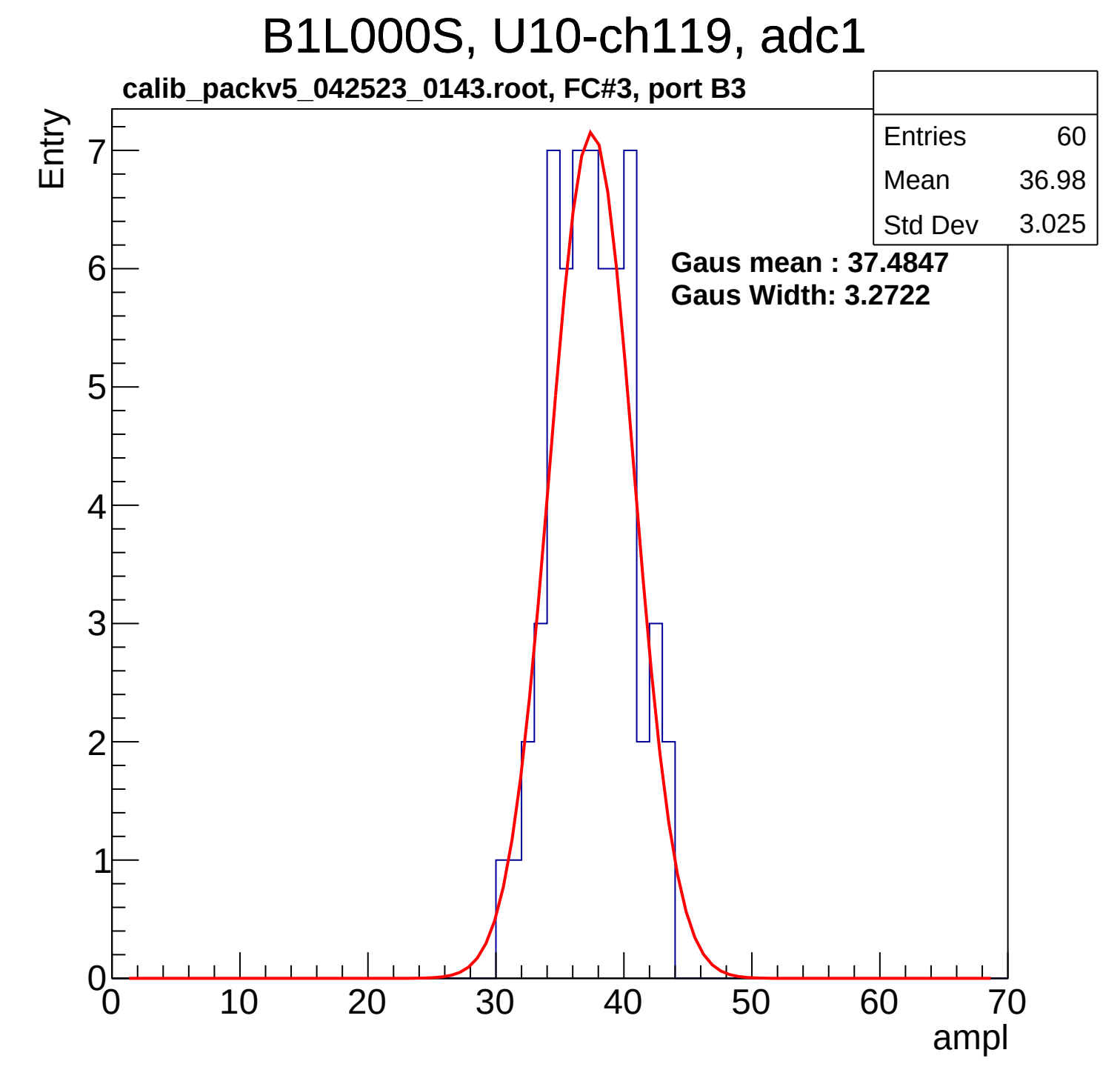
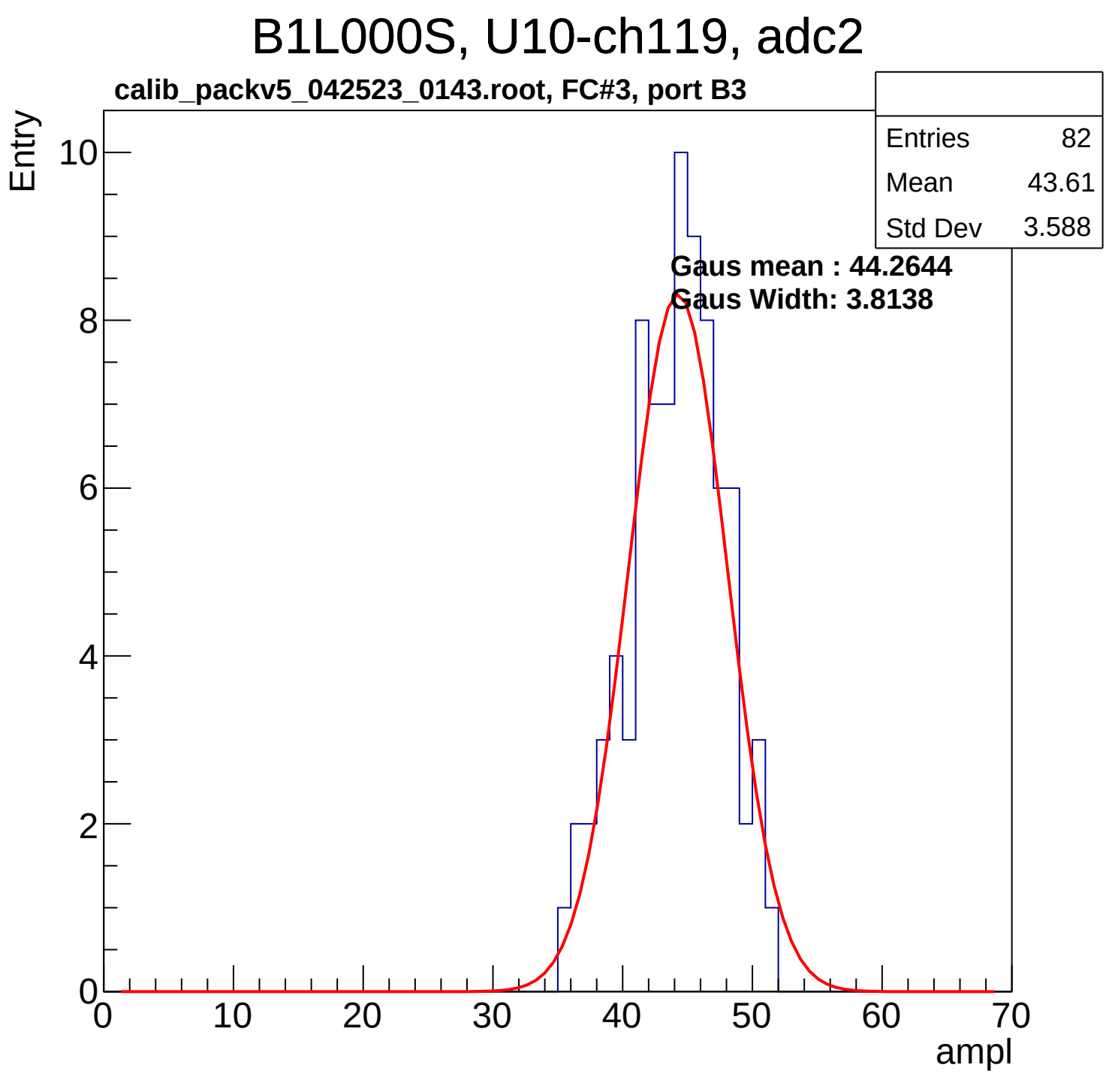
50
60
70
ampl


B1L000S, U10-ch119, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 30.32
Std Dev 2.992
Gaus mean : 30.6567
Gaus Width: 3.0776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch119, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 36.98
Std Dev 3.025
Gaus mean : 37.4847
Gaus Width: 3.2722
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch119, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 43.61
Std Dev 3.588
Gaus mean : 44.2644
Gaus Width: 3.8138
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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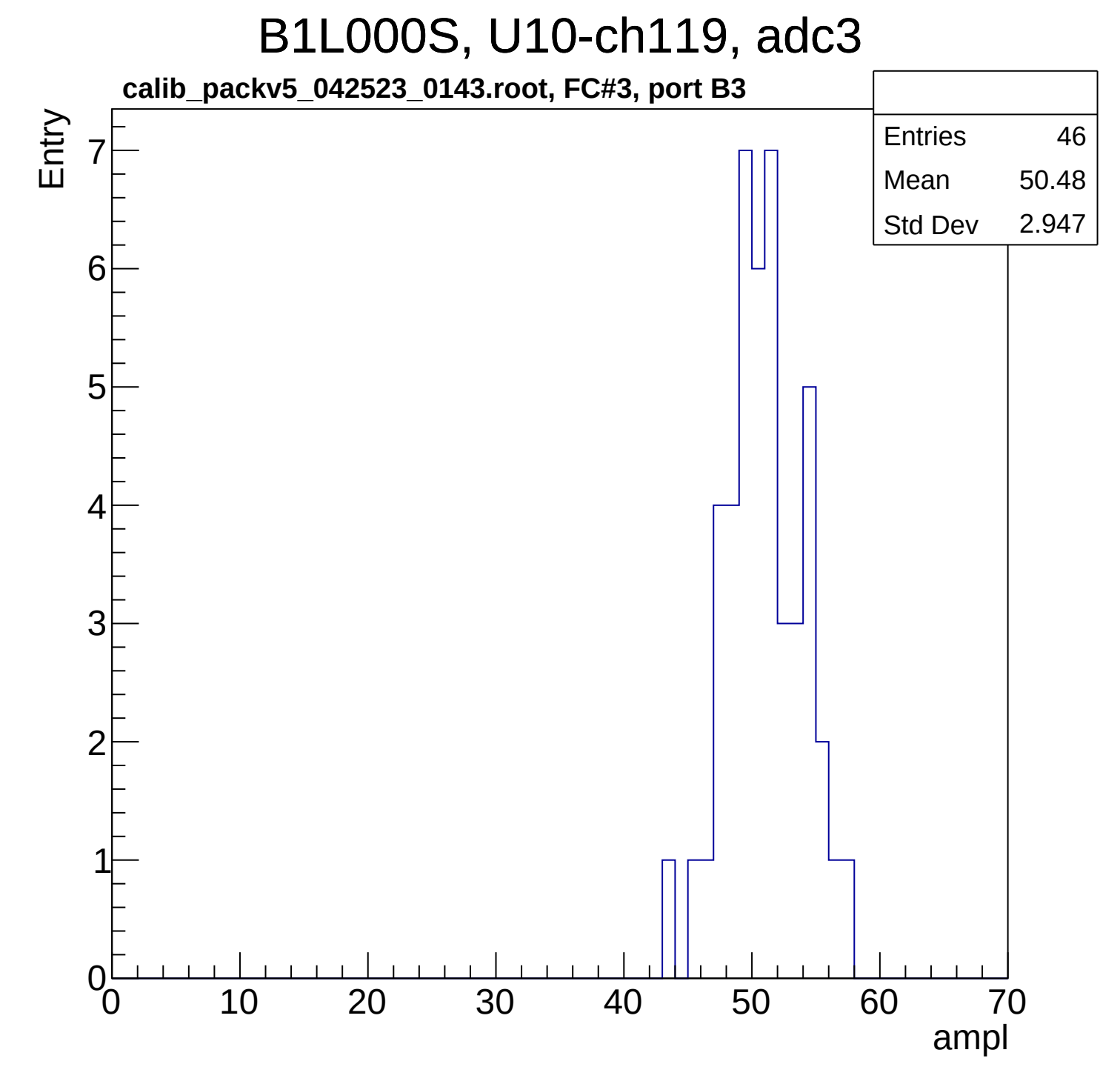
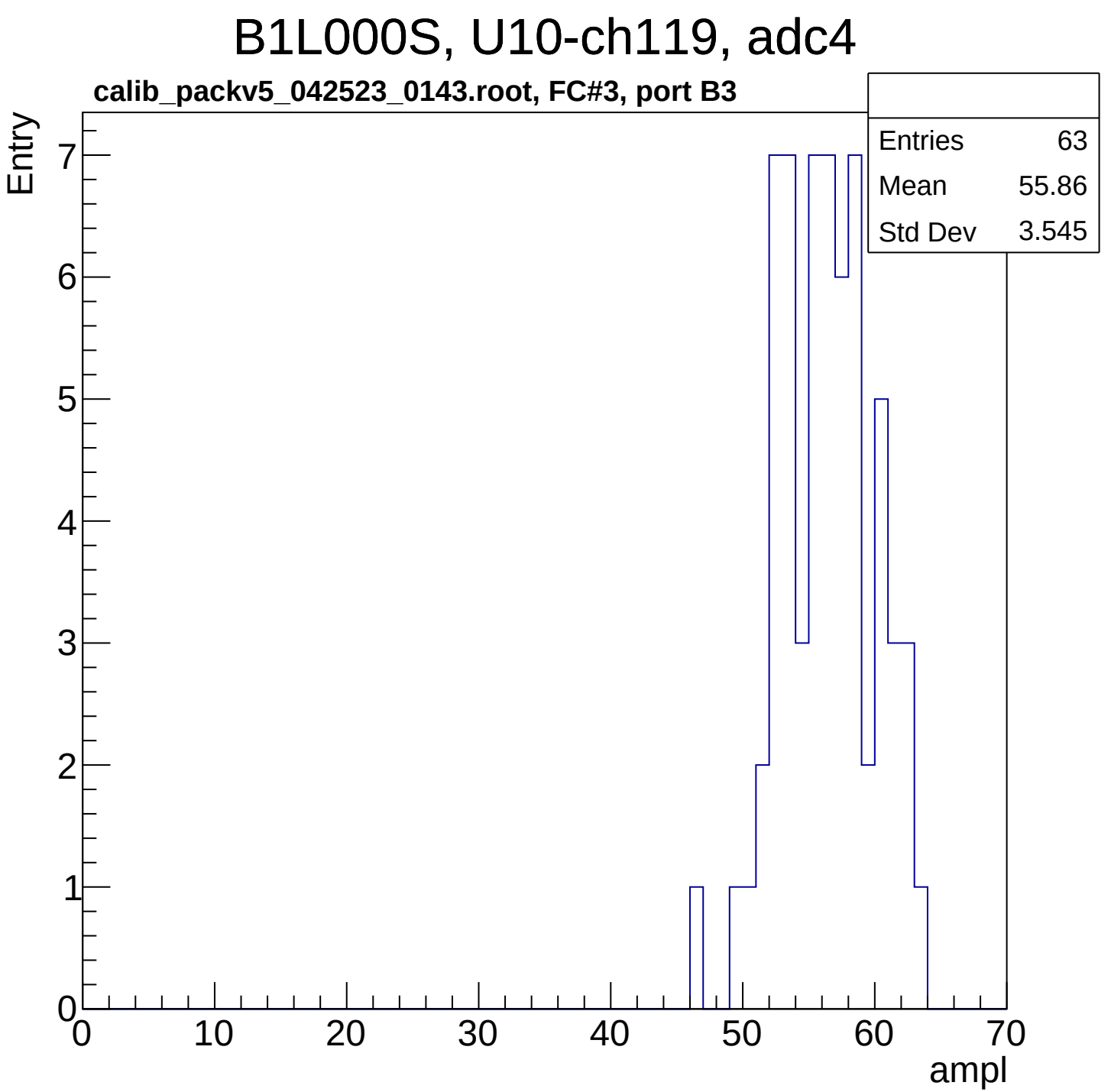
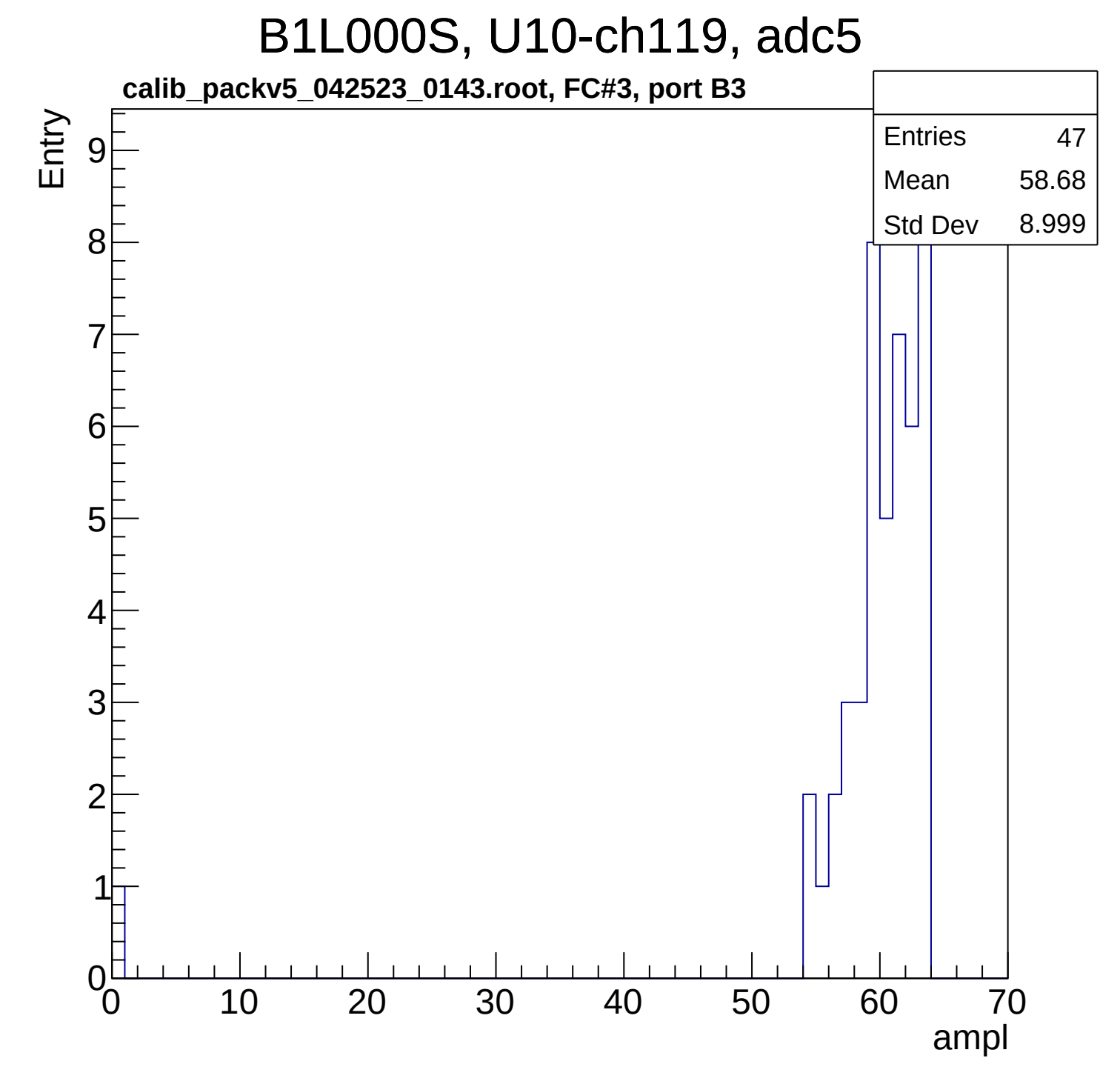
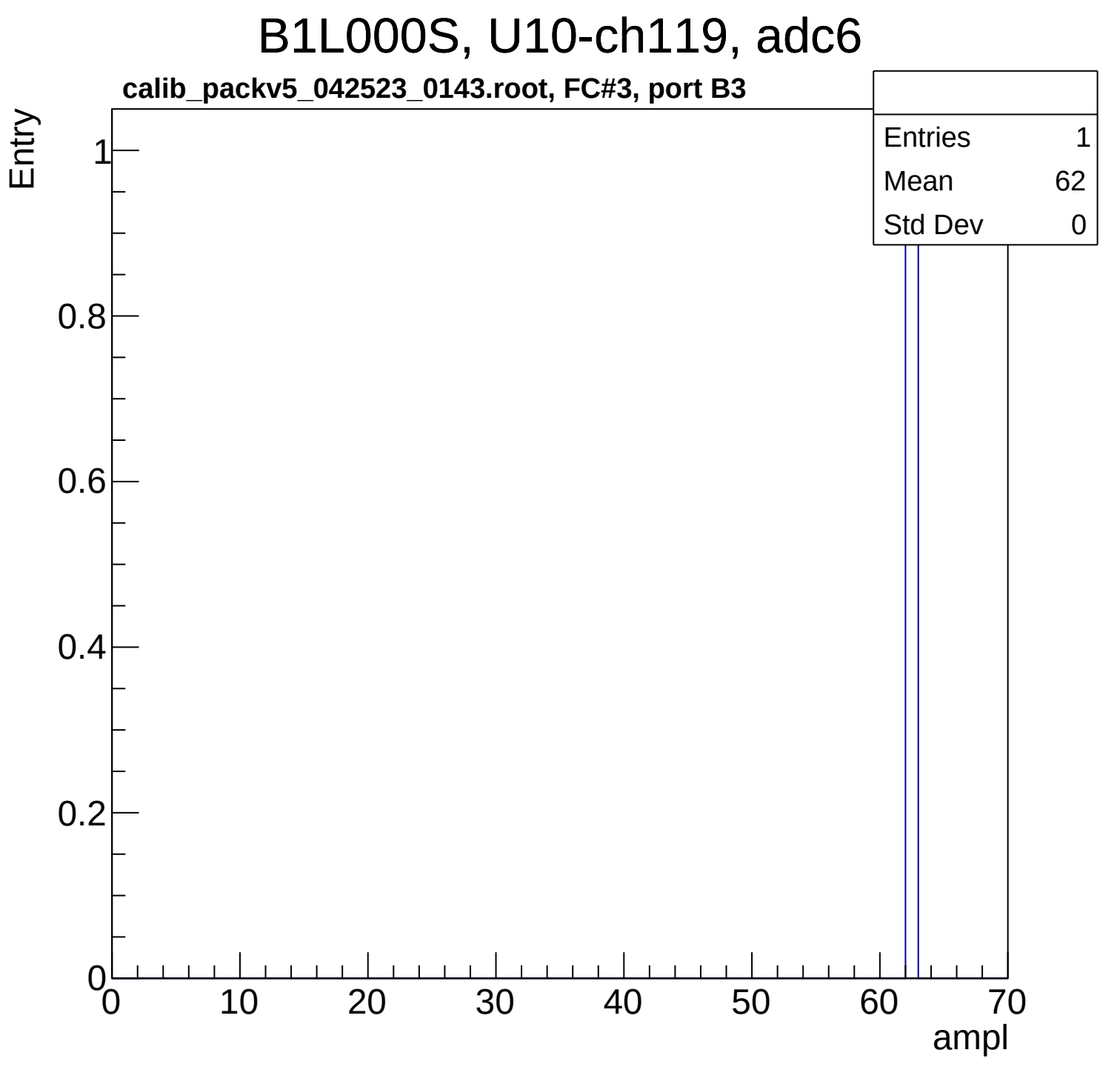
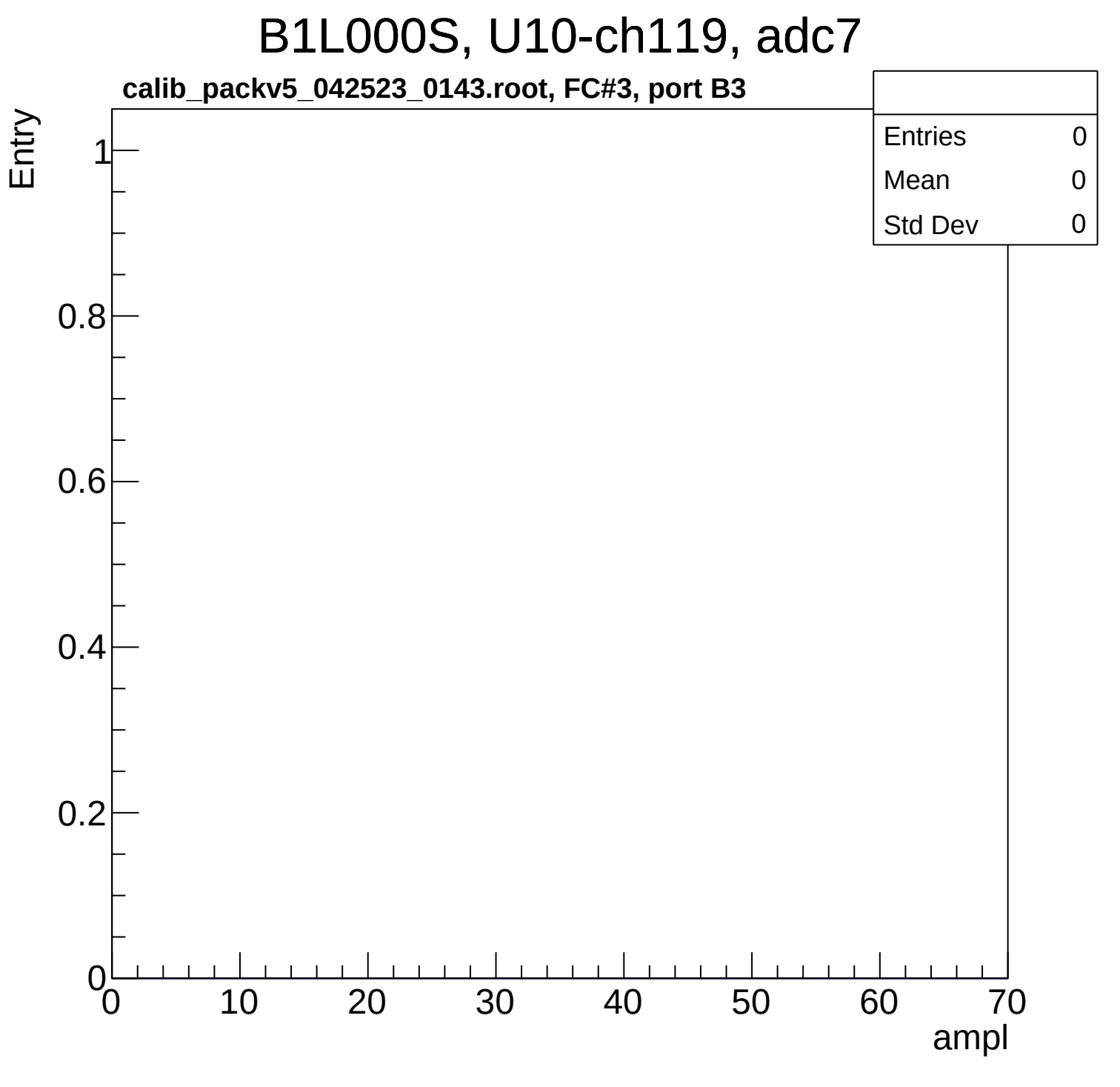

B1L000S, U10-ch119, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 50.48
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch119, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 55.86
Std Dev 3.545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch119, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 58.68
Std Dev 8.999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch119, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch119, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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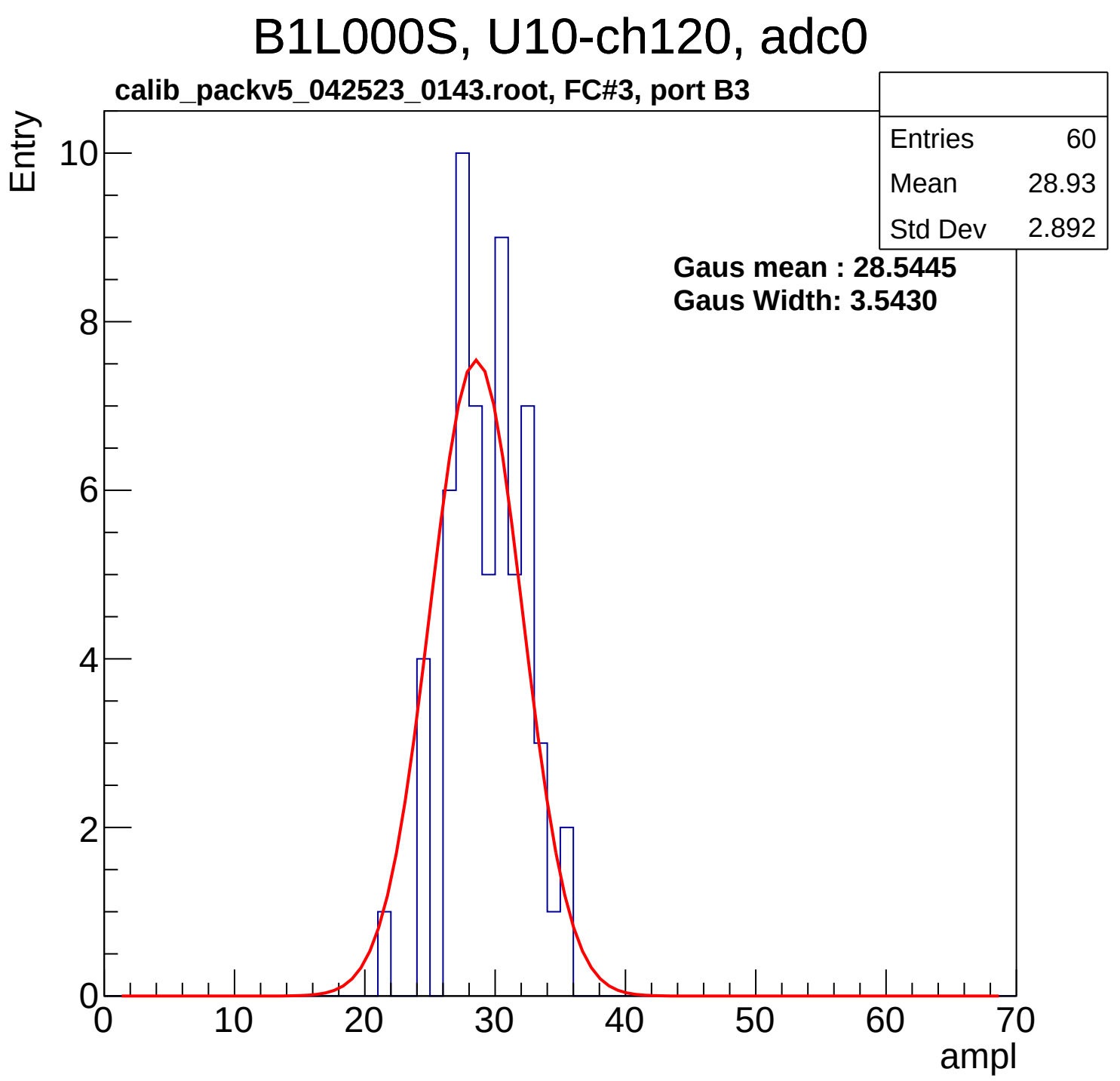
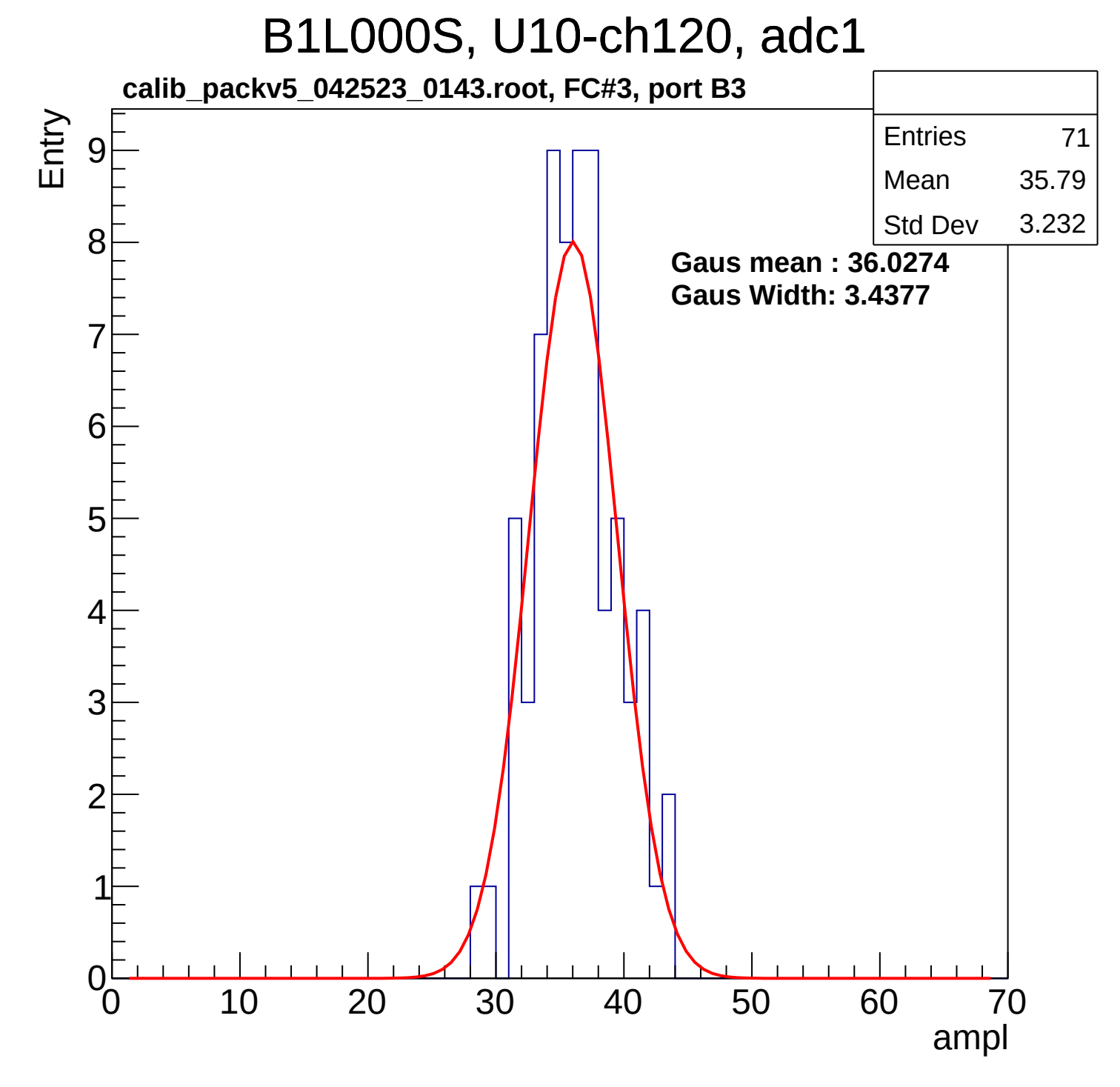
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch120, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.93
Std Dev 2.892
Gaus mean : 28.5445
Gaus Width: 3.5430
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch120, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 35.79
Std Dev 3.232
Gaus mean : 36.0274
Gaus Width: 3.4377
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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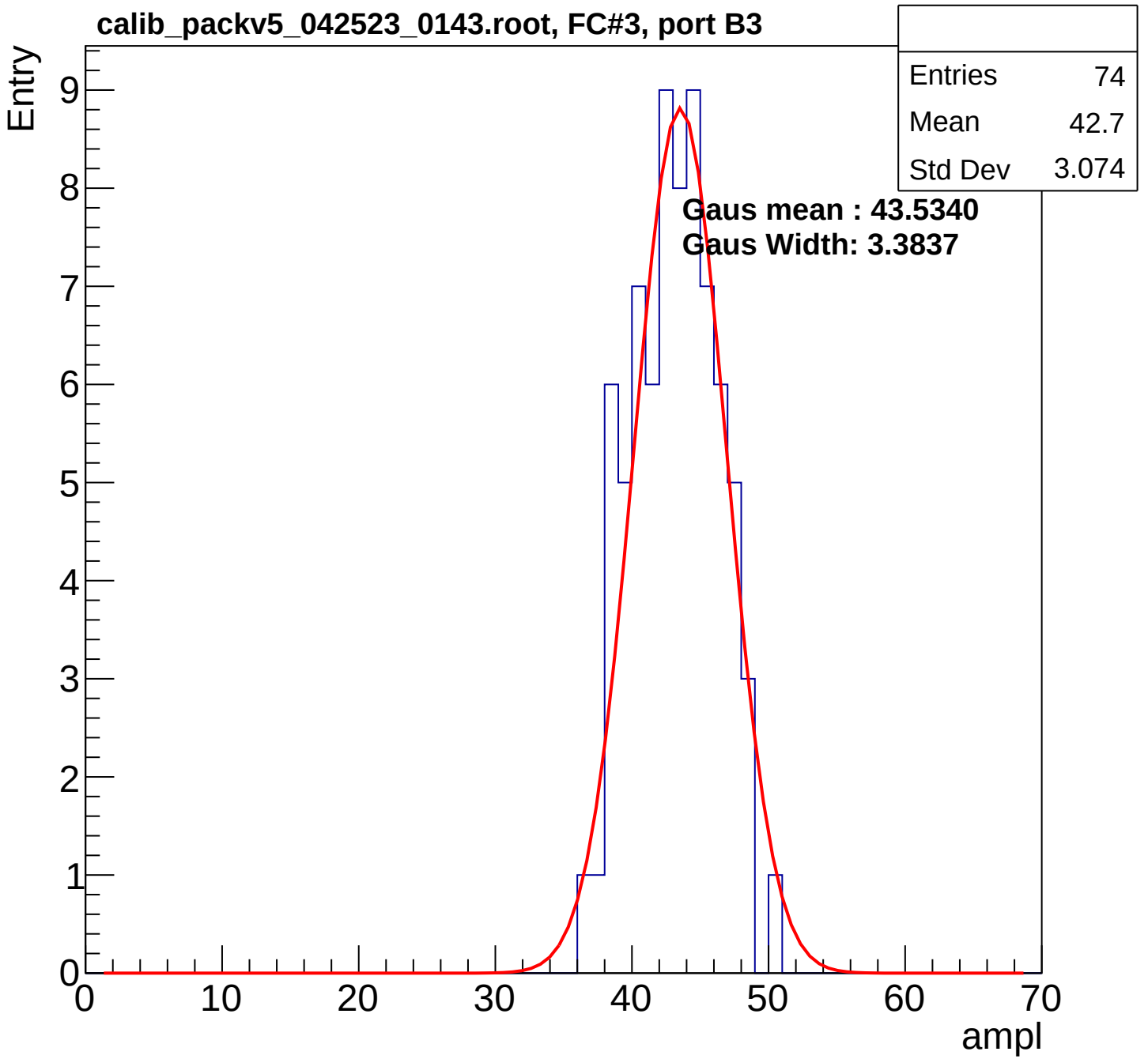
B1L000S, U10-ch120, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 42.7
Std Dev 3.074
Gaus mean : 43.5340
Gaus Width: 3.3837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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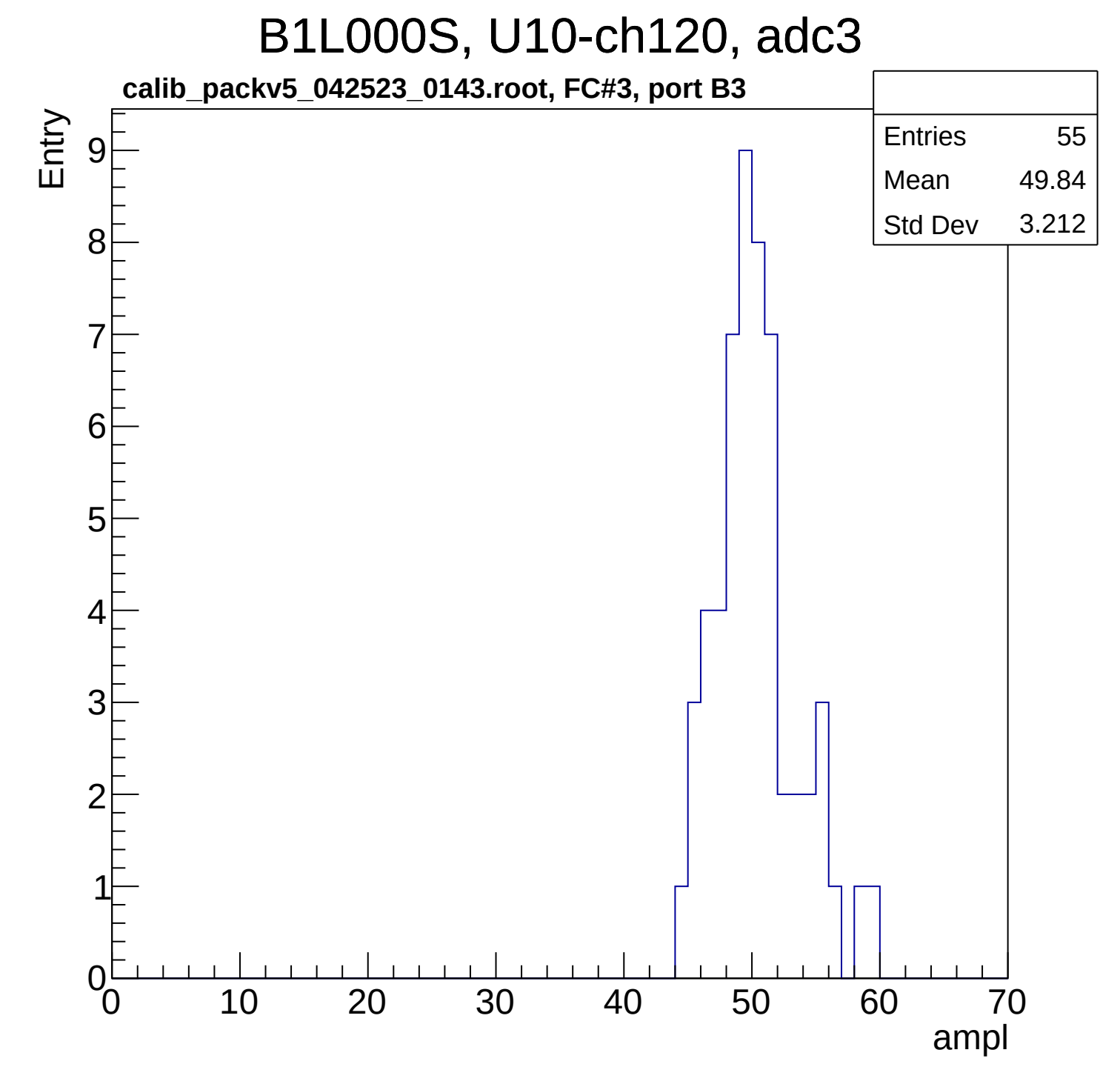
B1L000S, U10-ch120, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 49.84
Std Dev 3.212
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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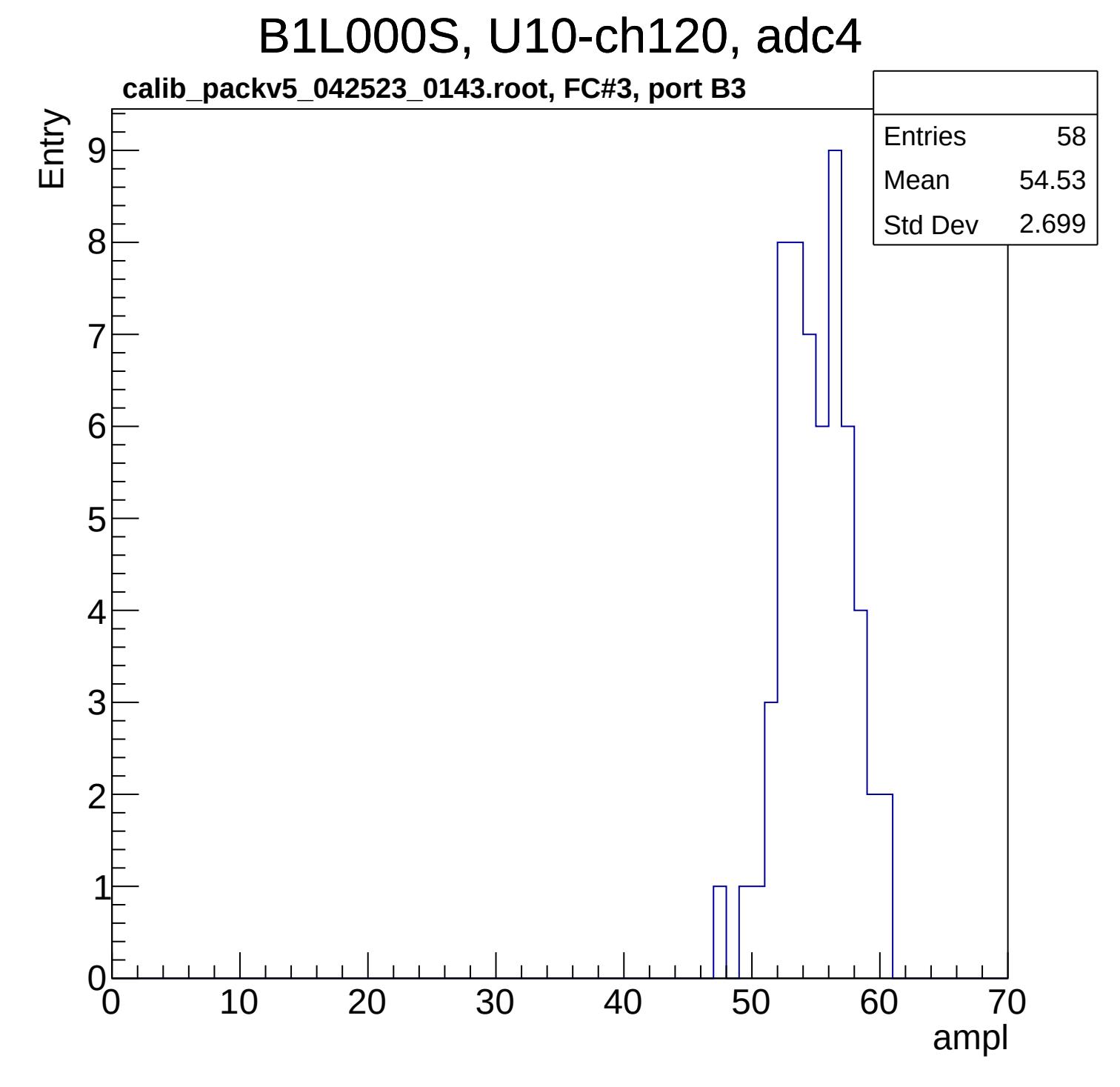
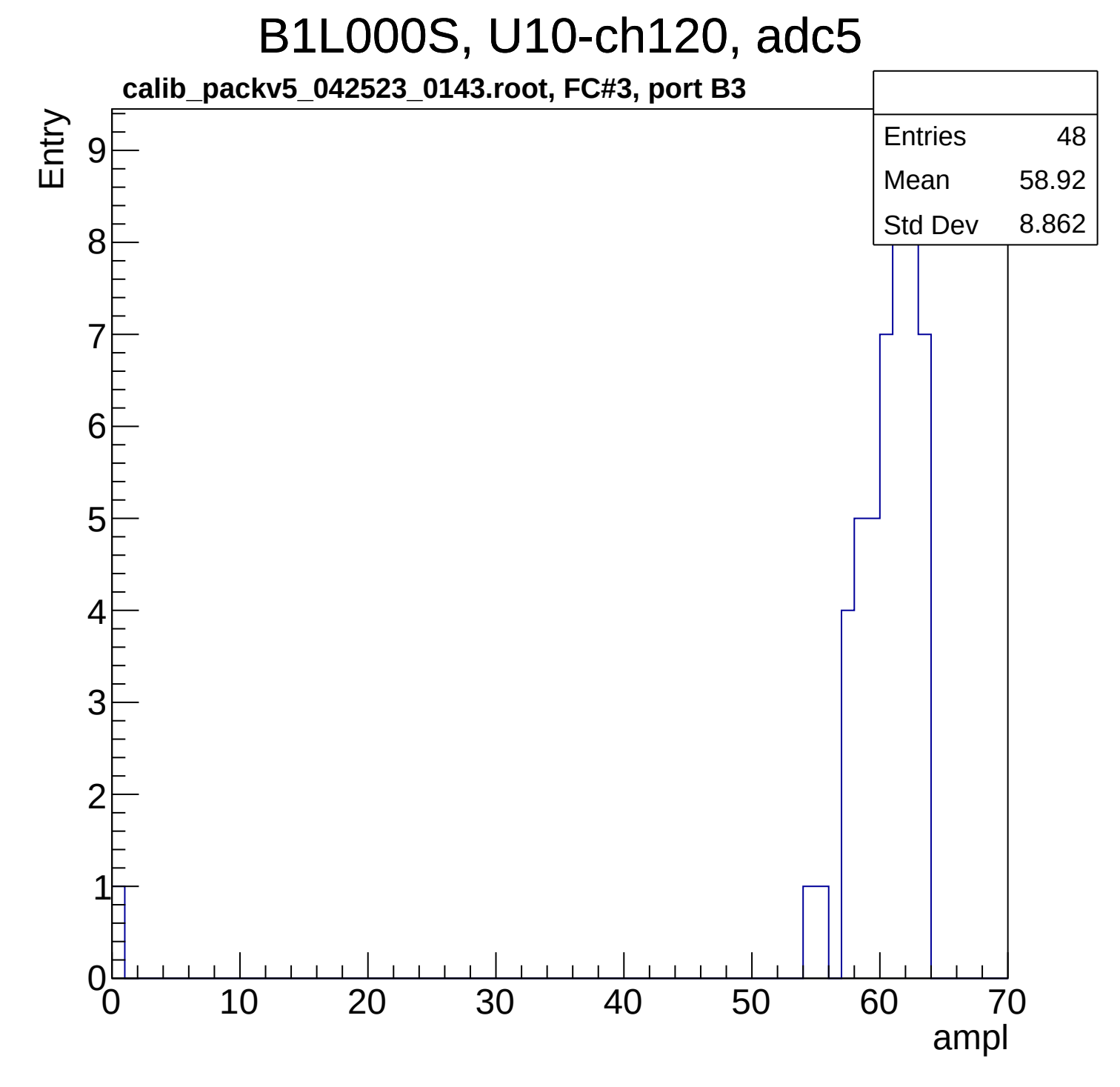
60
70


B1L000S, U10-ch120, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.53
Std Dev 2.699
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch120, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.92
Std Dev 8.862
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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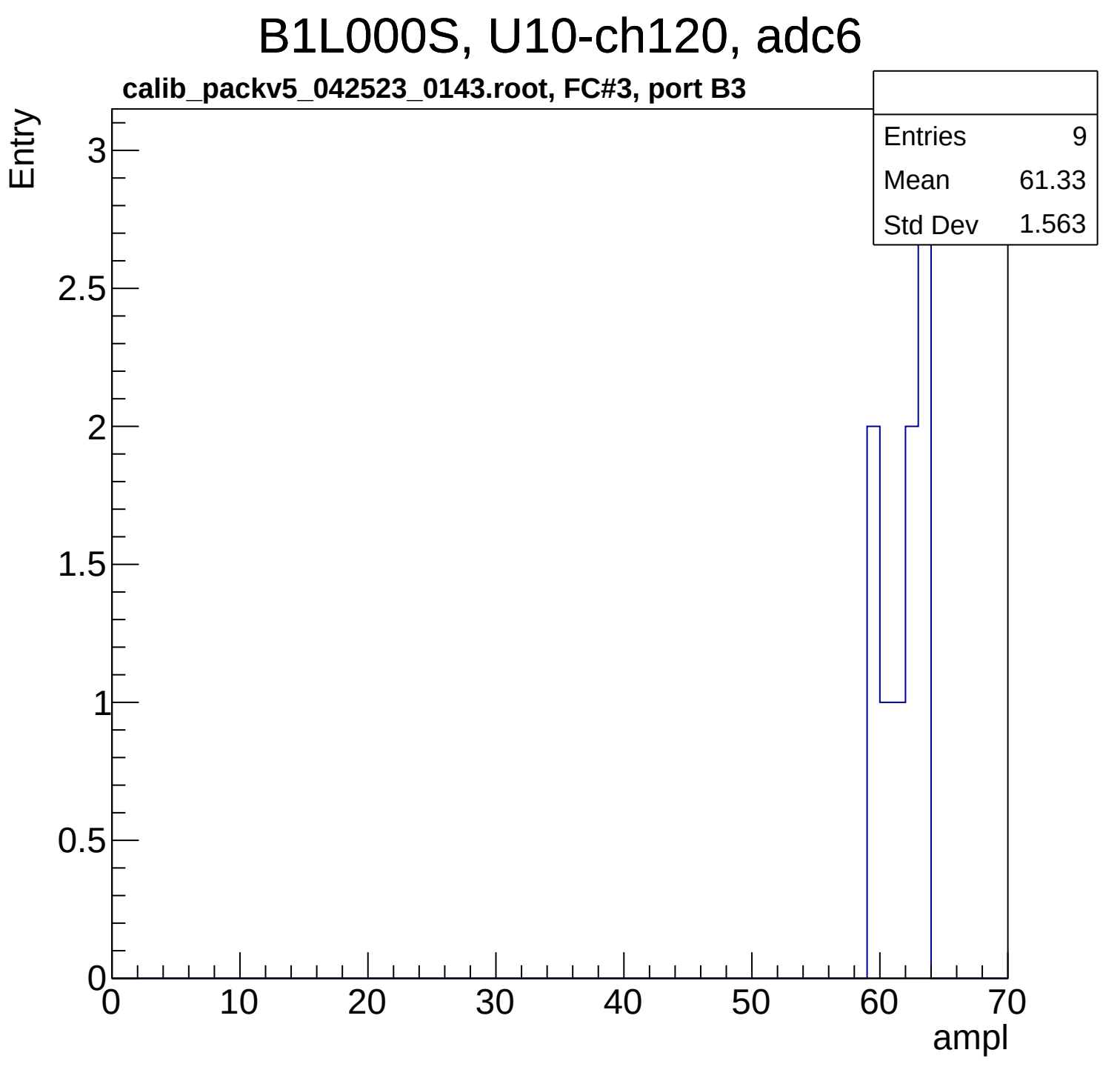
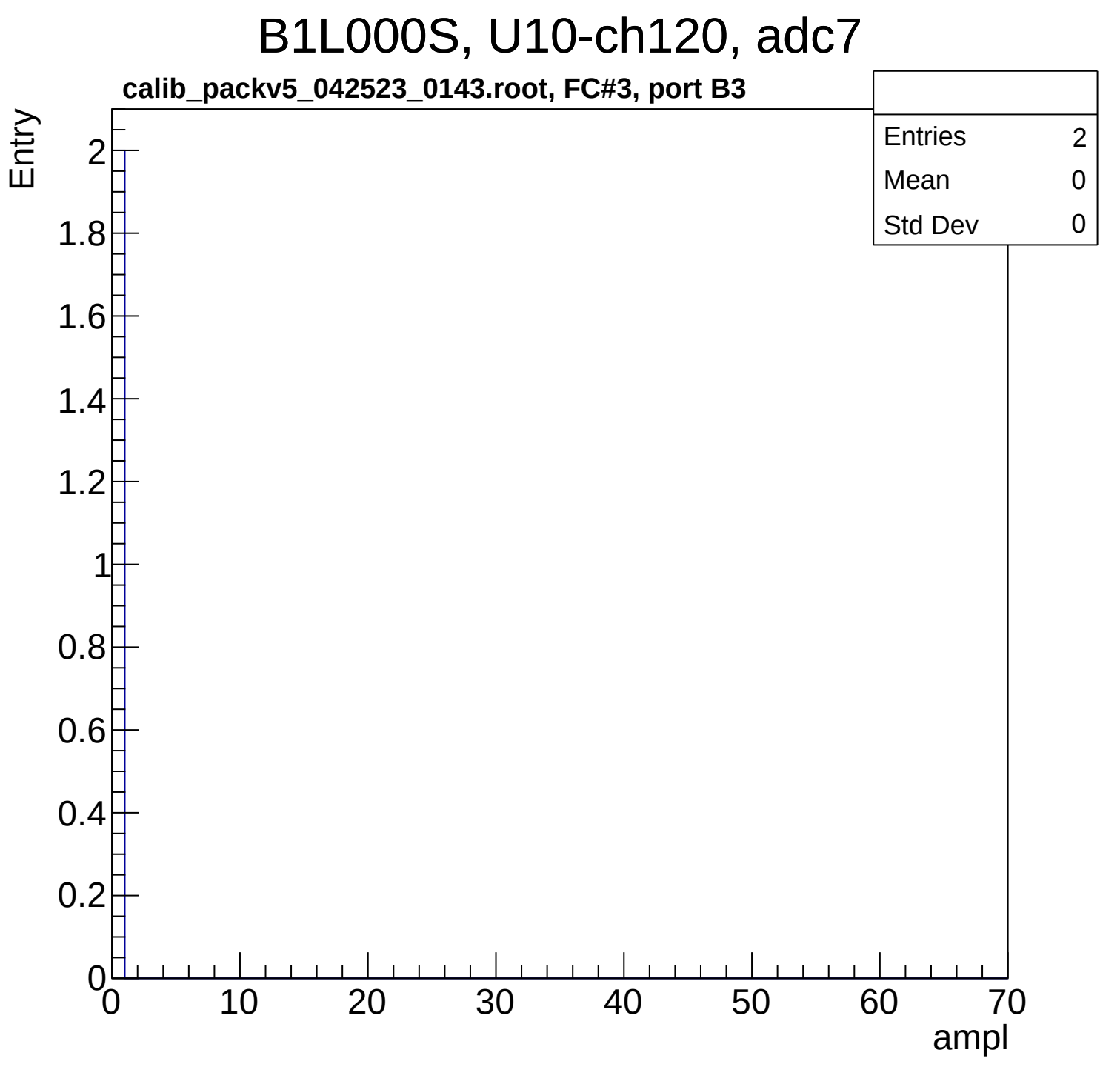

B1L000S, U10-ch120, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 61.33
Std Dev 1.563
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch120, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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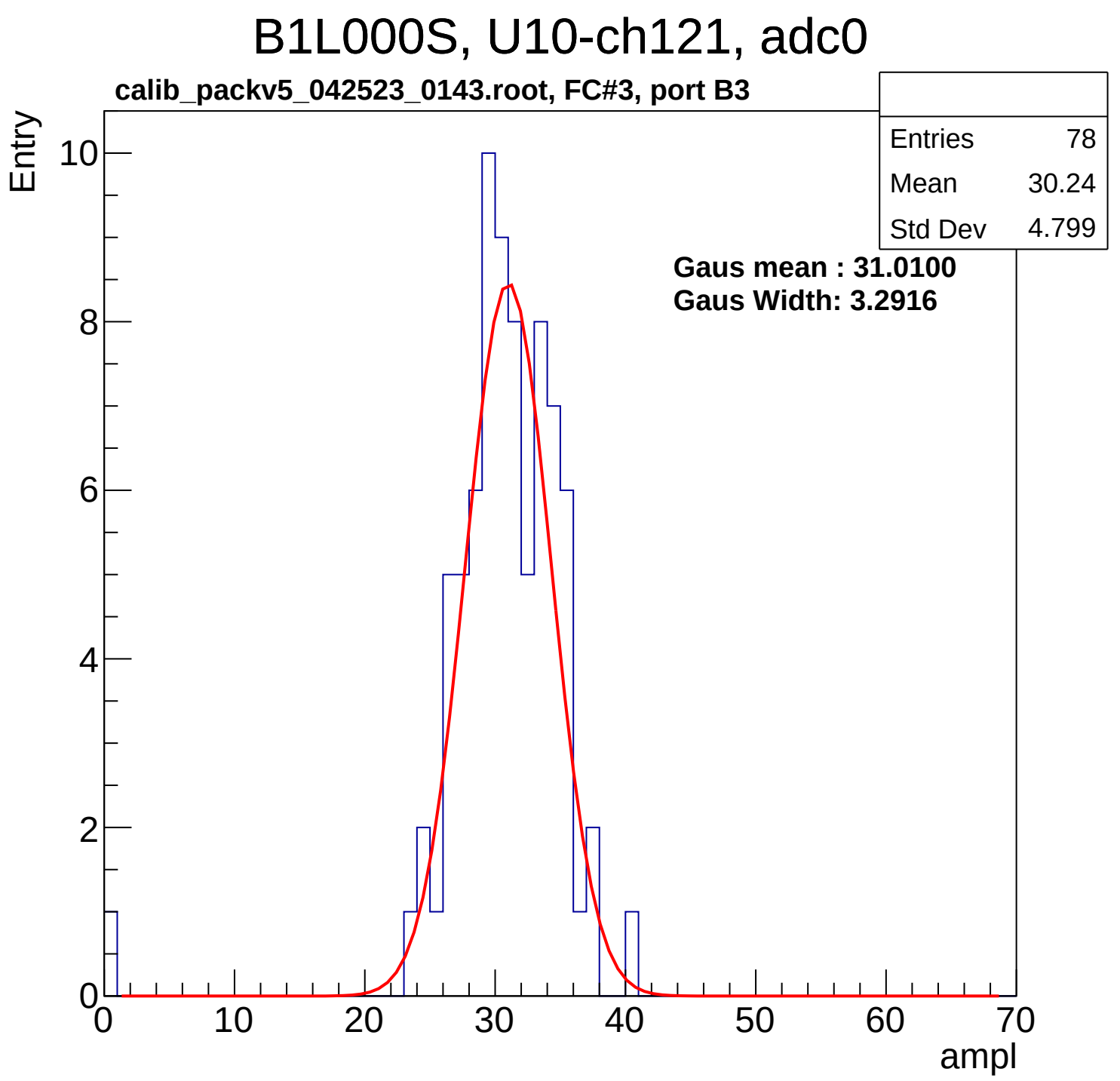
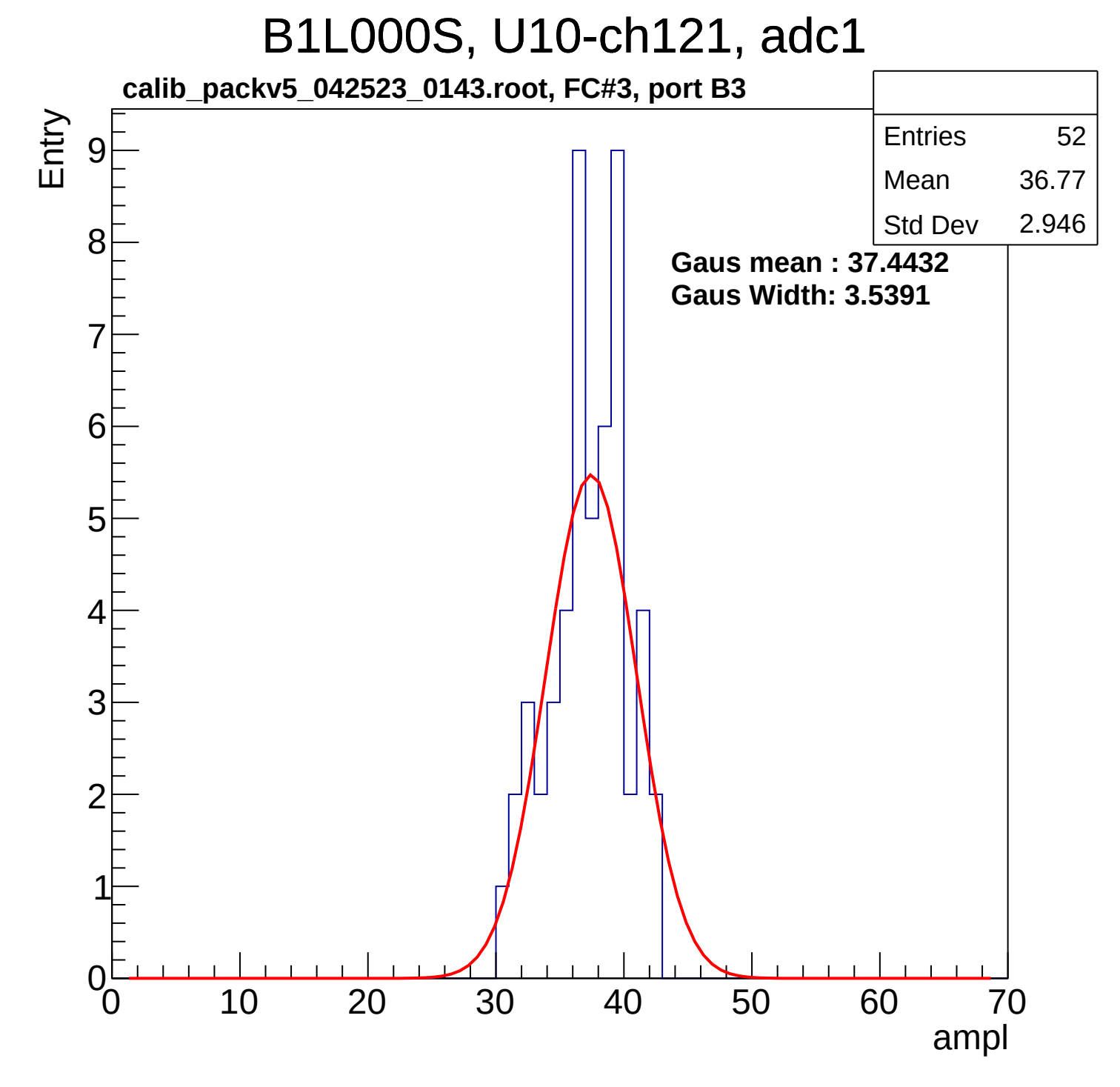

B1L000S, U10-ch121, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 30.24
Std Dev 4.799
Gaus mean : 31.0100
Gaus Width: 3.2916
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch121, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 36.77
Std Dev 2.946
Gaus mean : 37.4432
Gaus Width: 3.5391
Entry
ampl
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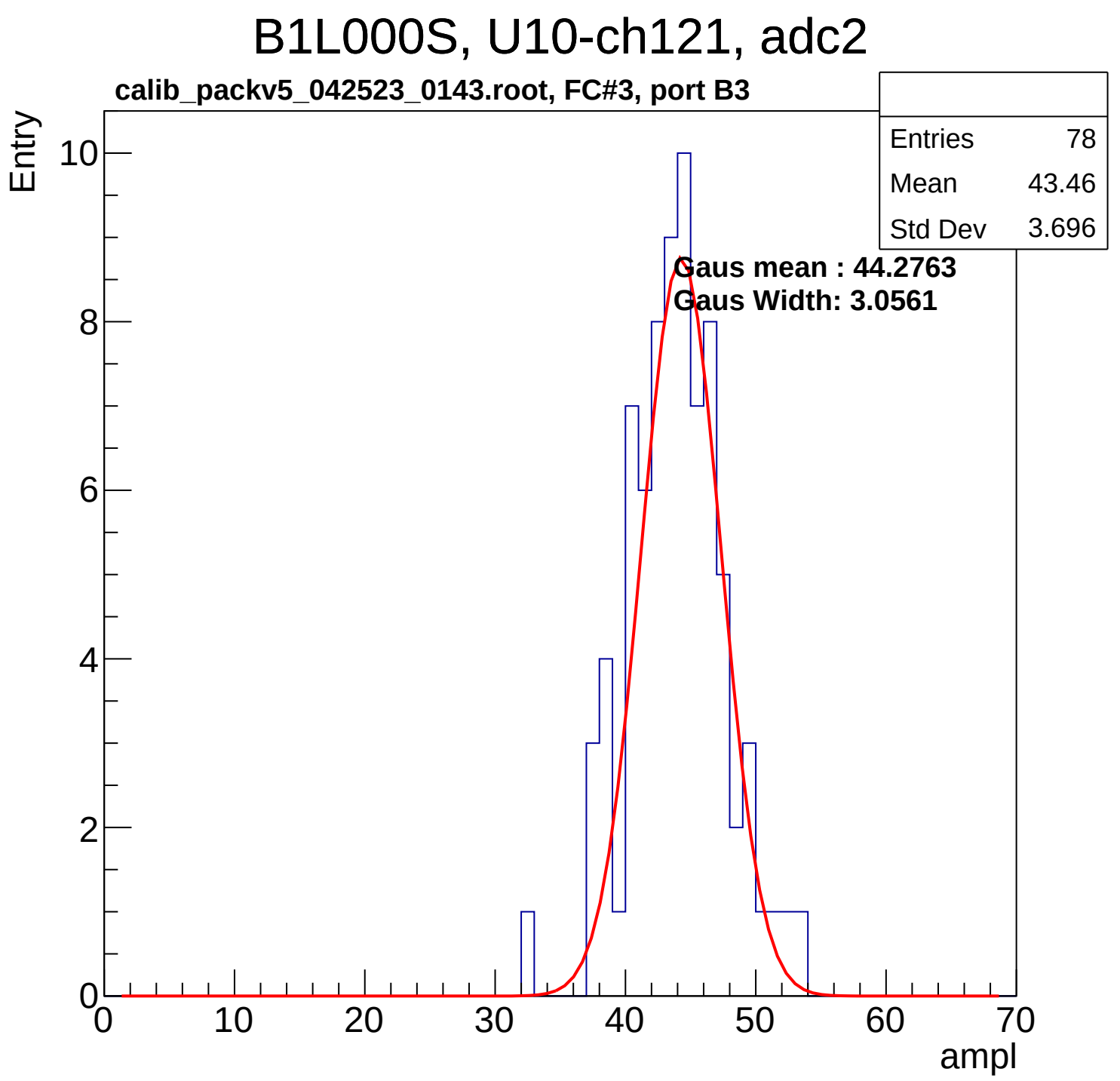
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch121, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 43.46
Std Dev 3.696
Gaus mean : 44.2763
Gaus Width: 3.0561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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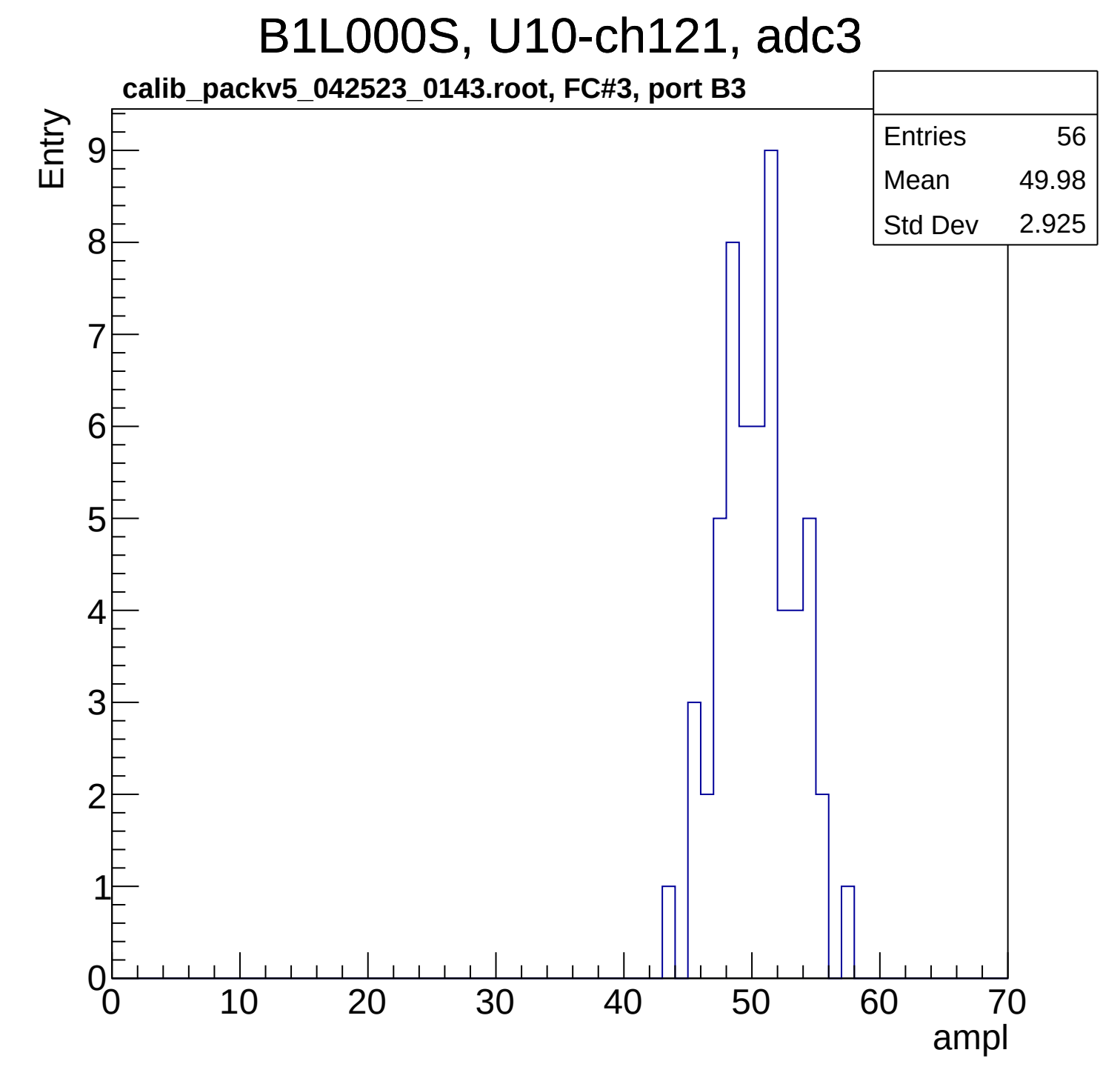
B1L000S, U10-ch121, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 49.98
Std Dev 2.925
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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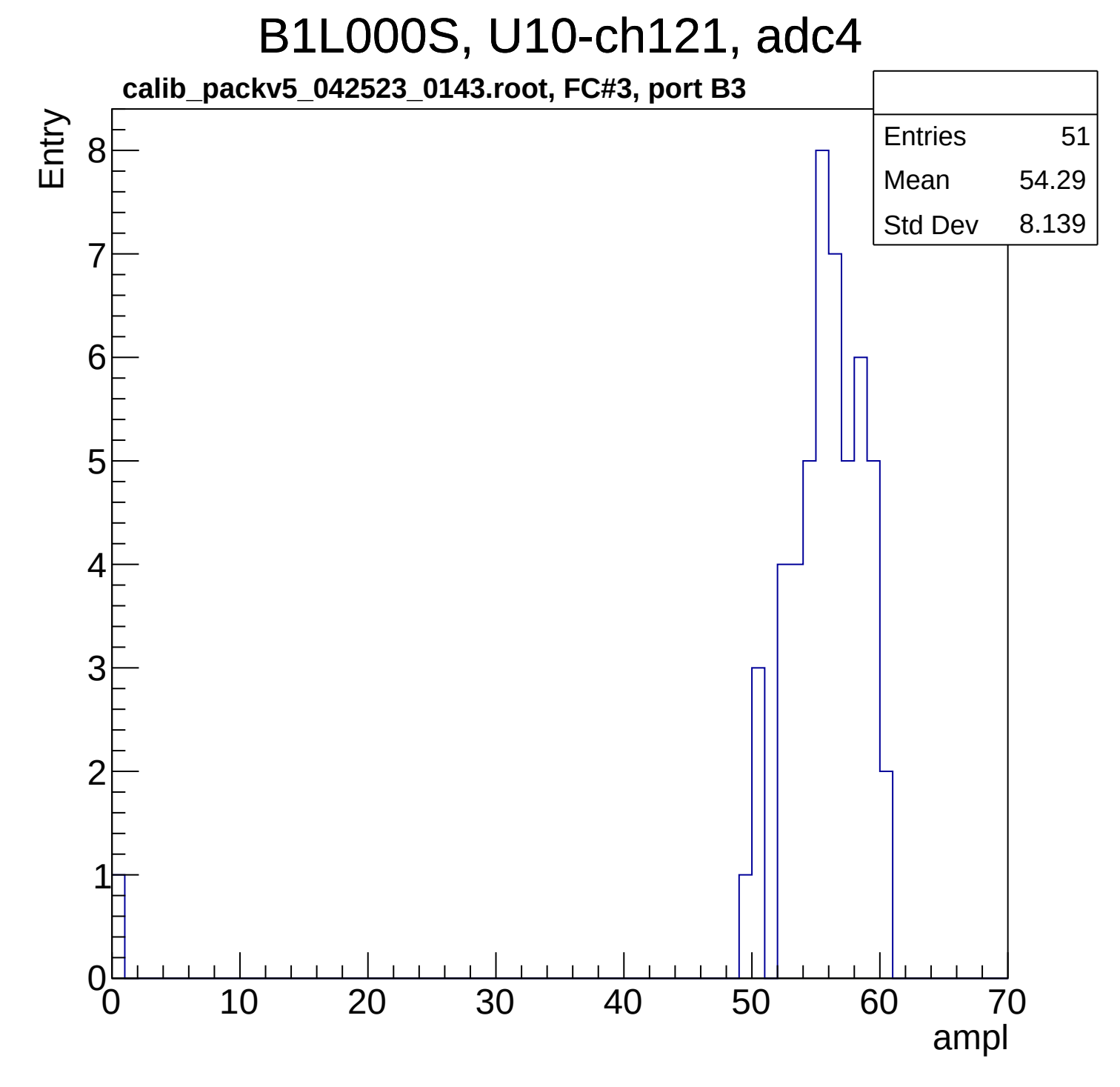
B1L000S, U10-ch121, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.29
Std Dev 8.139
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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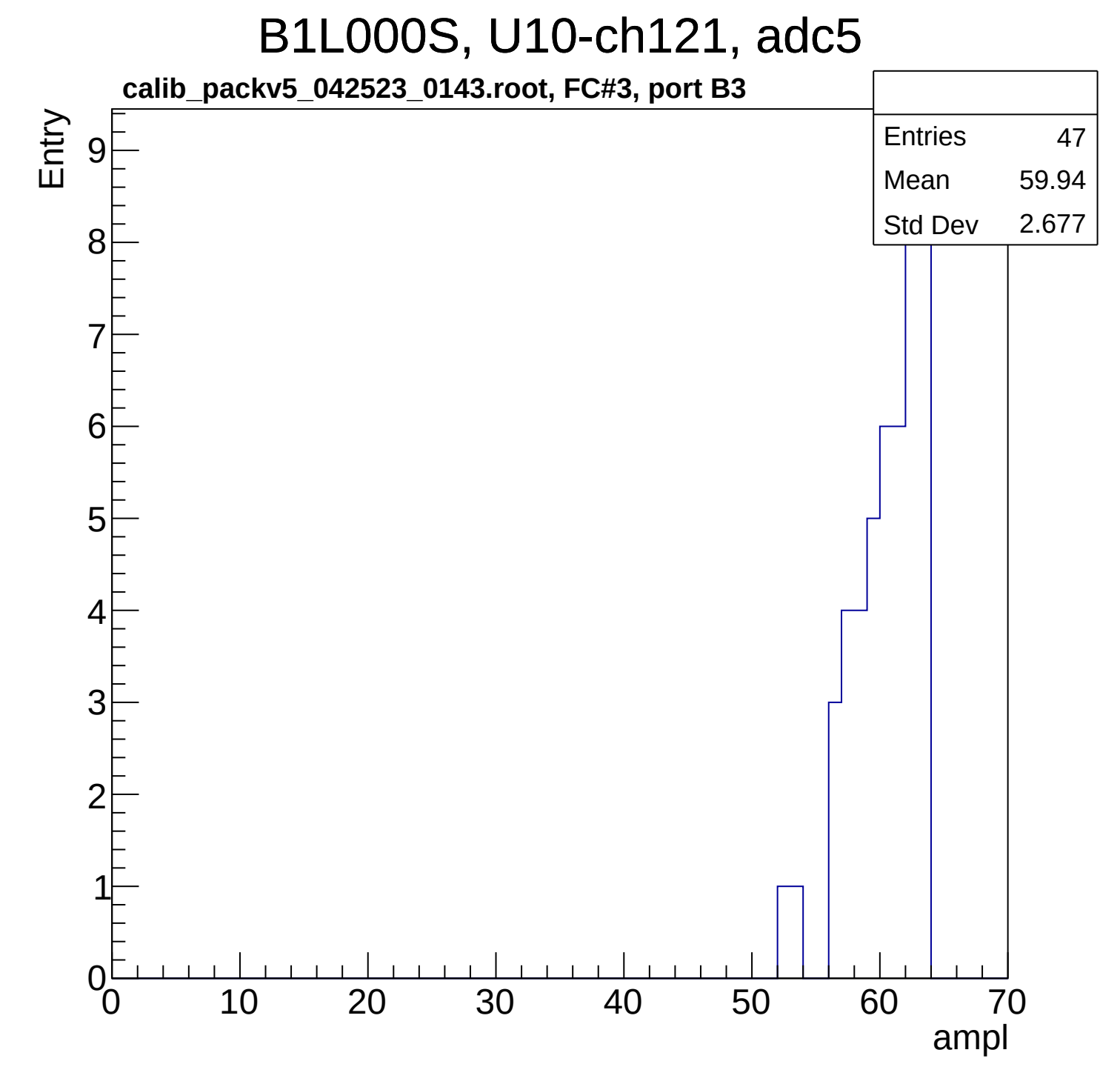
B1L000S, U10-ch121, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 59.94
Std Dev 2.677
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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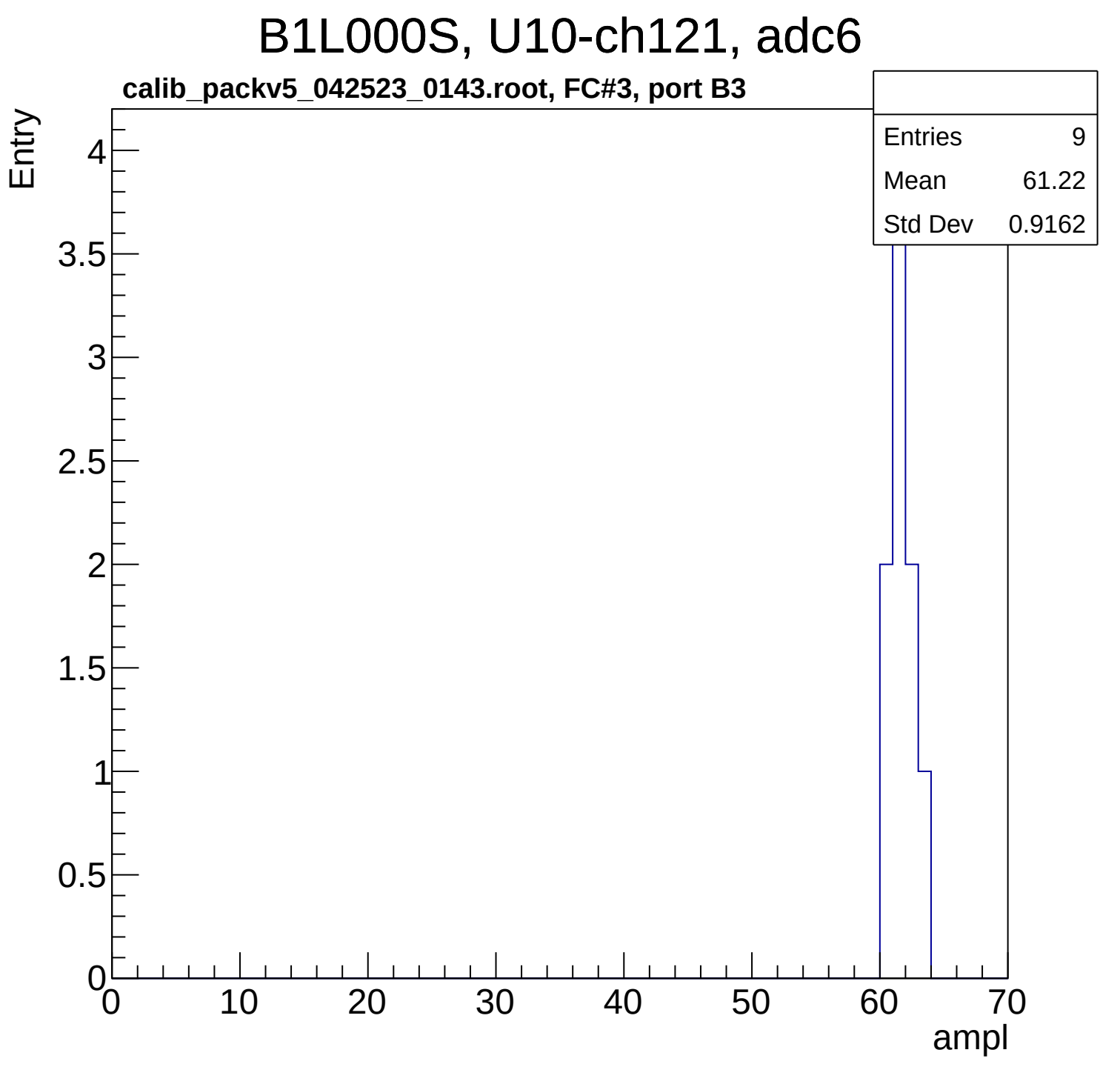
B1L000S, U10-ch121, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 61.22
Std Dev 0.9162
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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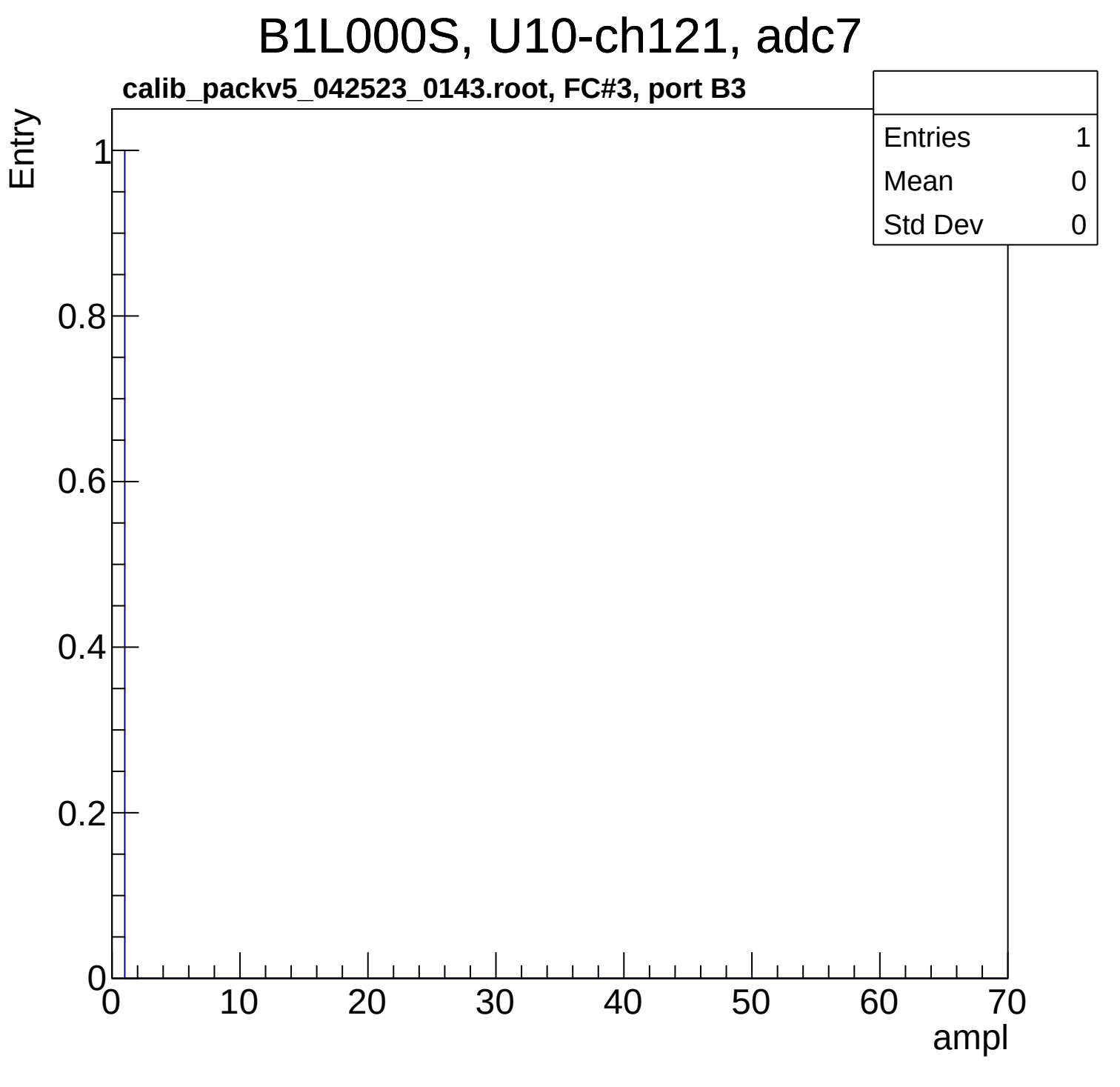
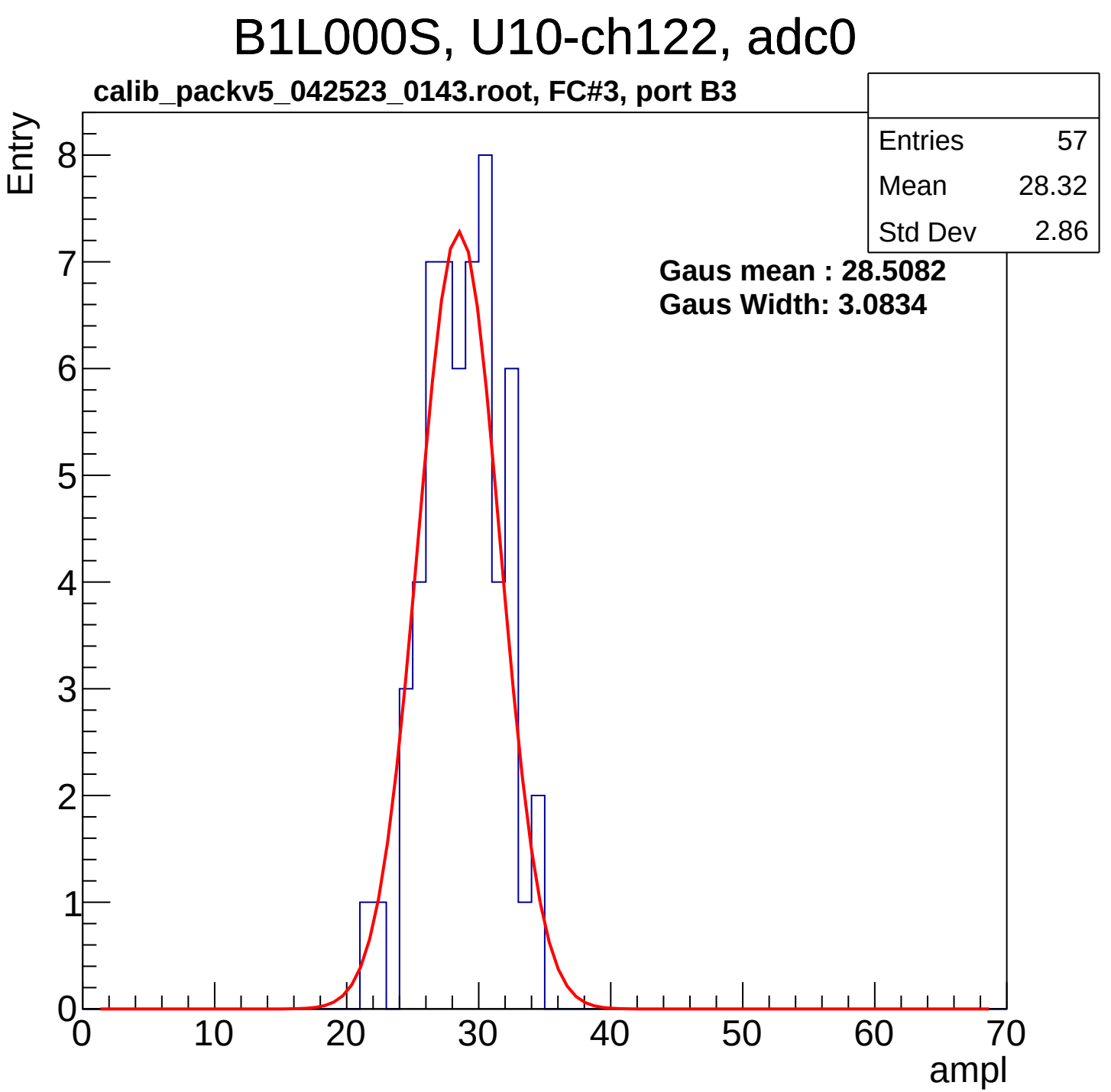

B1L000S, U10-ch121, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch122, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.32
Std Dev 2.86
Gaus mean : 28.5082
Gaus Width: 3.0834
Entry
ampl
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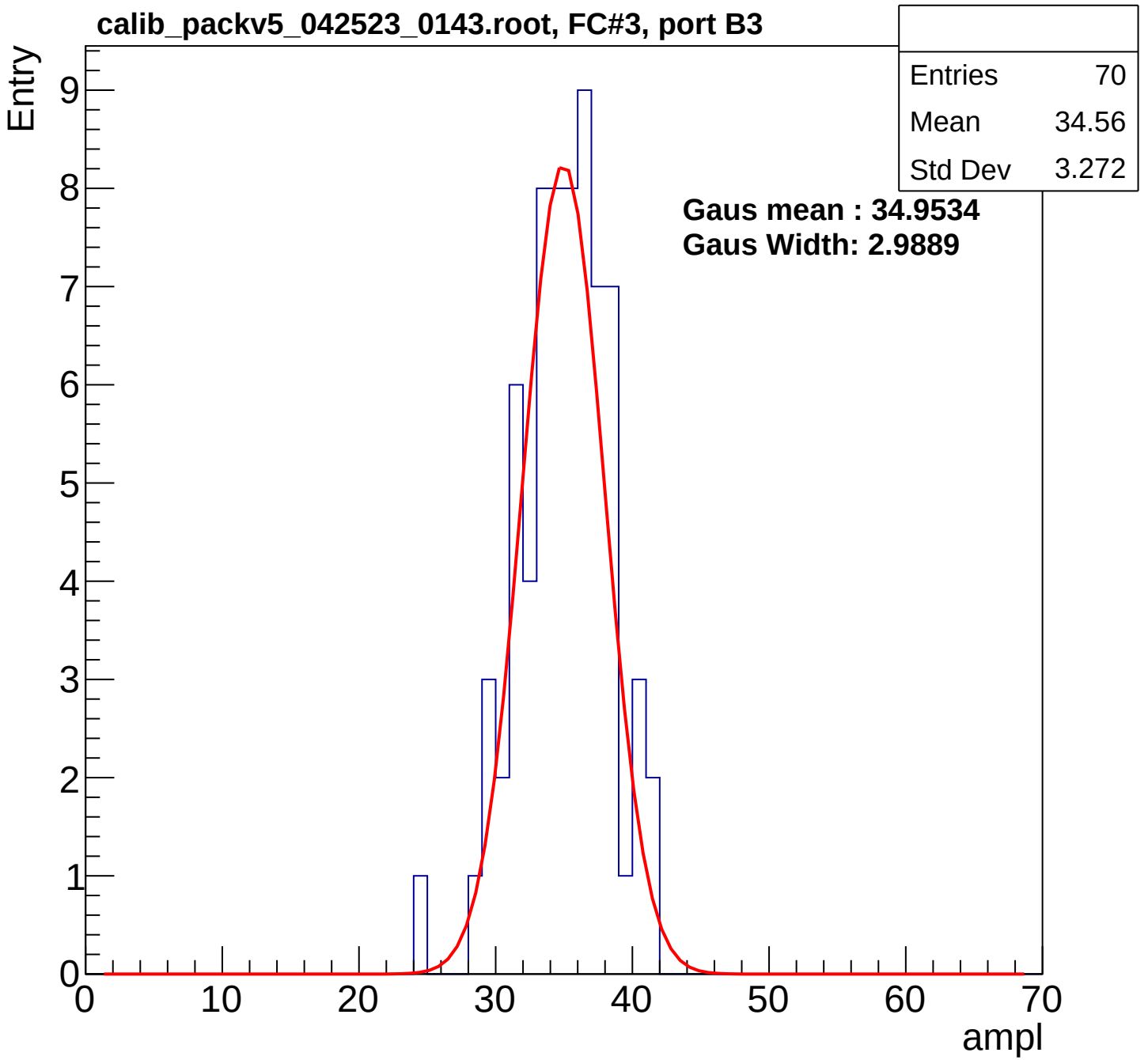
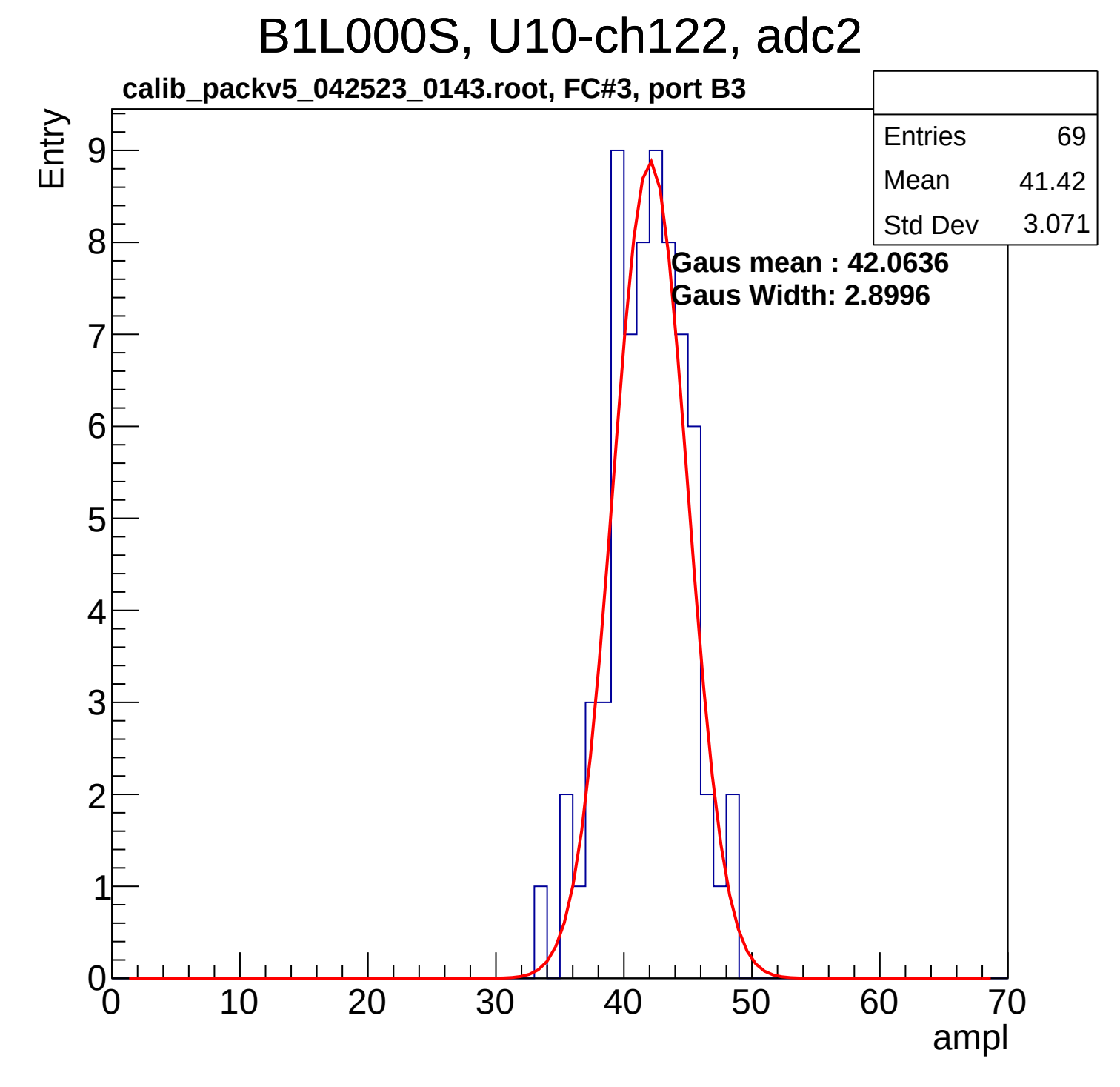
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch122, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 34.56
Std Dev 3.272
Gaus mean : 34.9534
Gaus Width: 2.9889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch122, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.42
Std Dev 3.071
Gaus mean : 42.0636
Gaus Width: 2.8996
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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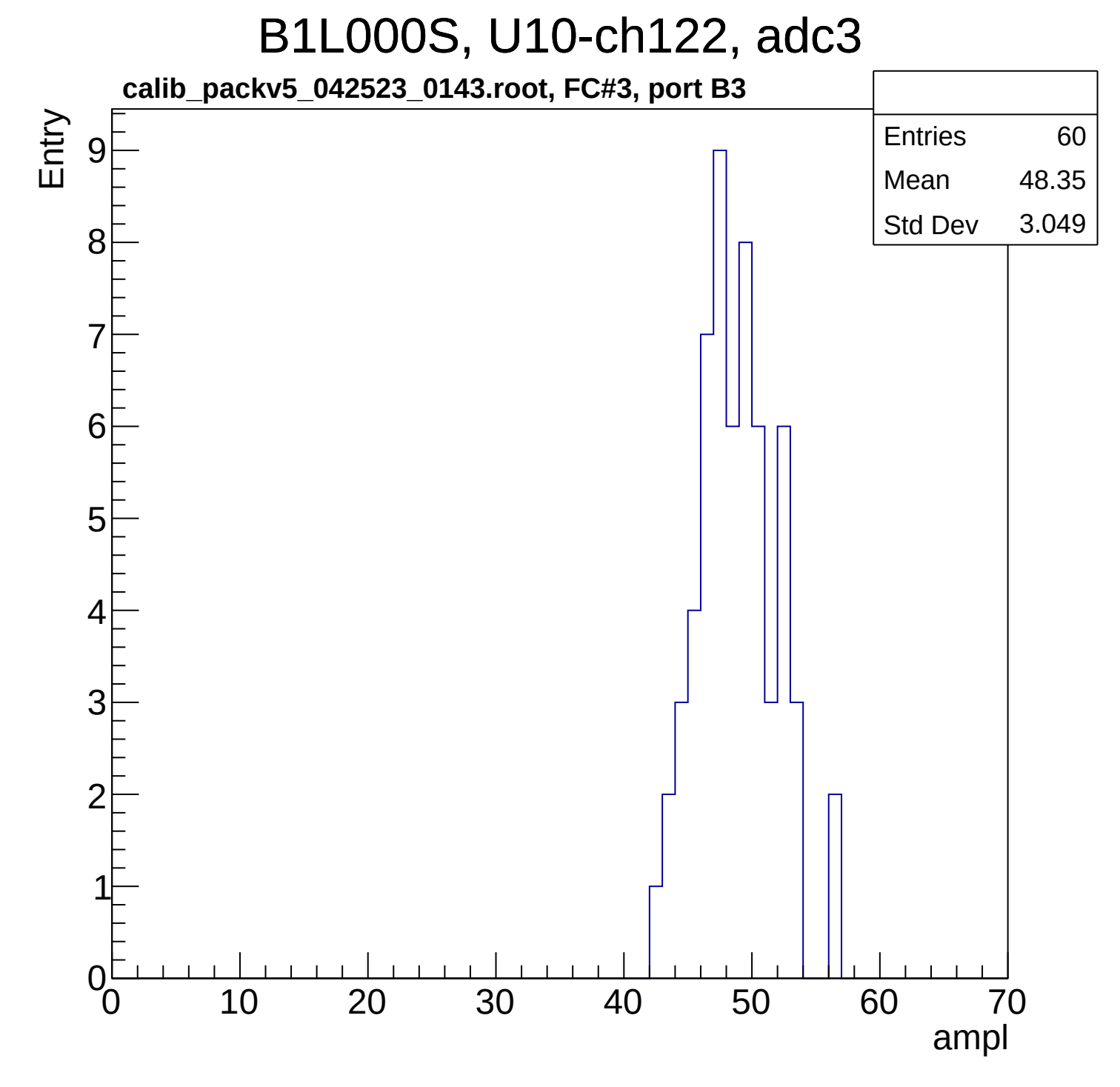
B1L000S, U10-ch122, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.35
Std Dev 3.049
Entry
ampl
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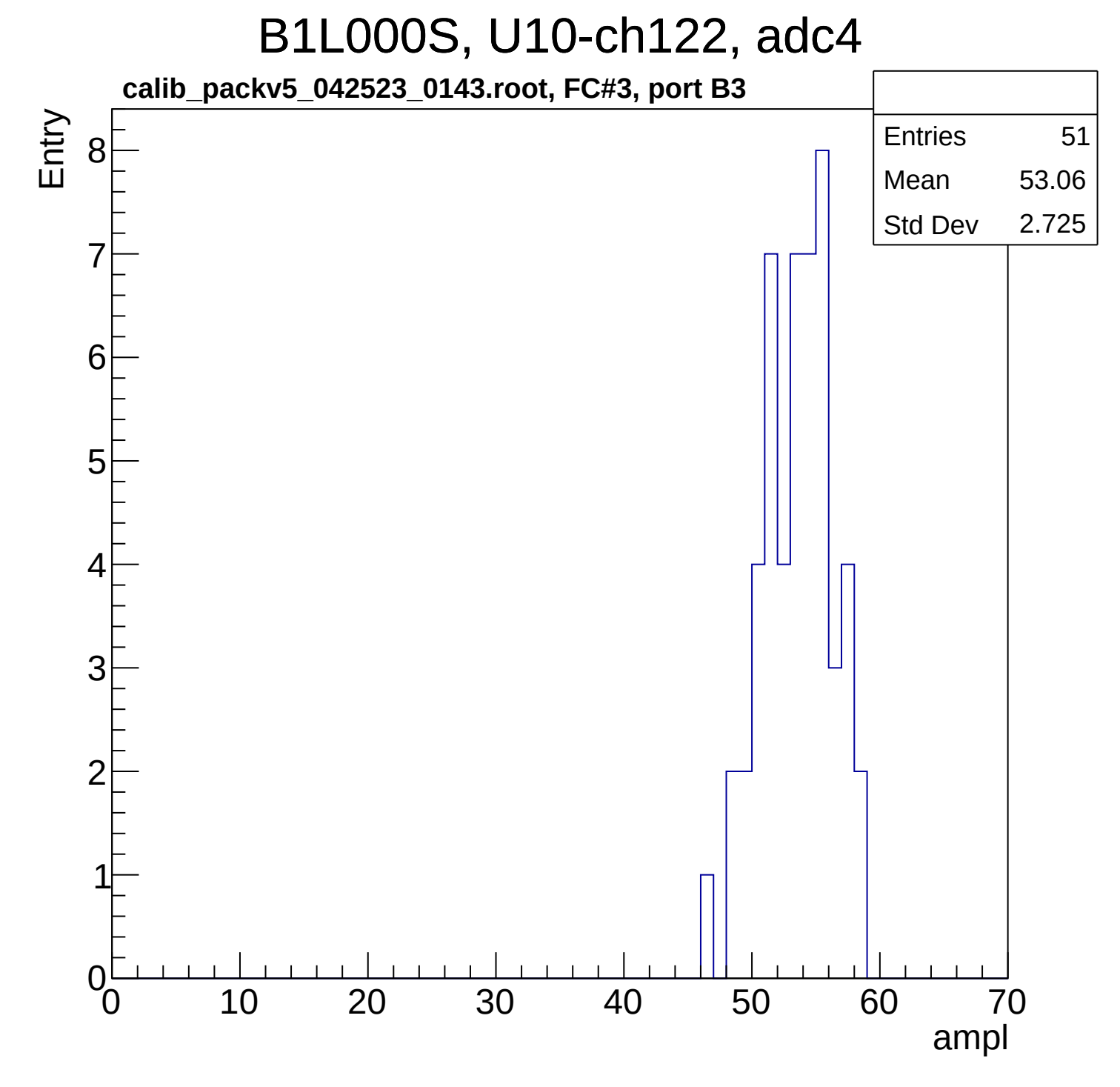
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch122, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 53.06
Std Dev 2.725
Entry
ampl
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1
2
3
4
5
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7
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0
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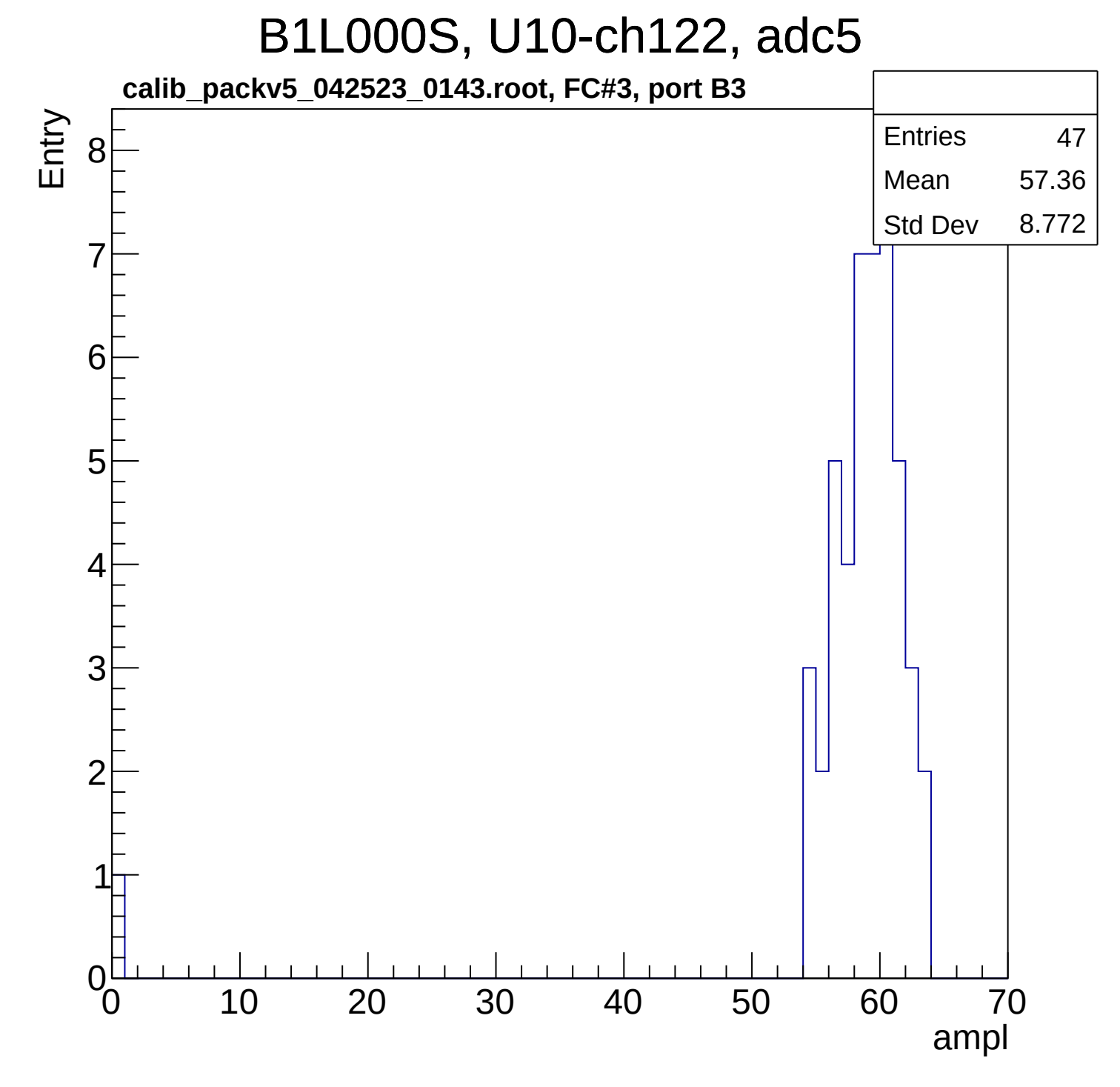
40
50
60
70


B1L000S, U10-ch122, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 57.36
Std Dev 8.772
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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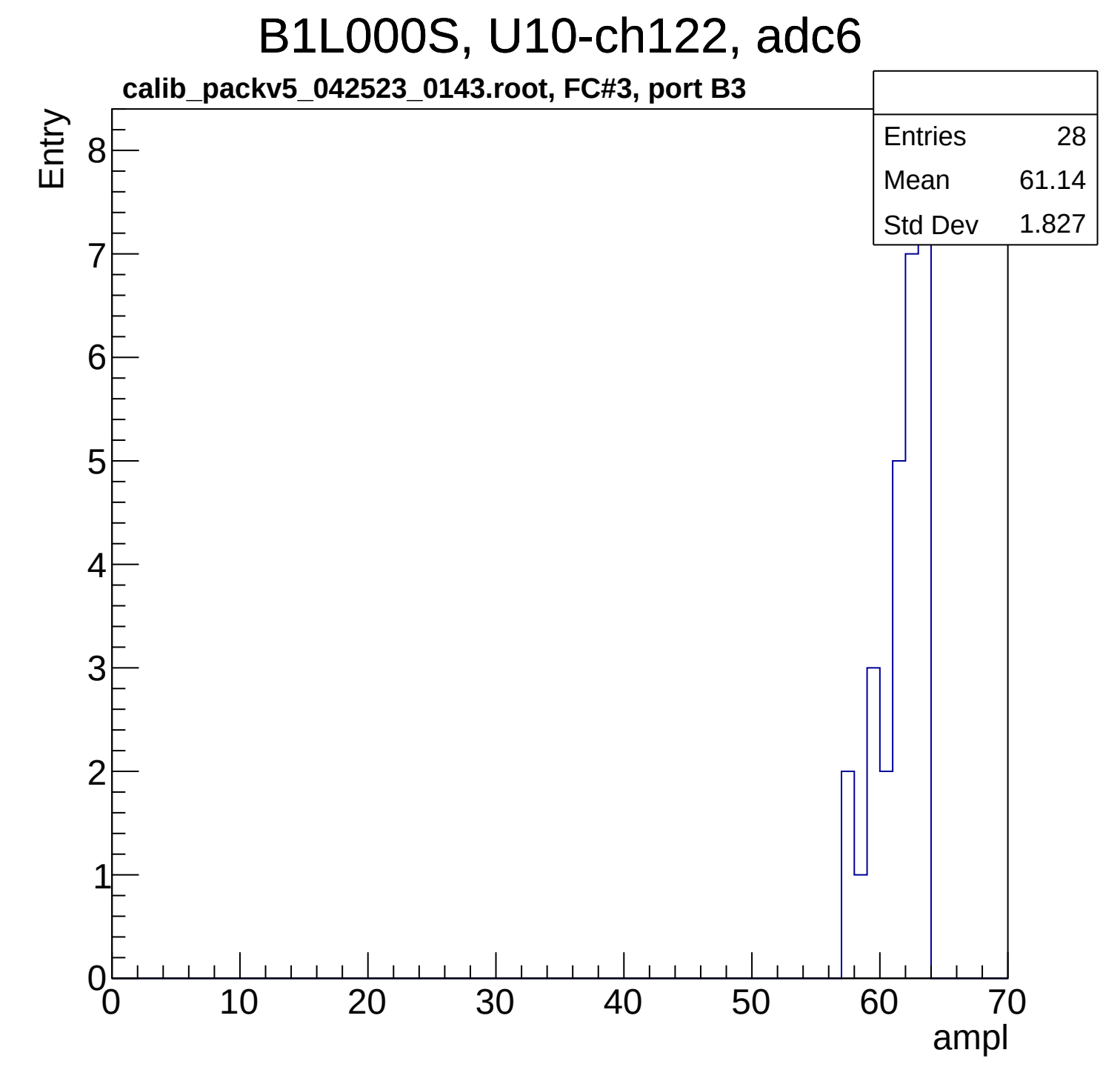
40
50
60
70


B1L000S, U10-ch122, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 61.14
Std Dev 1.827
Entry
ampl
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1
2
3
4
5
6
7
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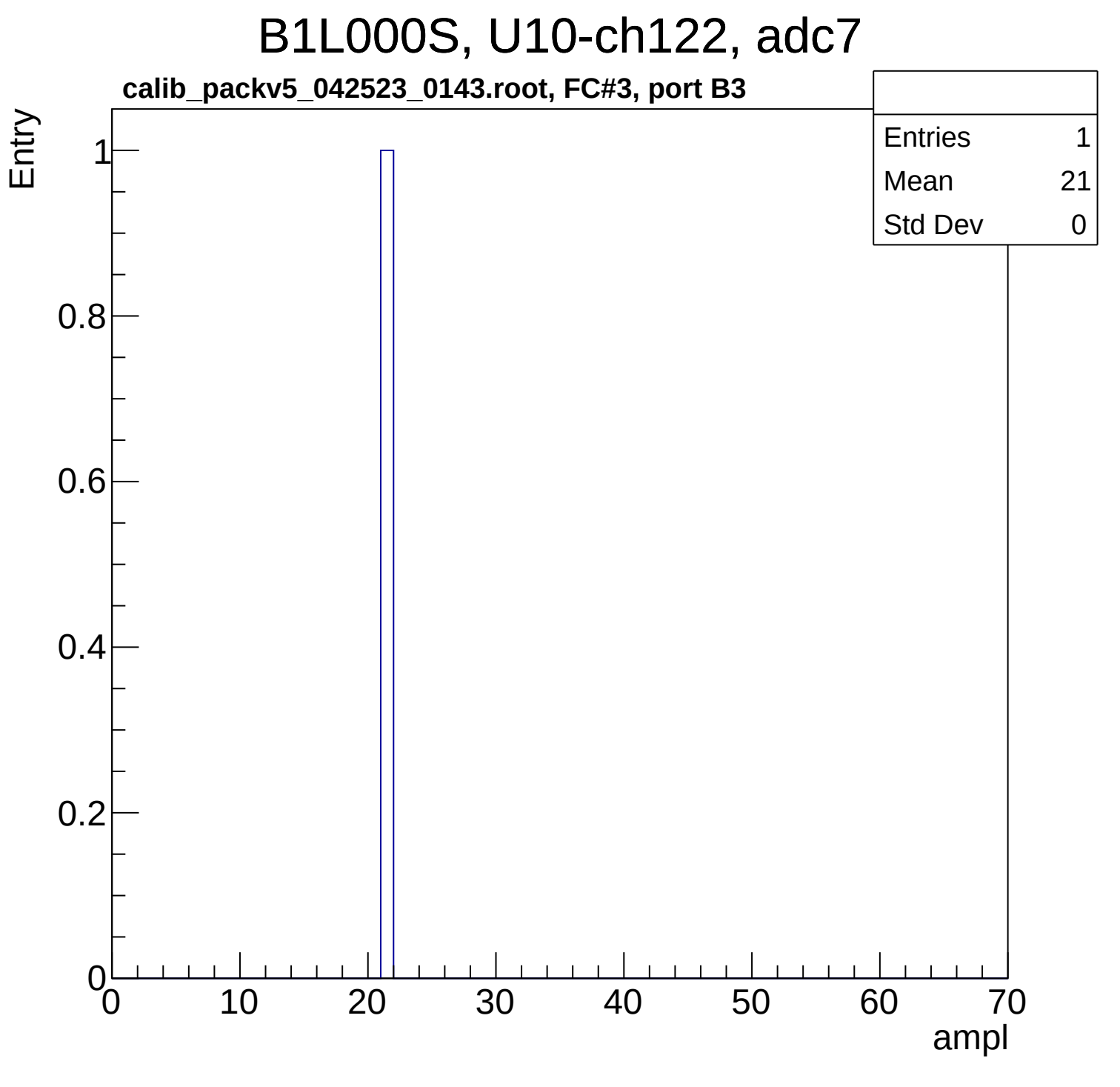
0
10
20
30
40
50
60
70


B1L000S, U10-ch122, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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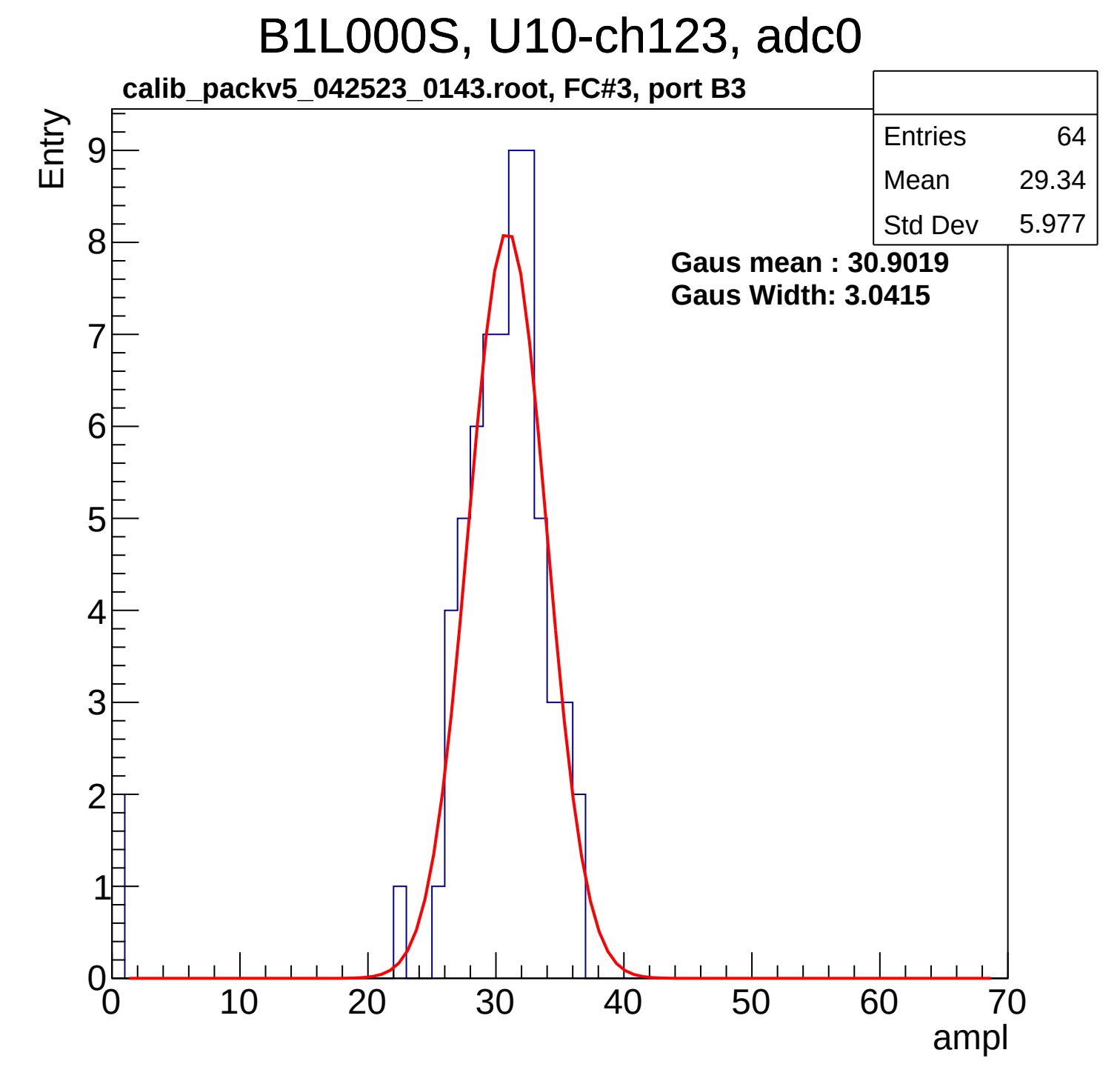
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch123, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 29.34
Std Dev 5.977
Gaus mean : 30.9019
Gaus Width: 3.0415
Entry
ampl
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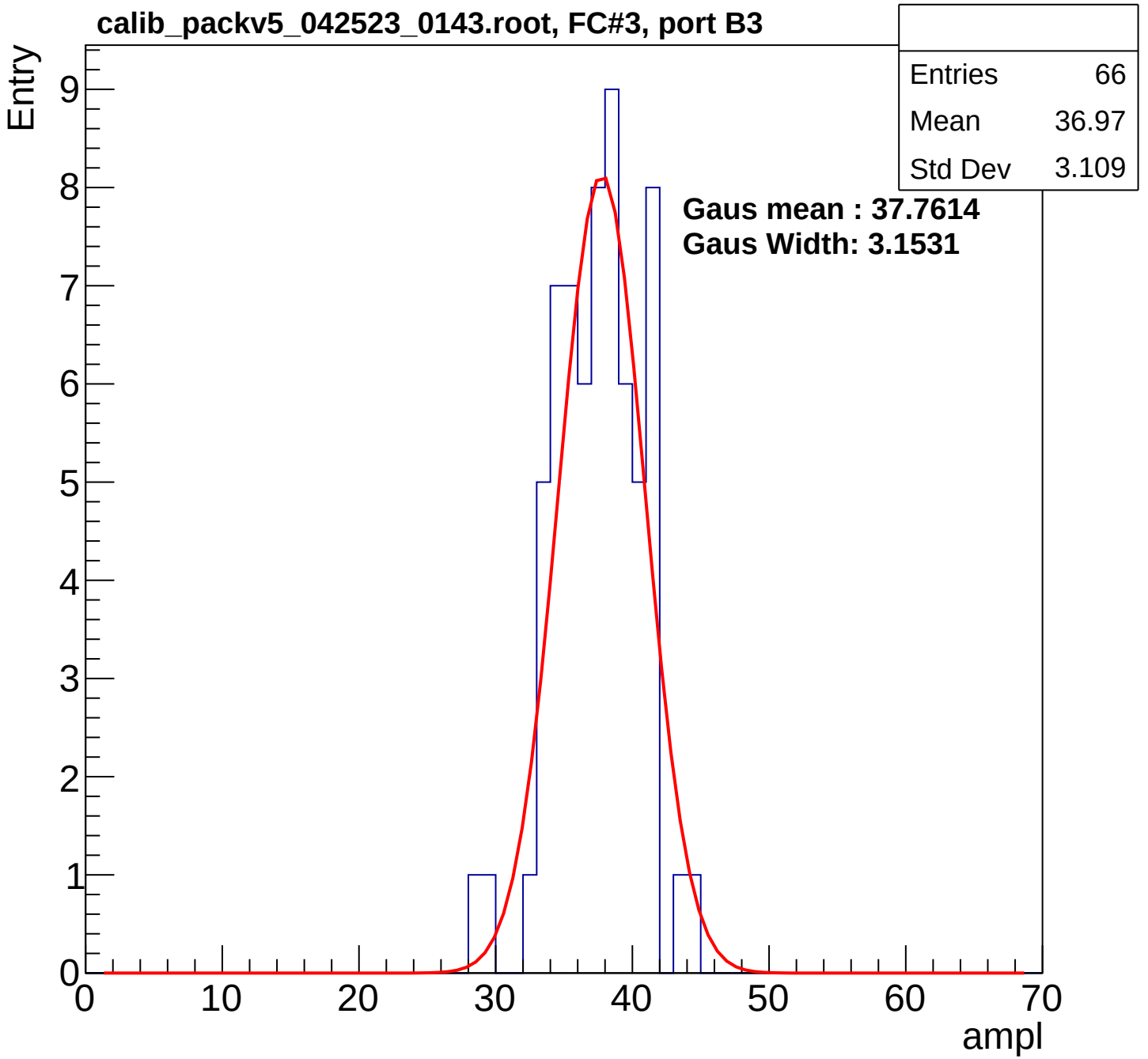
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch123, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 36.97
Std Dev 3.109
Gaus mean : 37.7614
Gaus Width: 3.1531
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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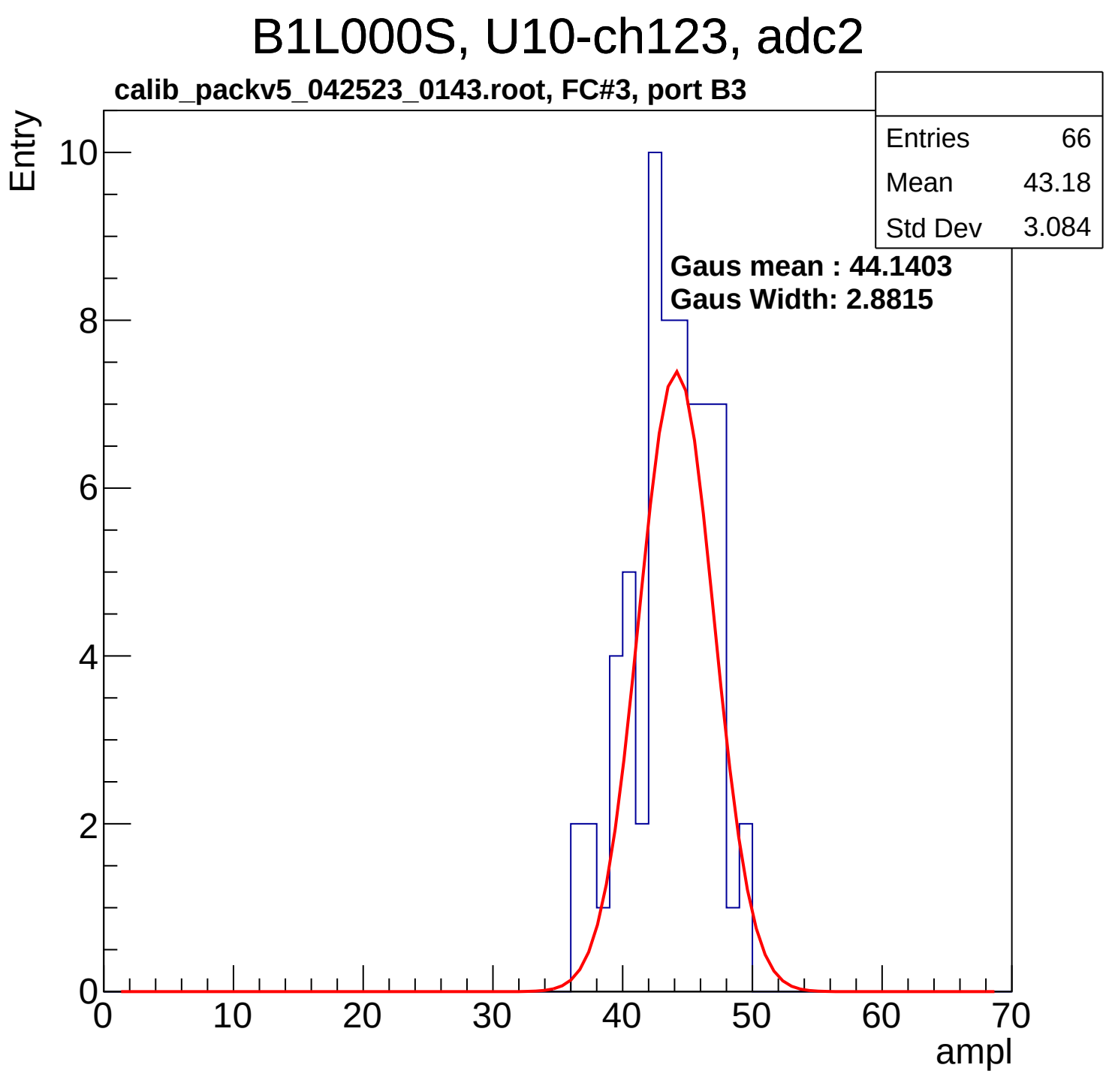
B1L000S, U10-ch123, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 43.18
Std Dev 3.084
Gaus mean : 44.1403
Gaus Width: 2.8815
Entry
ampl
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2
4
6
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10
0
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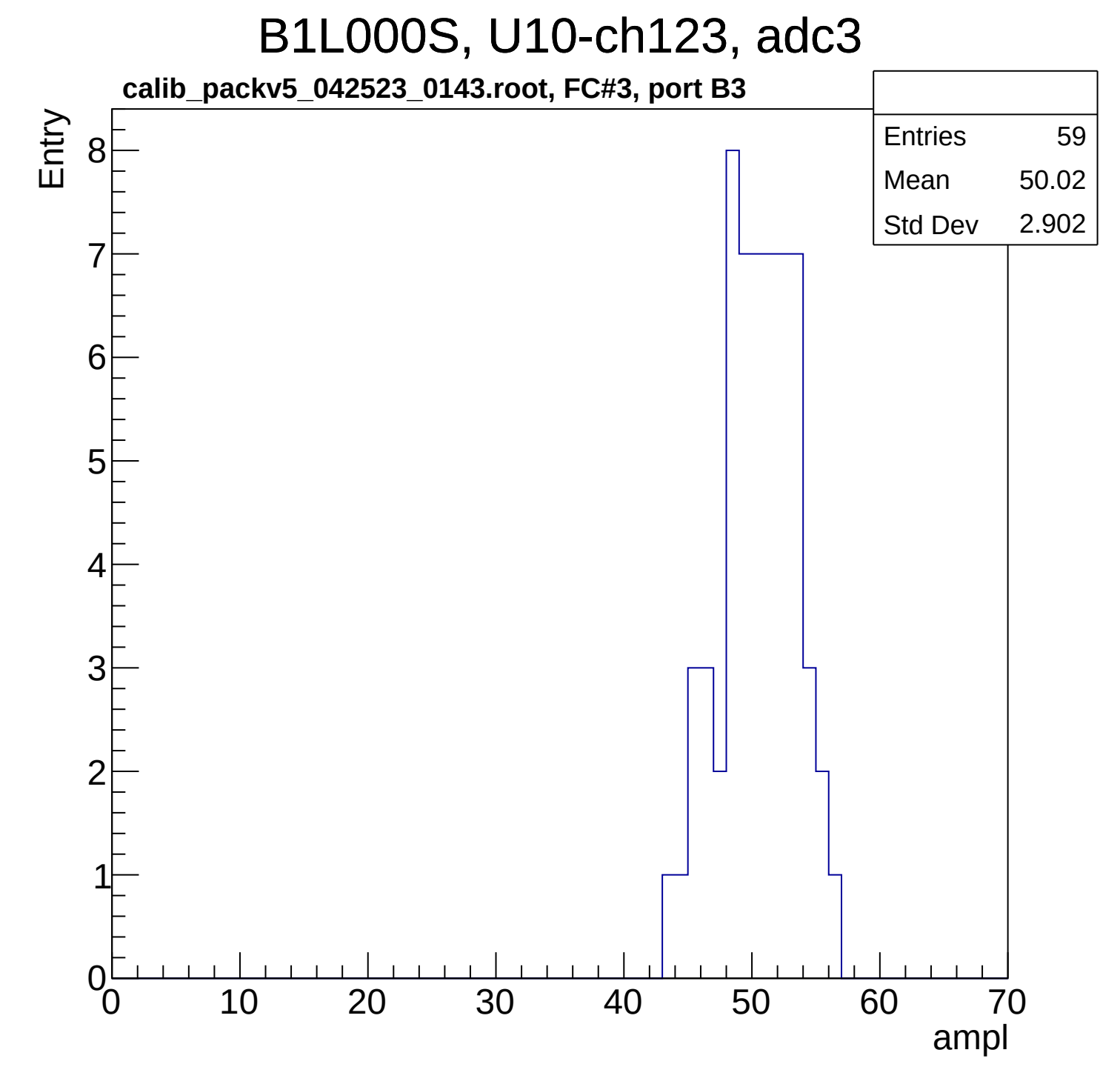
B1L000S, U10-ch123, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 50.02
Std Dev 2.902
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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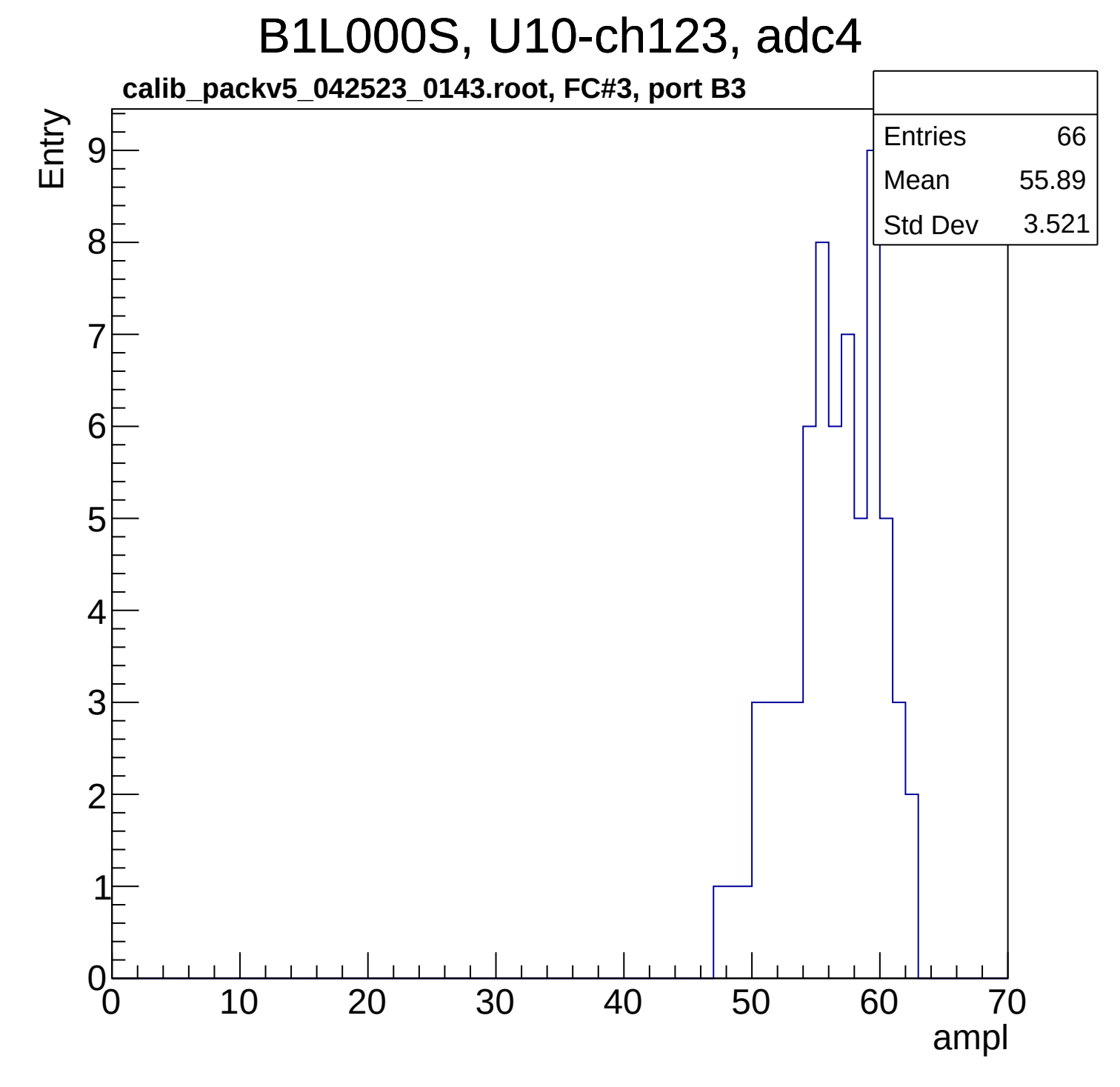
60
70


B1L000S, U10-ch123, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 55.89
Std Dev 3.521
Entry
ampl
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2
3
4
5
6
7
8
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0
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30
40
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60
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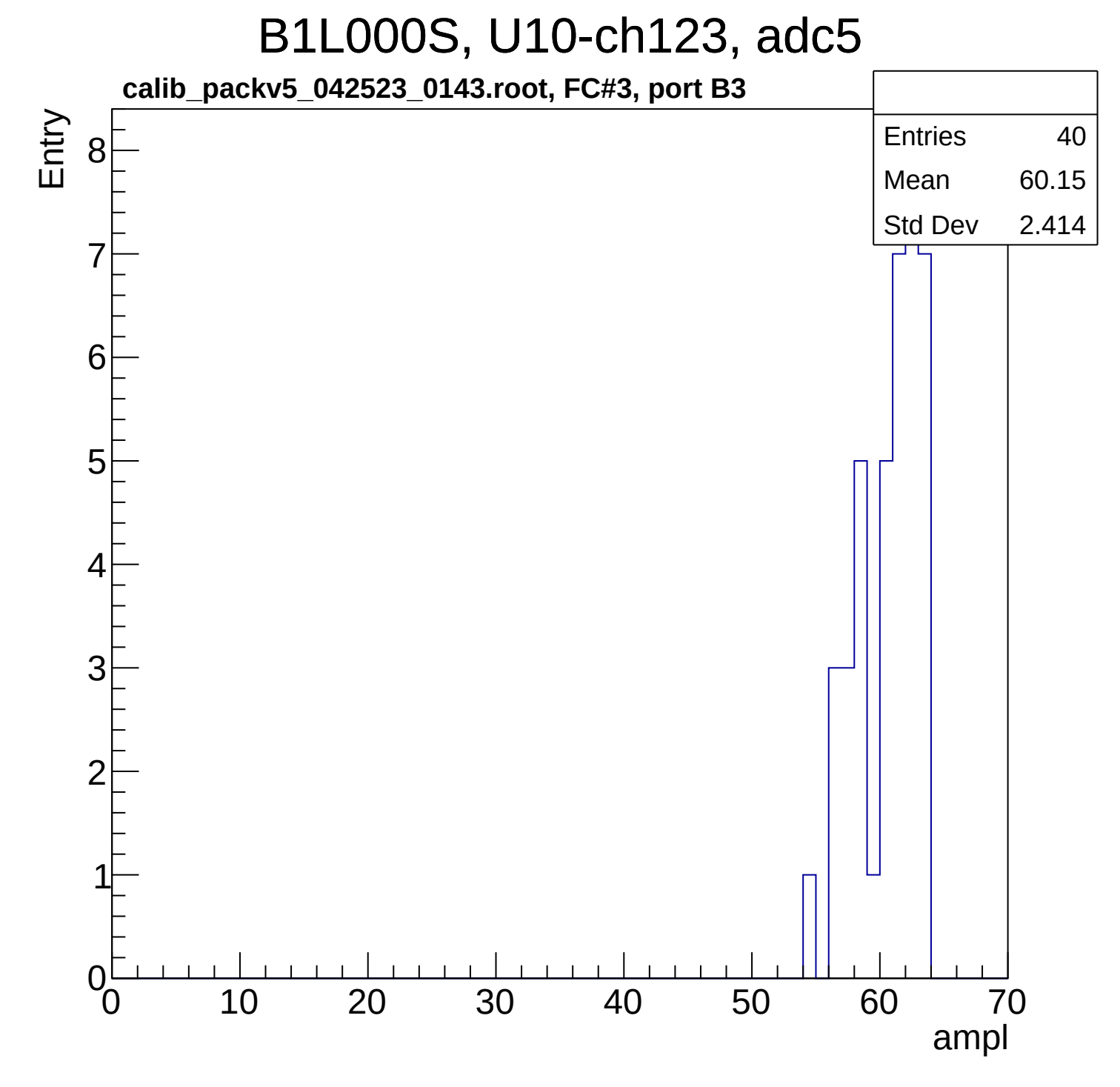
B1L000S, U10-ch123, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 60.15
Std Dev 2.414
Entry
ampl
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2
3
4
5
6
7
8
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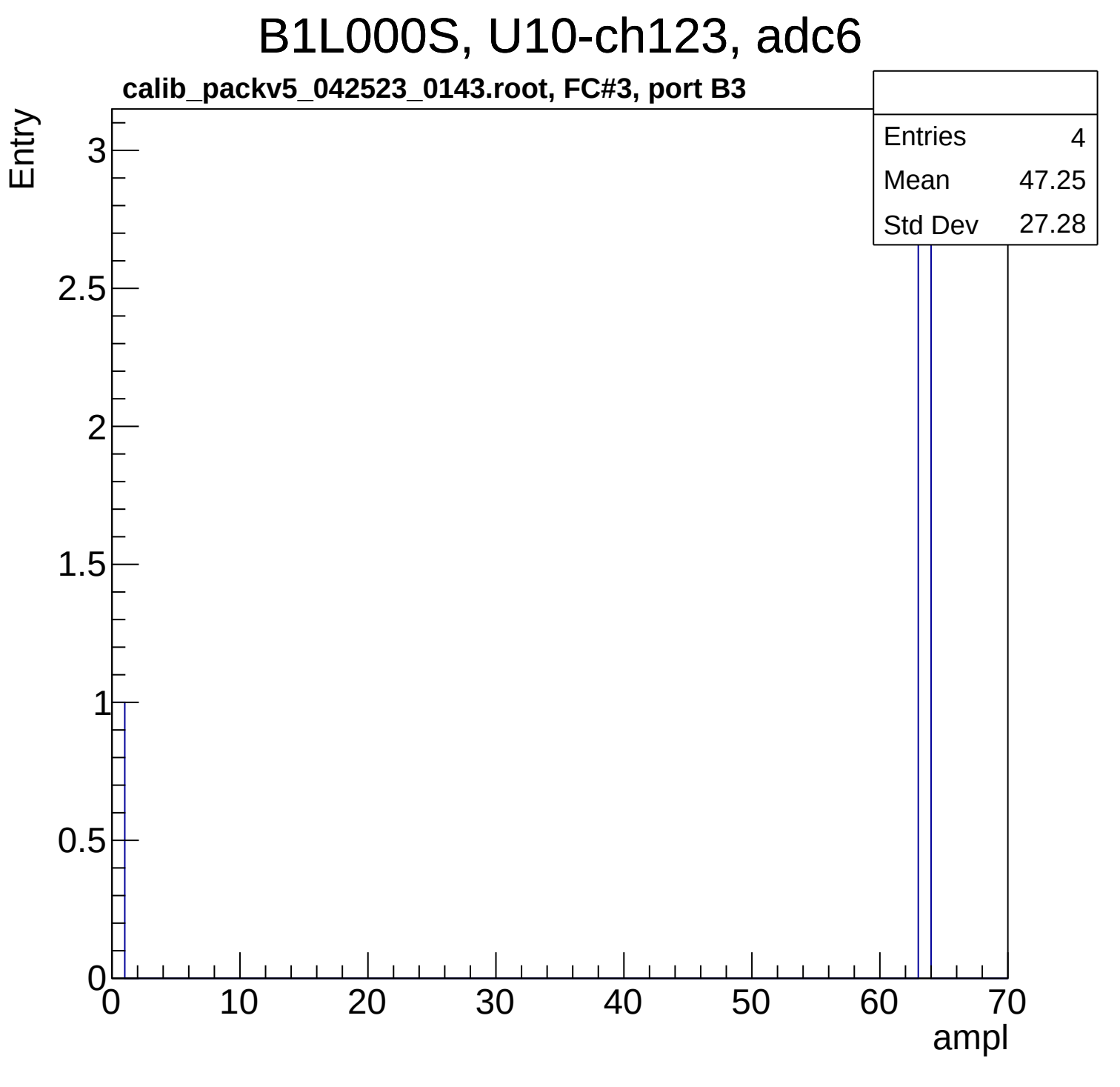
B1L000S, U10-ch123, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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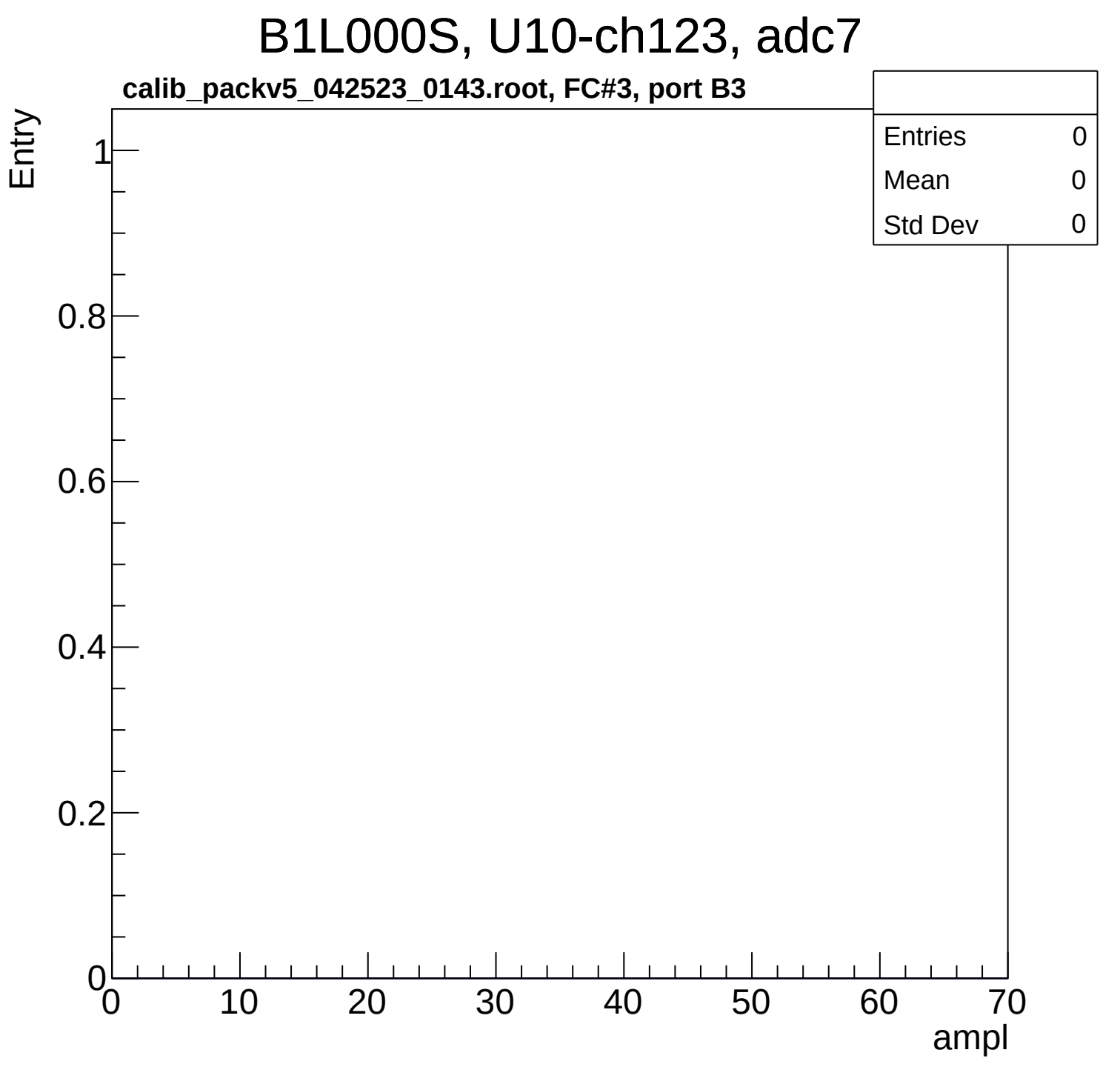
B1L000S, U10-ch123, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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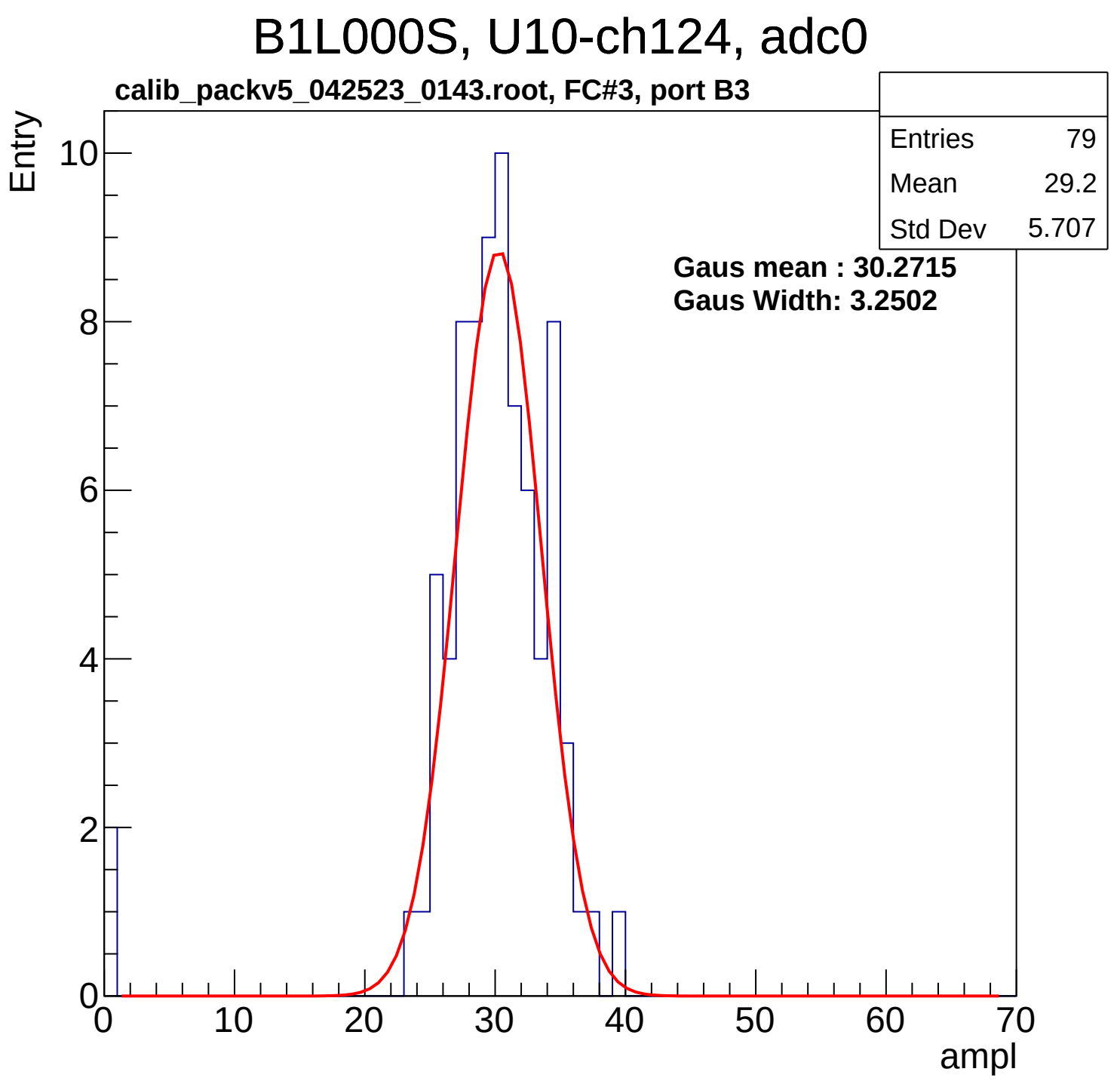
30
40
50
60
70
ampl


B1L000S, U10-ch124, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 29.2
Std Dev 5.707
Gaus mean : 30.2715
Gaus Width: 3.2502
Entry
ampl
0
2
4
6
8
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0
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20
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40
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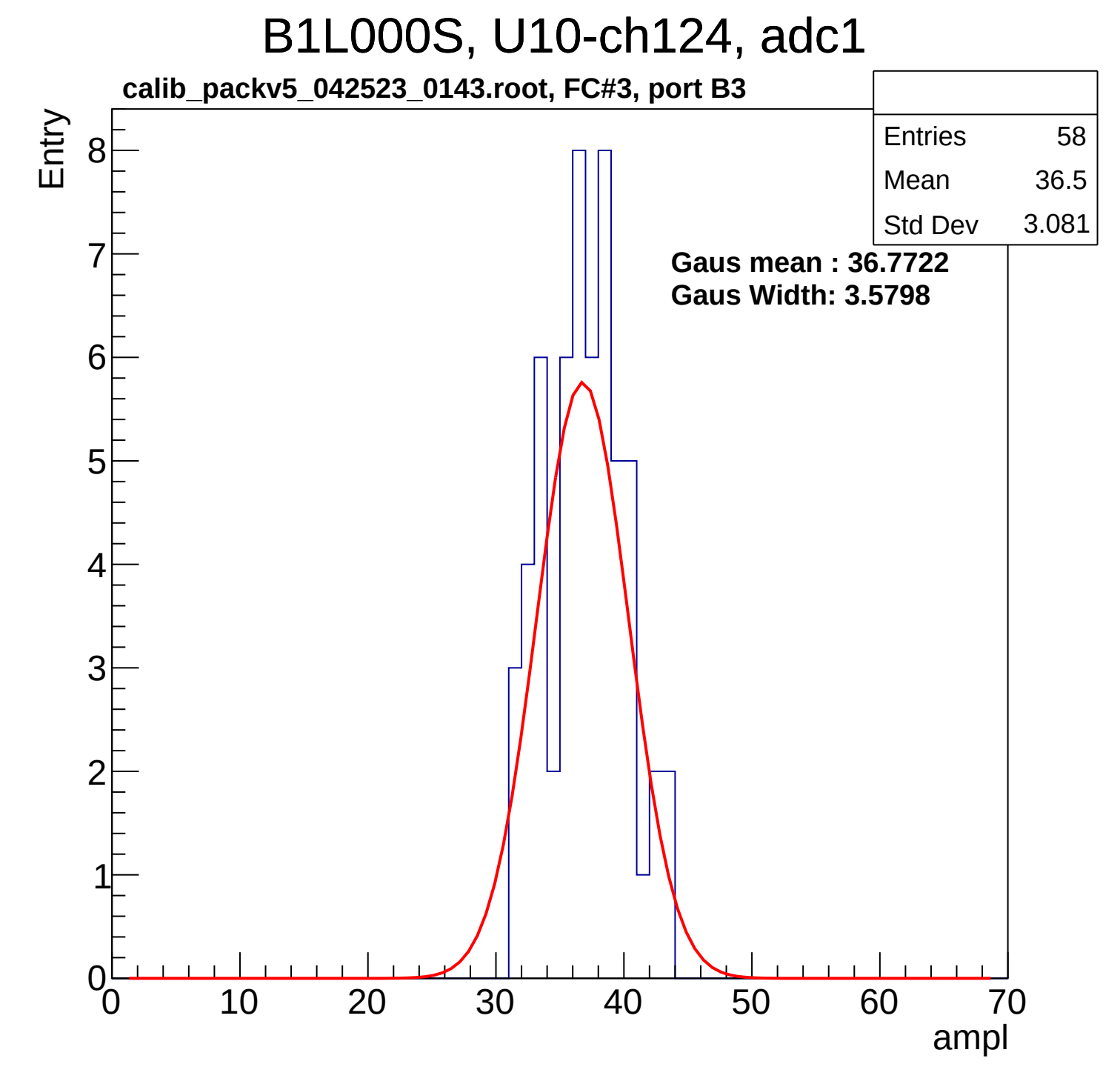
B1L000S, U10-ch124, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 36.5
Std Dev 3.081
Gaus mean : 36.7722
Gaus Width: 3.5798
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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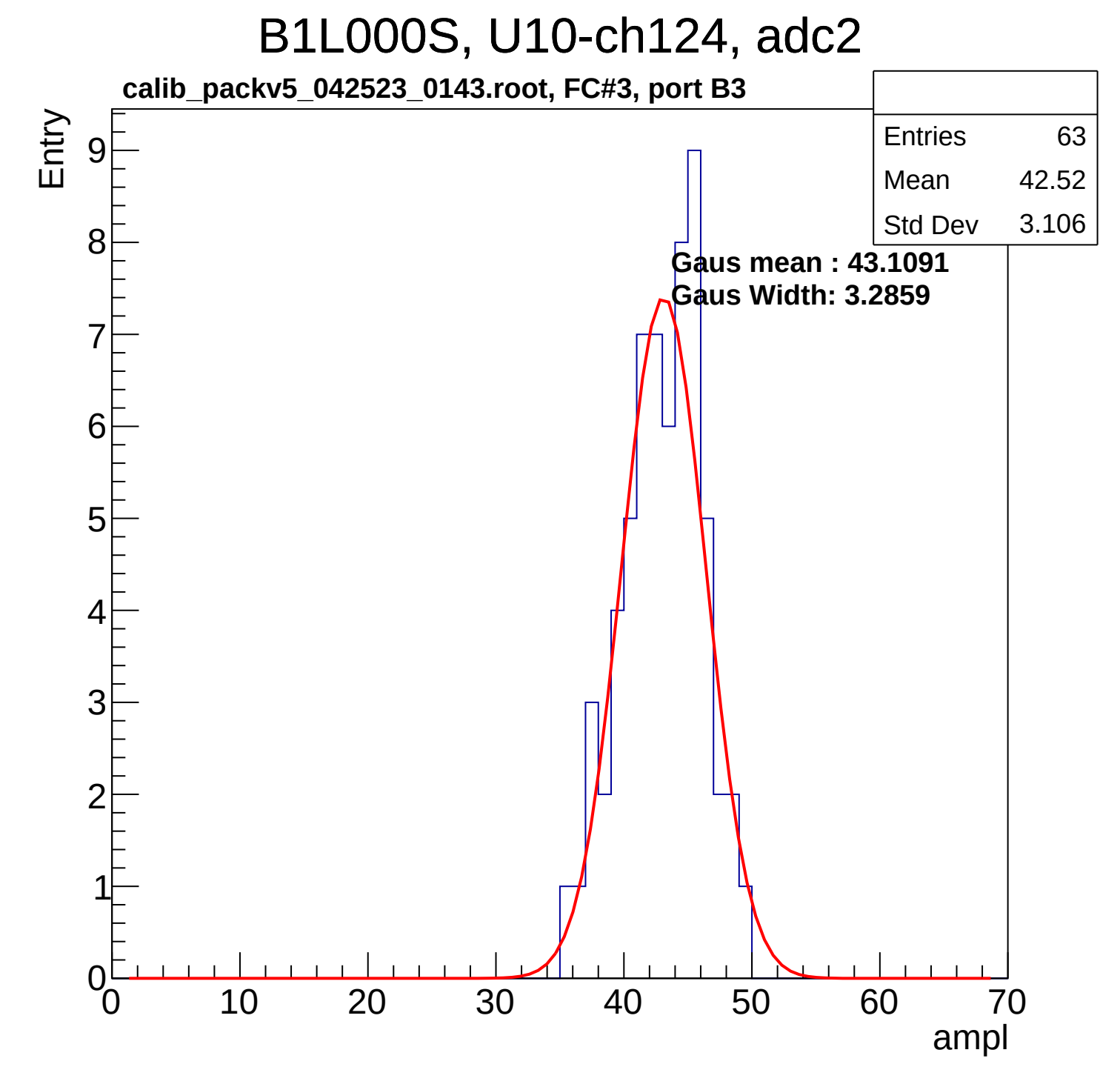
B1L000S, U10-ch124, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 42.52
Std Dev 3.106
Gaus mean : 43.1091
Gaus Width: 3.2859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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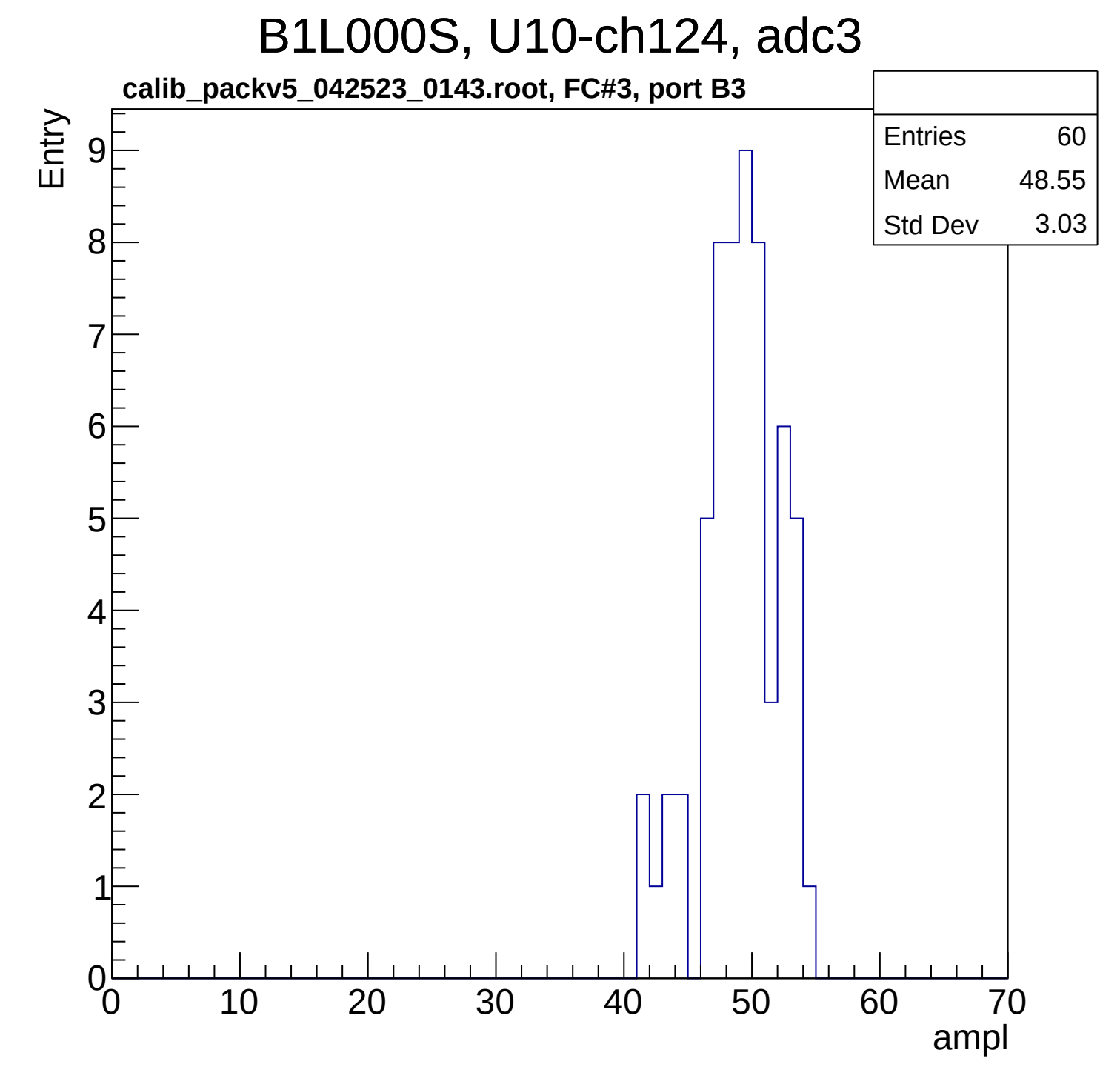
B1L000S, U10-ch124, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.55
Std Dev 3.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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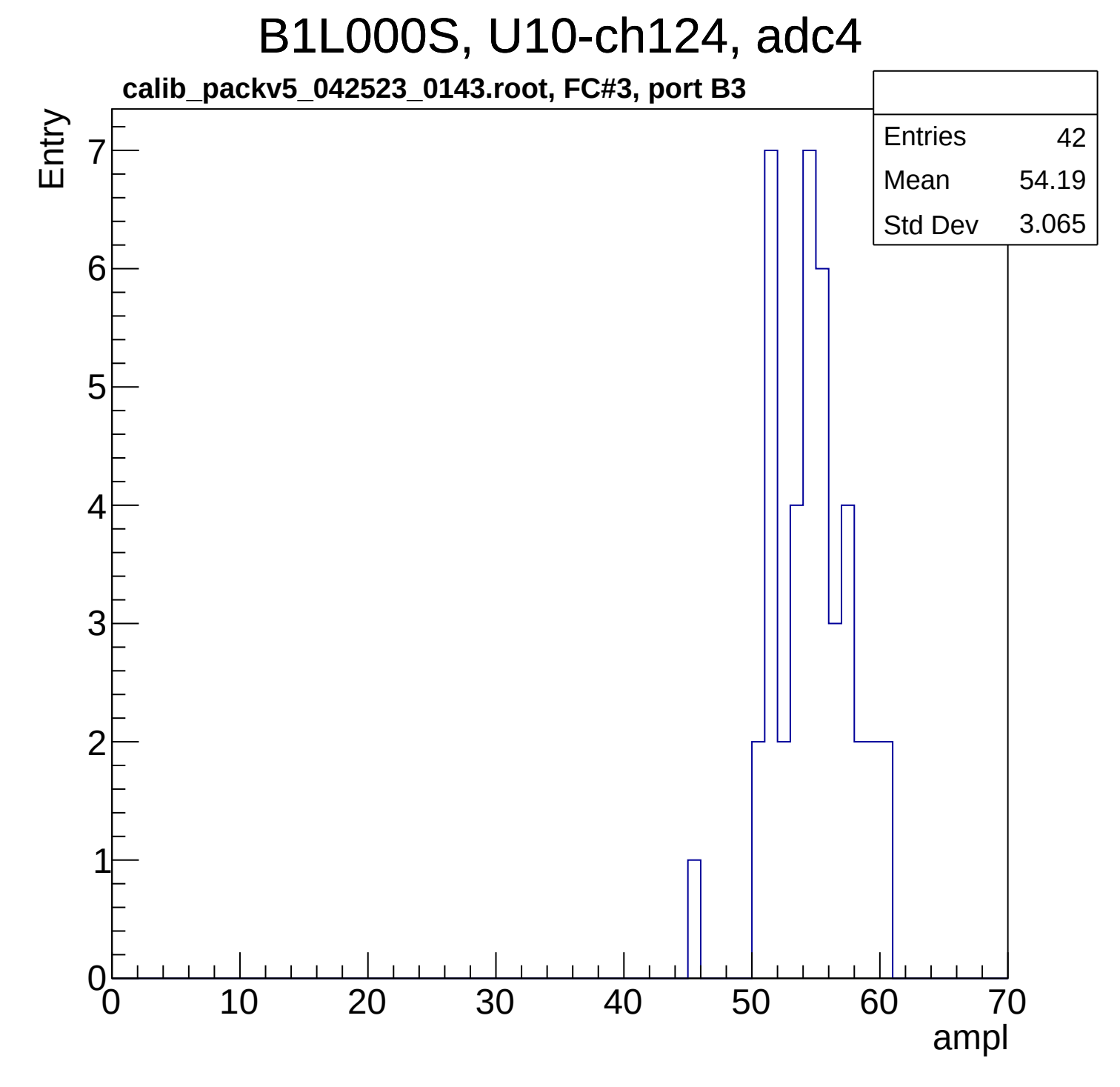
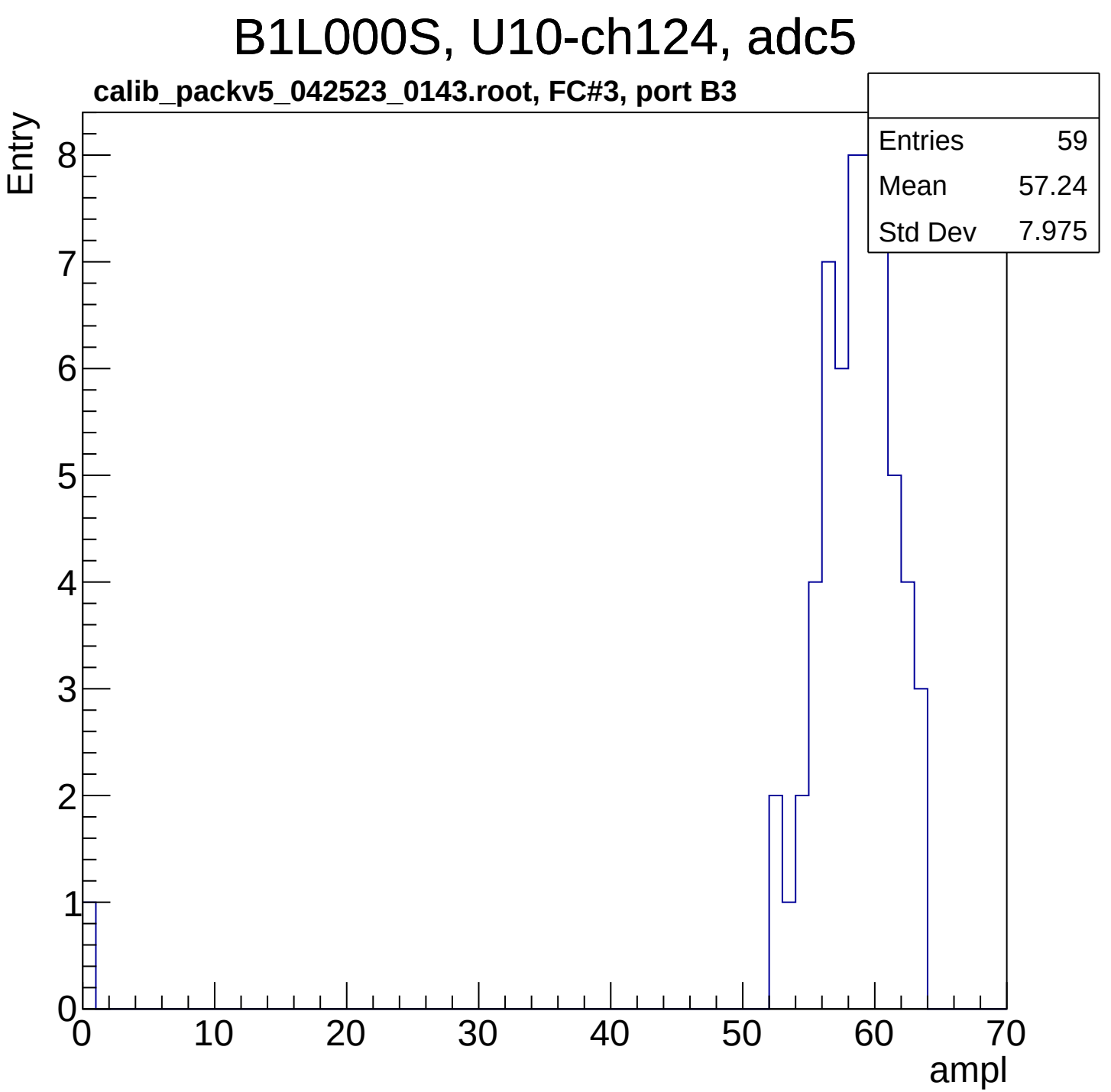

B1L000S, U10-ch124, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 54.19
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch124, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.24
Std Dev 7.975
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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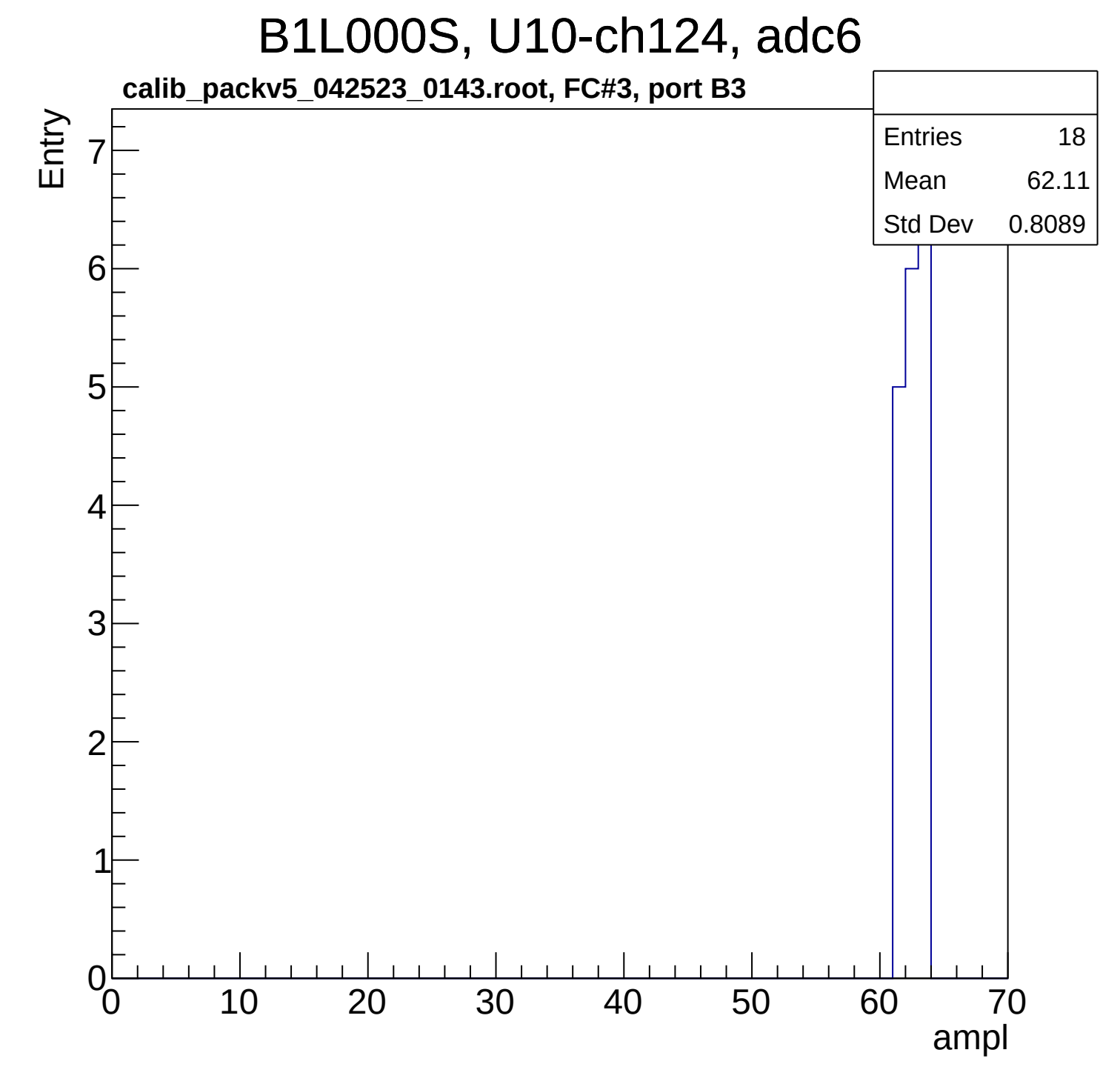
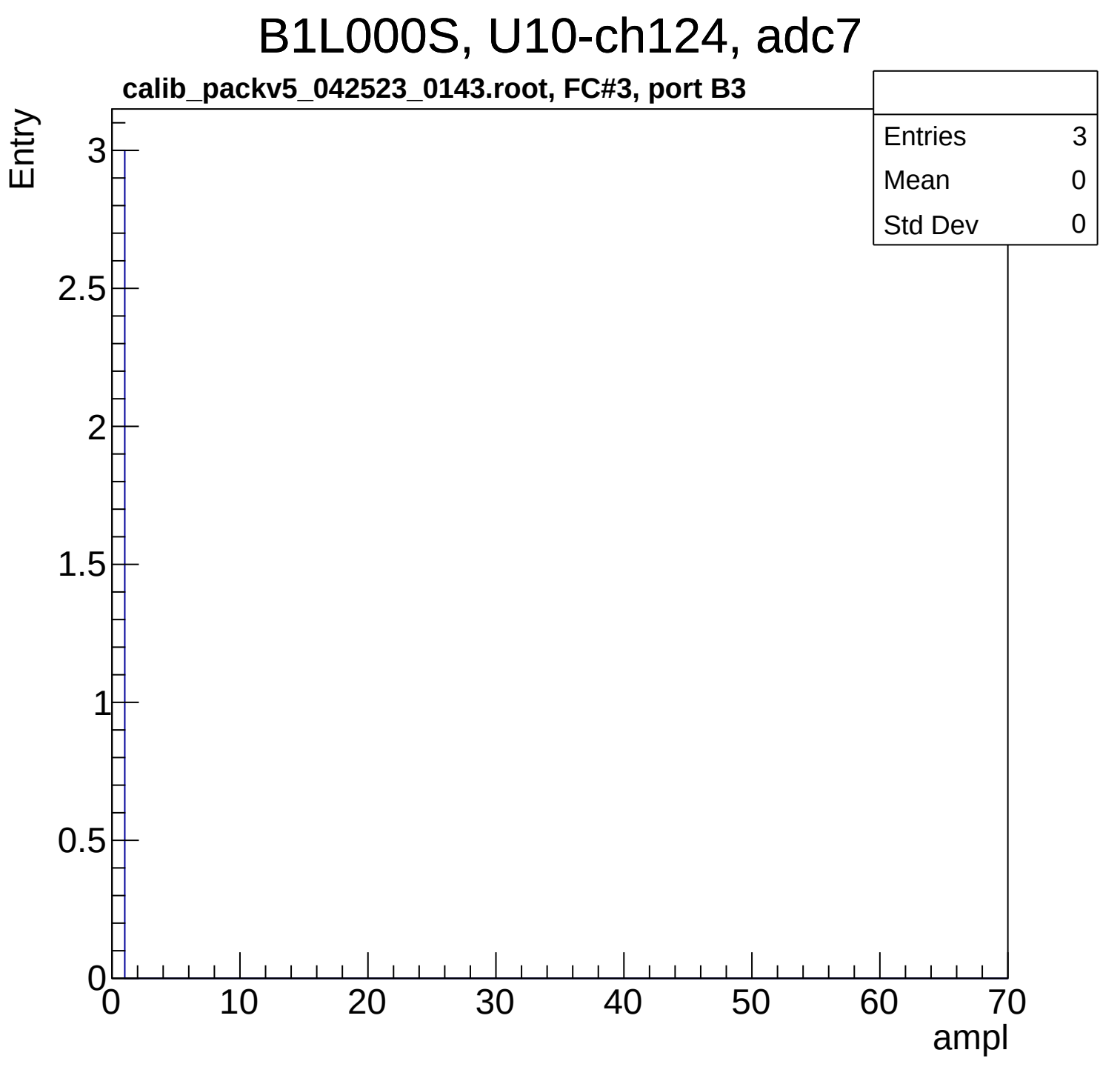
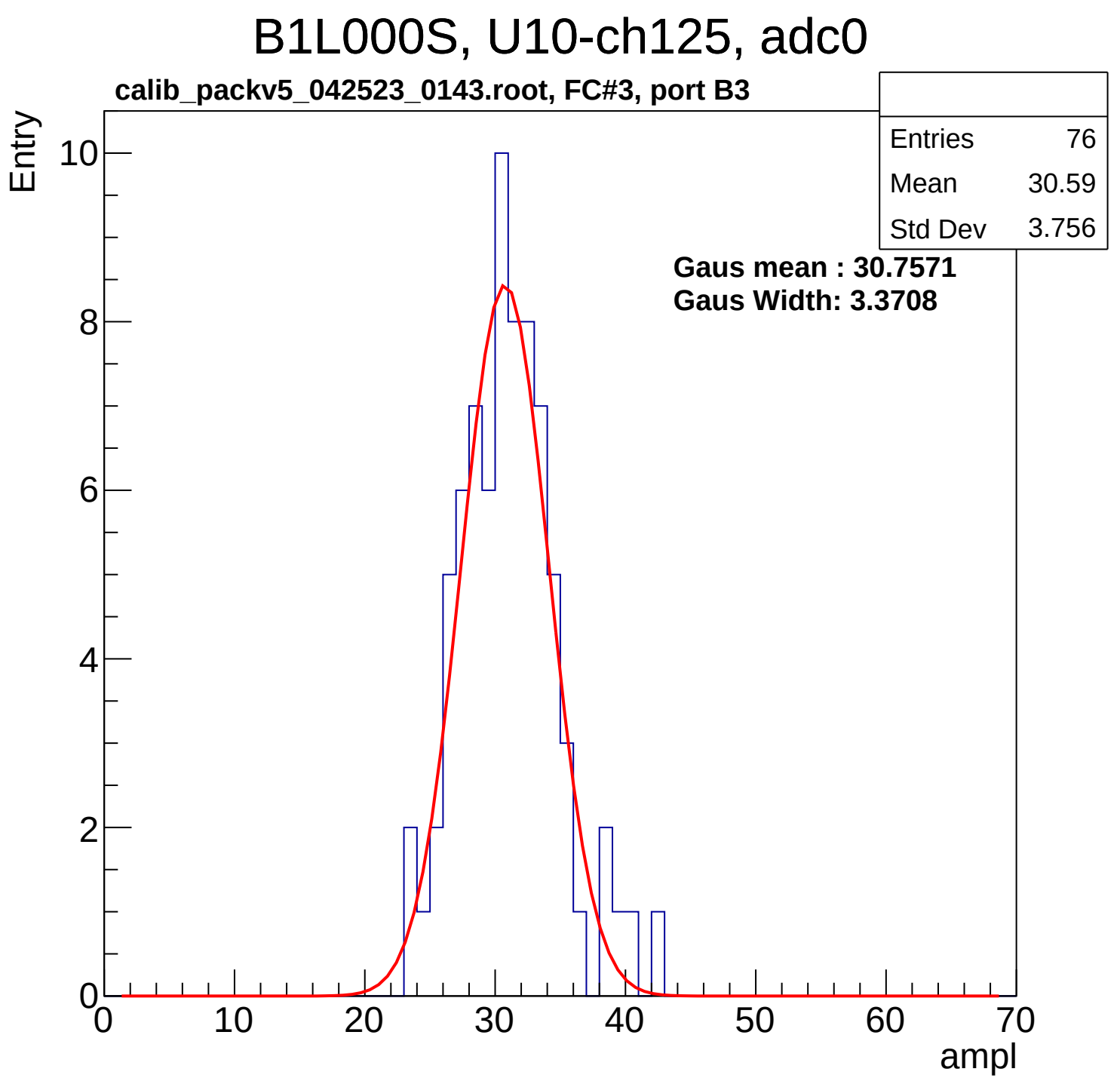
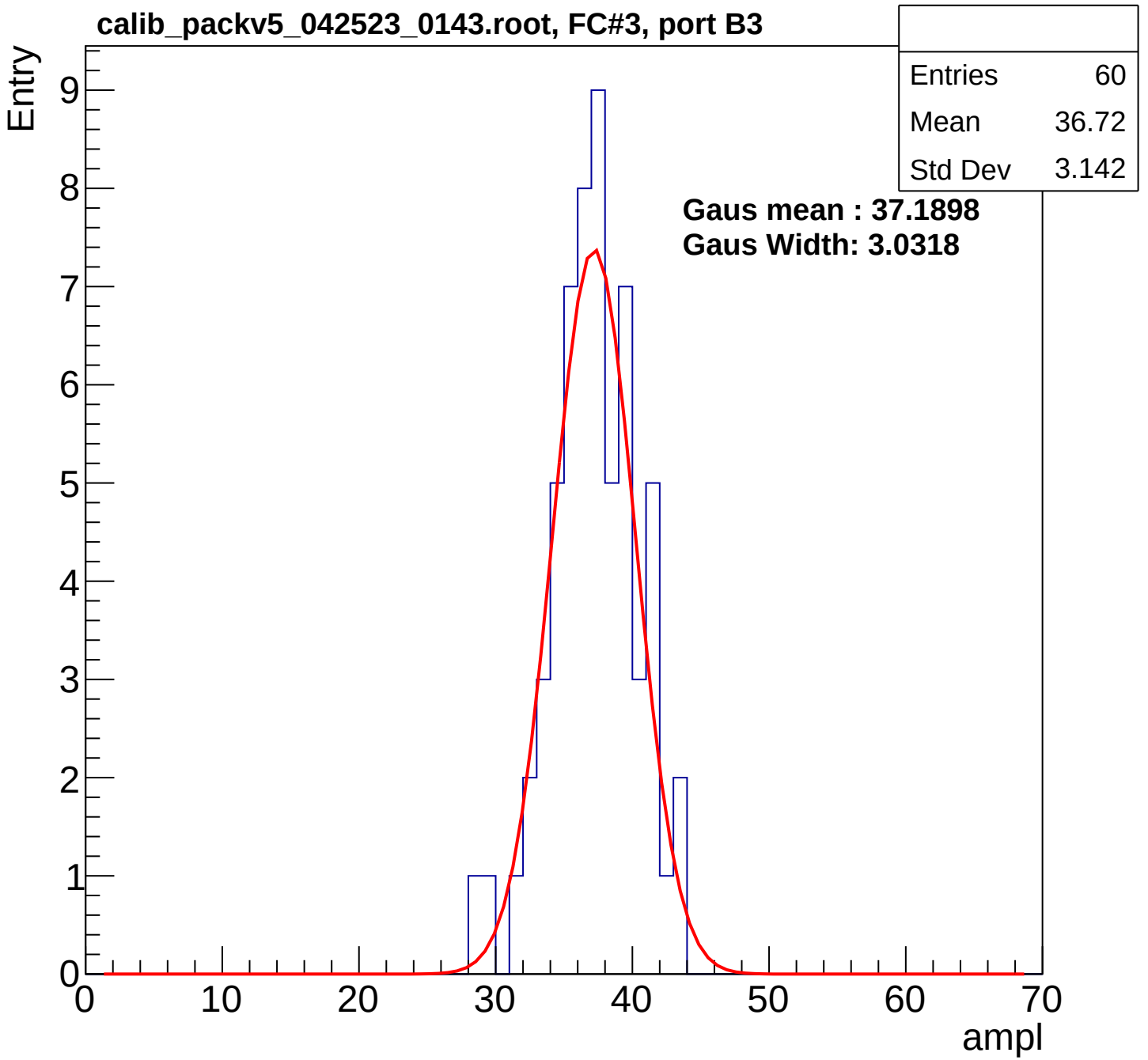

B1L000S, U10-ch124, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 62.11
Std Dev 0.8089
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U10-ch124, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch125, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 30.59
Std Dev 3.756
Gaus mean : 30.7571
Gaus Width: 3.3708
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch125, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 36.72
Std Dev 3.142
Gaus mean : 37.1898
Gaus Width: 3.0318
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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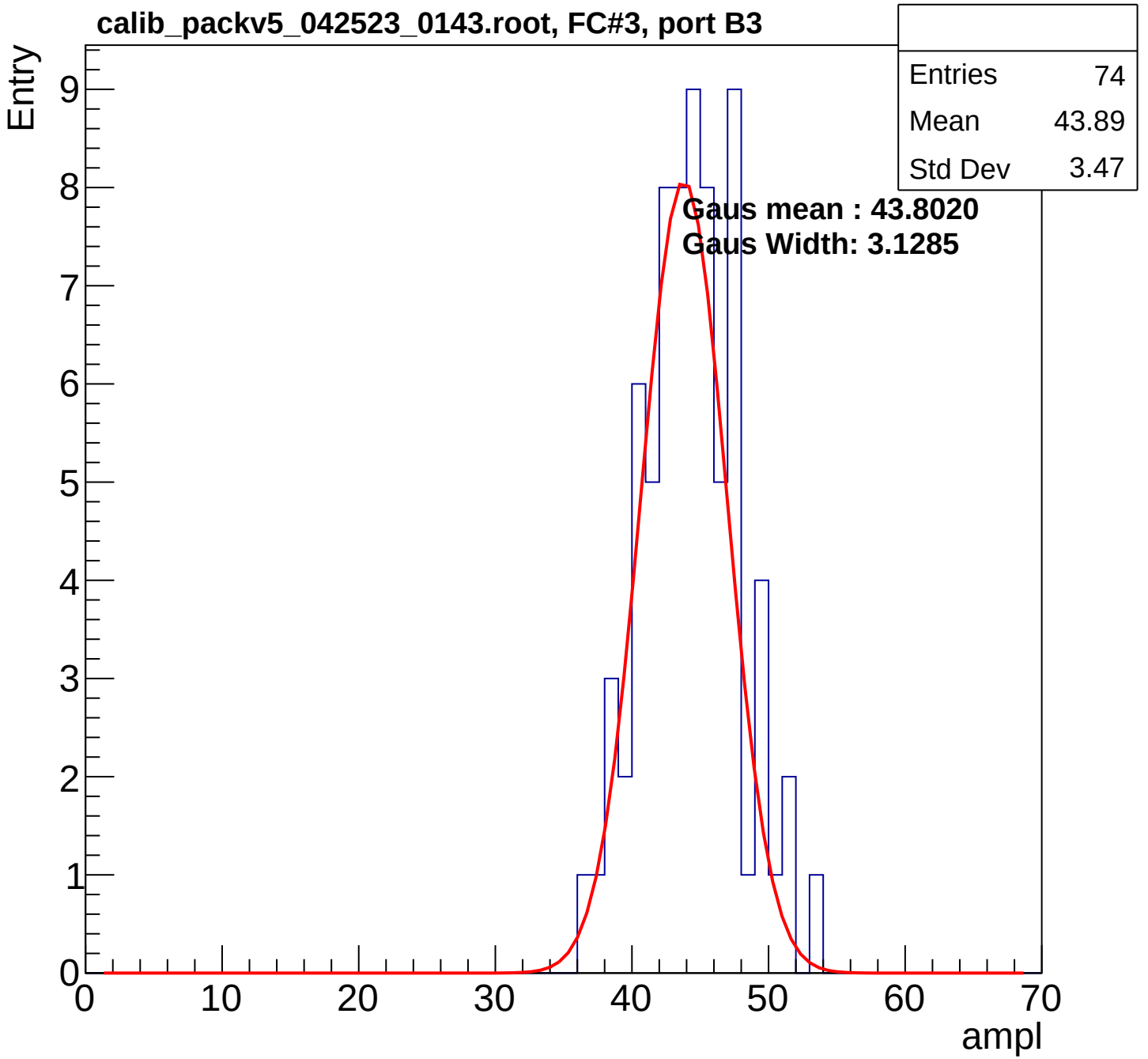
B1L000S, U10-ch125, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 43.89
Std Dev 3.47
Gaus mean : 43.8020
Gaus Width: 3.1285
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
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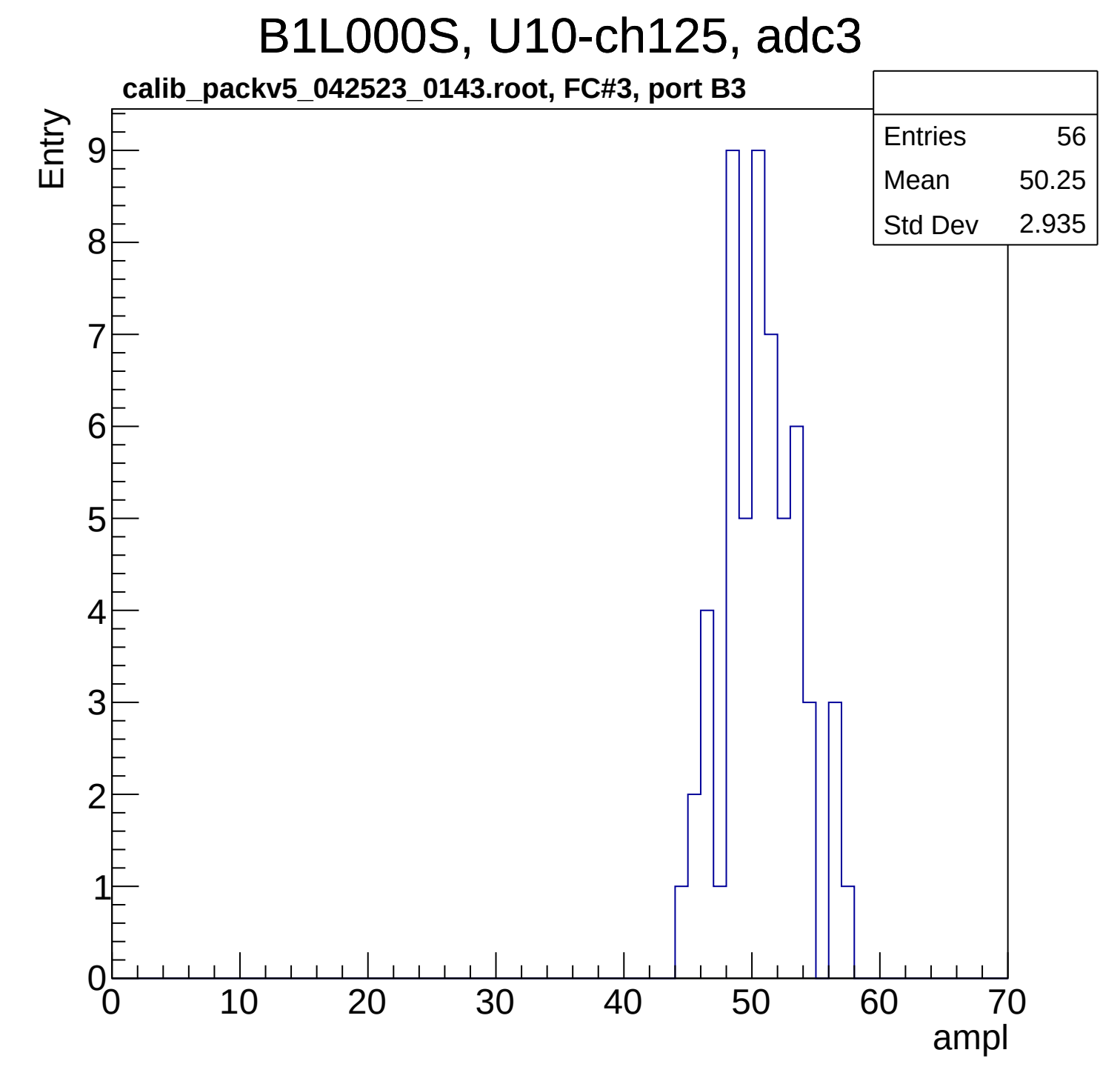
B1L000S, U10-ch125, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 50.25
Std Dev 2.935
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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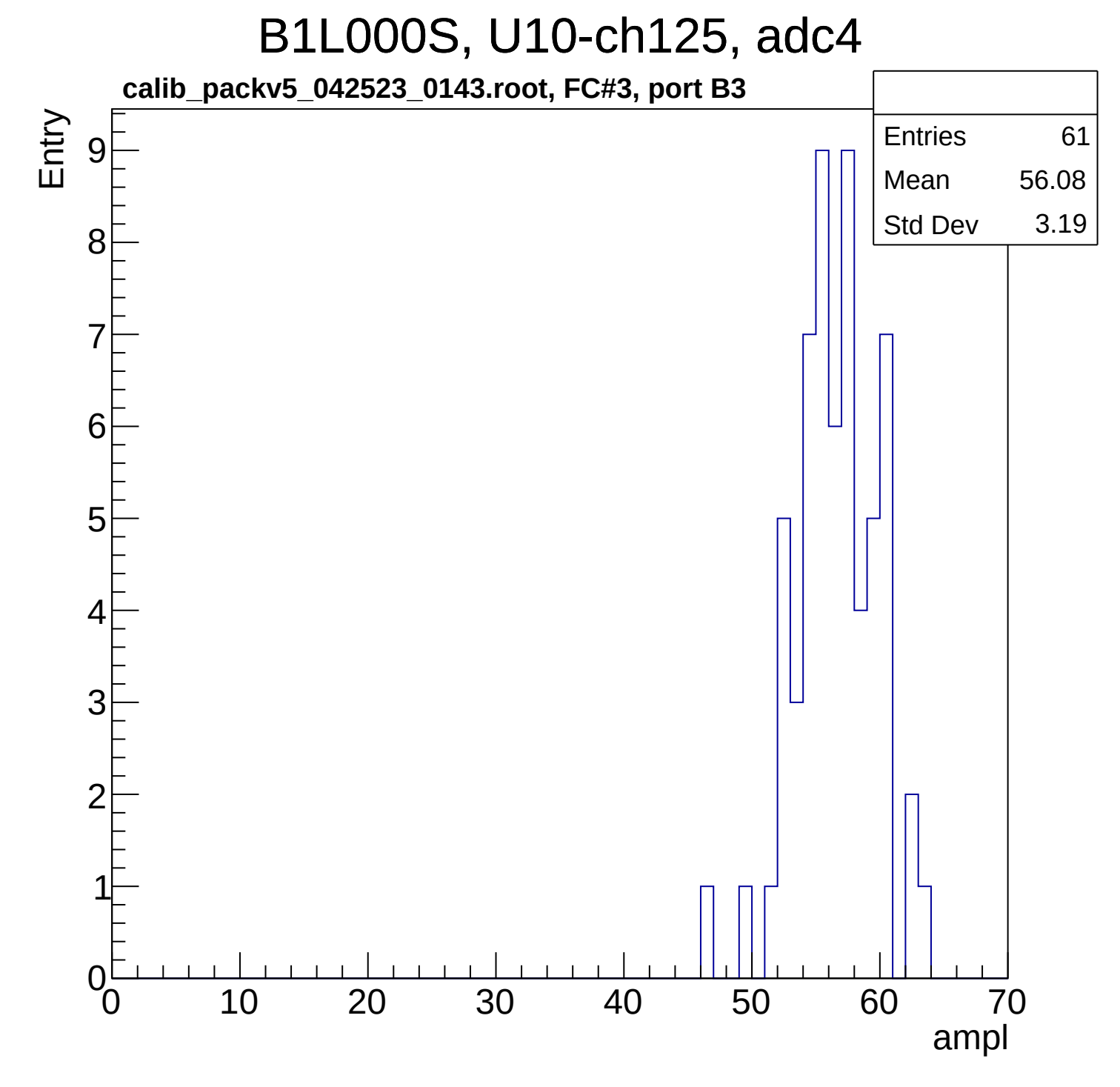
B1L000S, U10-ch125, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 56.08
Std Dev 3.19
Entry
ampl
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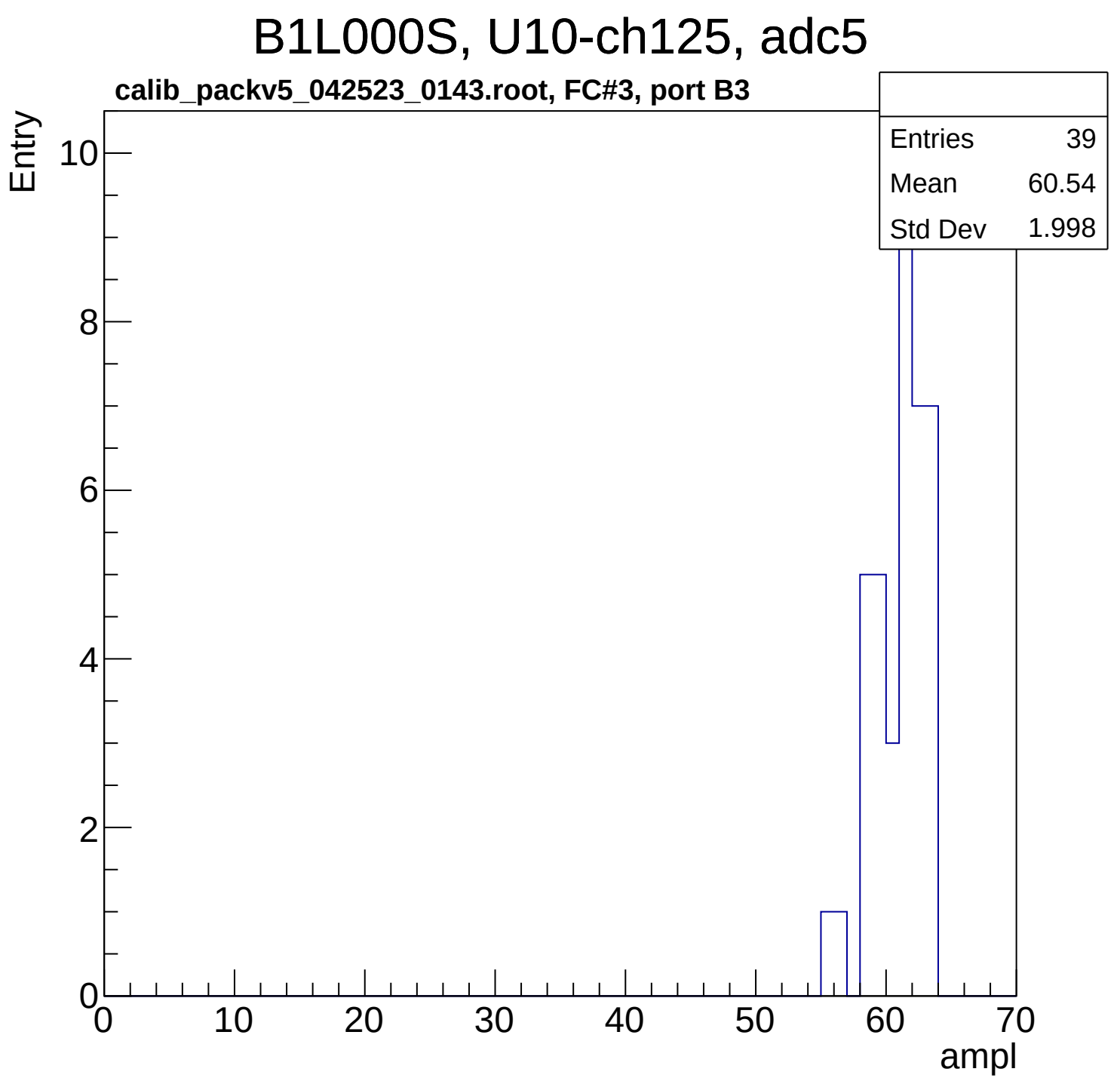
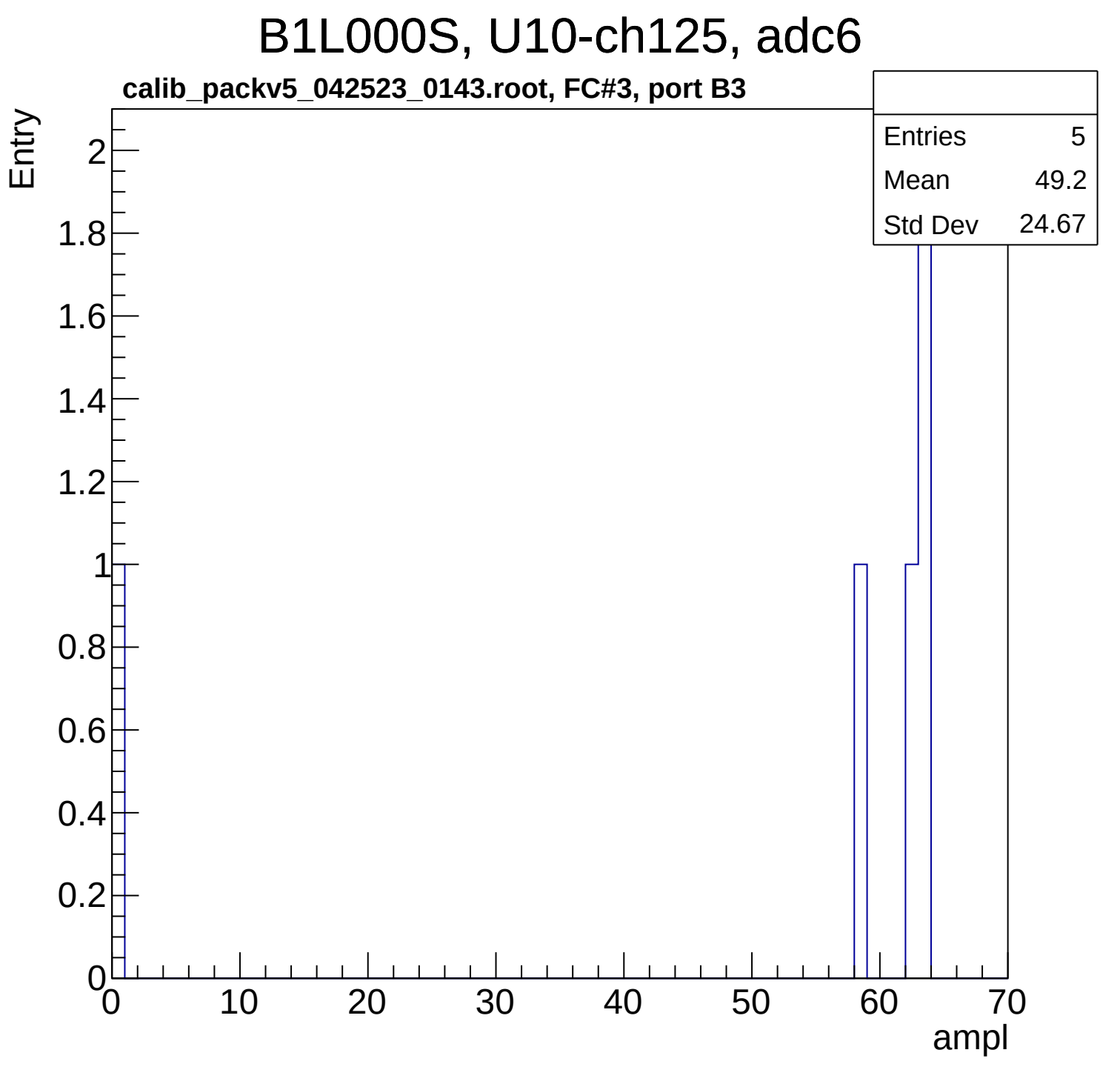
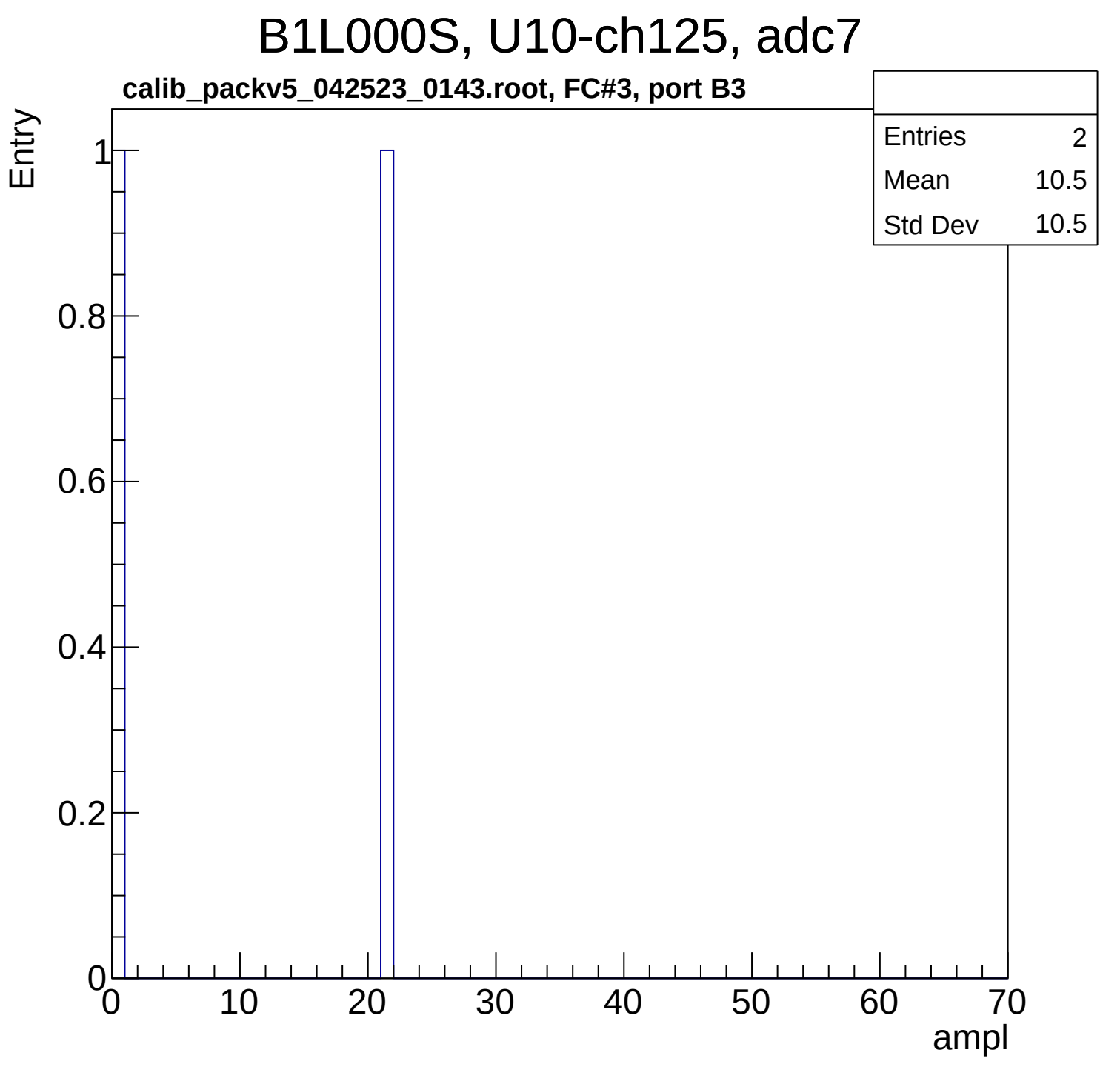
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch125, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 60.54
Std Dev 1.998
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch125, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 49.2
Std Dev 24.67
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U10-ch125, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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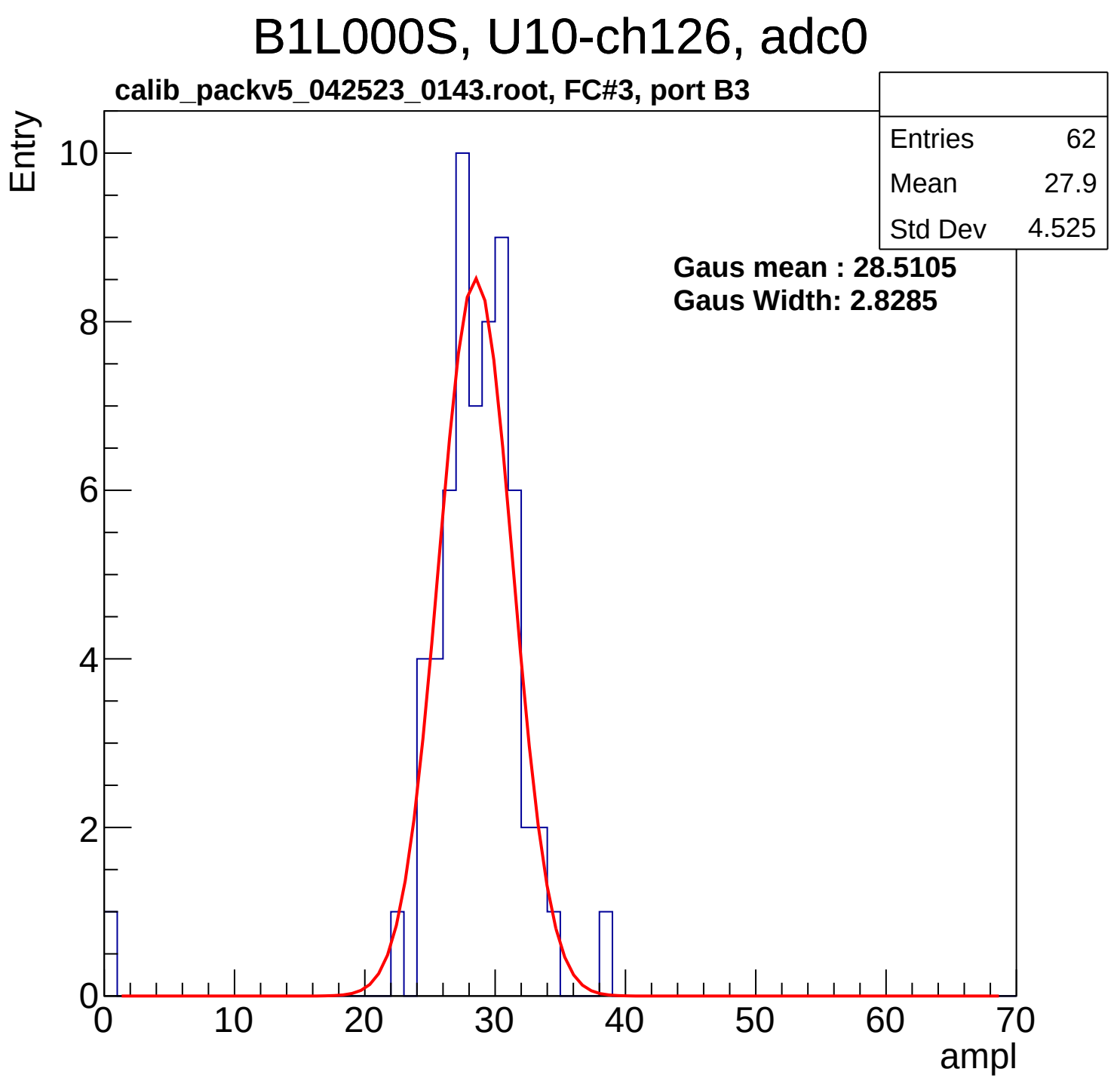
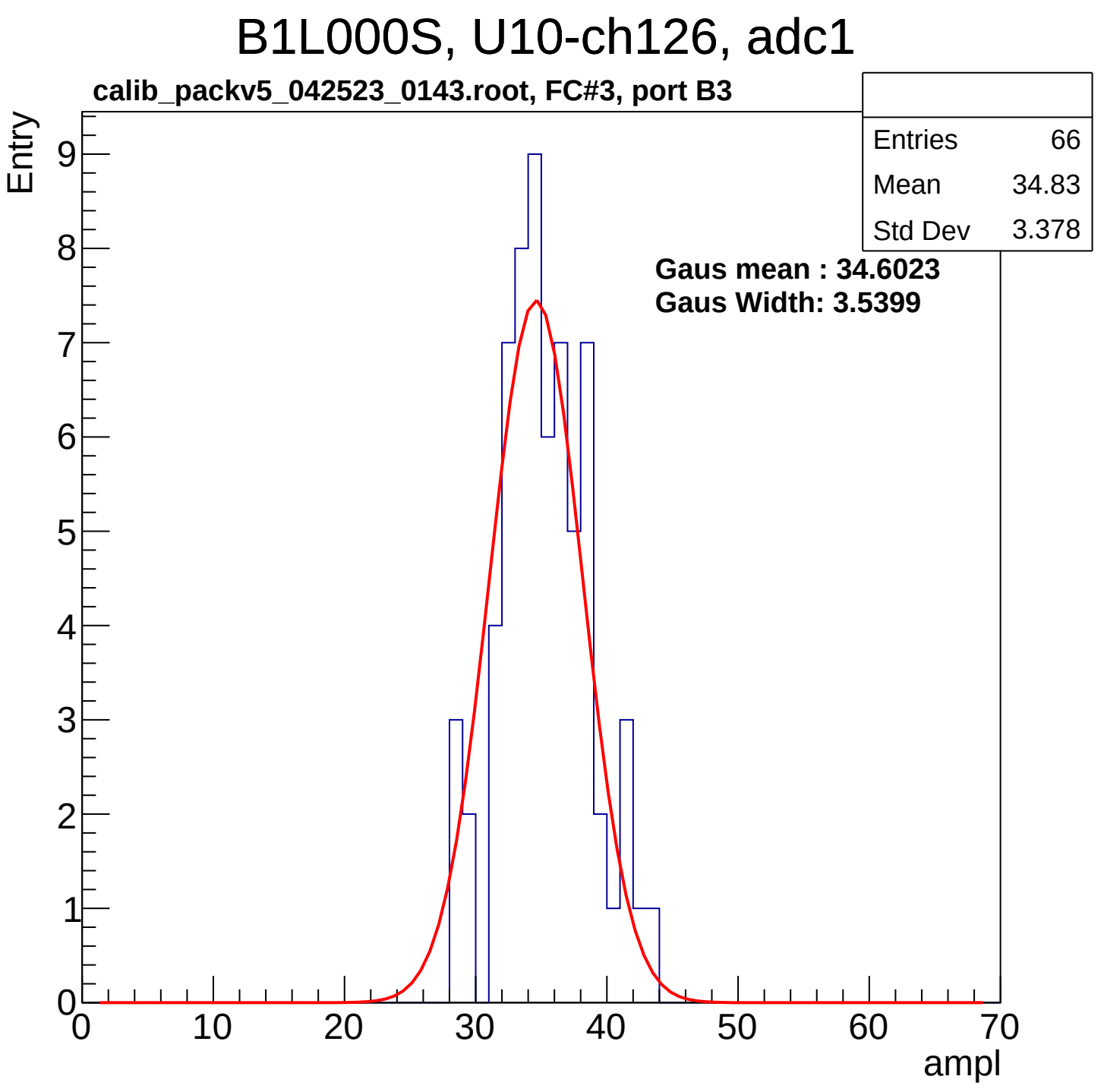

B1L000S, U10-ch126, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.9
Std Dev 4.525
Gaus mean : 28.5105
Gaus Width: 2.8285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U10-ch126, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.83
Std Dev 3.378
Gaus mean : 34.6023
Gaus Width: 3.5399
Entry
ampl
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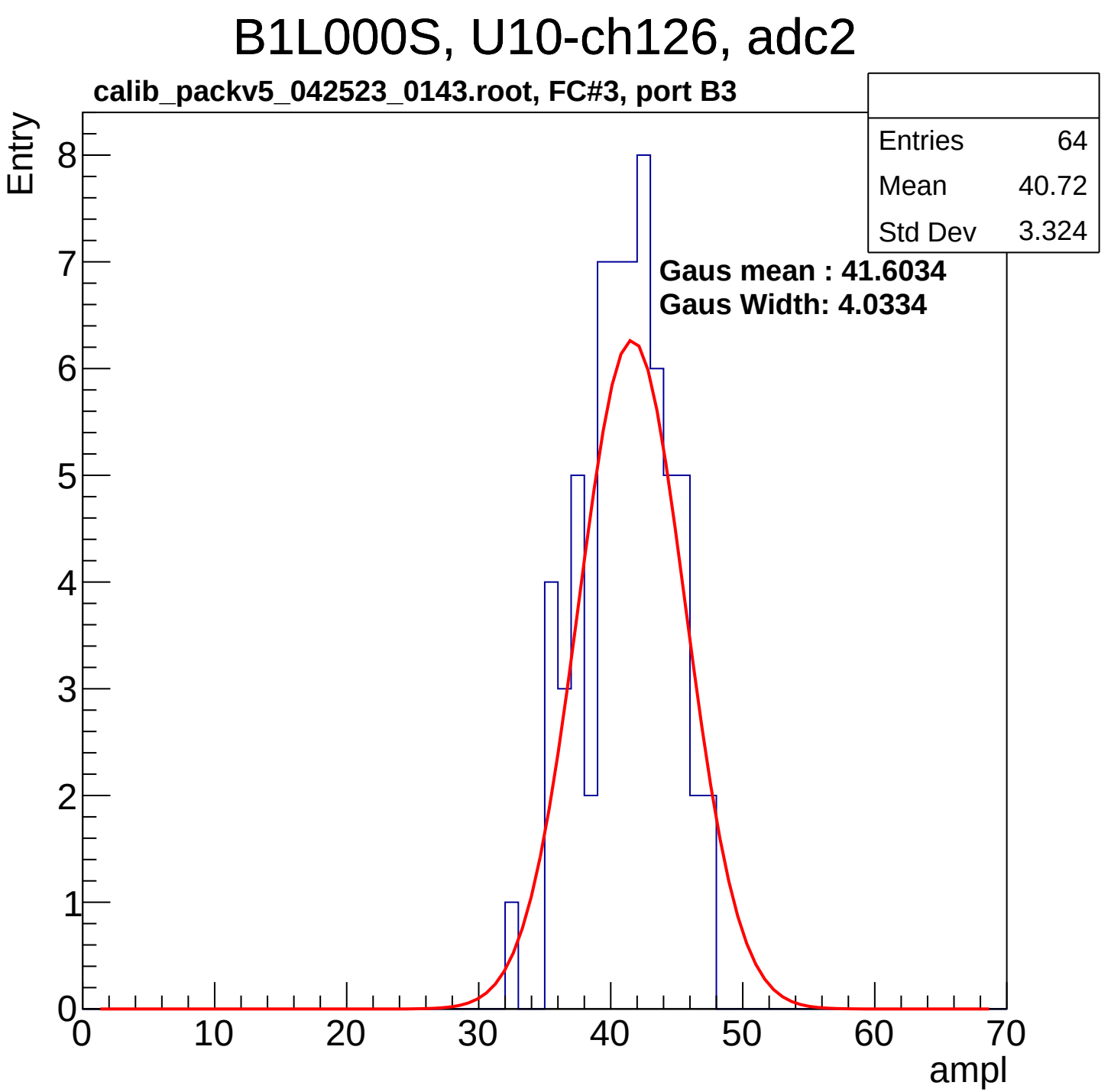
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U10-ch126, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 40.72
Std Dev 3.324
Gaus mean : 41.6034
Gaus Width: 4.0334
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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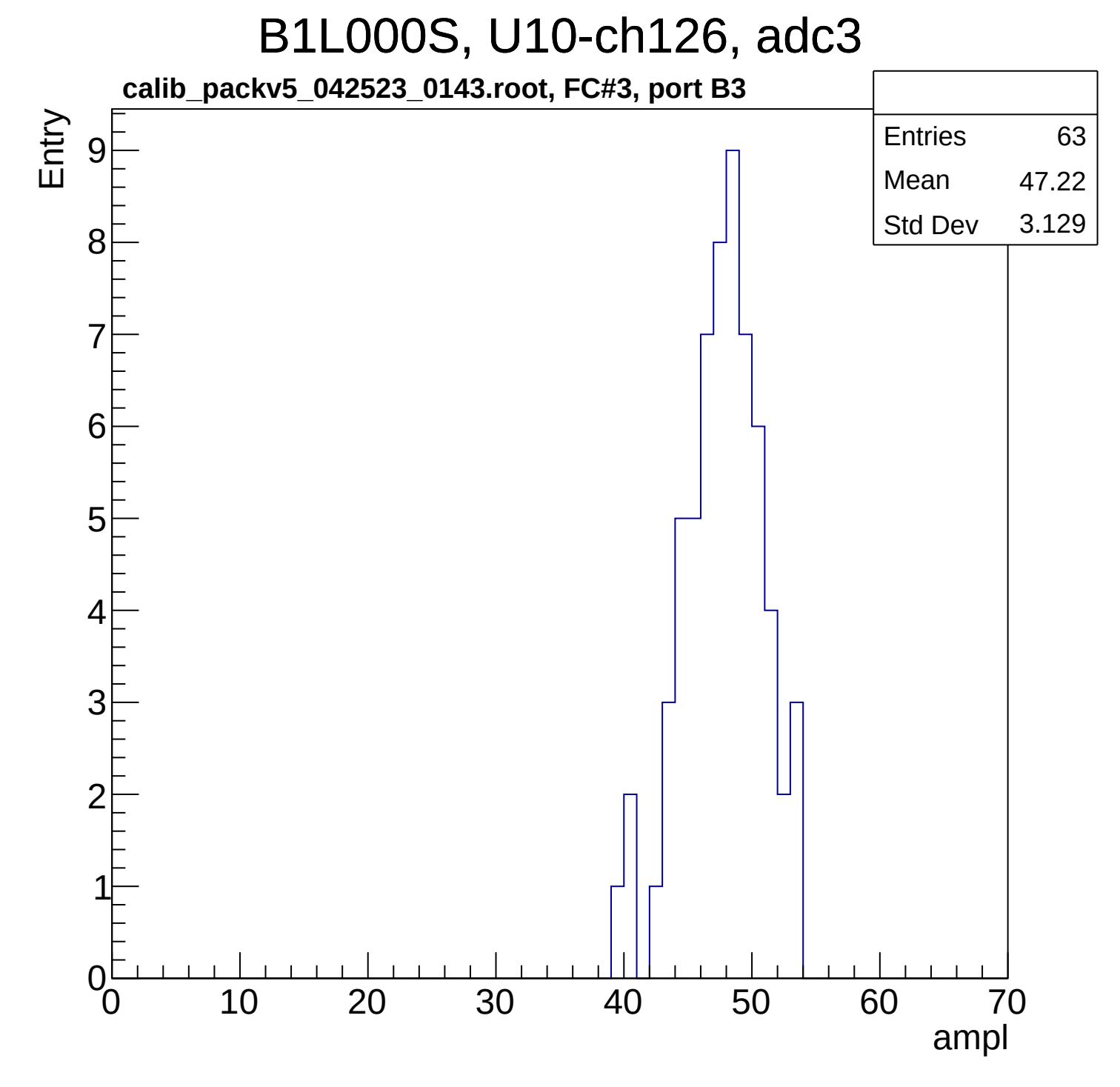
B1L000S, U10-ch126, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.22
Std Dev 3.129
Entry
ampl
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2
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4
5
6
7
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0
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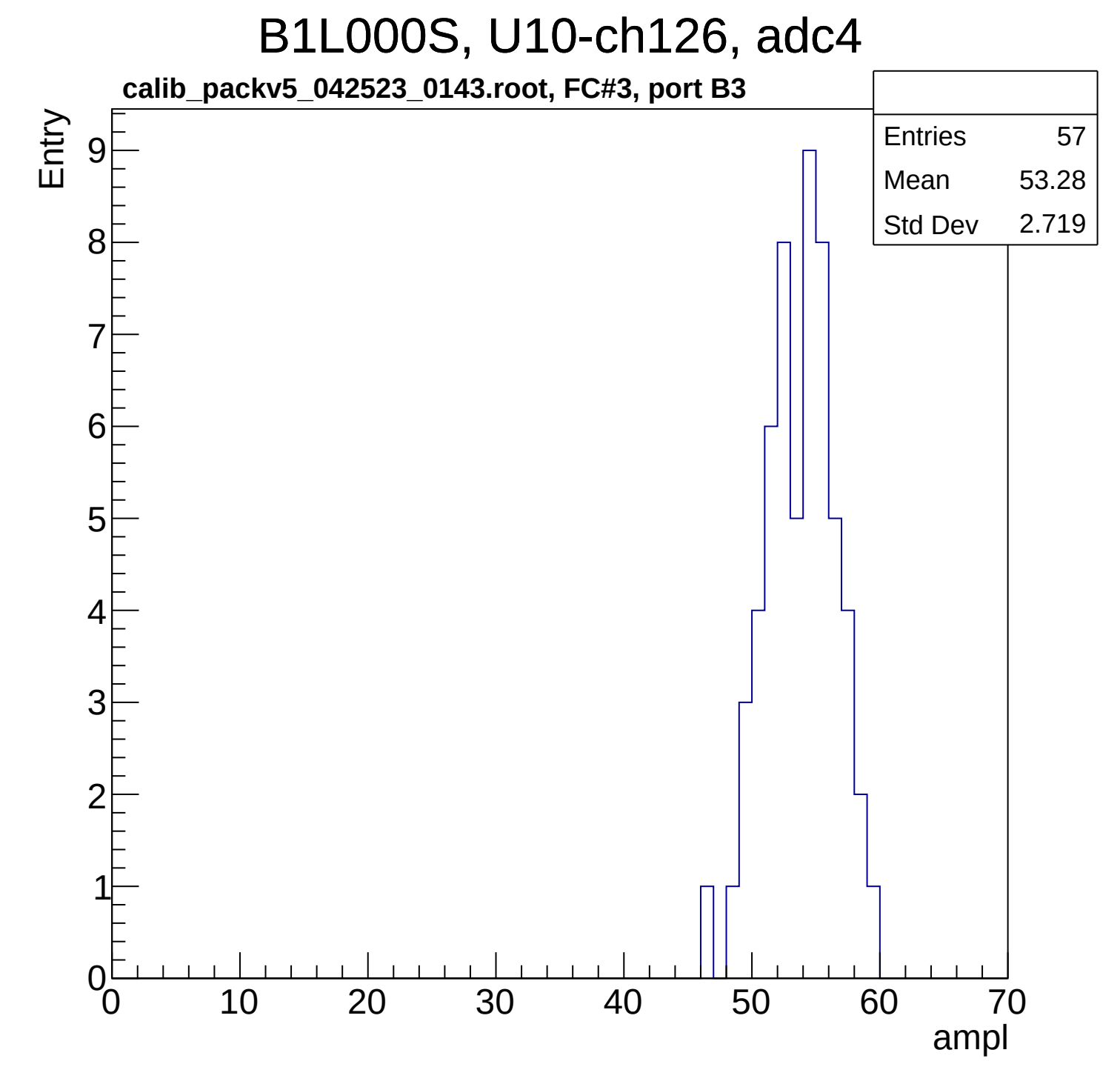
B1L000S, U10-ch126, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.28
Std Dev 2.719
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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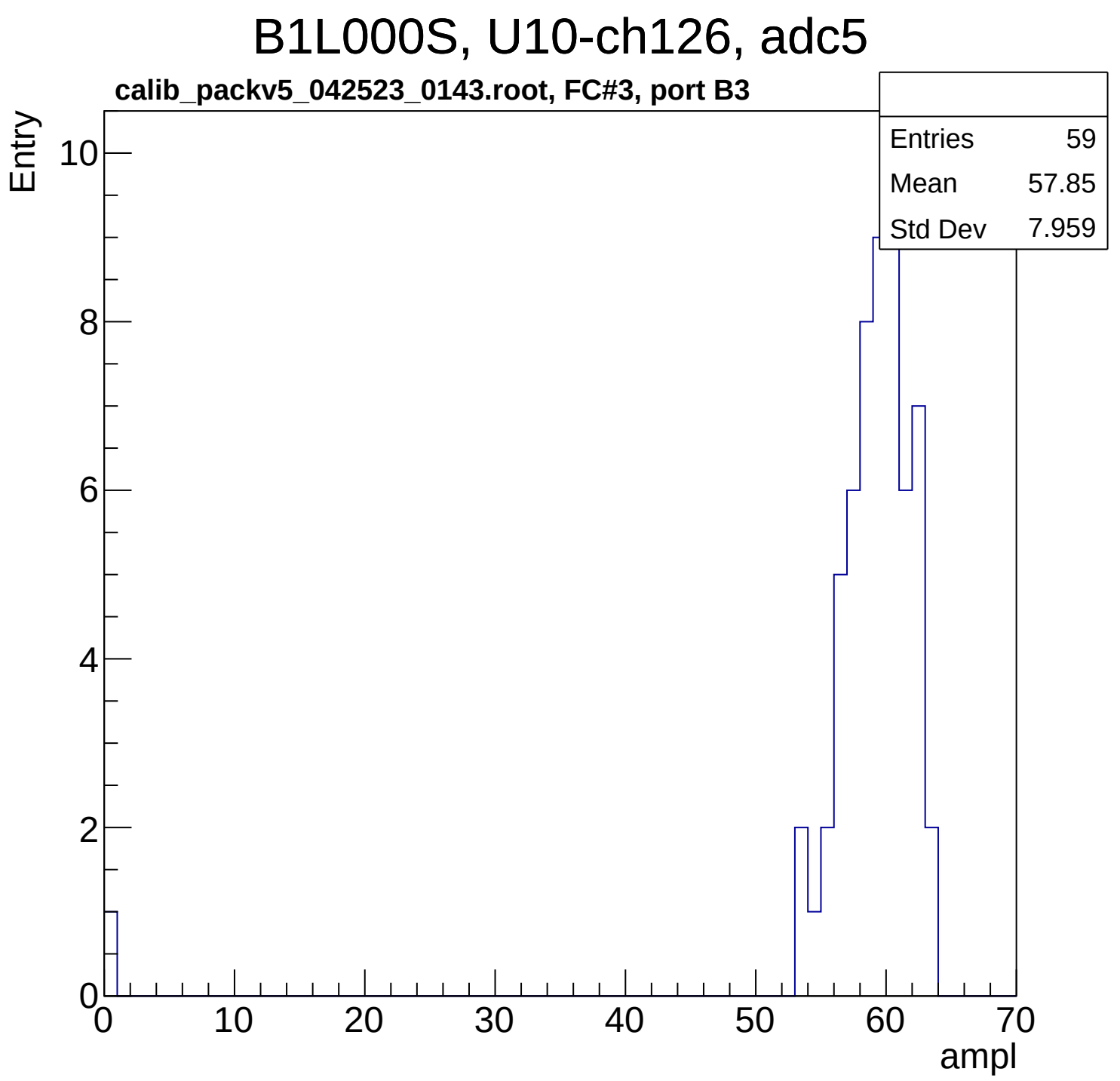
B1L000S, U10-ch126, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 57.85
Std Dev 7.959
Entry
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2
4
6
8
10
0
10
20
30
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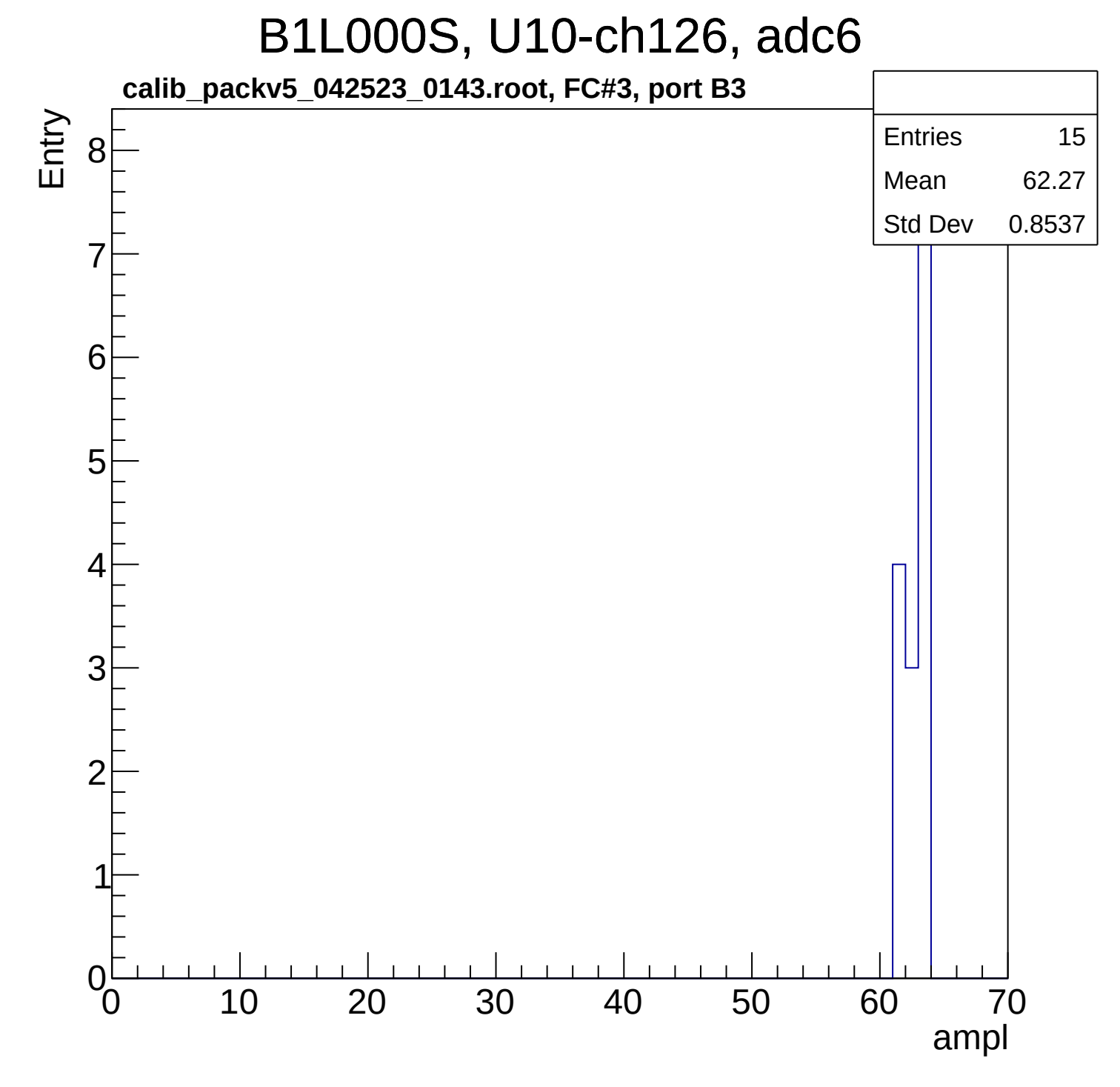
50
60
70
ampl


B1L000S, U10-ch126, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 62.27
Std Dev 0.8537
Entry
ampl
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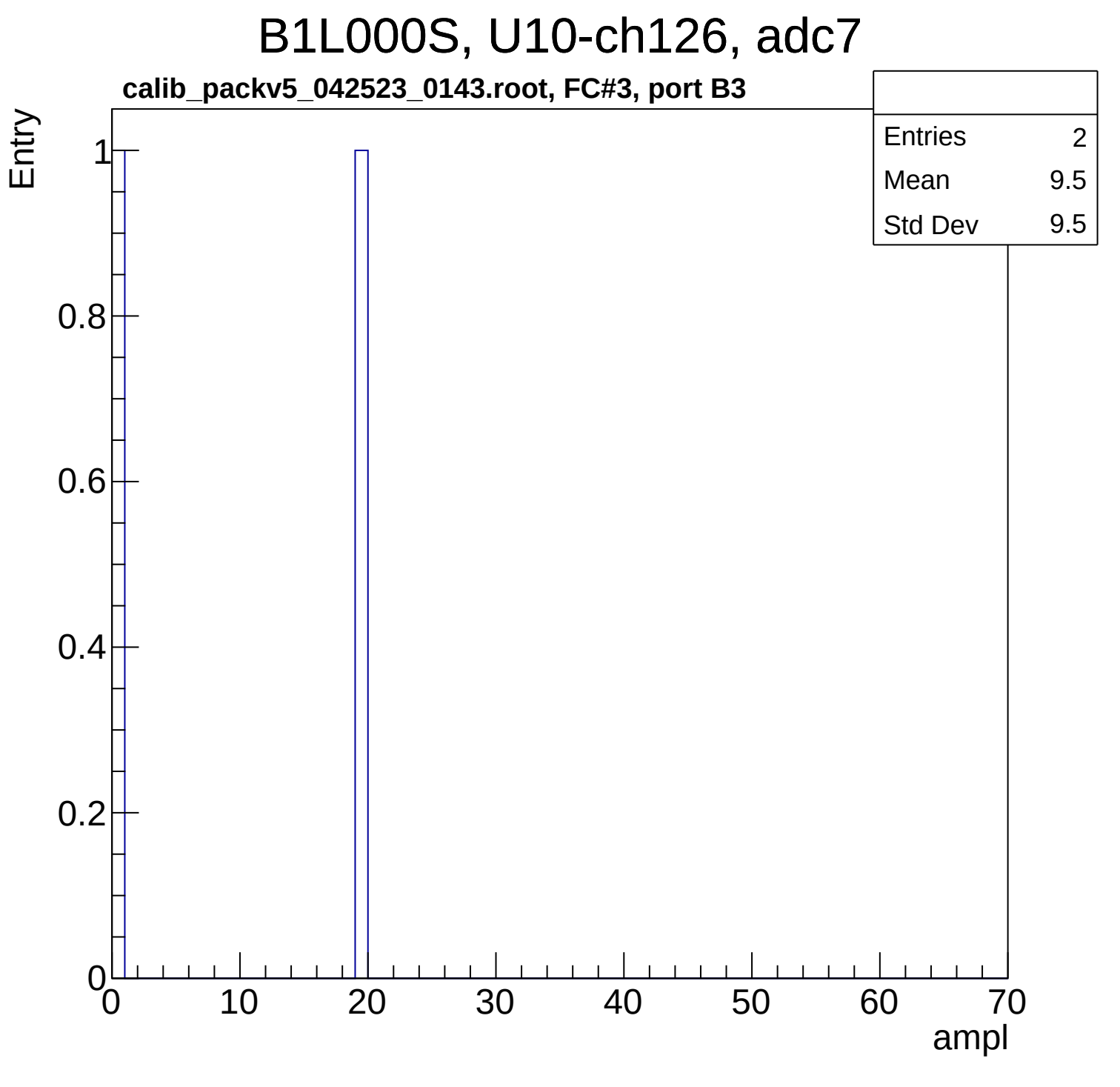
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U10-ch126, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
ampl
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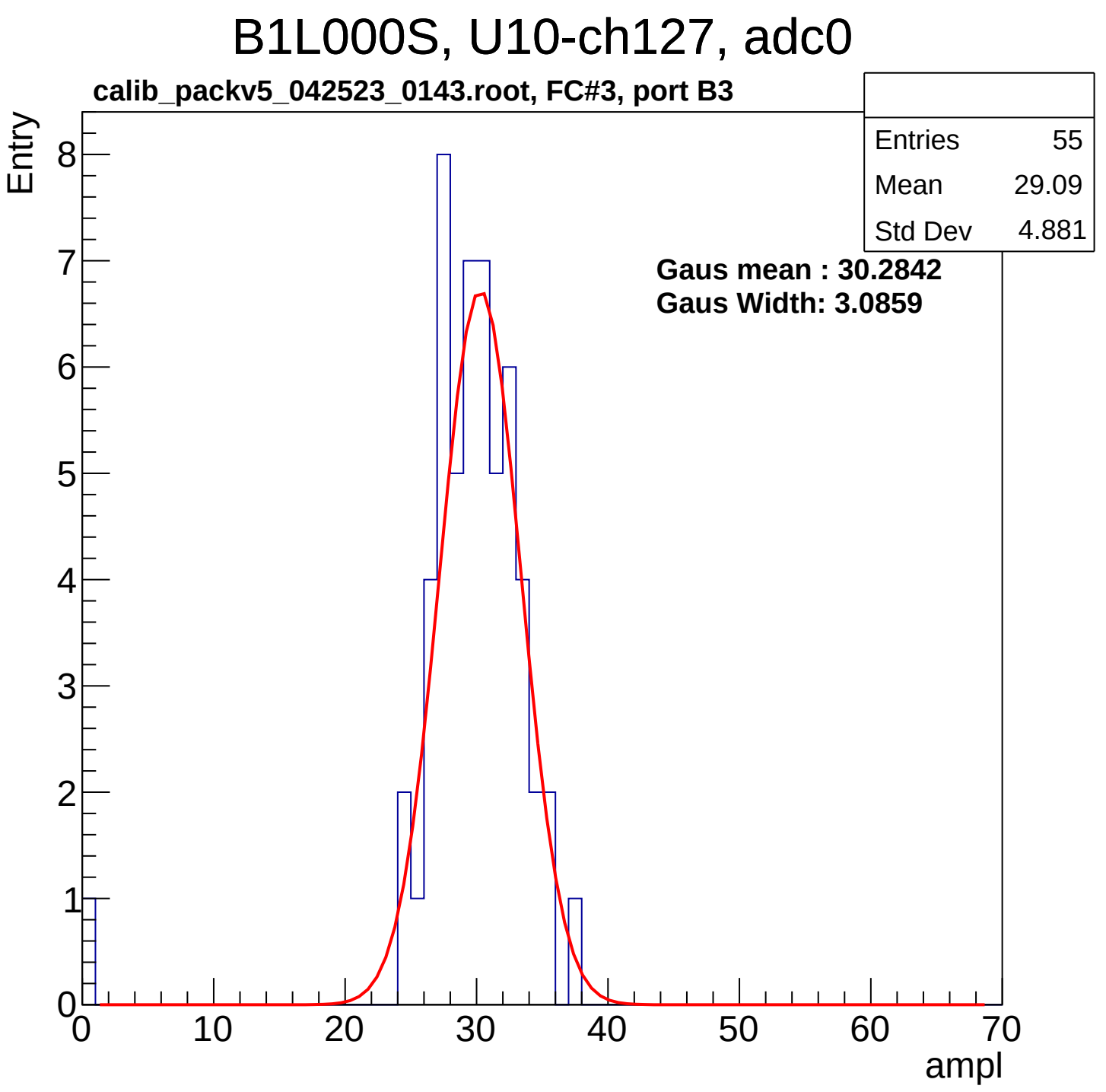
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U10-ch127, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 29.09
Std Dev 4.881
Gaus mean : 30.2842
Gaus Width: 3.0859
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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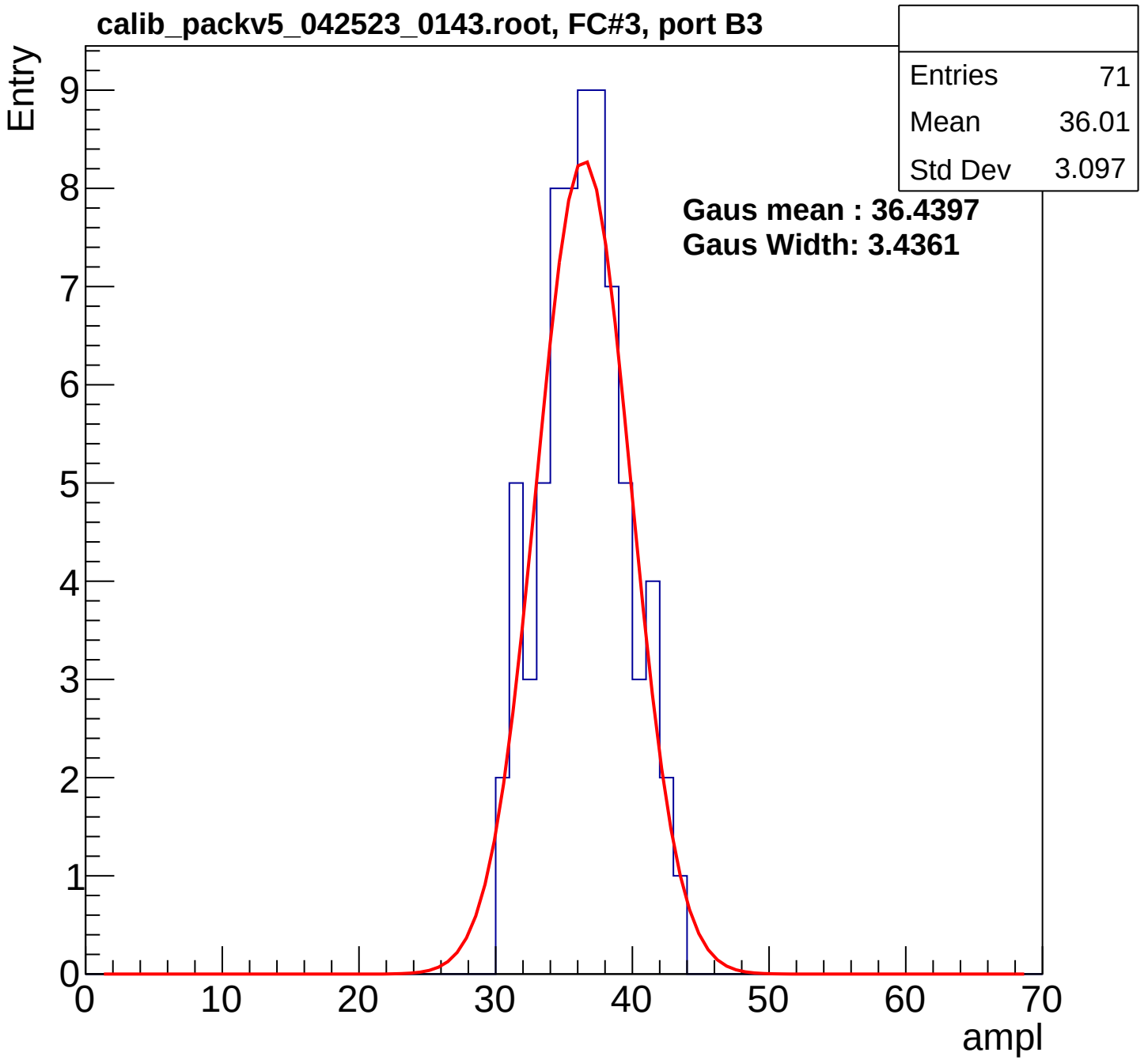
B1L000S, U10-ch127, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 36.01
Std Dev 3.097
Gaus mean : 36.4397
Gaus Width: 3.4361
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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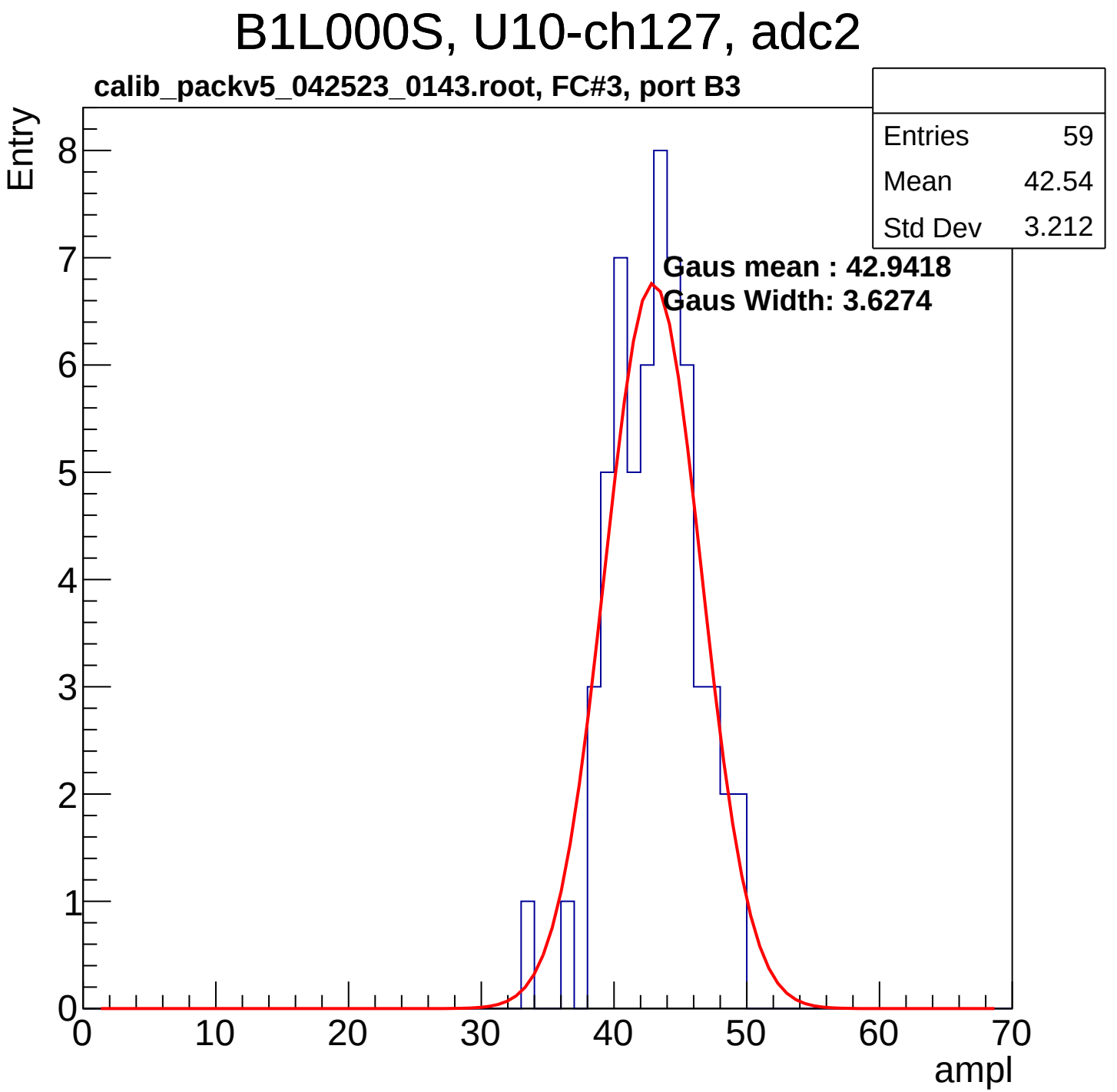
B1L000S, U10-ch127, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.54
Std Dev 3.212
Gaus mean : 42.9418
Gaus Width: 3.6274
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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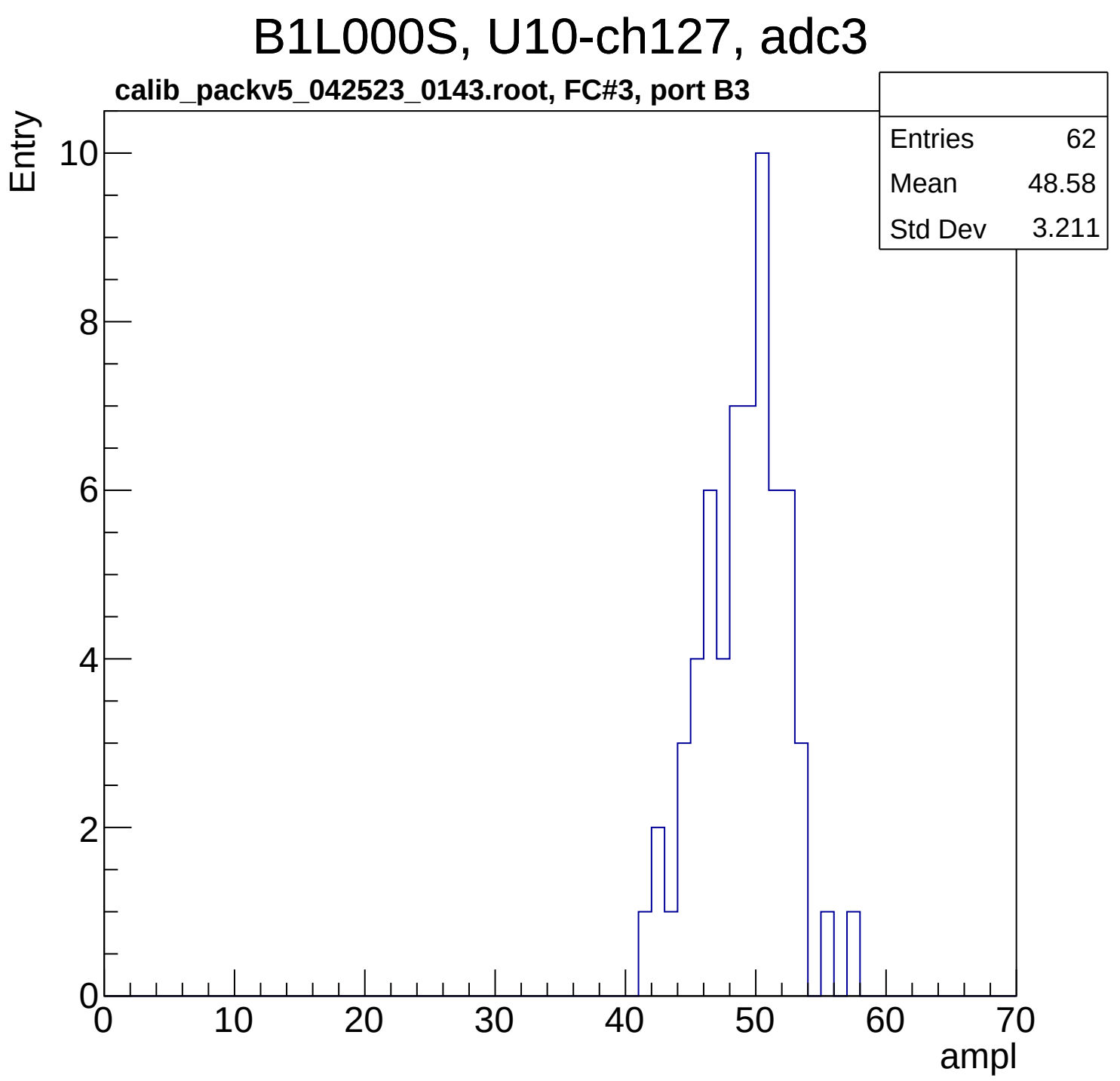
B1L000S, U10-ch127, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 48.58
Std Dev 3.211
Entry
ampl
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2
4
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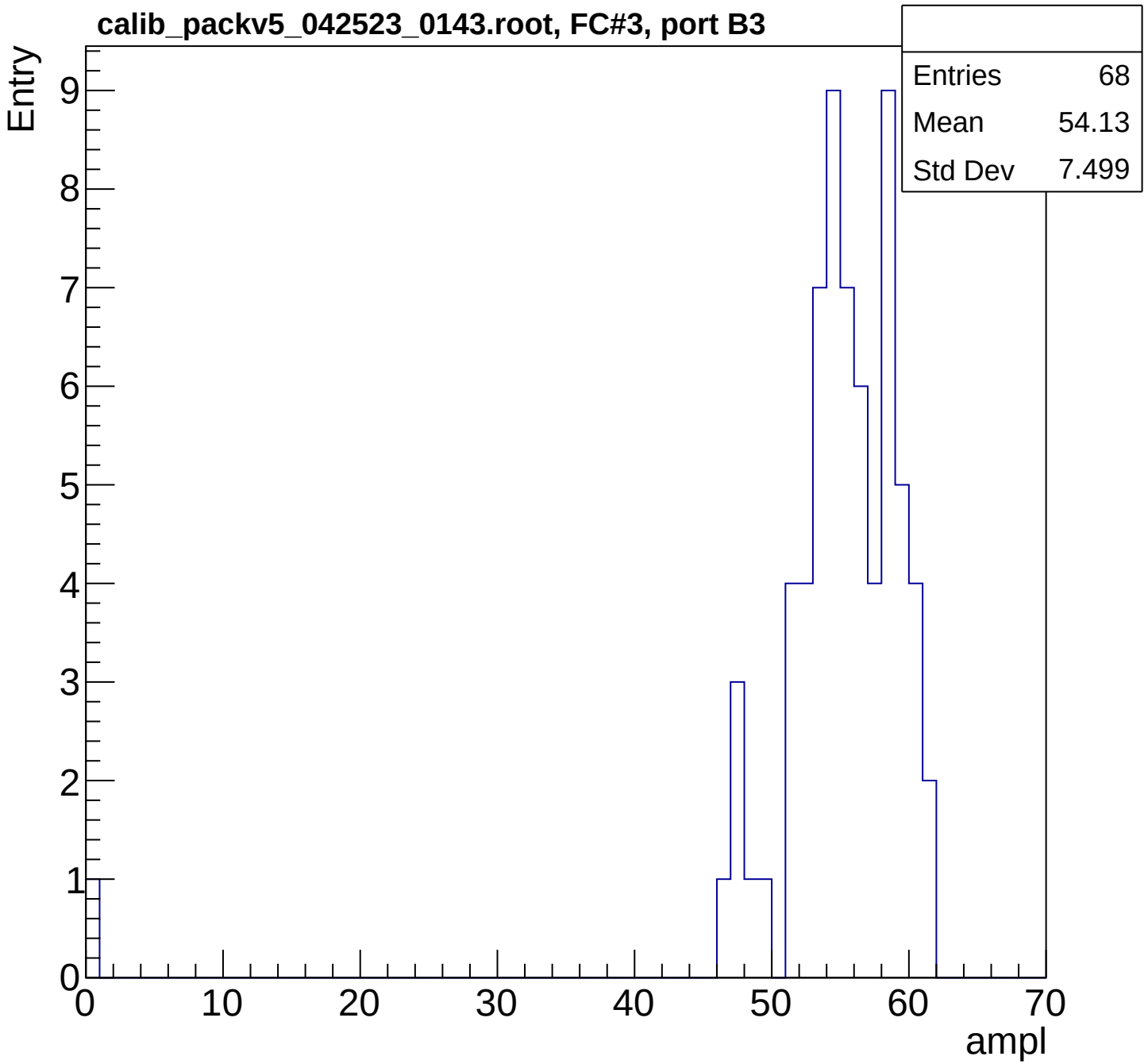
B1L000S, U10-ch127, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 54.13
Std Dev 7.499
Entry
ampl
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3
4
5
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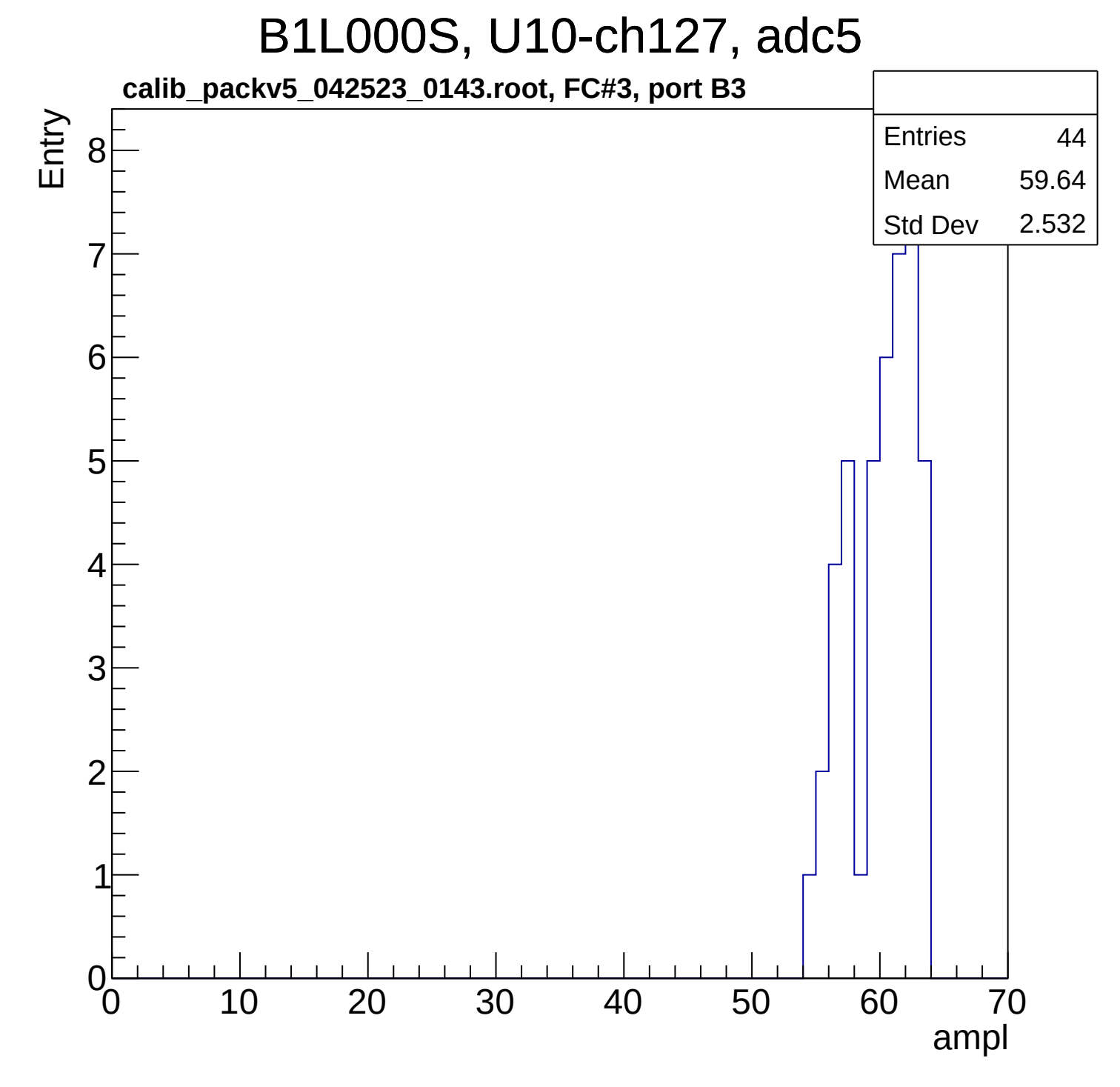
B1L000S, U10-ch127, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.64
Std Dev 2.532
Entry
ampl
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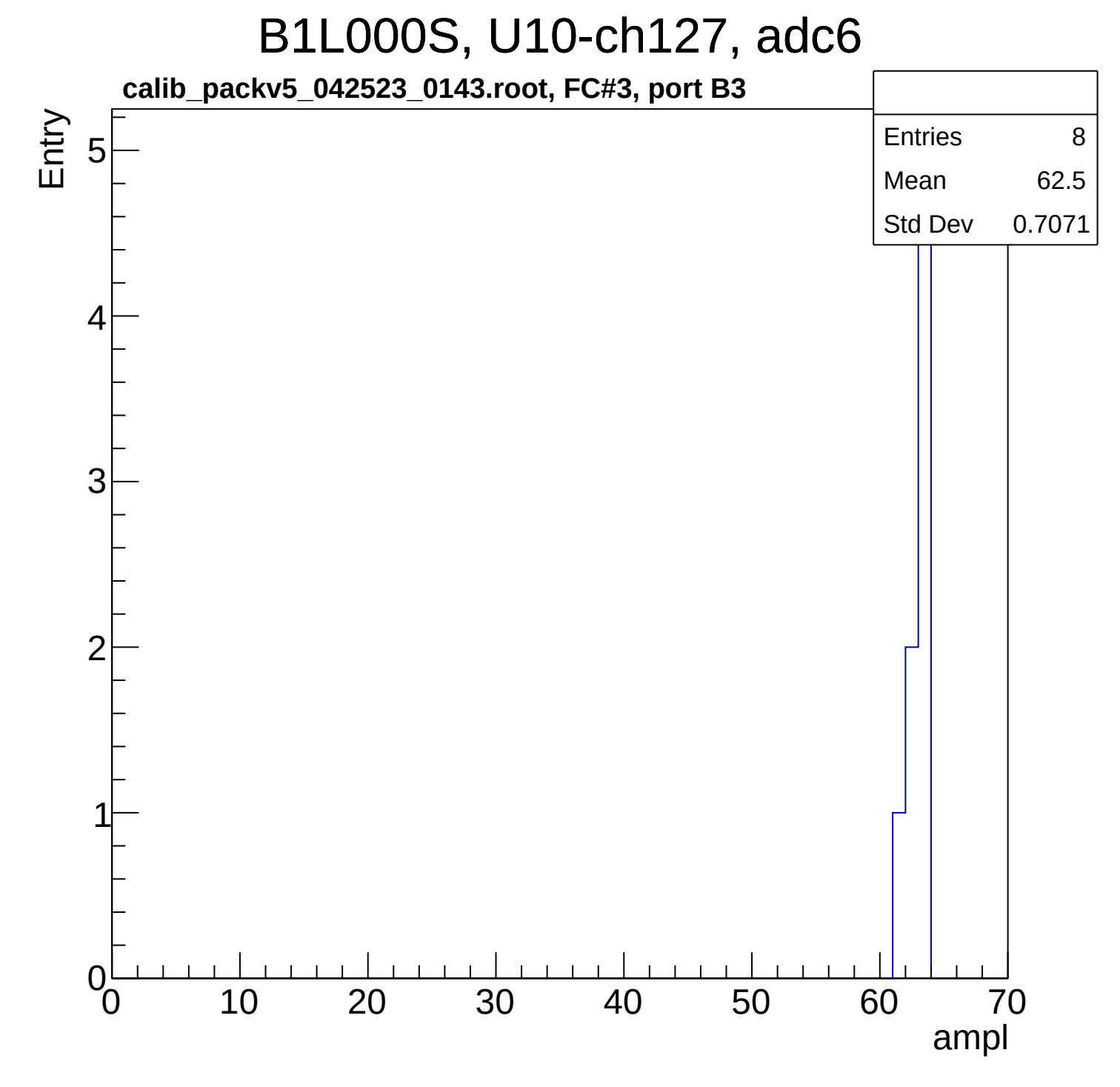
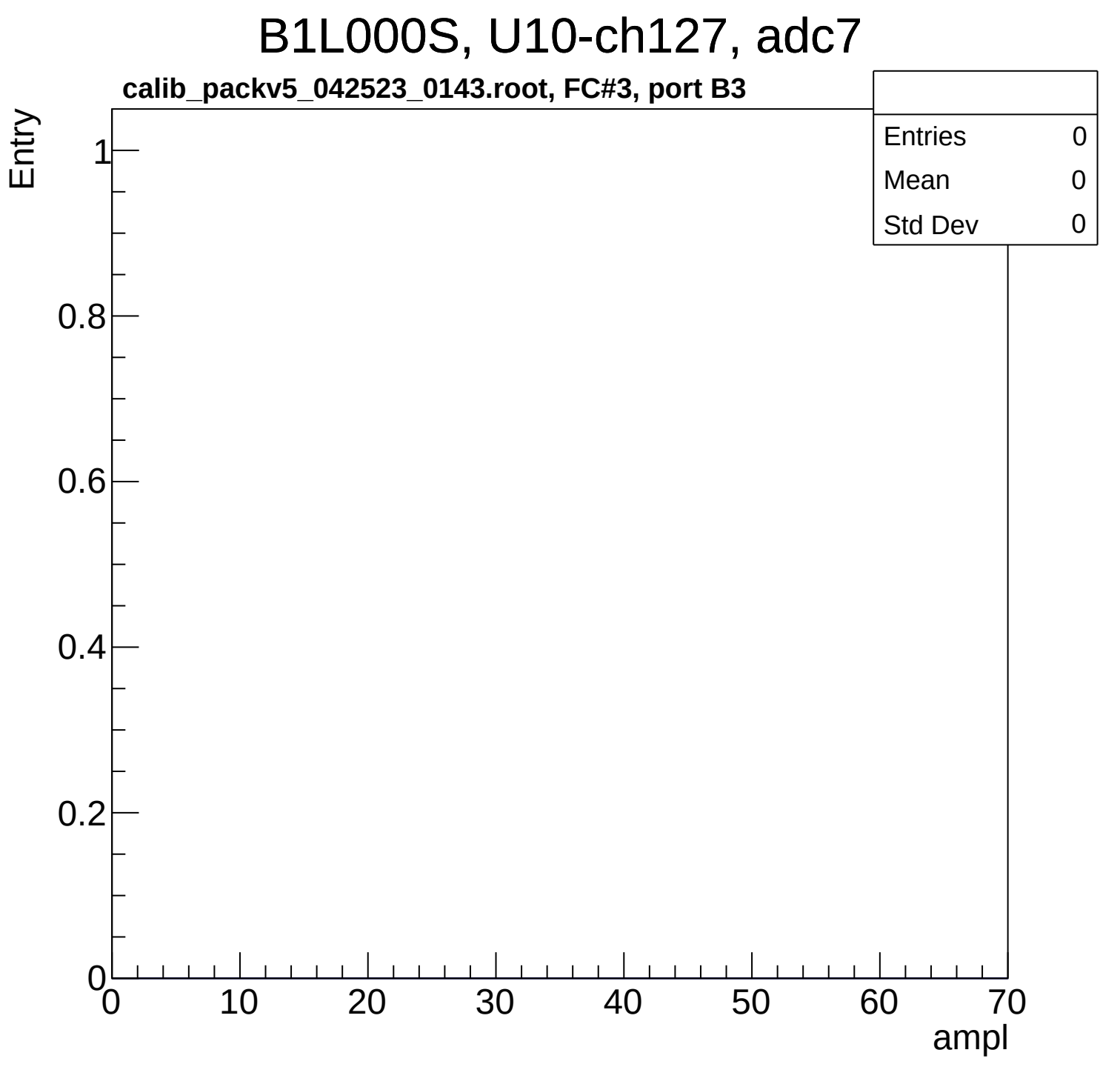
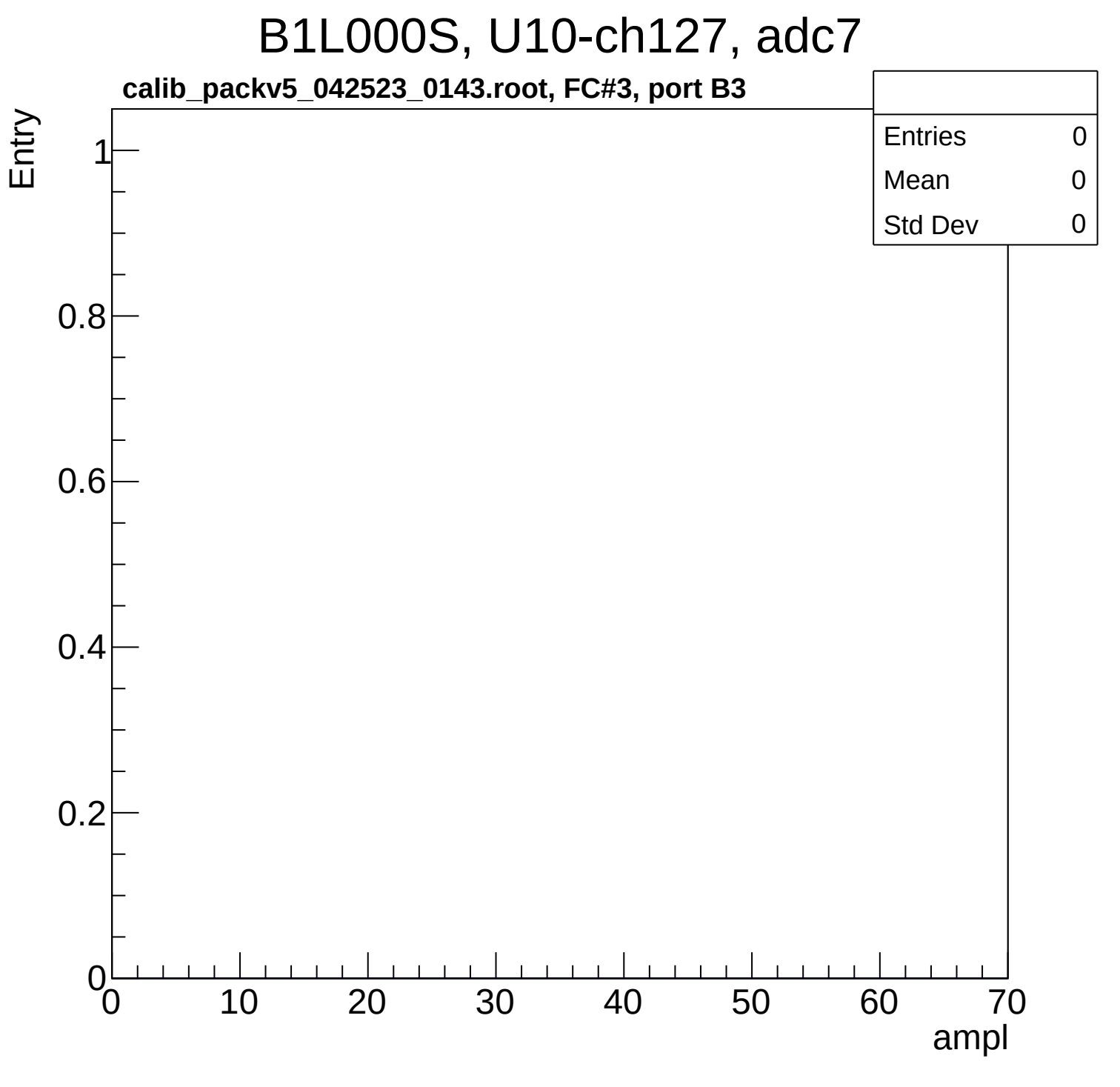

B1L000S, U10-ch127, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.5
Std Dev 0.7071
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl