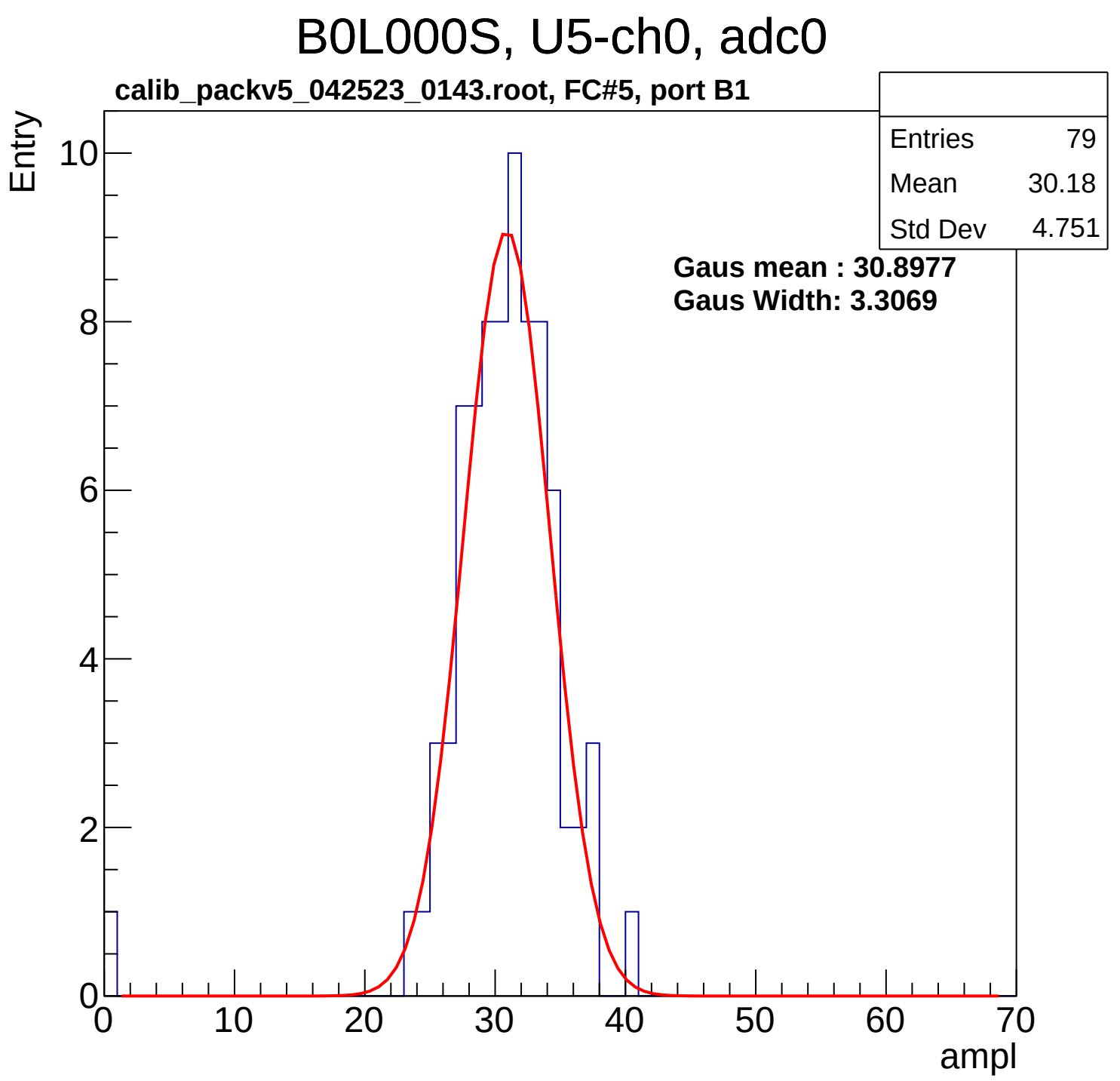
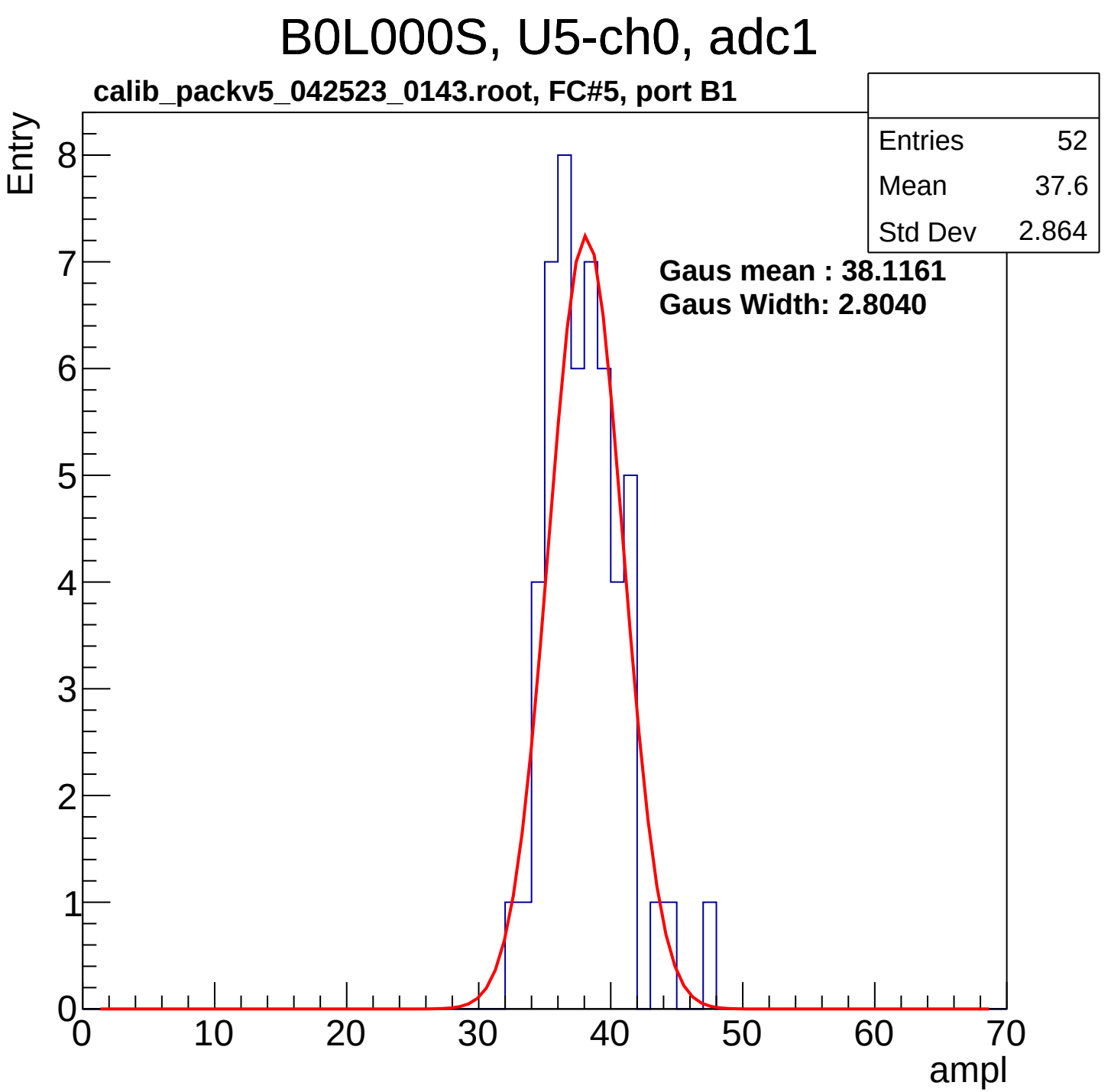

B0L000S, U5-ch0, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 30.18
Std Dev 4.751
Gaus mean : 30.8977
Gaus Width: 3.3069
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch0, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 37.6
Std Dev 2.864
Gaus mean : 38.1161
Gaus Width: 2.8040
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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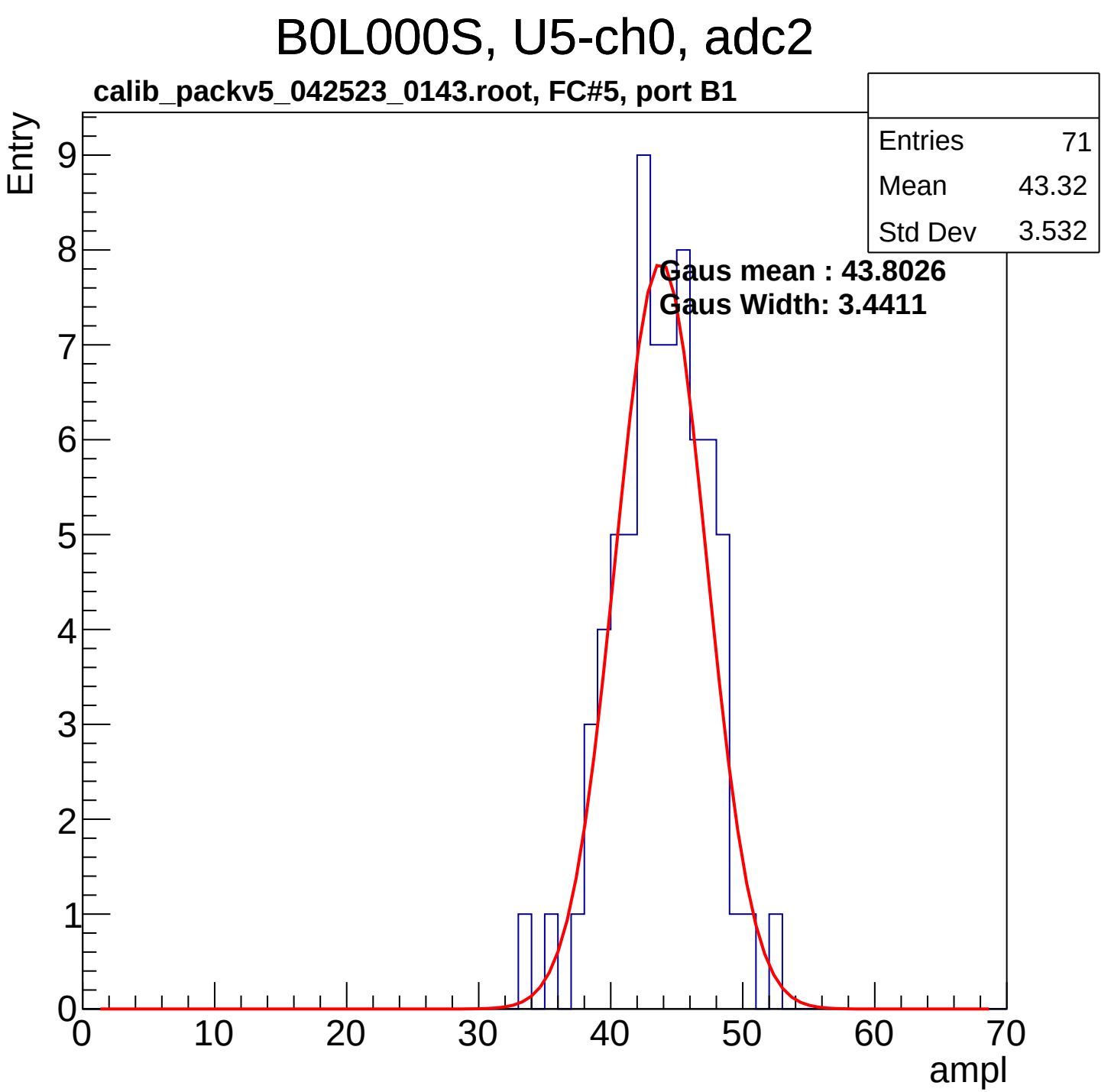
B0L000S, U5-ch0, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.32
Std Dev 3.532
Gaus mean : 43.8026
Gaus Width: 3.4411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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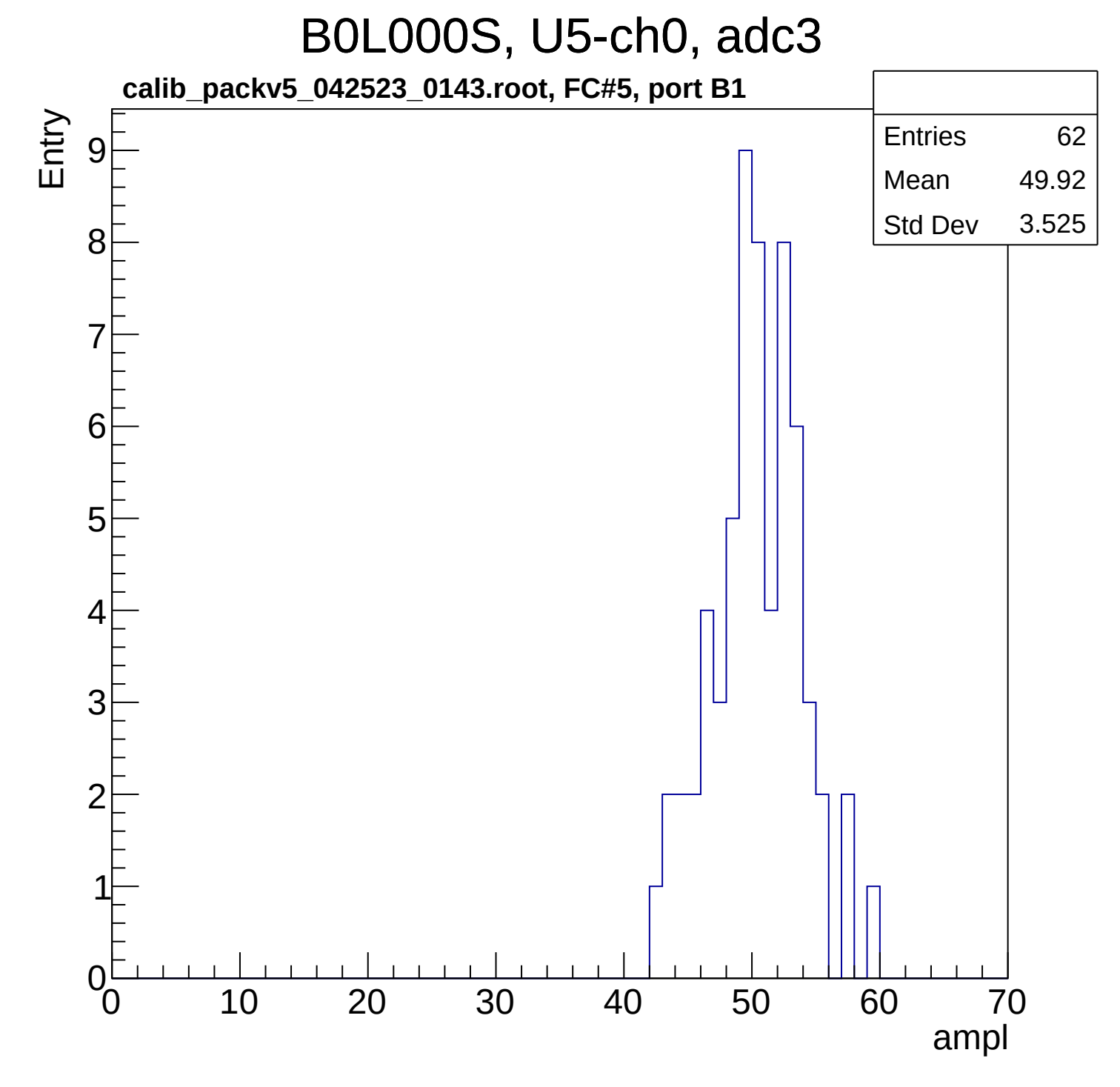
B0L000S, U5-ch0, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.92
Std Dev 3.525
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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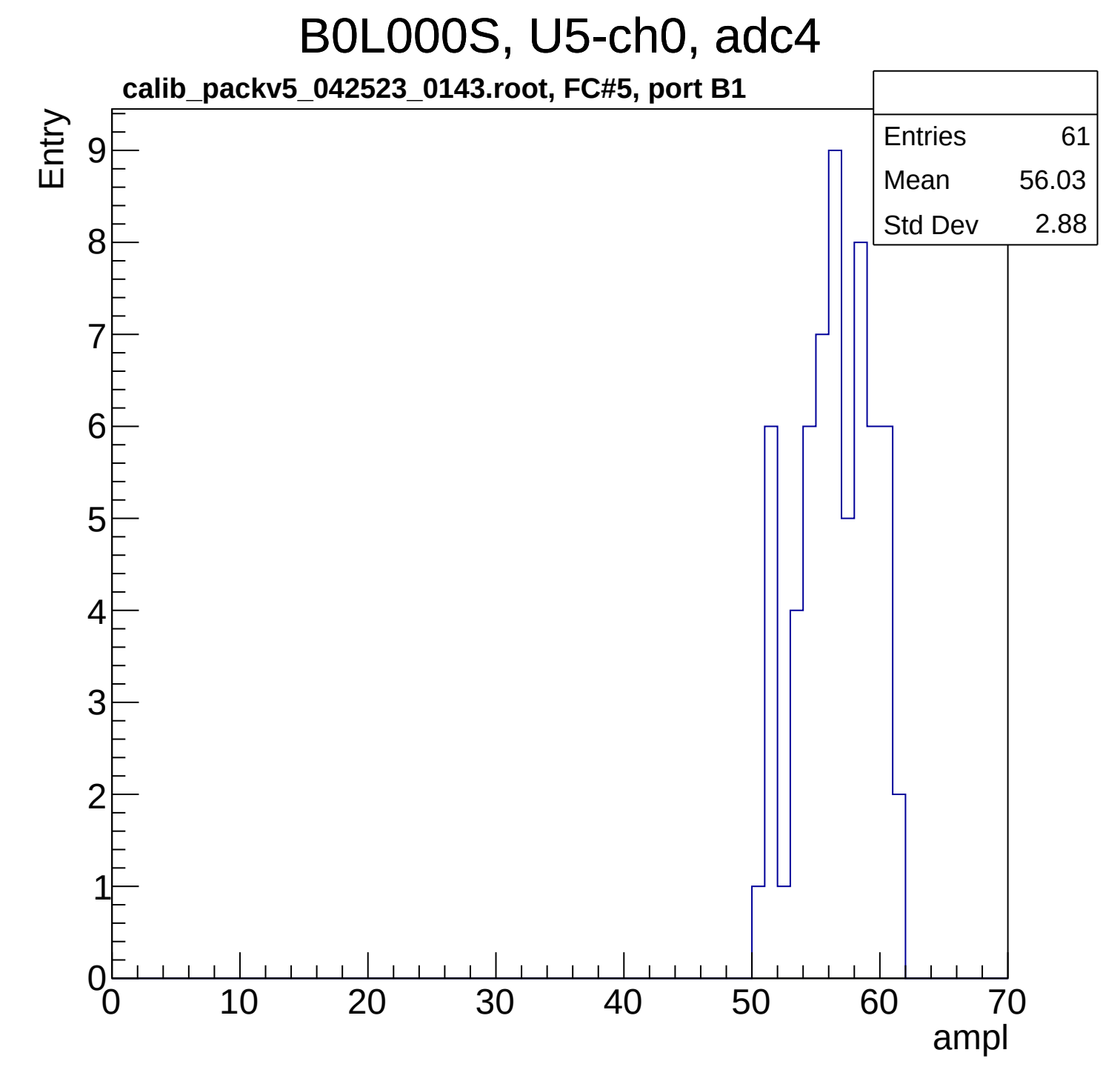
B0L000S, U5-ch0, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.03
Std Dev 2.88
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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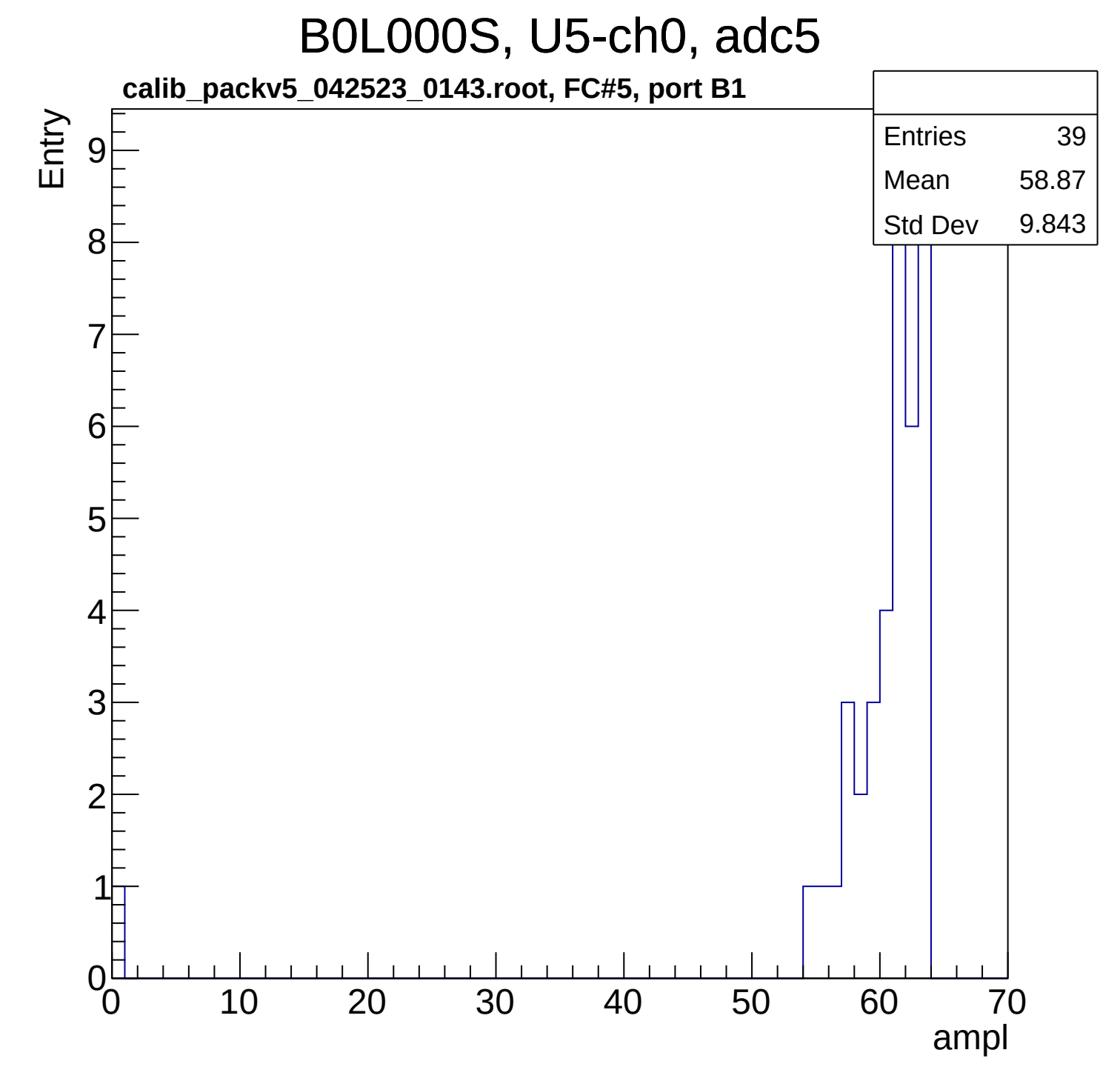
B0L000S, U5-ch0, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 58.87
Std Dev 9.843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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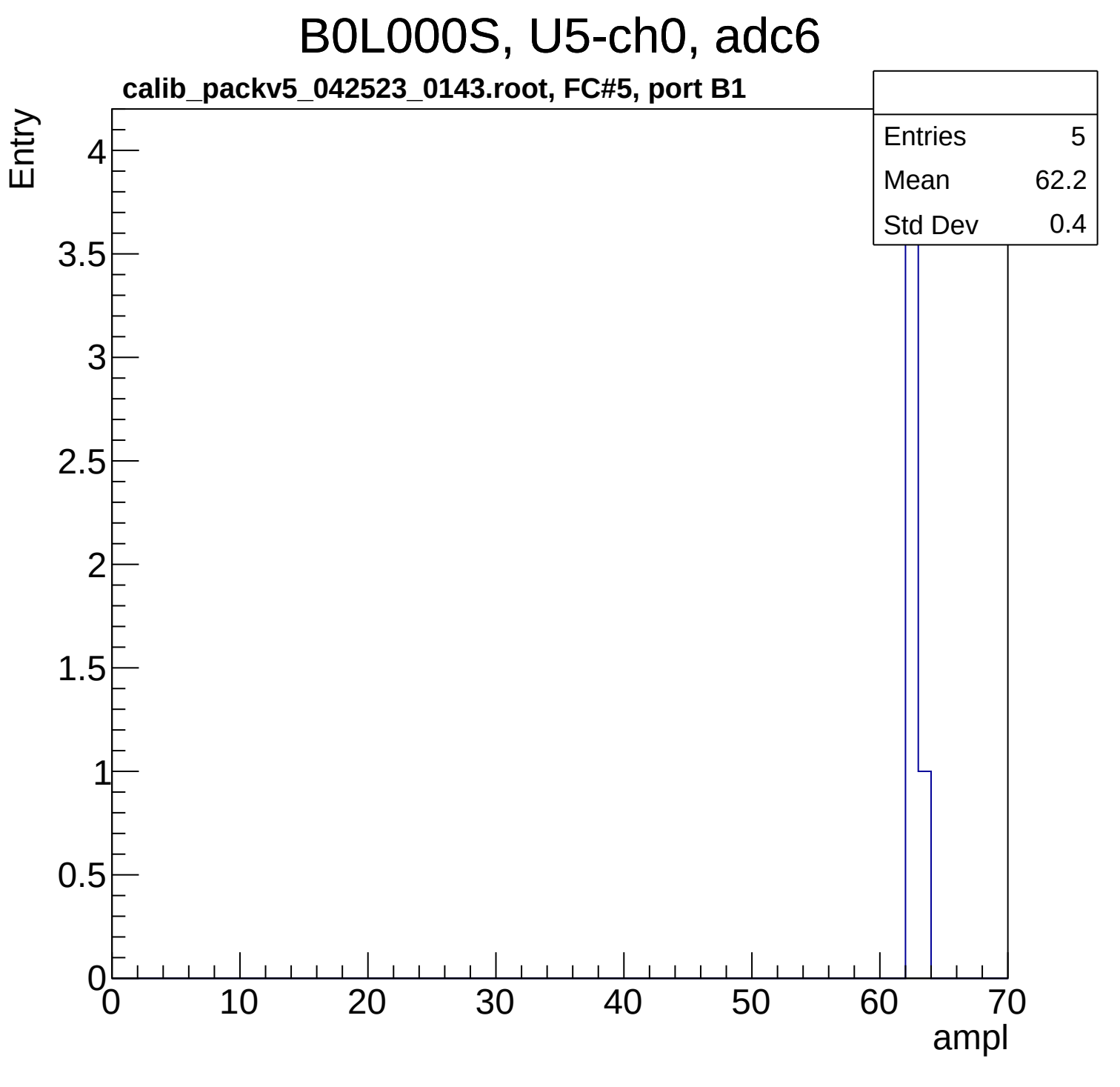
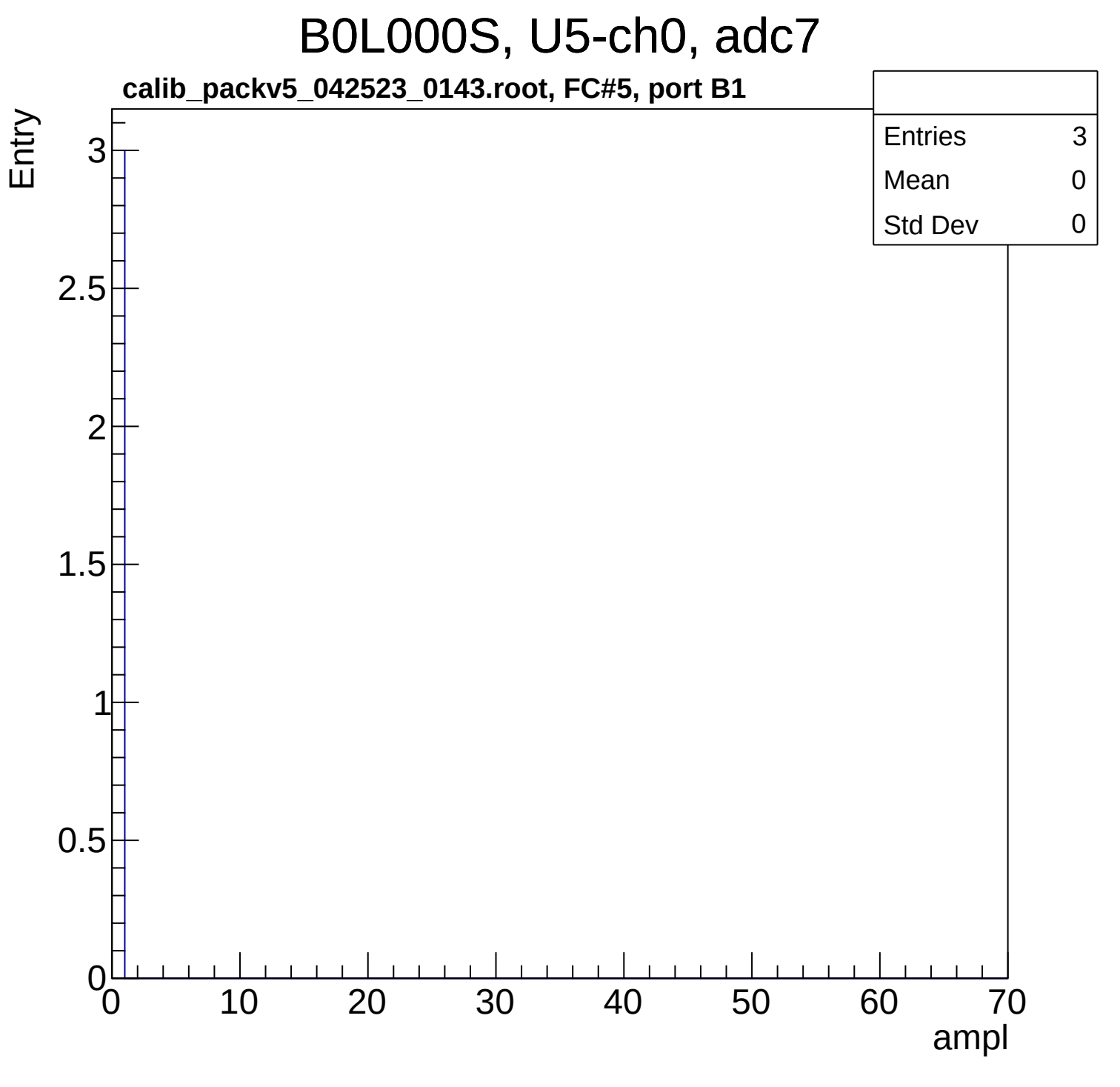

B0L000S, U5-ch0, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.4
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch0, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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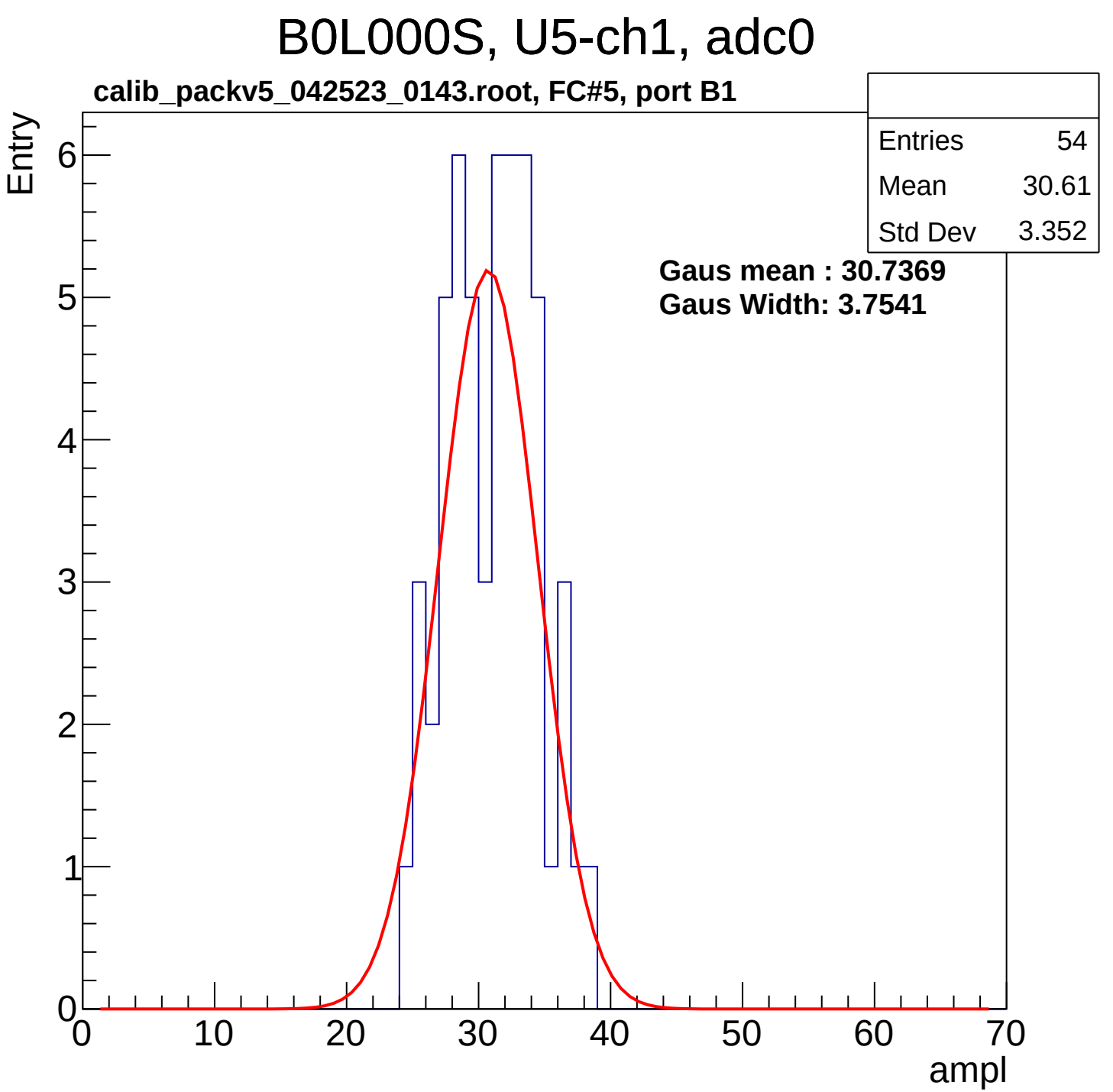
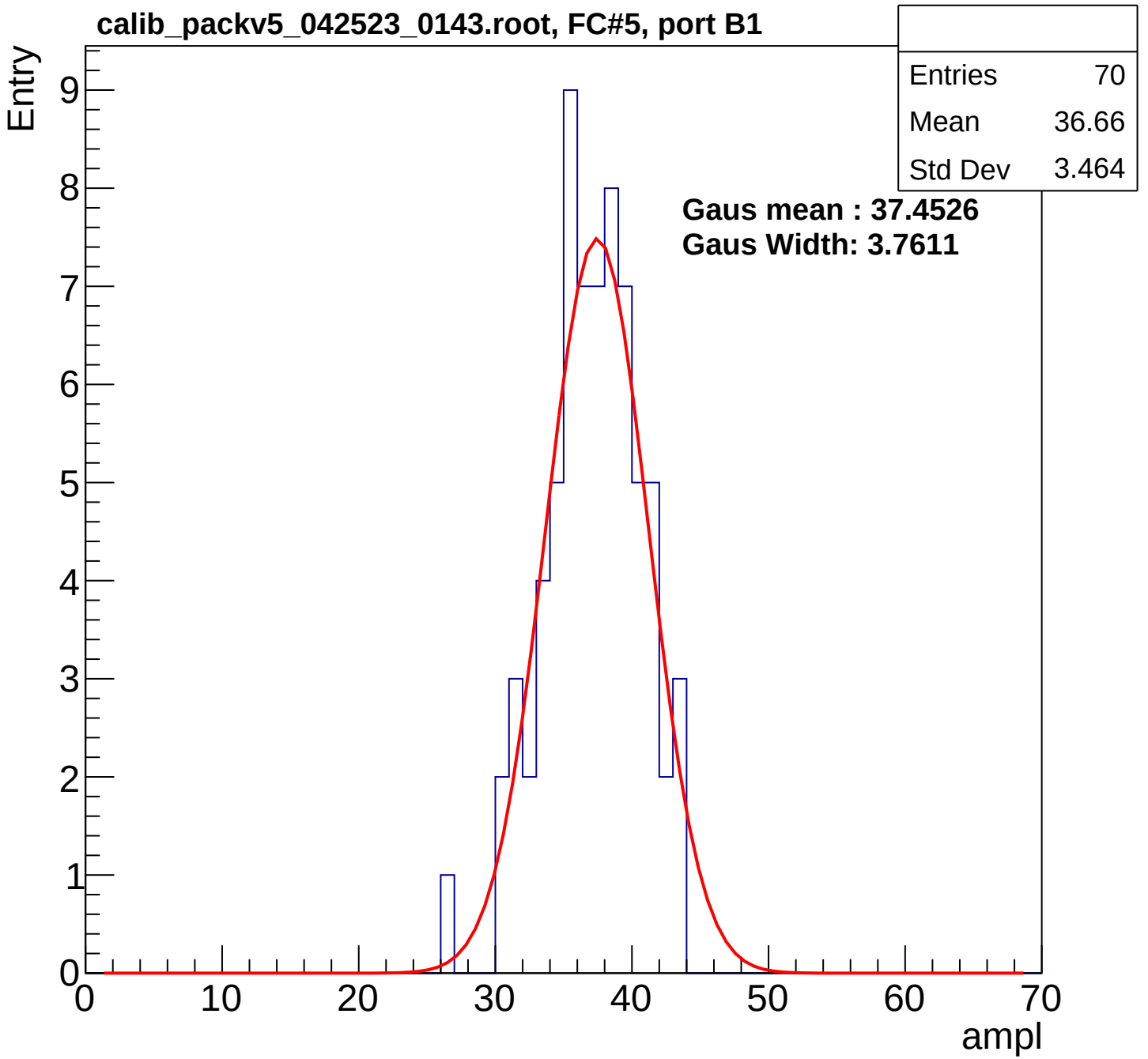
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch1, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 30.61
Std Dev 3.352
Gaus mean : 30.7369
Gaus Width: 3.7541
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch1, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.66
Std Dev 3.464
Gaus mean : 37.4526
Gaus Width: 3.7611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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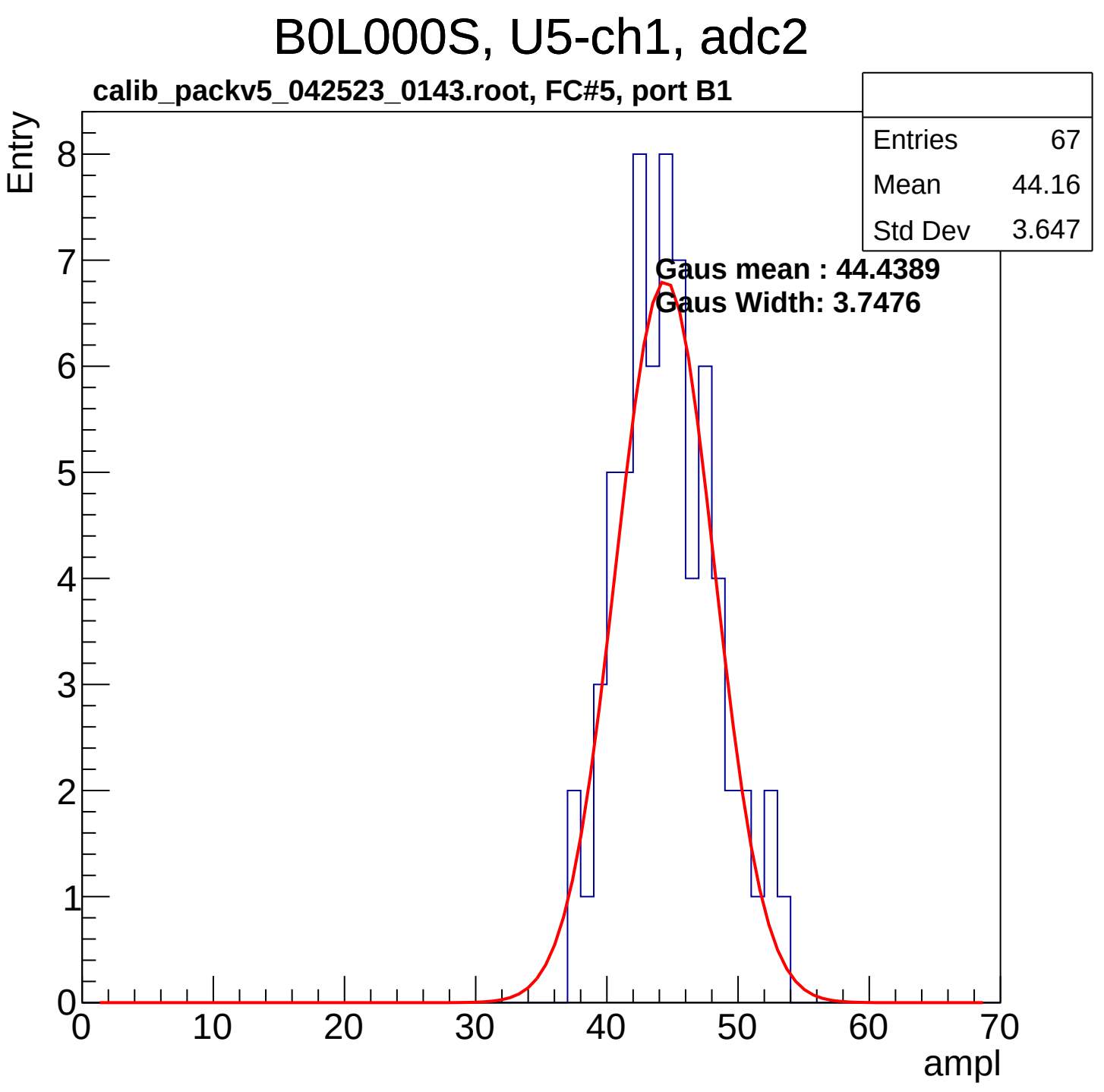
B0L000S, U5-ch1, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 44.16
Std Dev 3.647
Gaus mean : 44.4389
Gaus Width: 3.7476
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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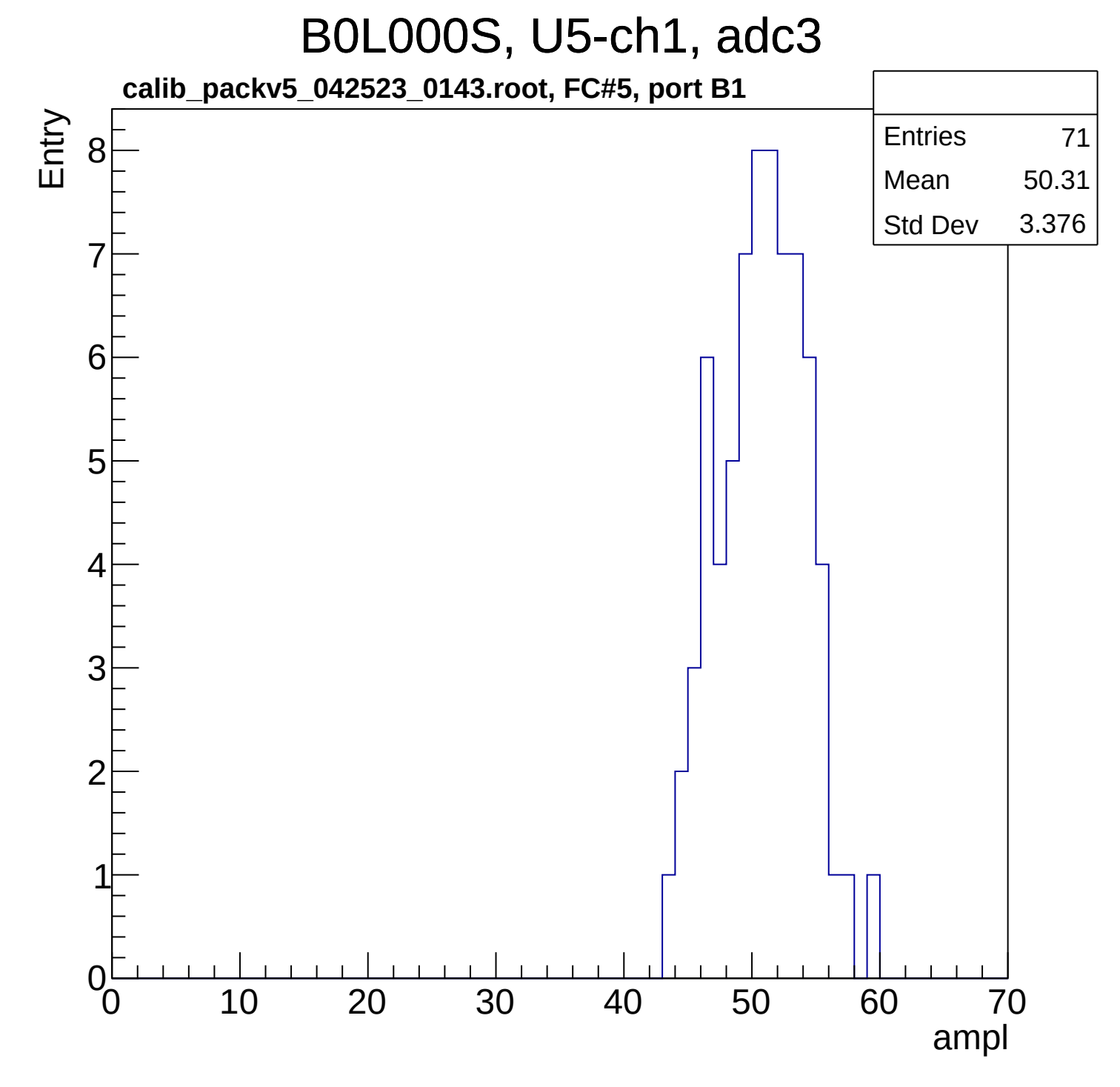
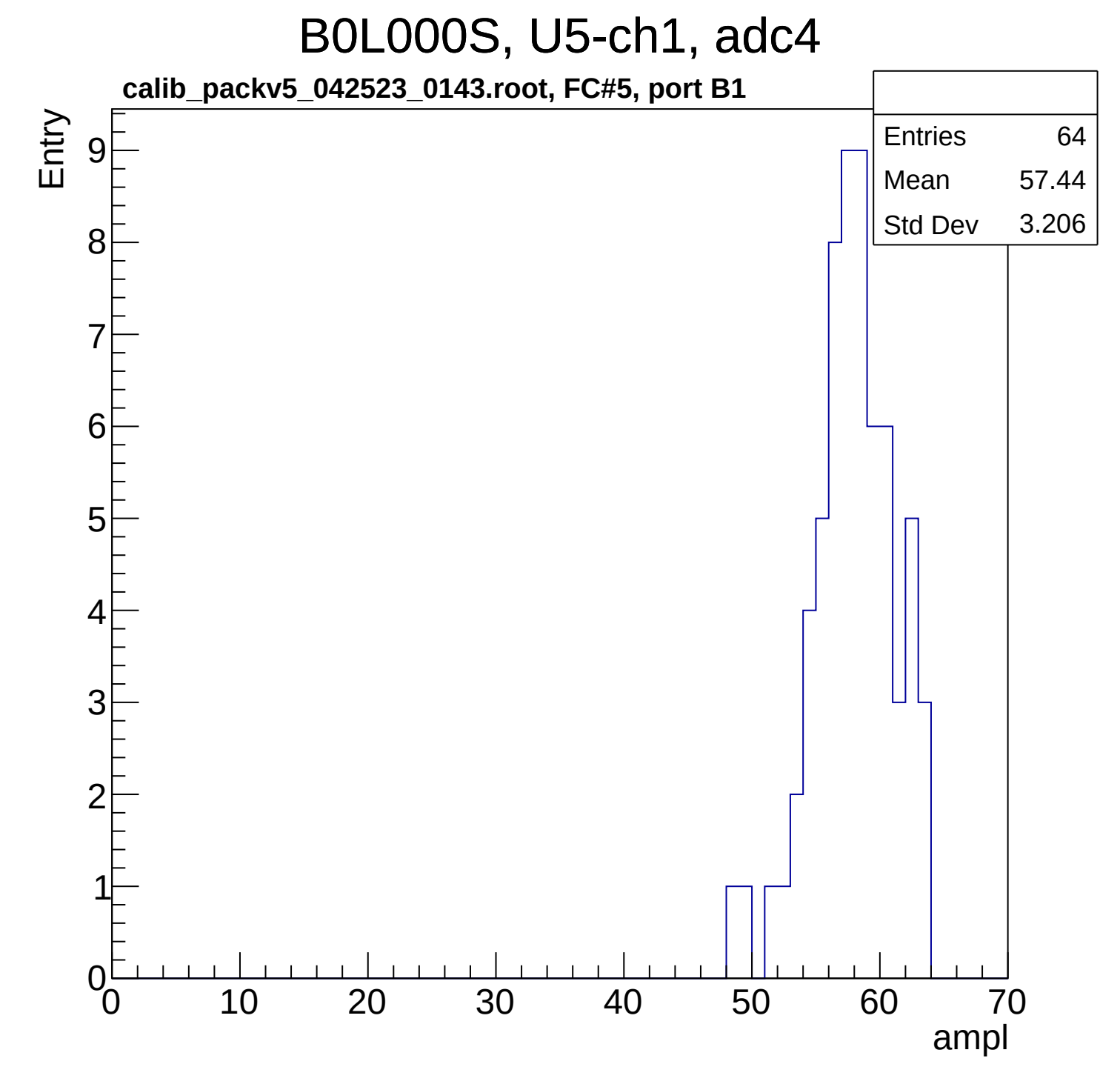

B0L000S, U5-ch1, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.31
Std Dev 3.376
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch1, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 57.44
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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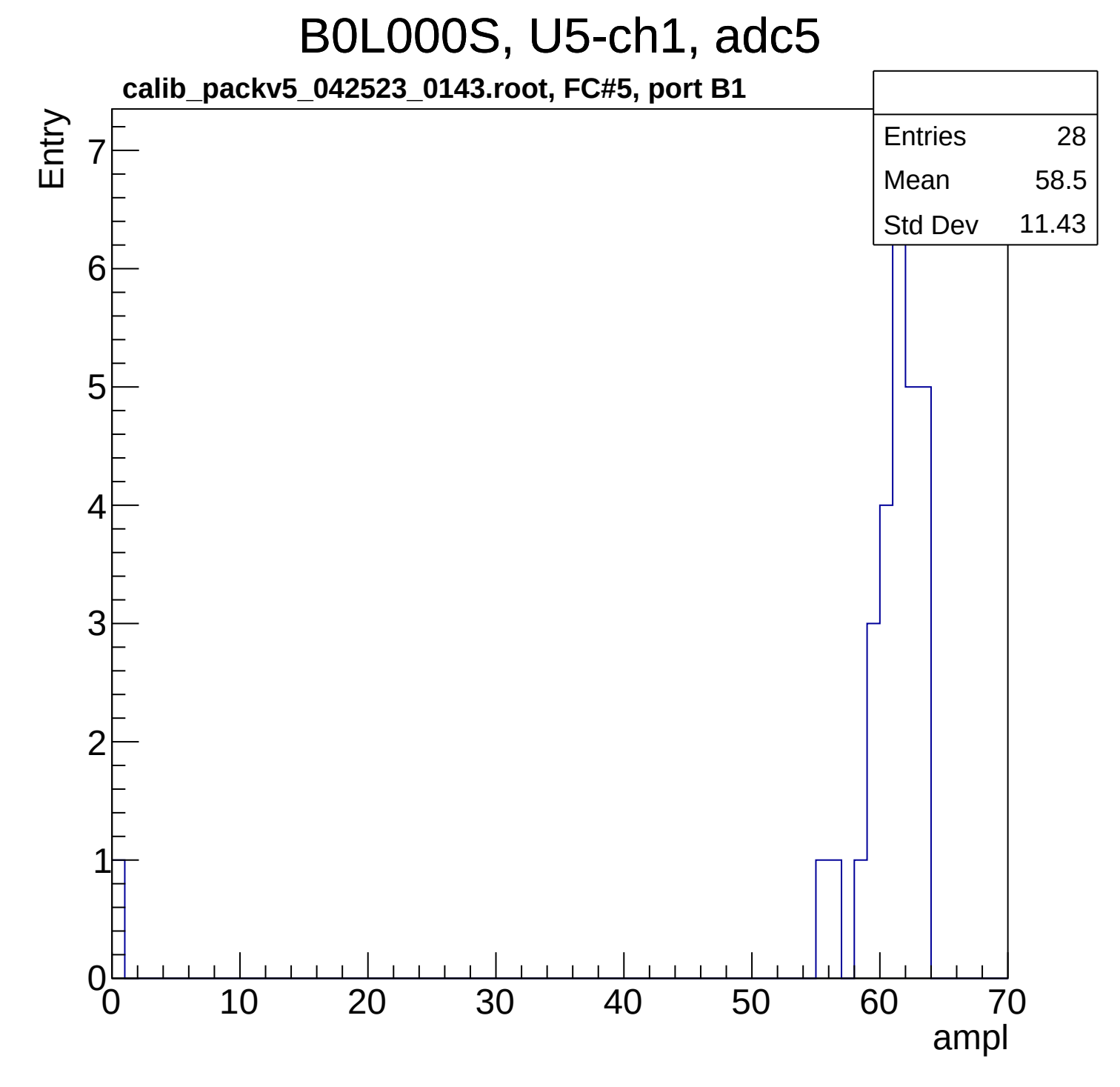
60
70


B0L000S, U5-ch1, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 28
Mean 58.5
Std Dev 11.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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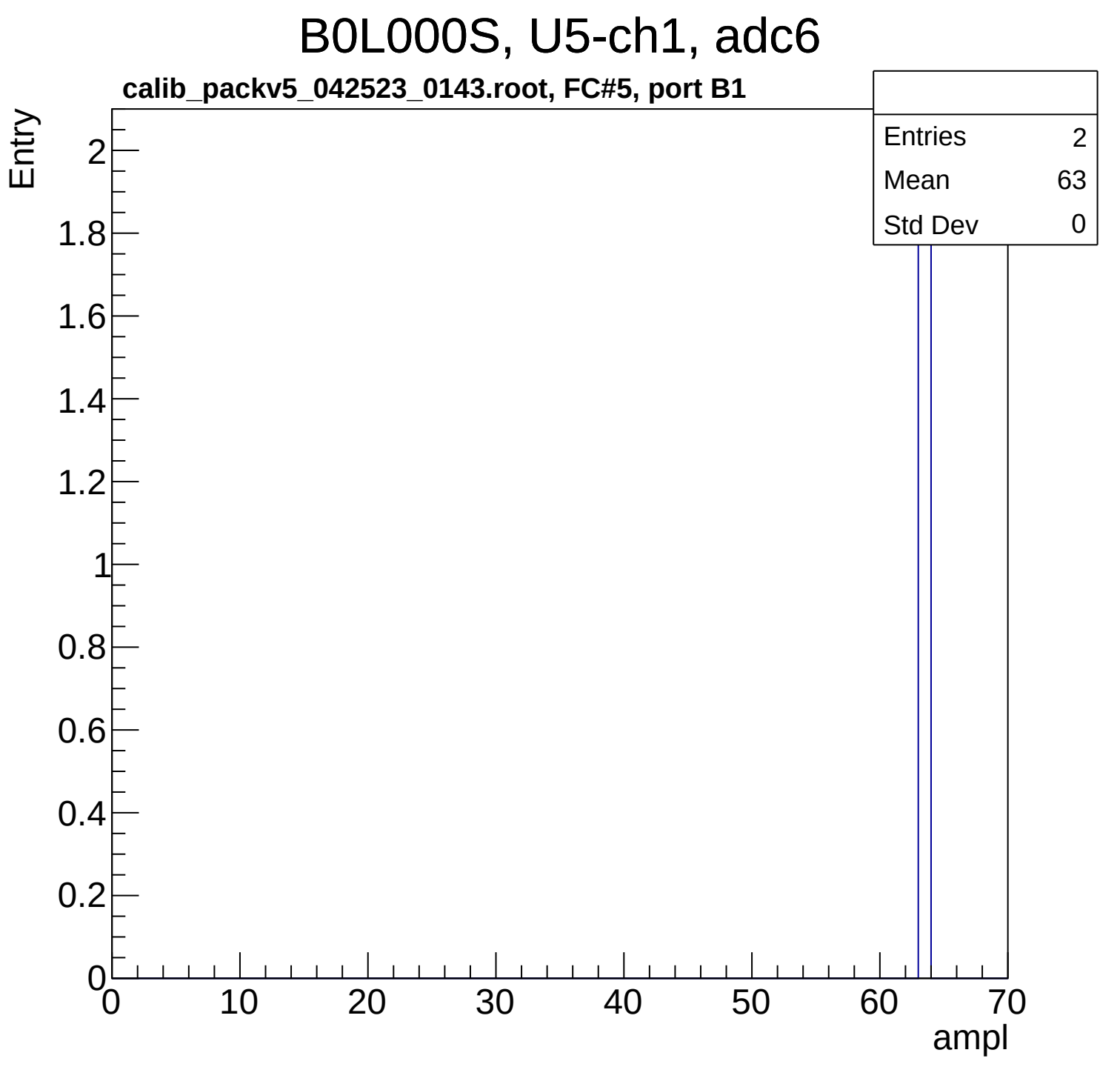
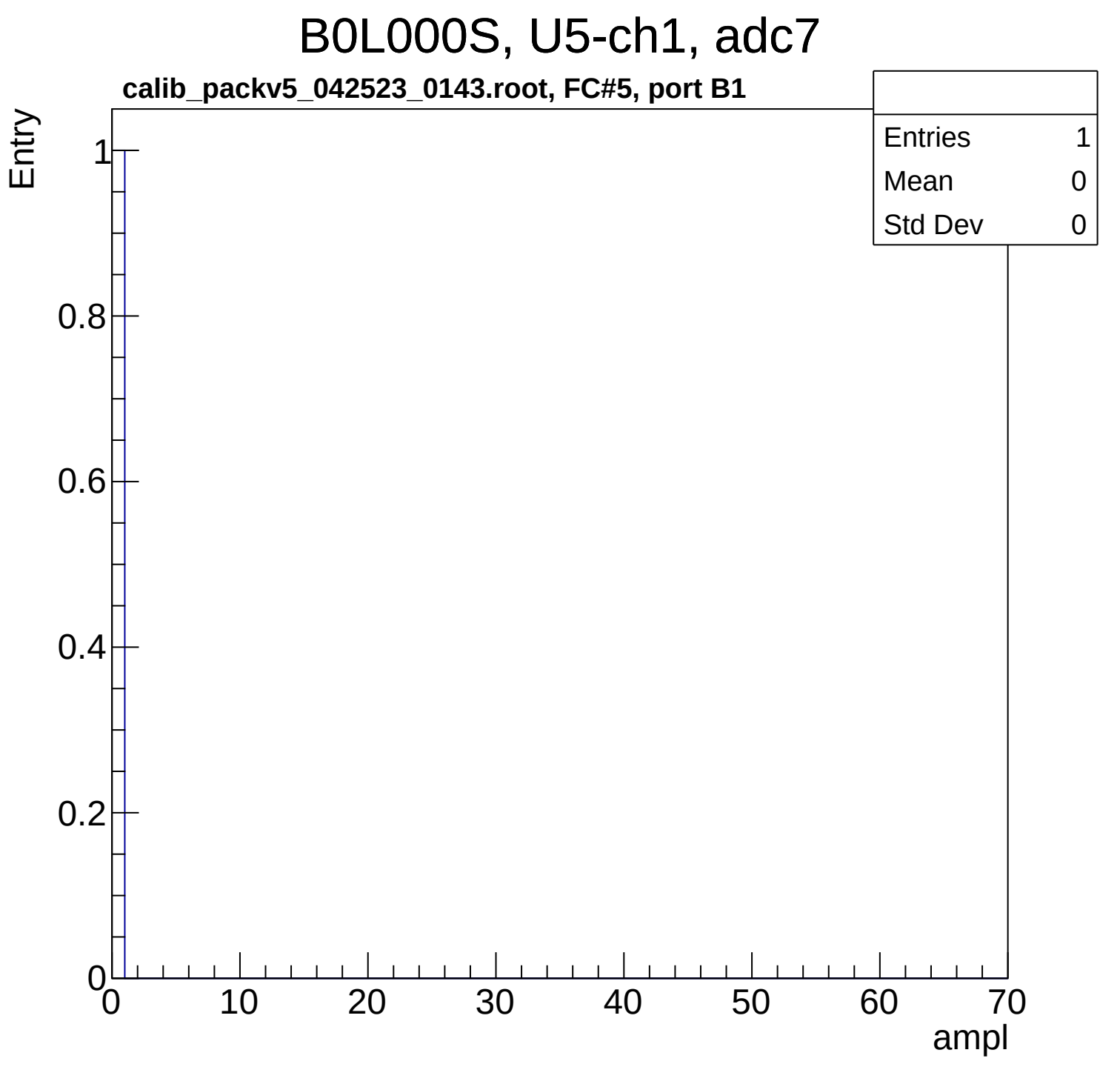
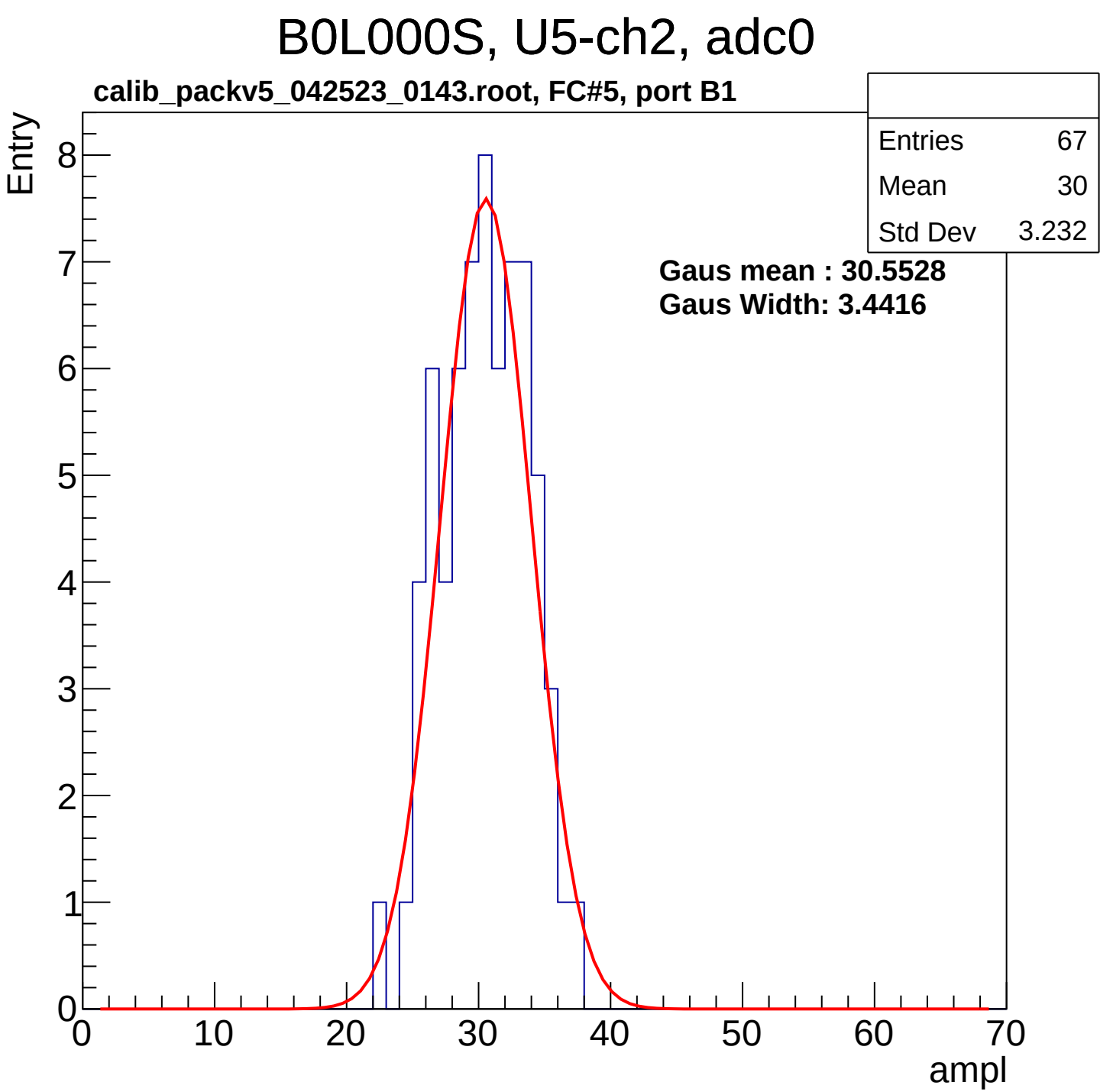
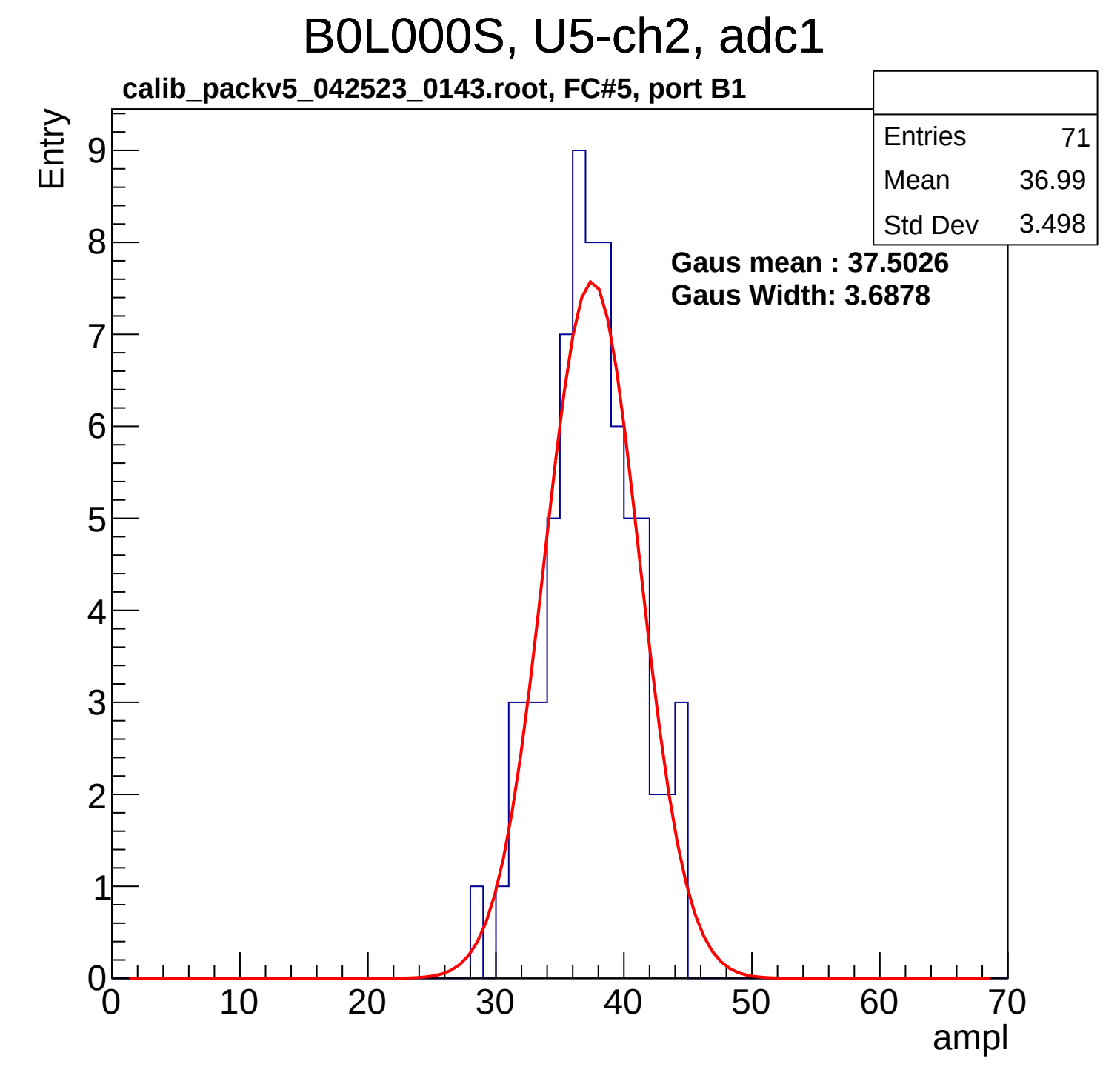

B0L000S, U5-ch1, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch1, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch2, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30
Std Dev 3.232
Gaus mean : 30.5528
Gaus Width: 3.4416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch2, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.99
Std Dev 3.498
Gaus mean : 37.5026
Gaus Width: 3.6878
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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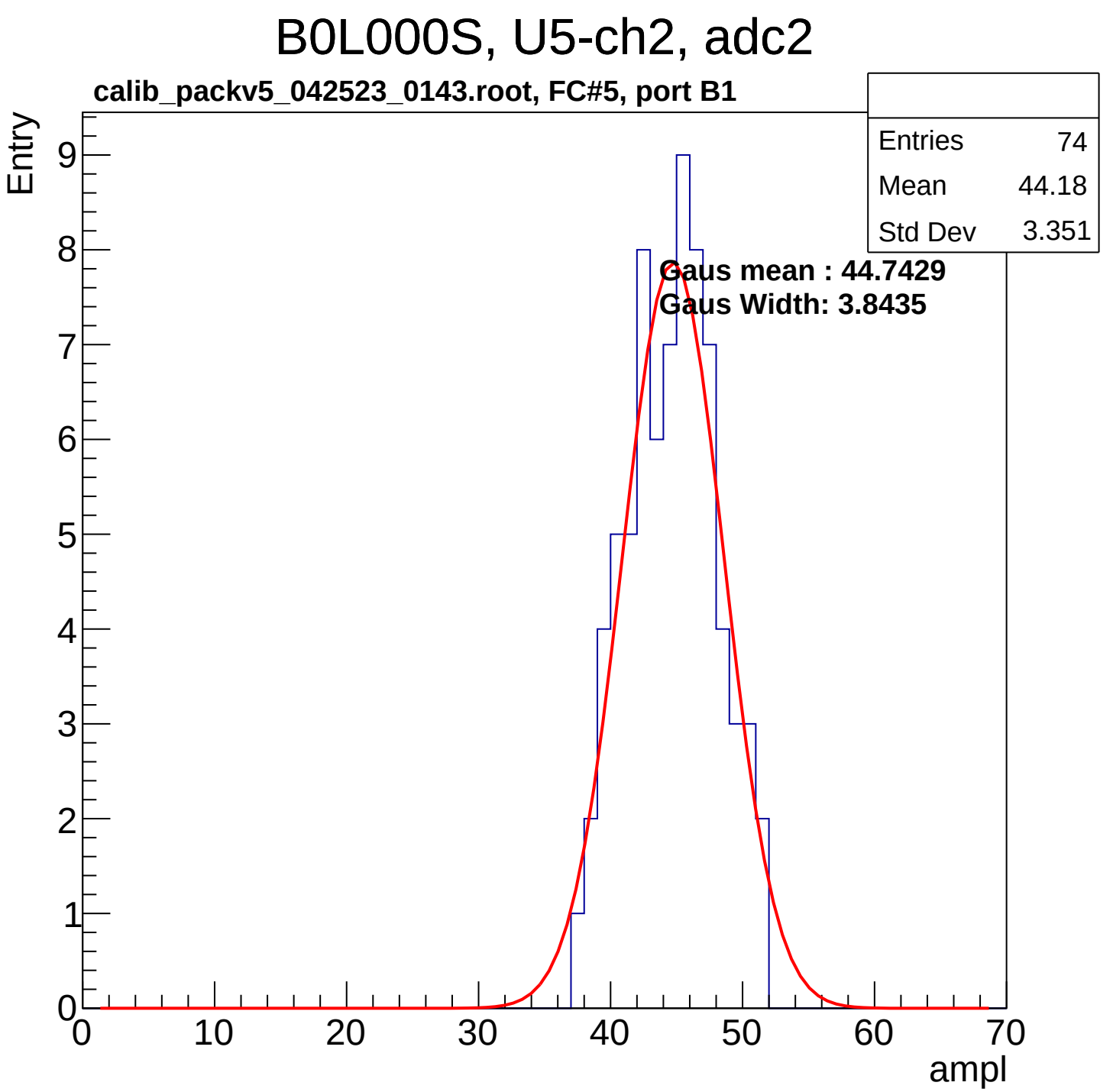
B0L000S, U5-ch2, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 44.18
Std Dev 3.351
Gaus mean : 44.7429
Gaus Width: 3.8435
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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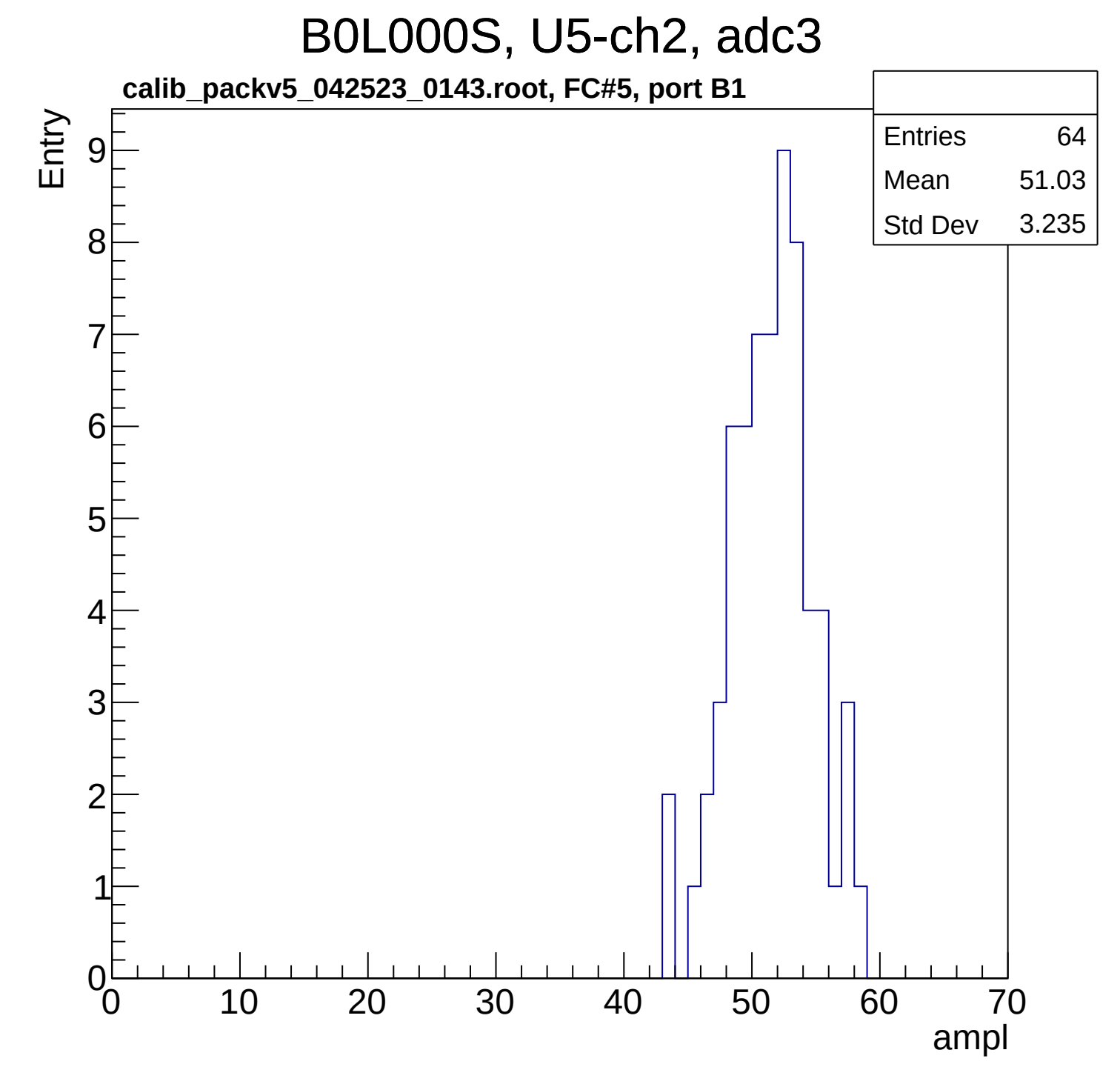
B0L000S, U5-ch2, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 51.03
Std Dev 3.235
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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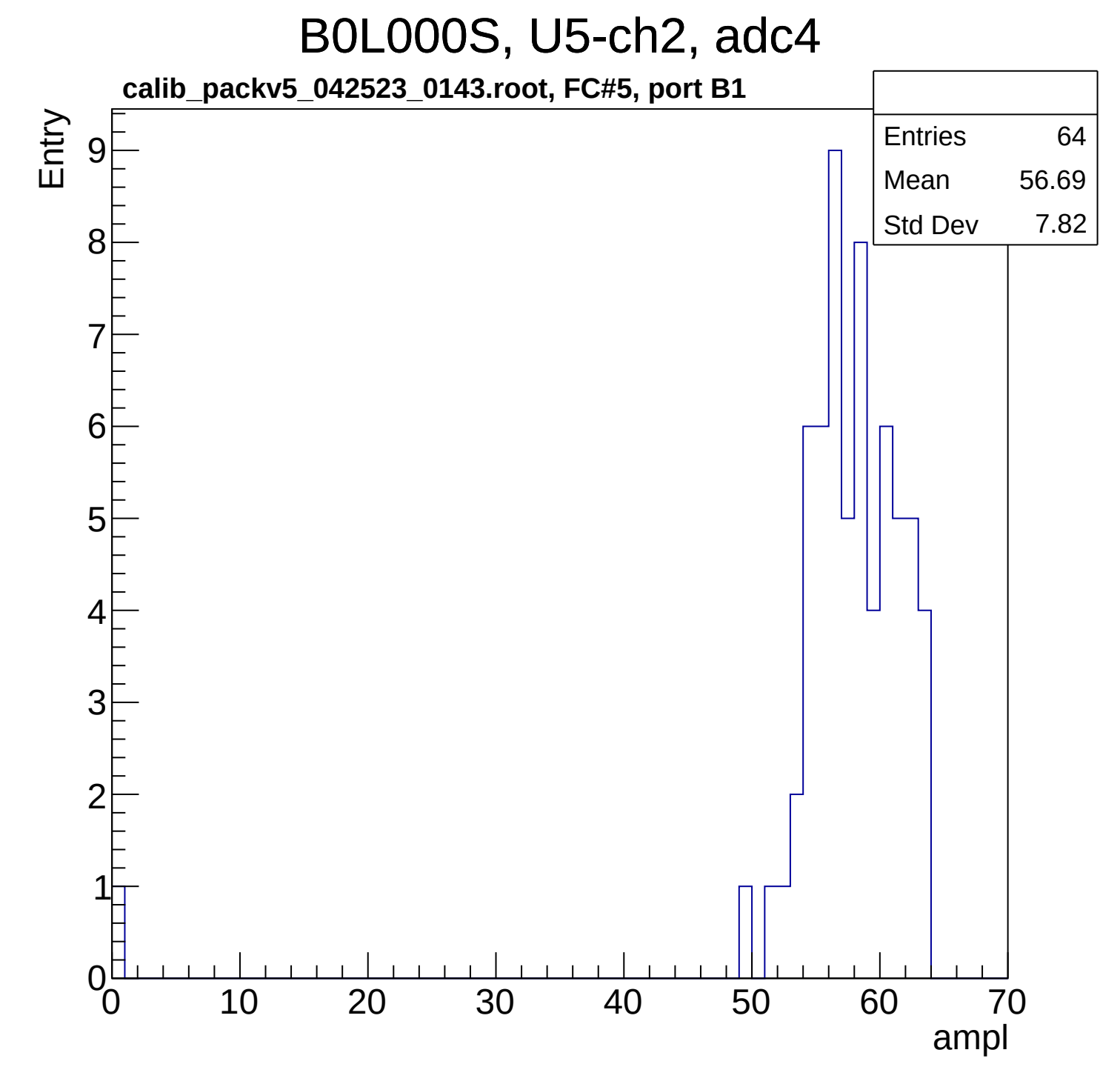
50
60
70


B0L000S, U5-ch2, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 56.69
Std Dev 7.82
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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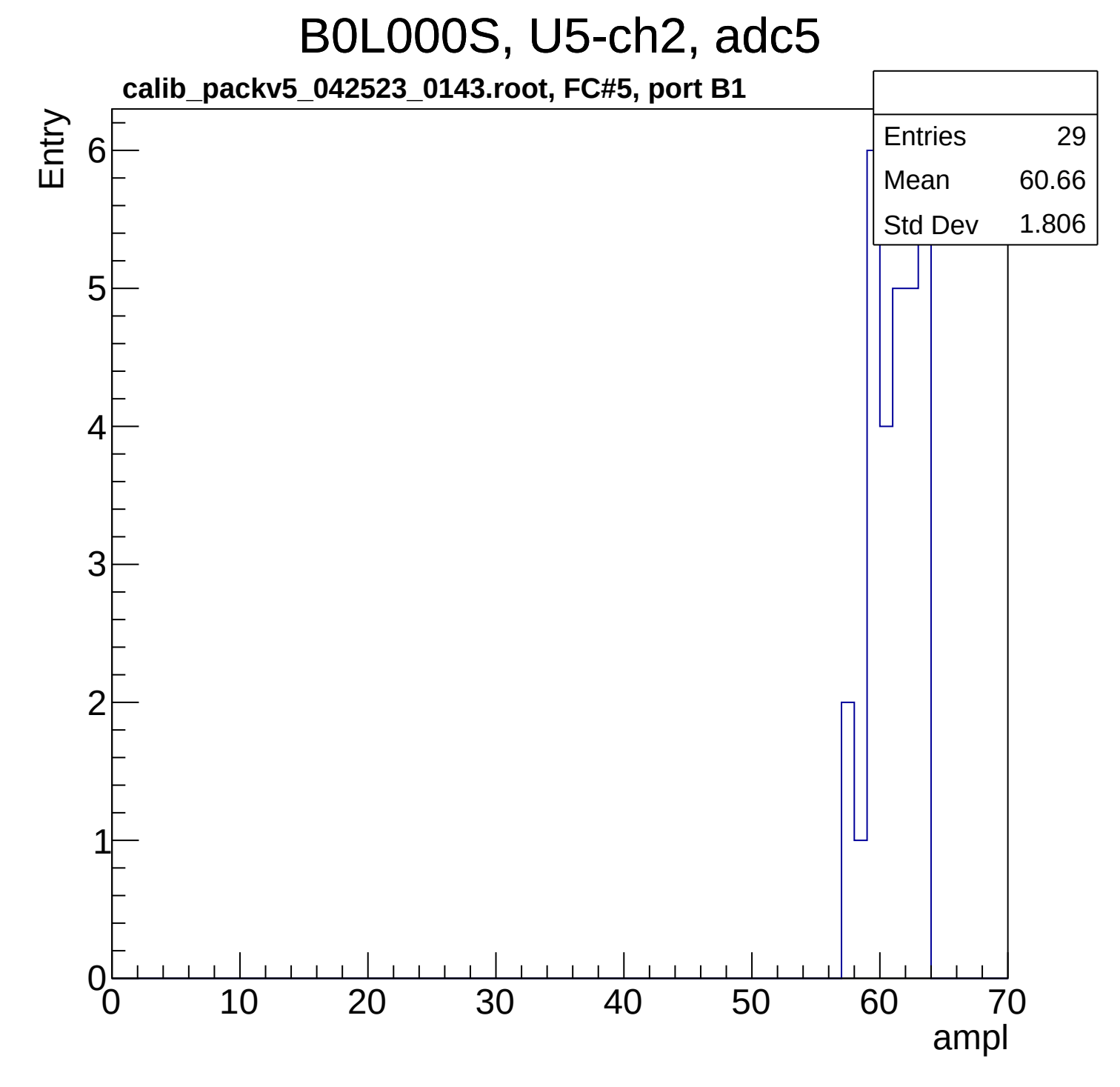
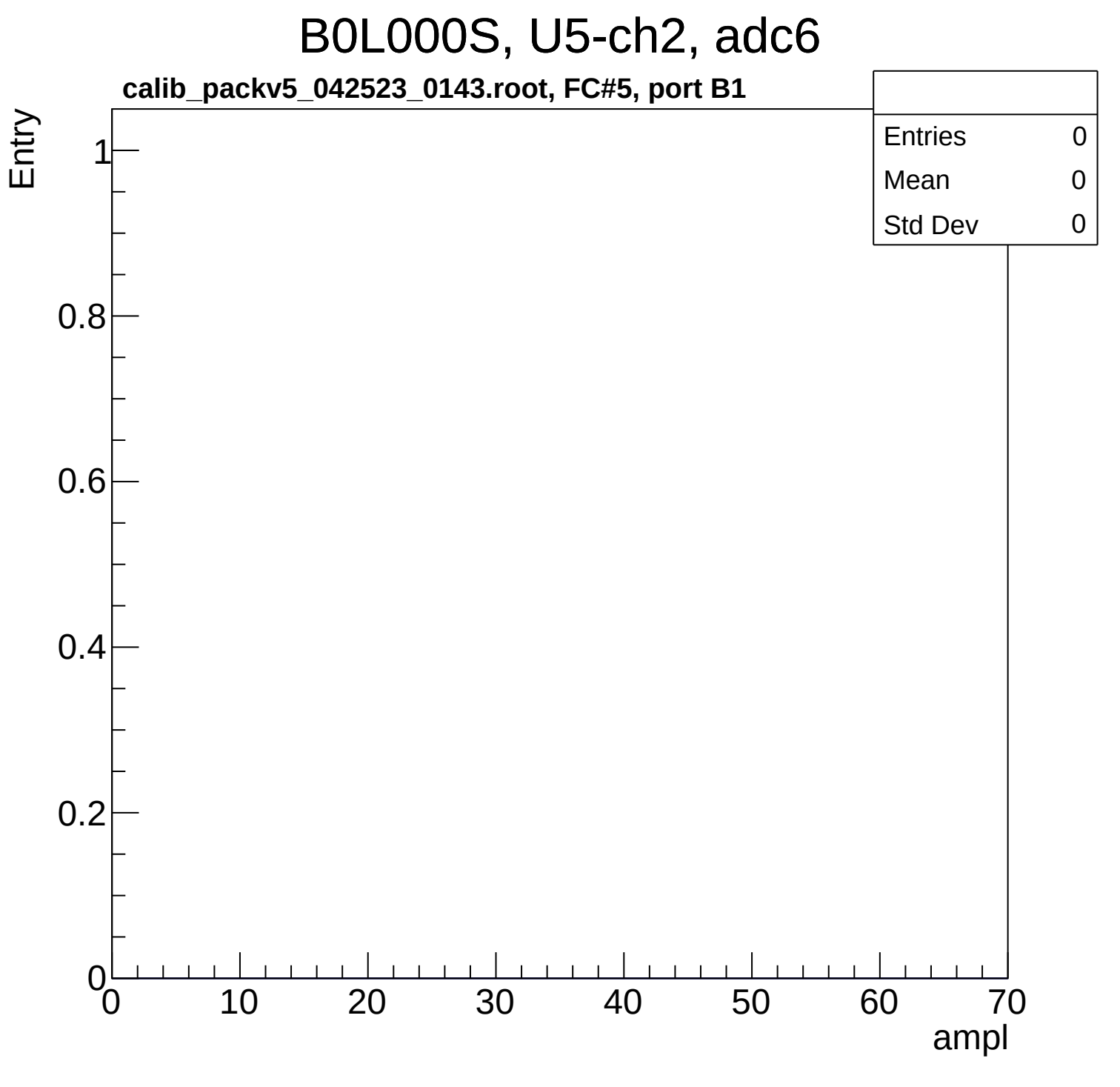
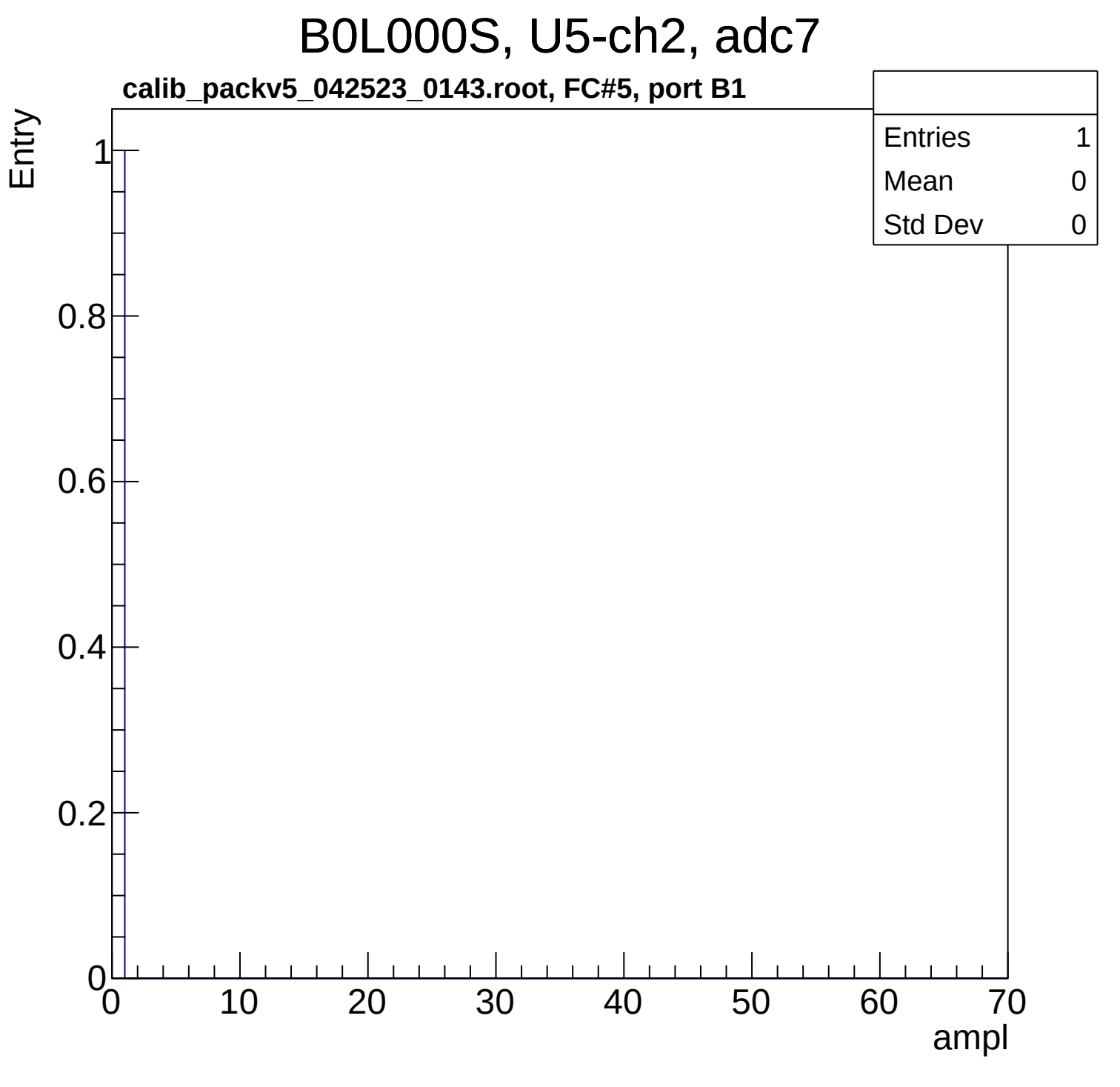

B0L000S, U5-ch2, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 60.66
Std Dev 1.806
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch2, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch2, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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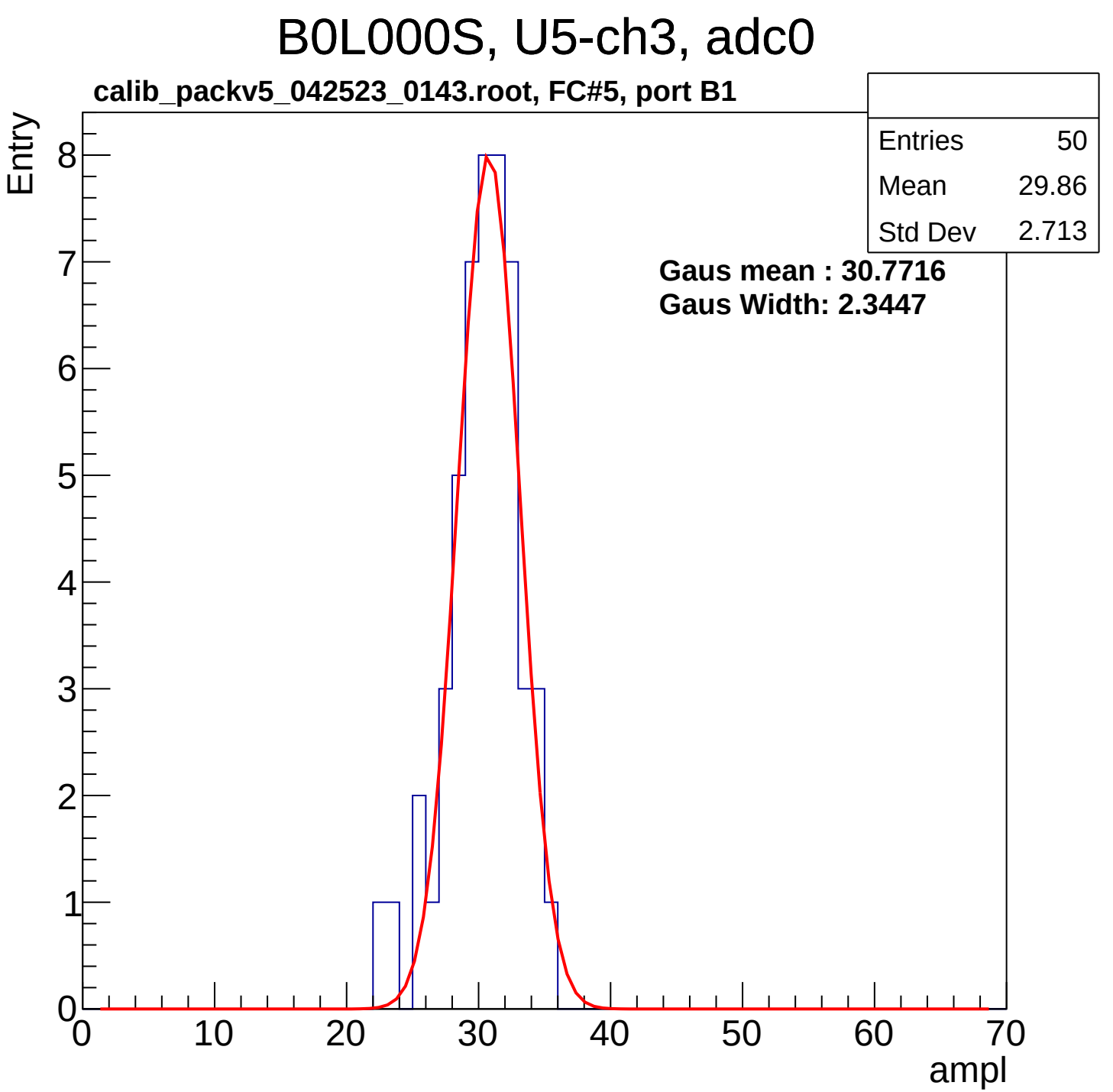
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch3, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 29.86
Std Dev 2.713
Gaus mean : 30.7716
Gaus Width: 2.3447
Entry
ampl
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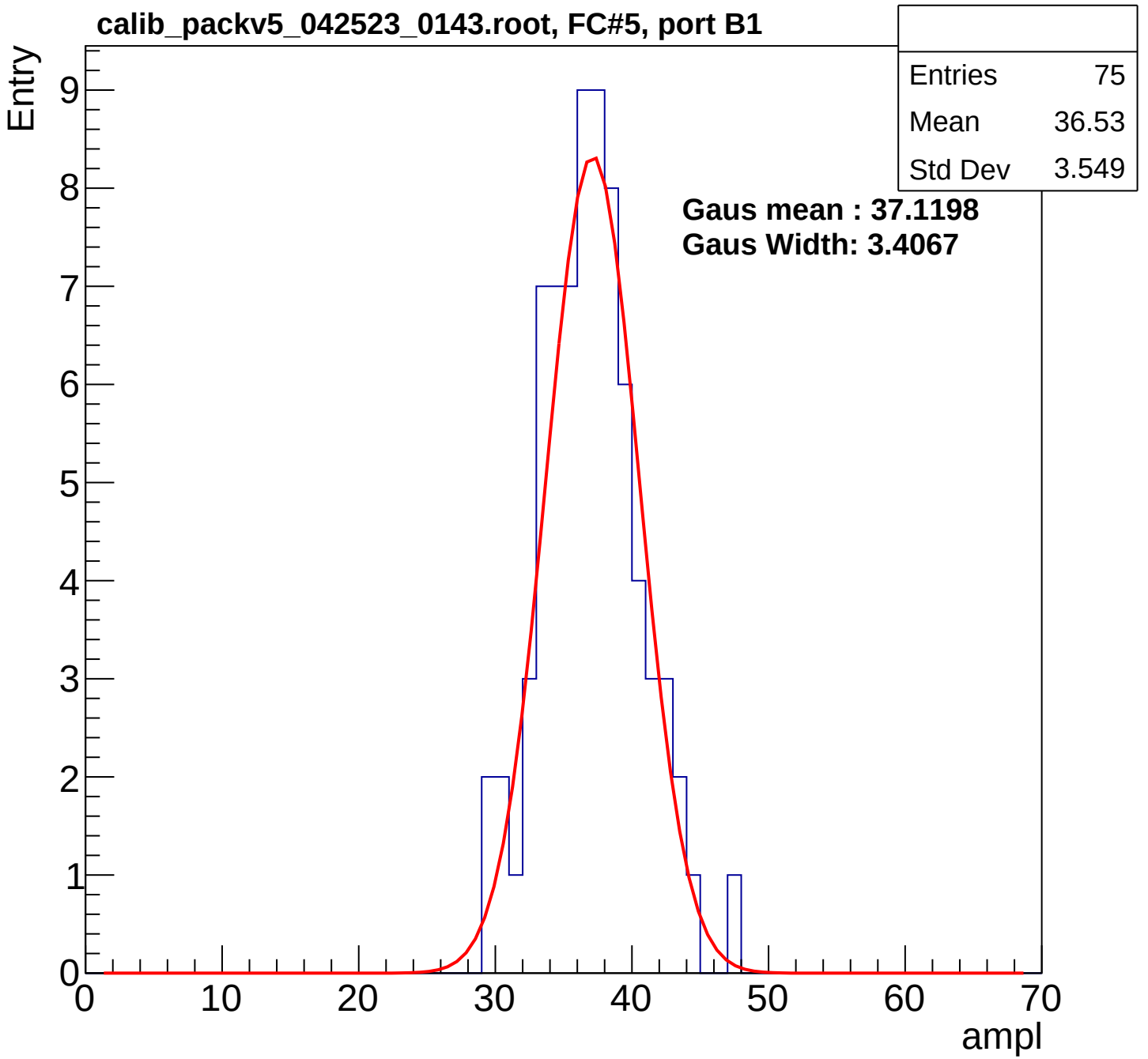
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch3, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.53
Std Dev 3.549
Gaus mean : 37.1198
Gaus Width: 3.4067
Entry
ampl
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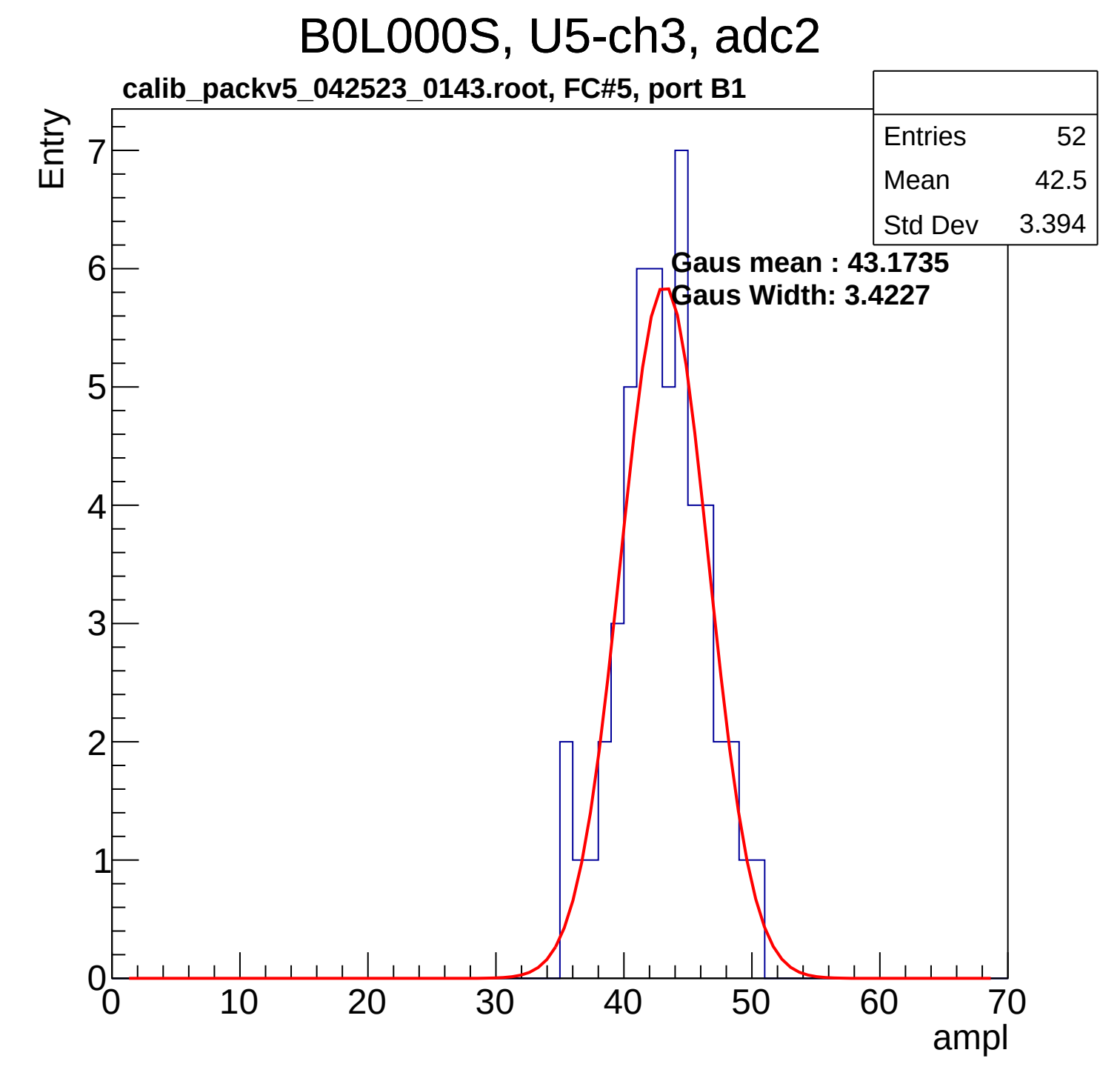
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U5-ch3, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.5
Std Dev 3.394
Gaus mean : 43.1735
Gaus Width: 3.4227
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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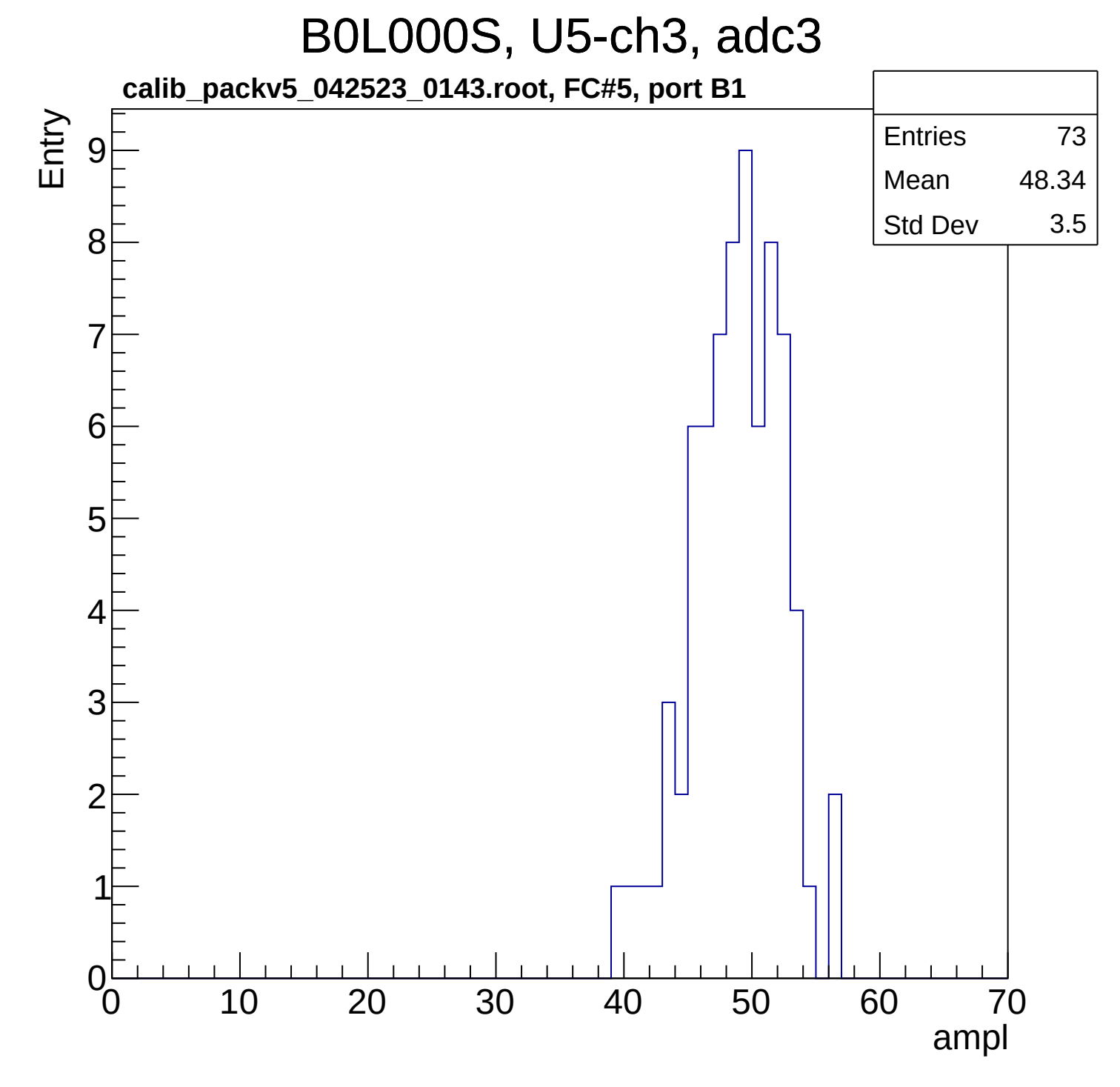
B0L000S, U5-ch3, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 48.34
Std Dev 3.5
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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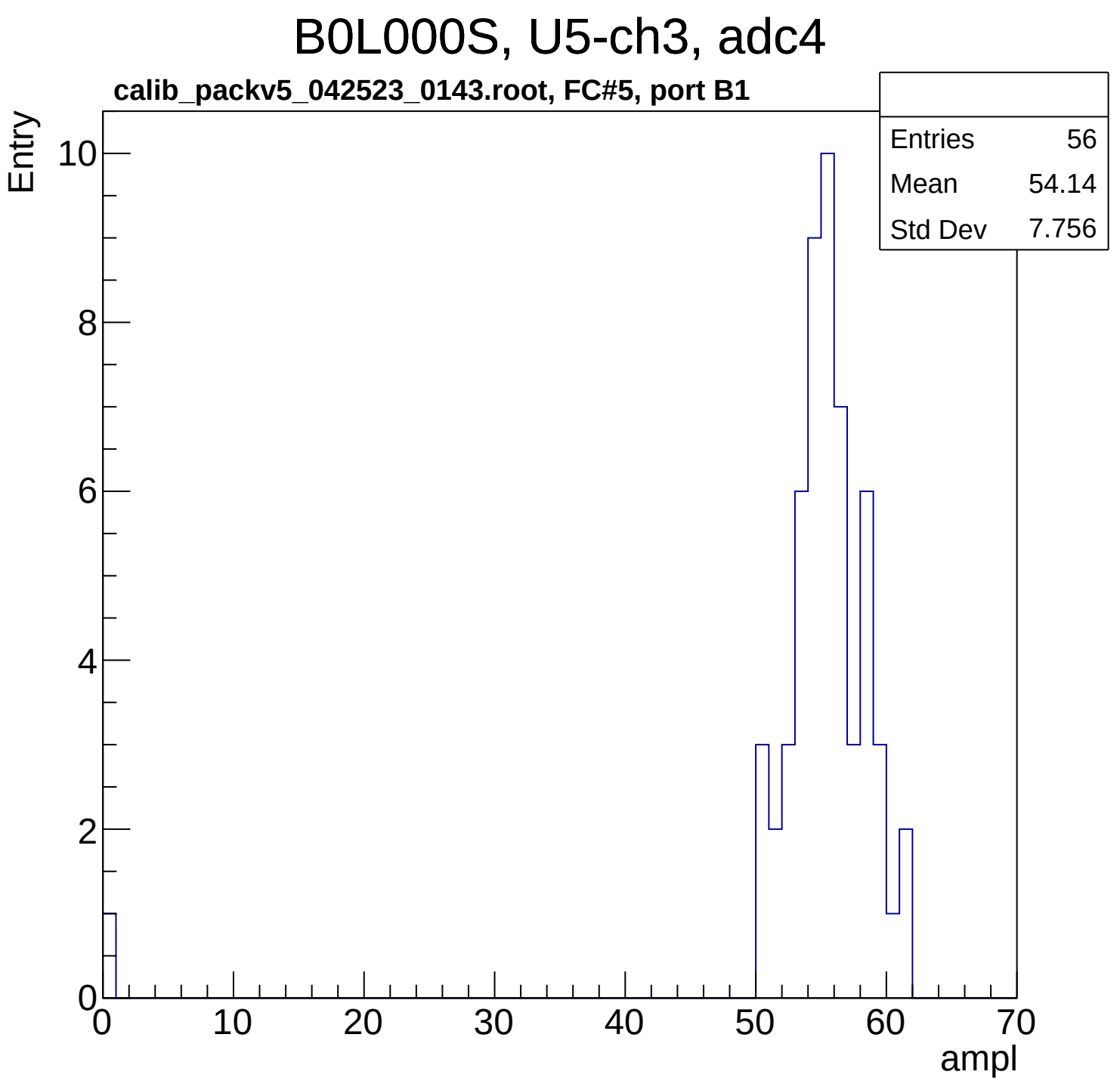
B0L000S, U5-ch3, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 54.14
Std Dev 7.756
Entry
ampl
0
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4
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8
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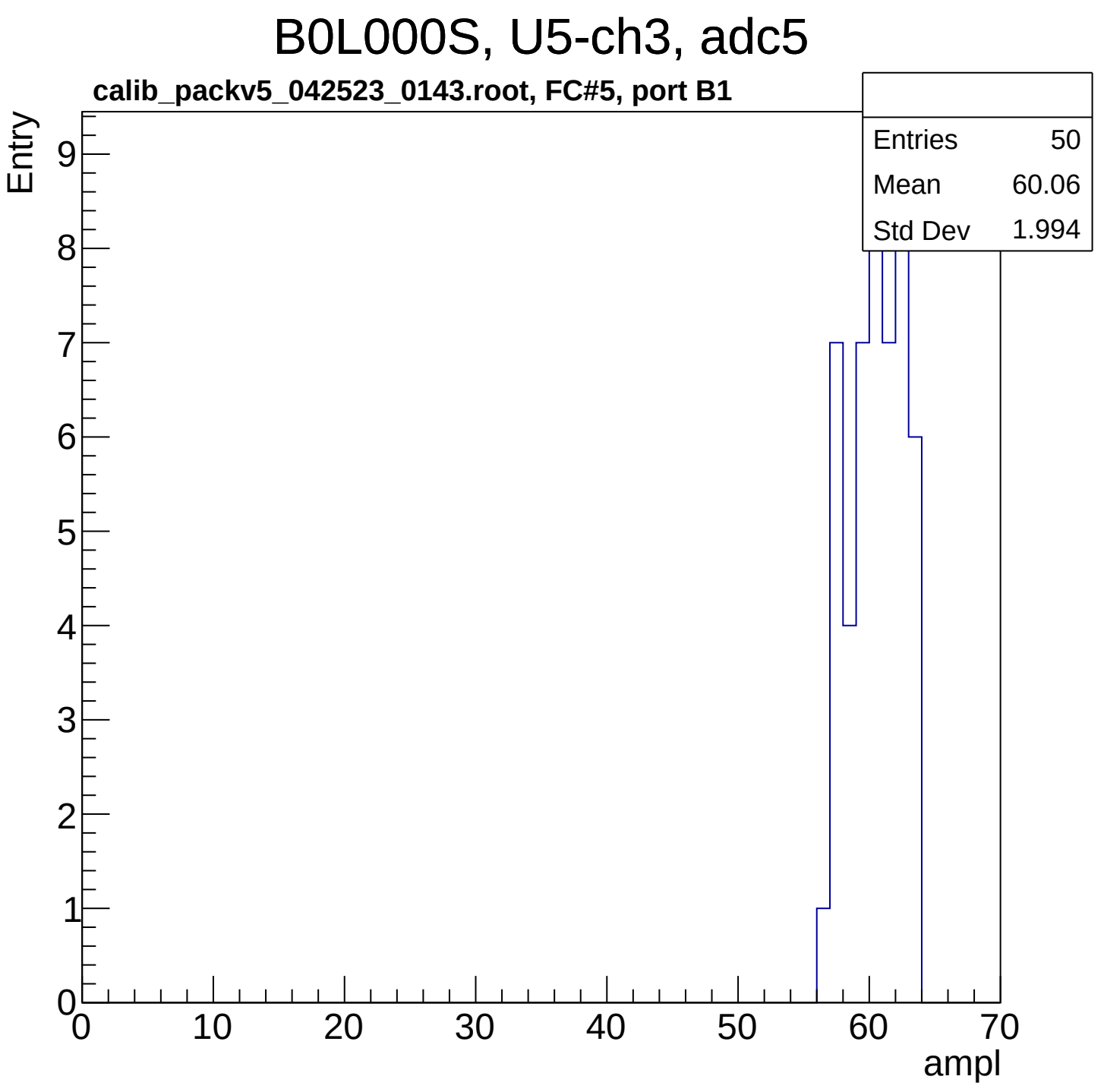
B0L000S, U5-ch3, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 60.06
Std Dev 1.994
Entry
ampl
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3
4
5
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7
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0
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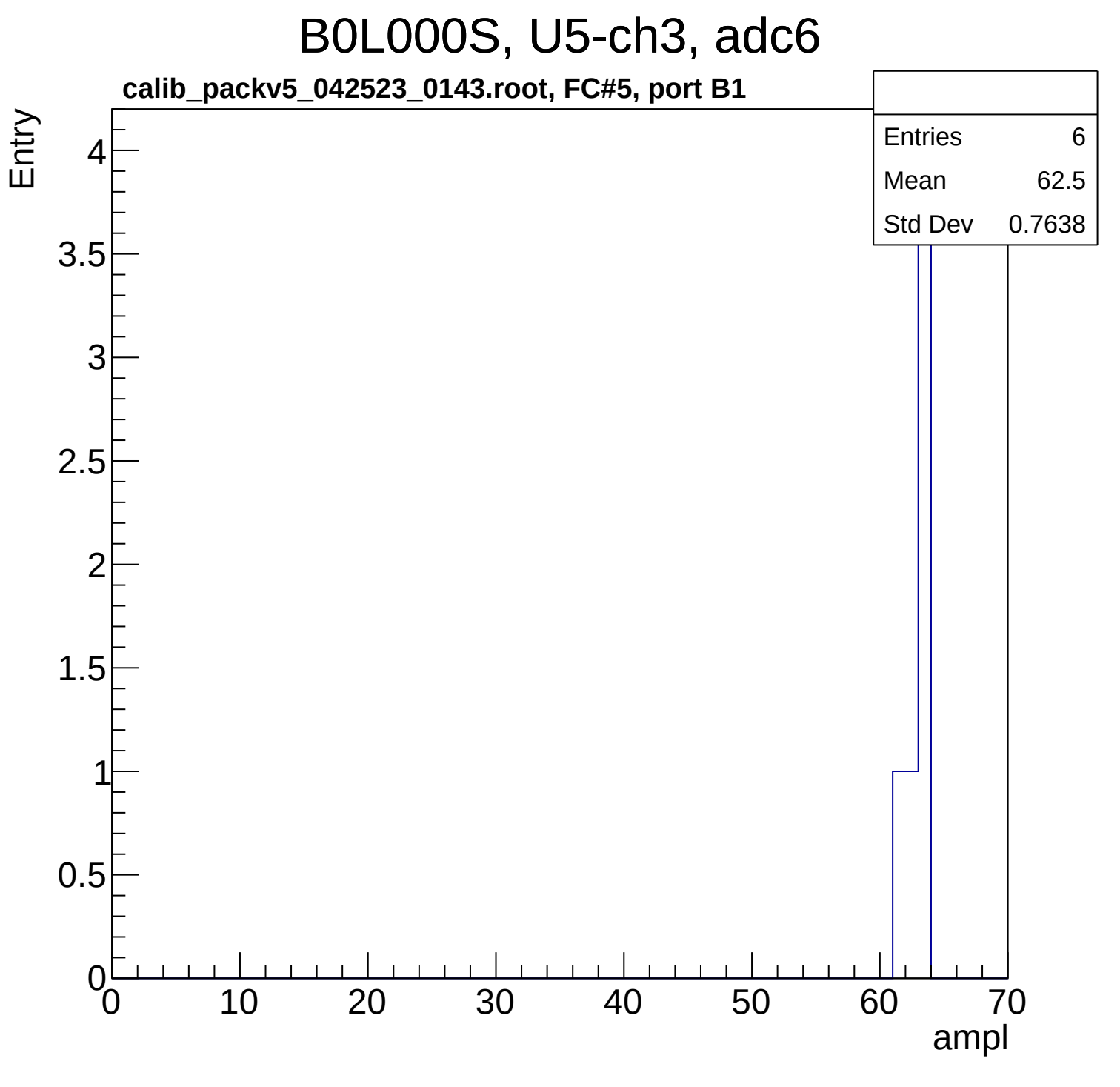
B0L000S, U5-ch3, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
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40
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60
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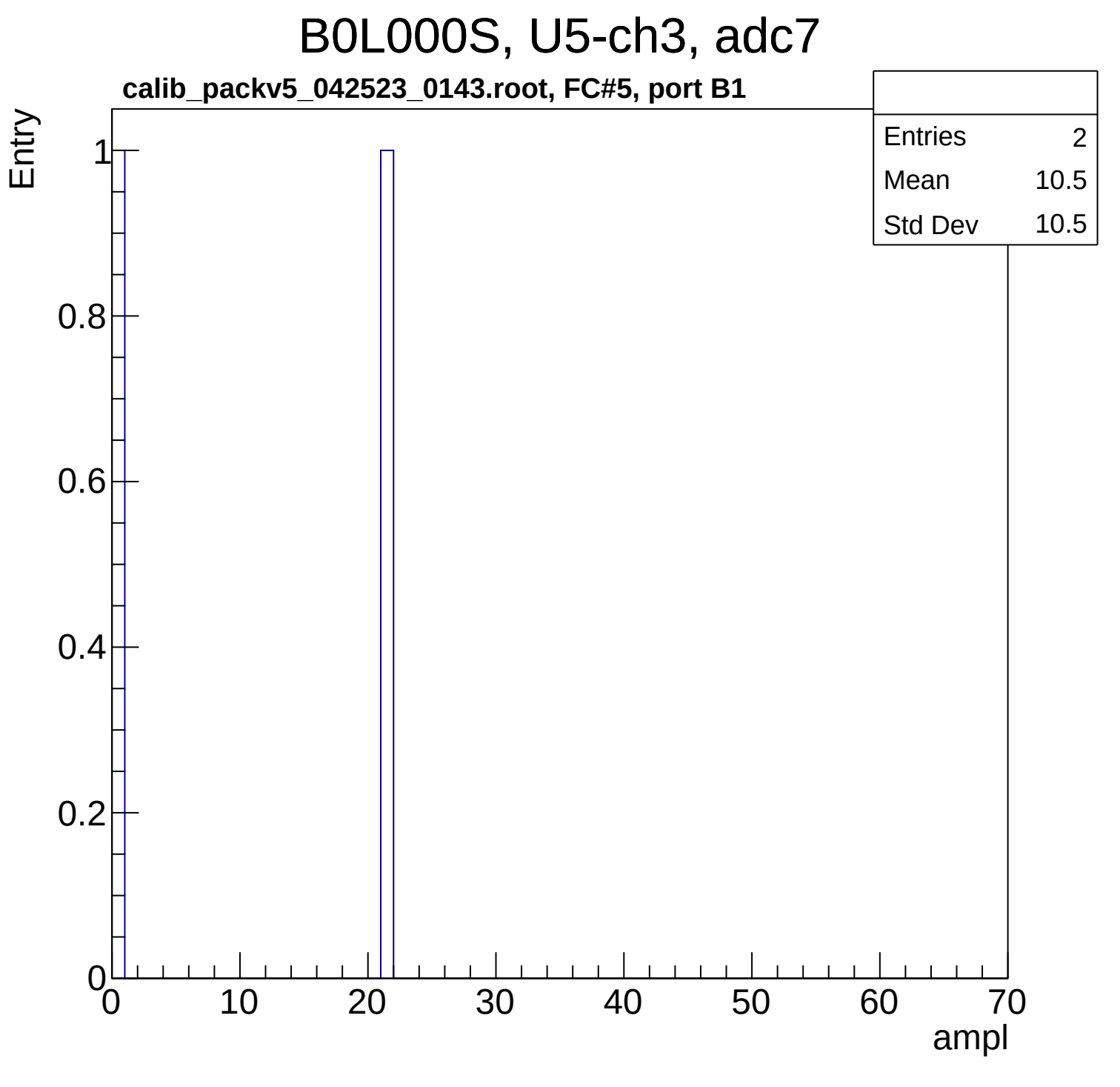
B0L000S, U5-ch3, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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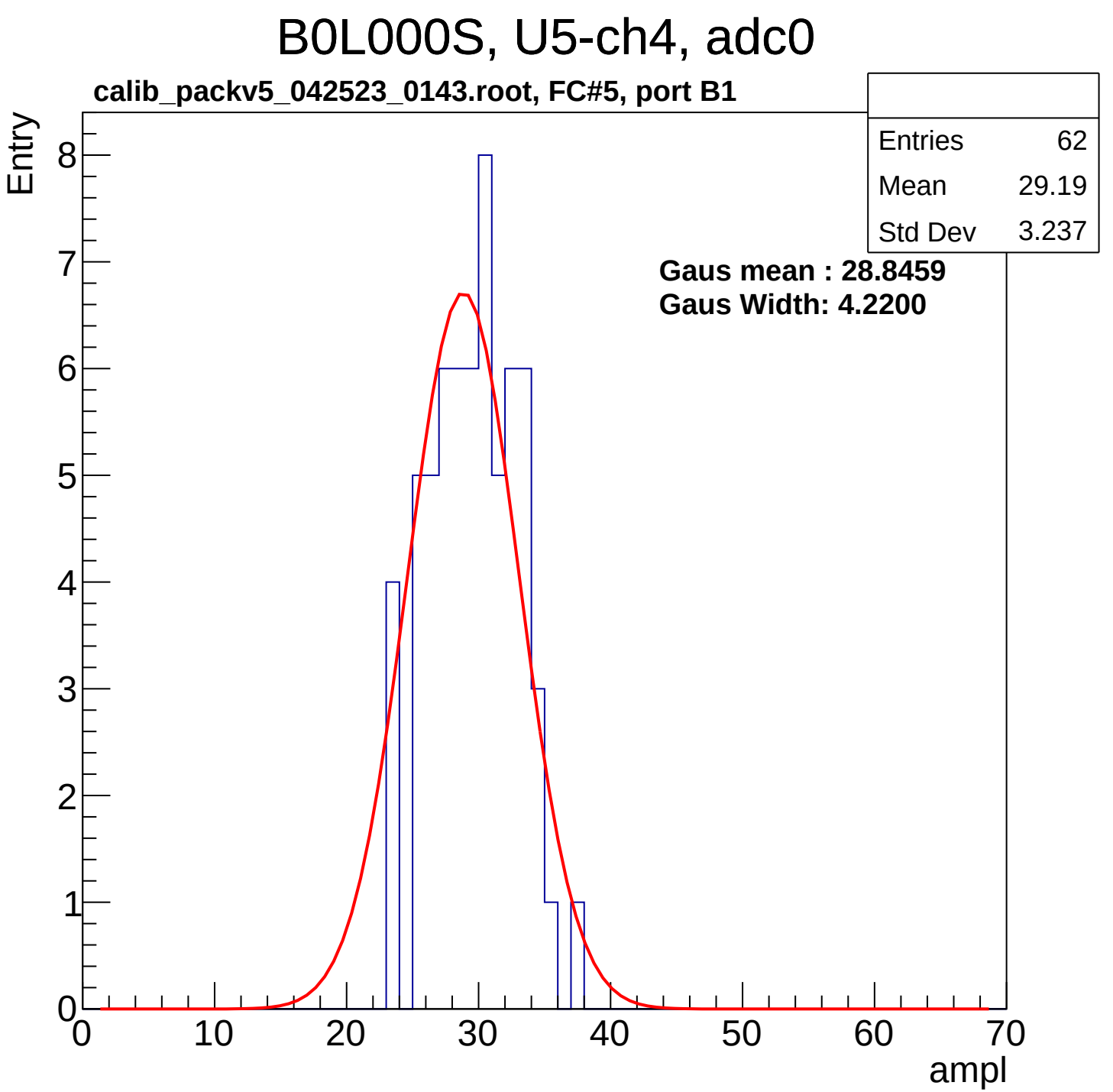
30
40
50
60
70
ampl


B0L000S, U5-ch4, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.19
Std Dev 3.237
Gaus mean : 28.8459
Gaus Width: 4.2200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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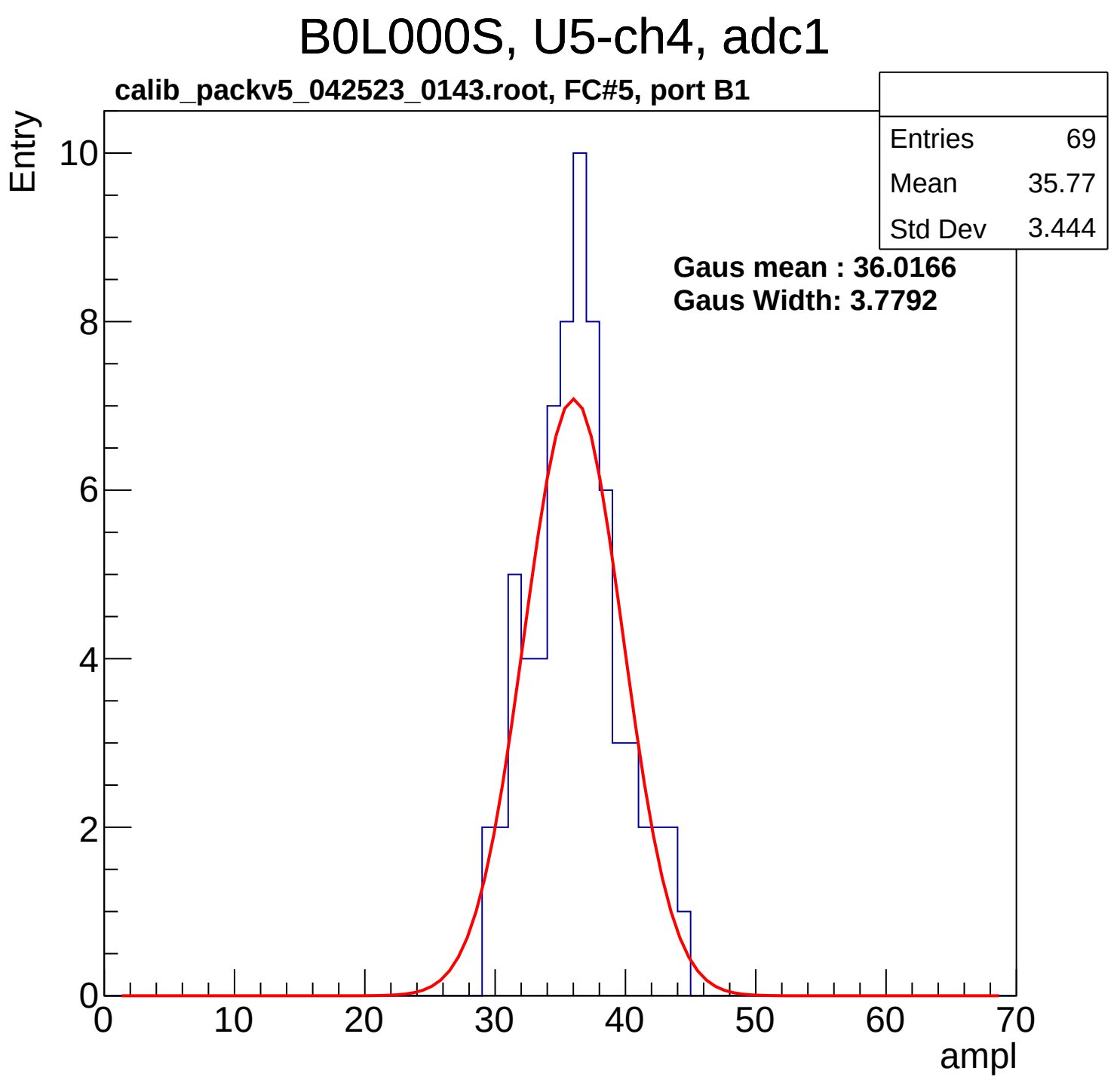
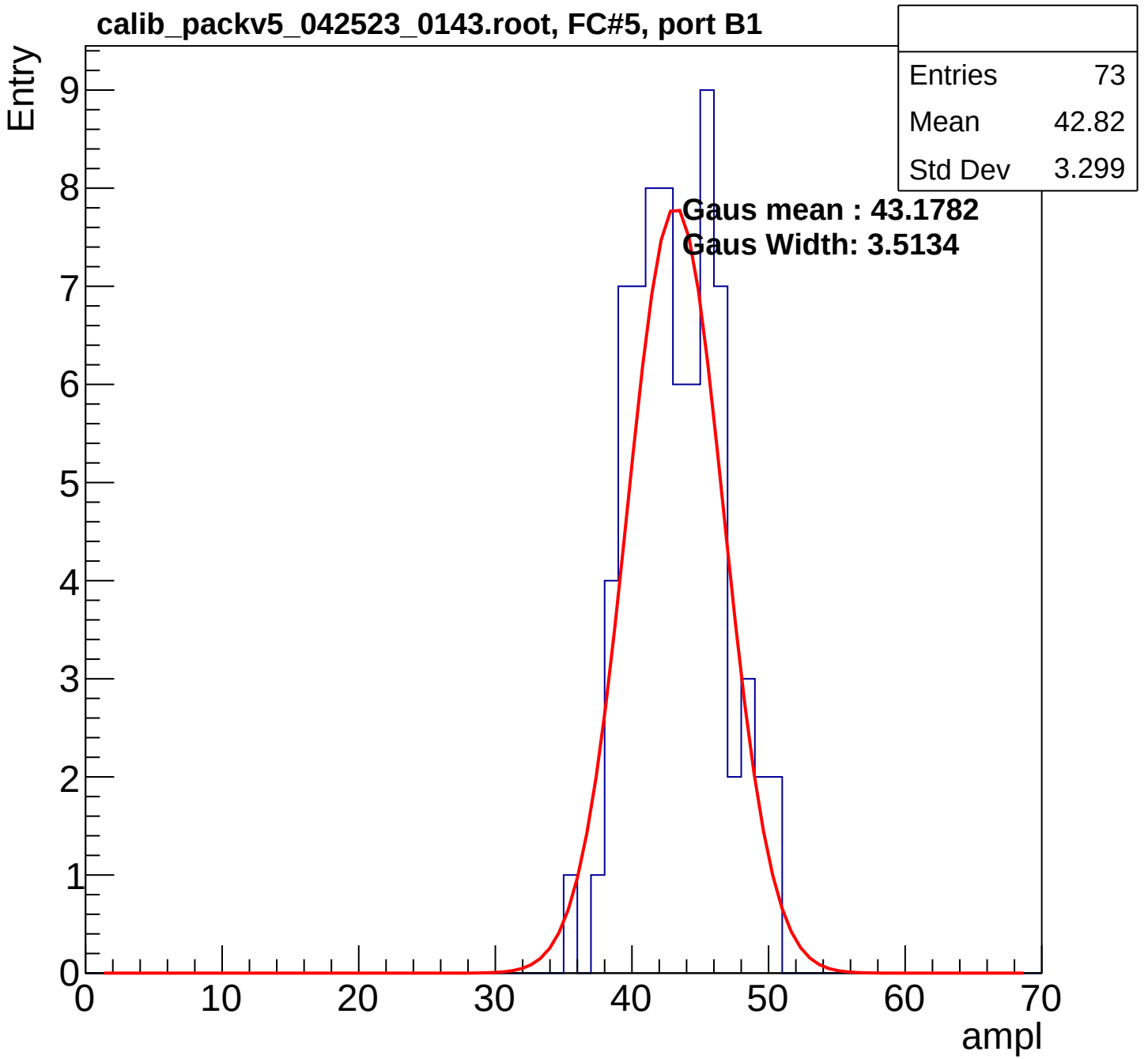

B0L000S, U5-ch4, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.77
Std Dev 3.444
Gaus mean : 36.0166
Gaus Width: 3.7792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch4, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.82
Std Dev 3.299
Gaus mean : 43.1782
Gaus Width: 3.5134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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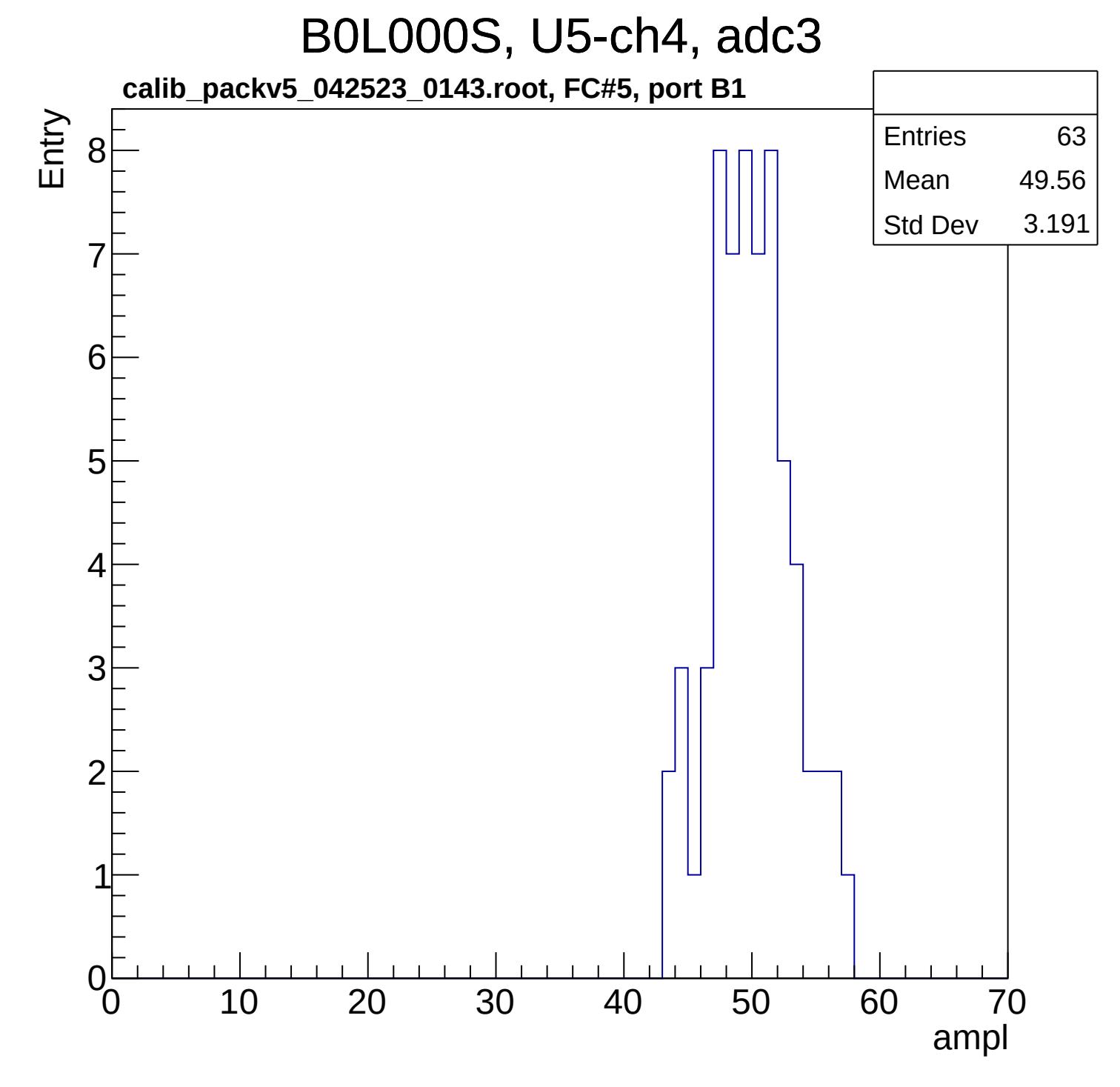
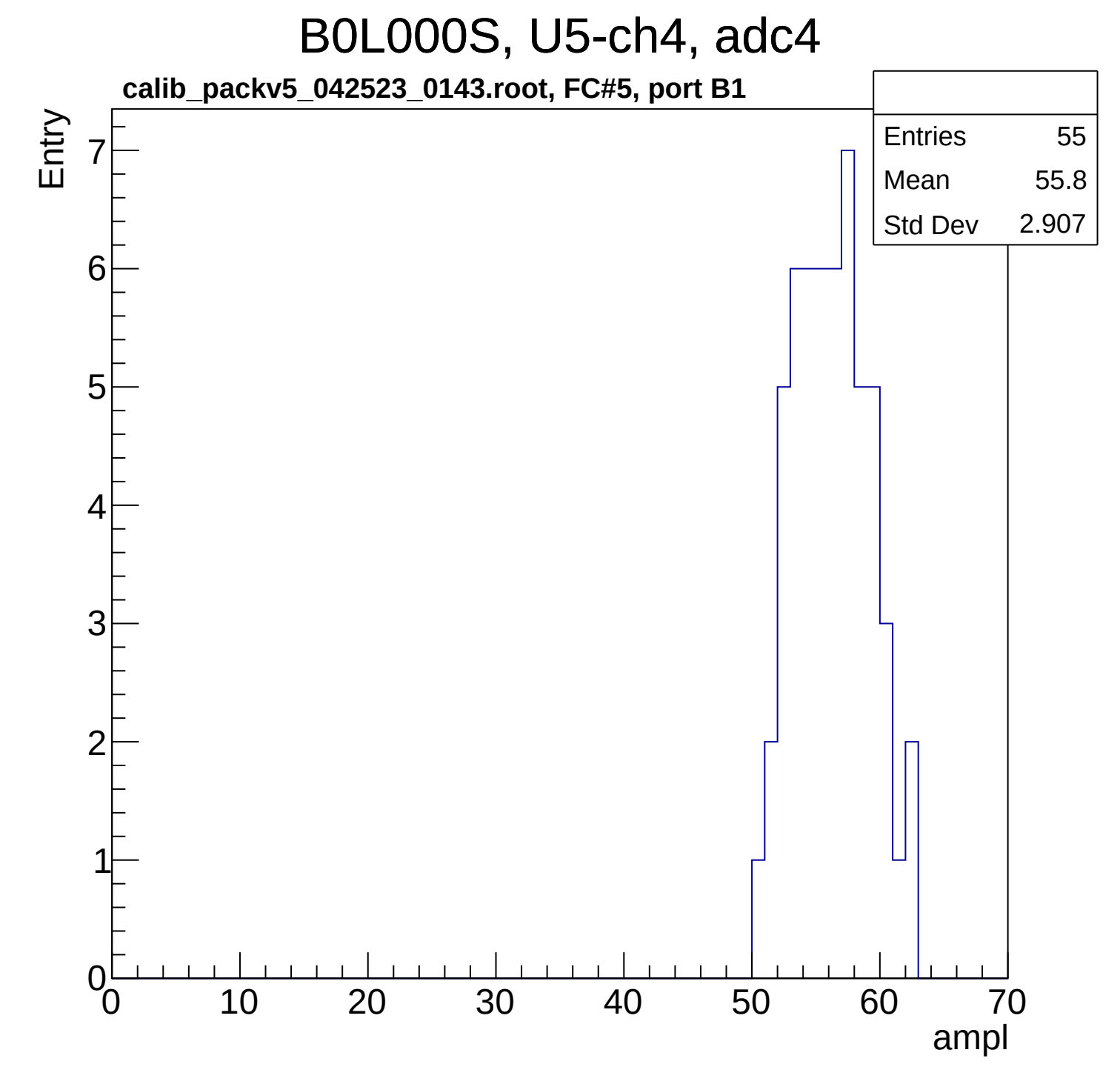
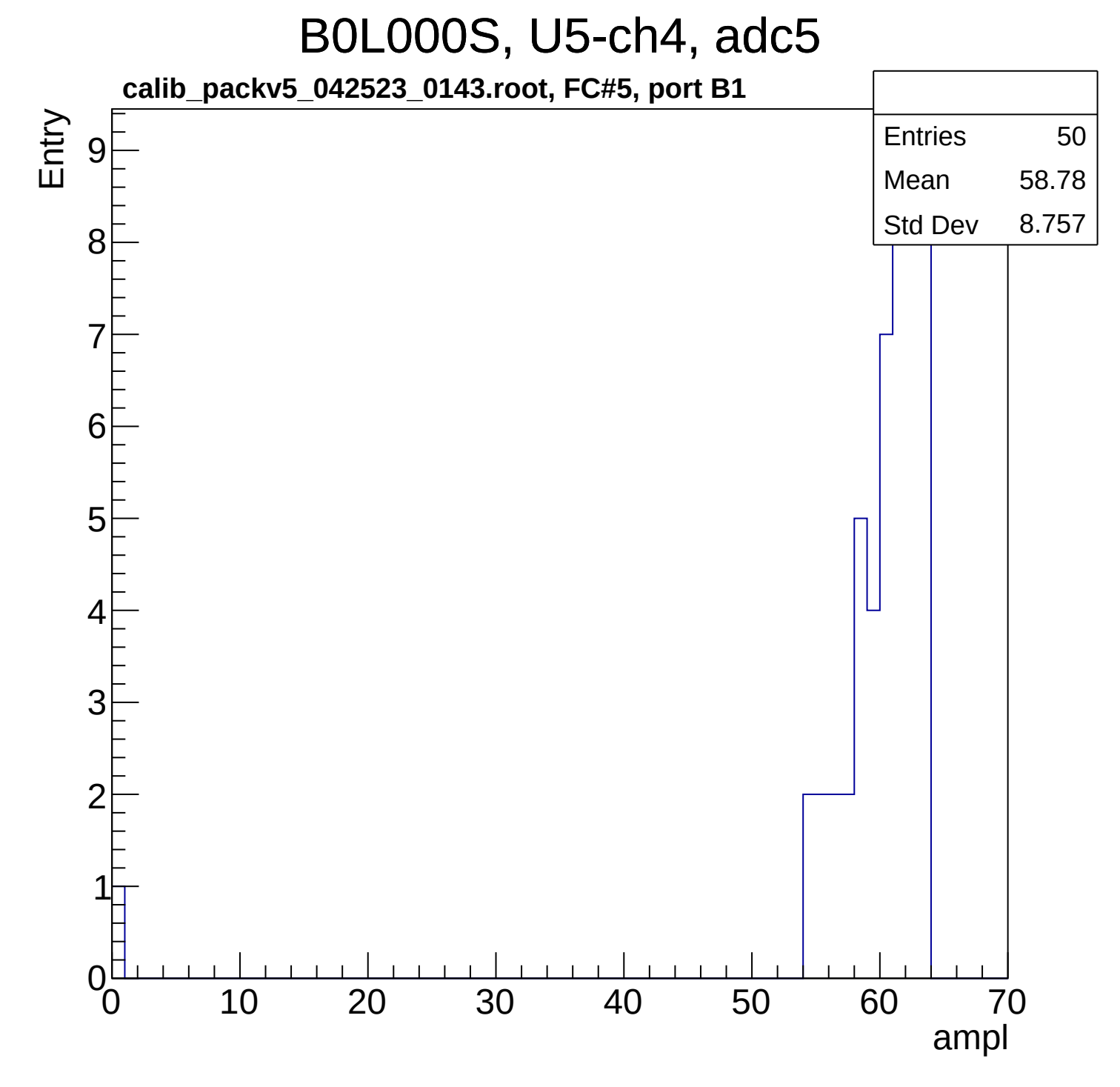
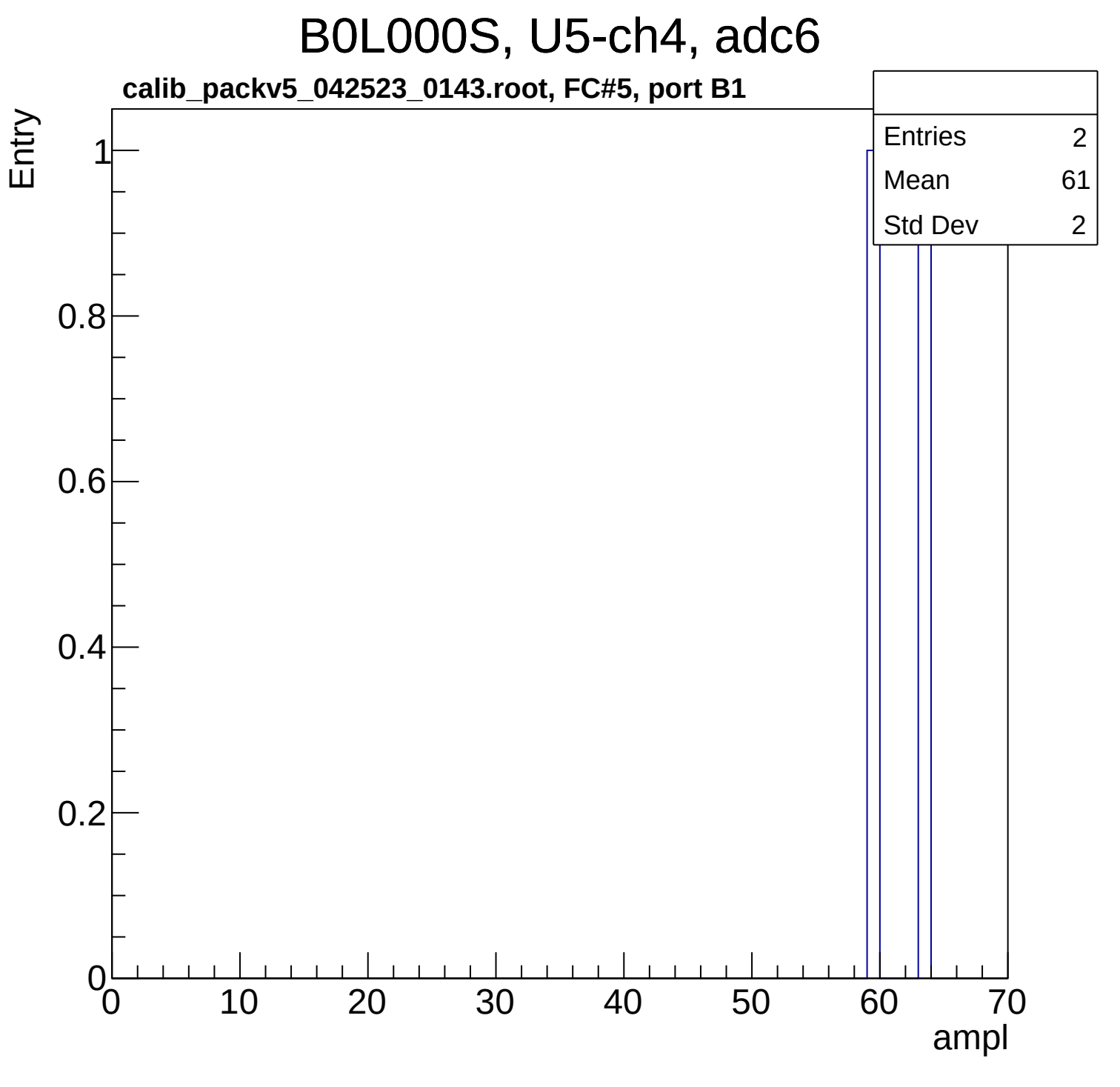
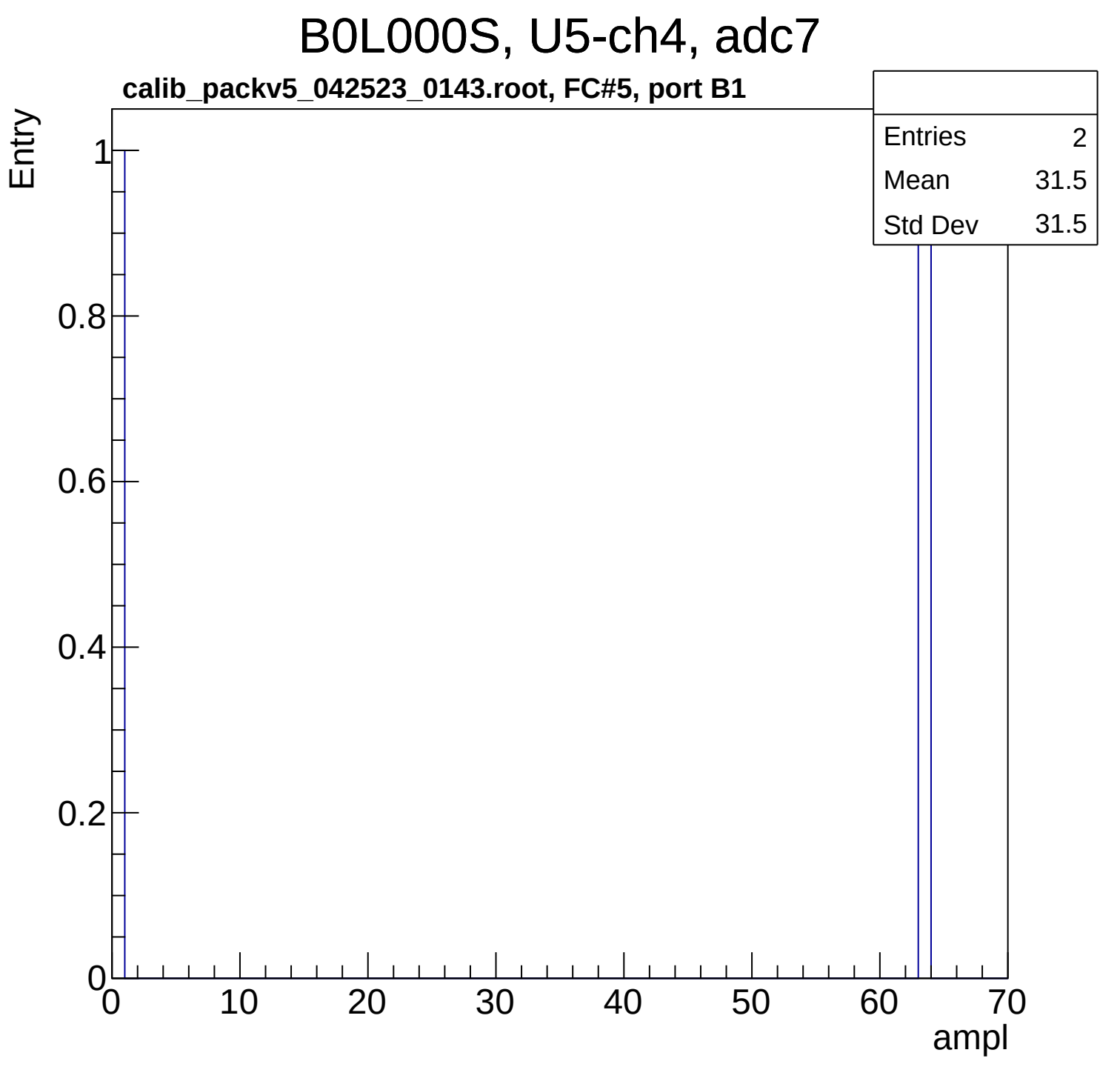

B0L000S, U5-ch4, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.56
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch4, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.8
Std Dev 2.907
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch4, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.78
Std Dev 8.757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch4, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch4, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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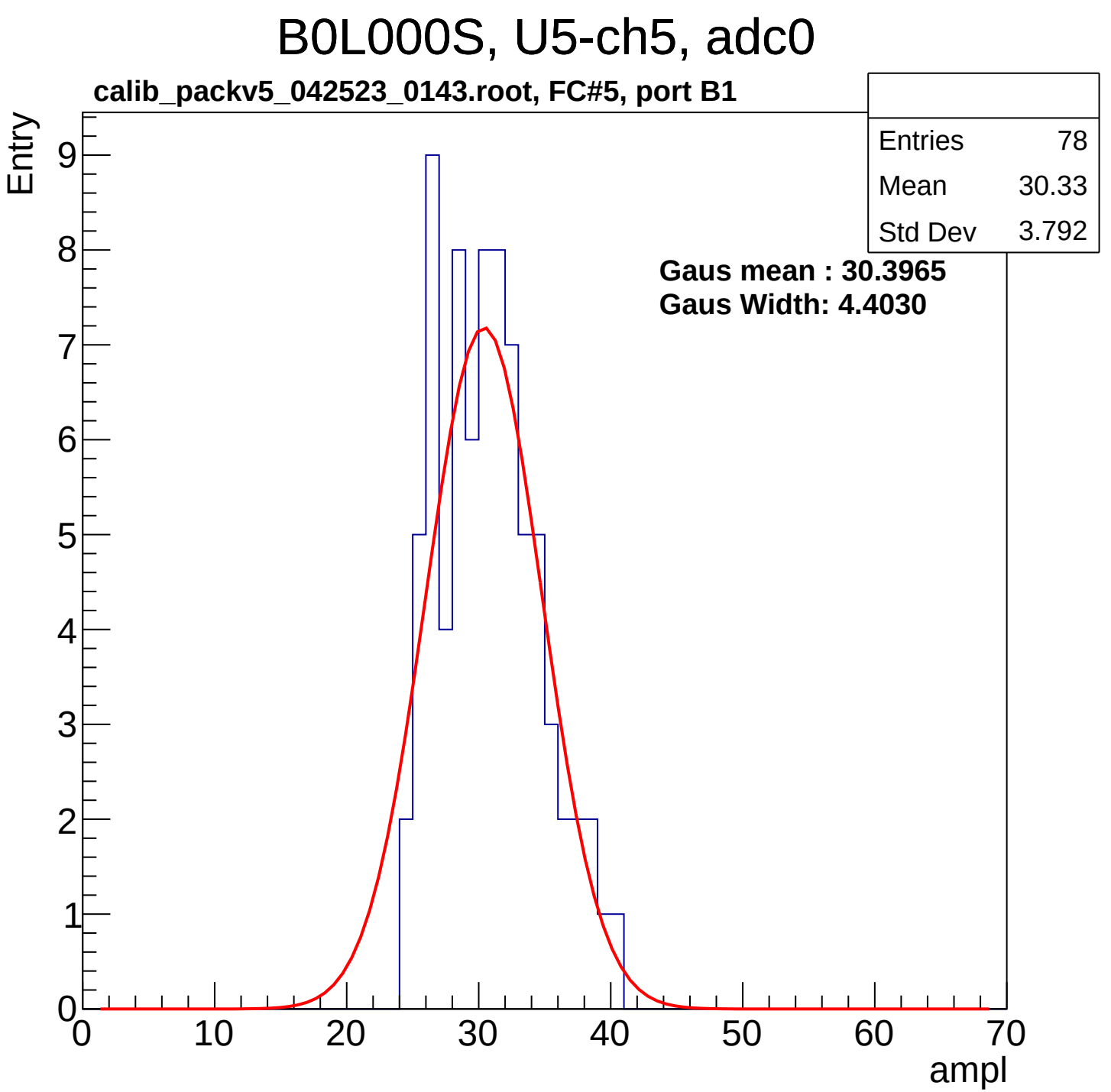
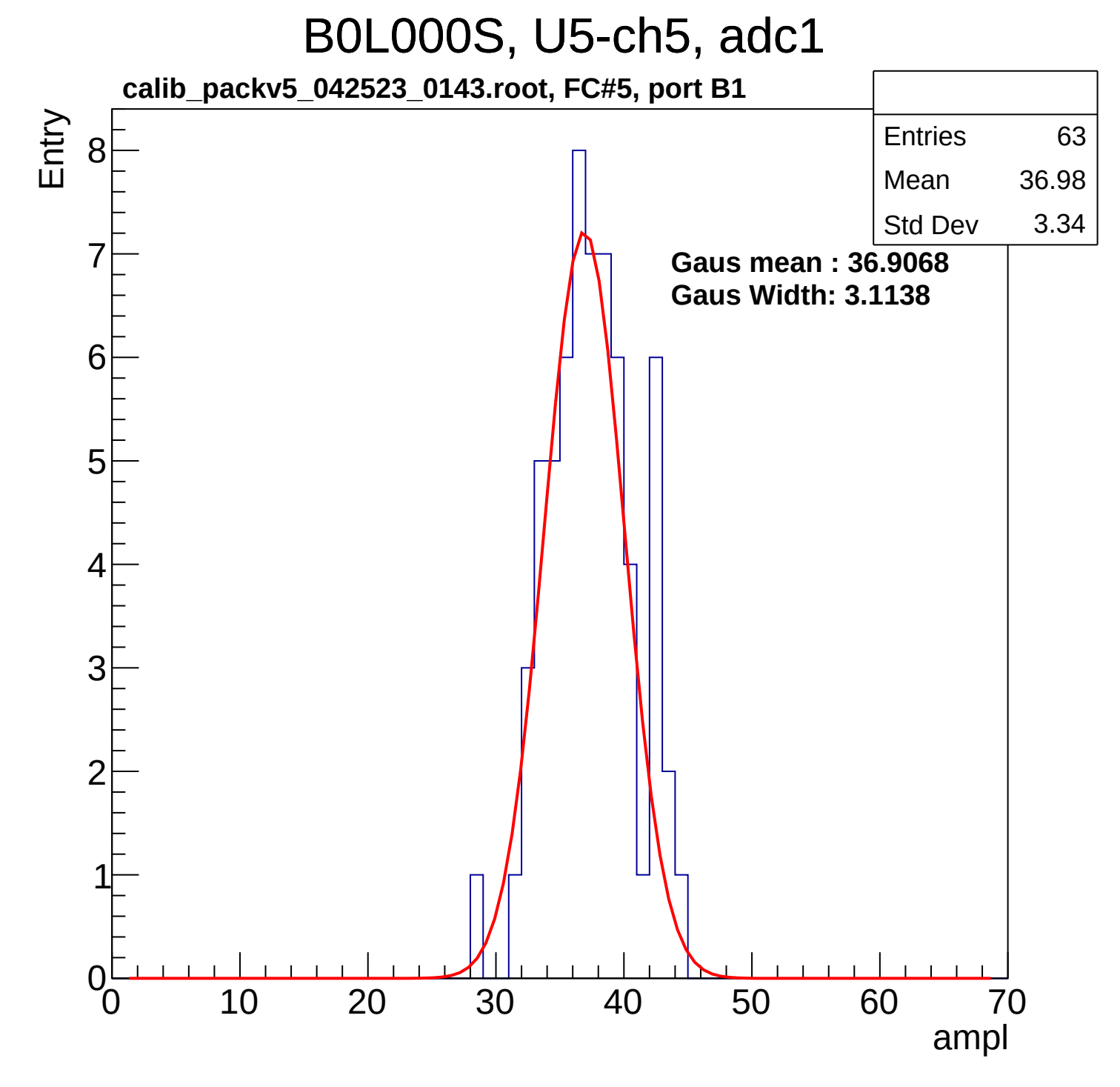
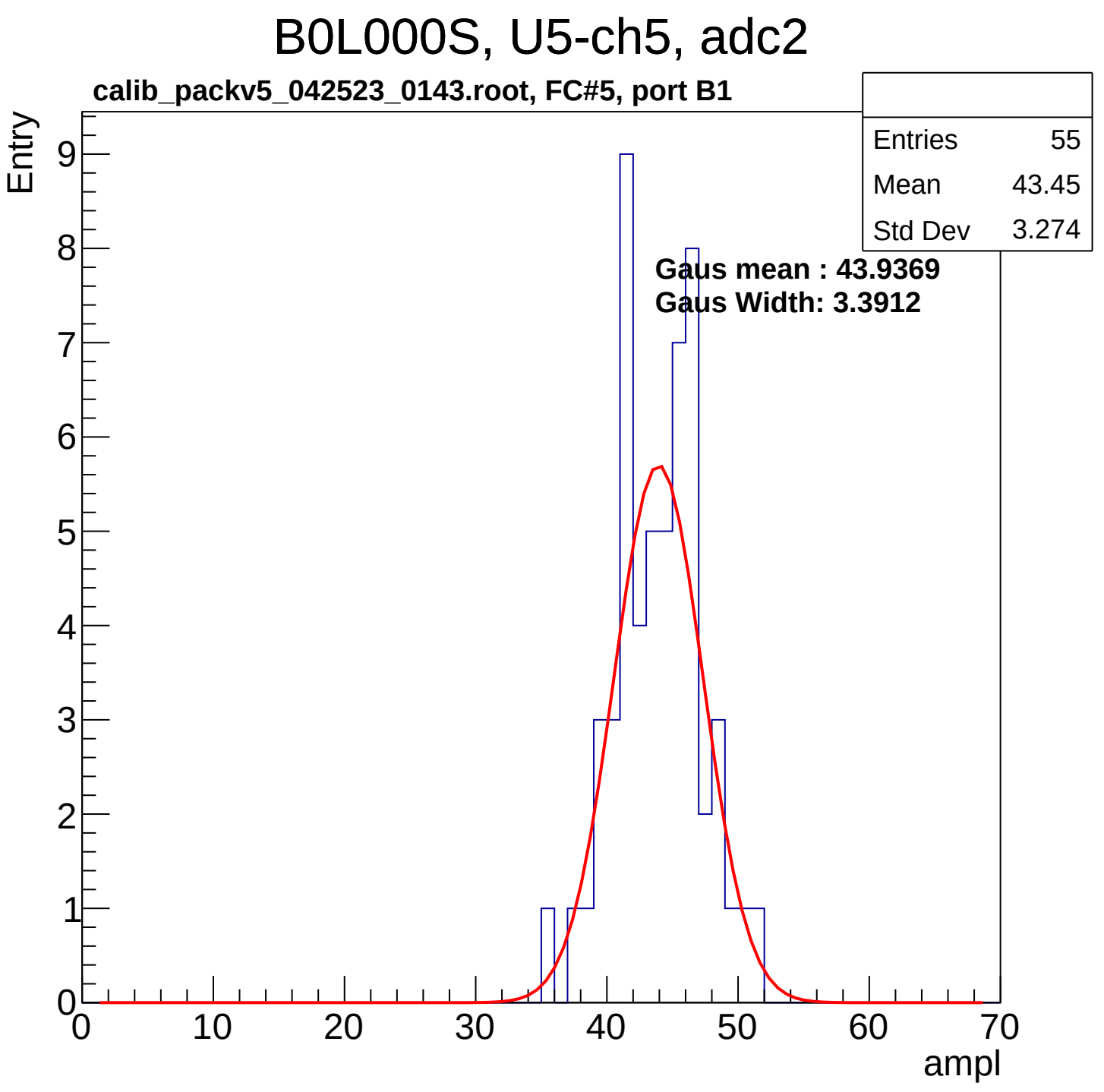
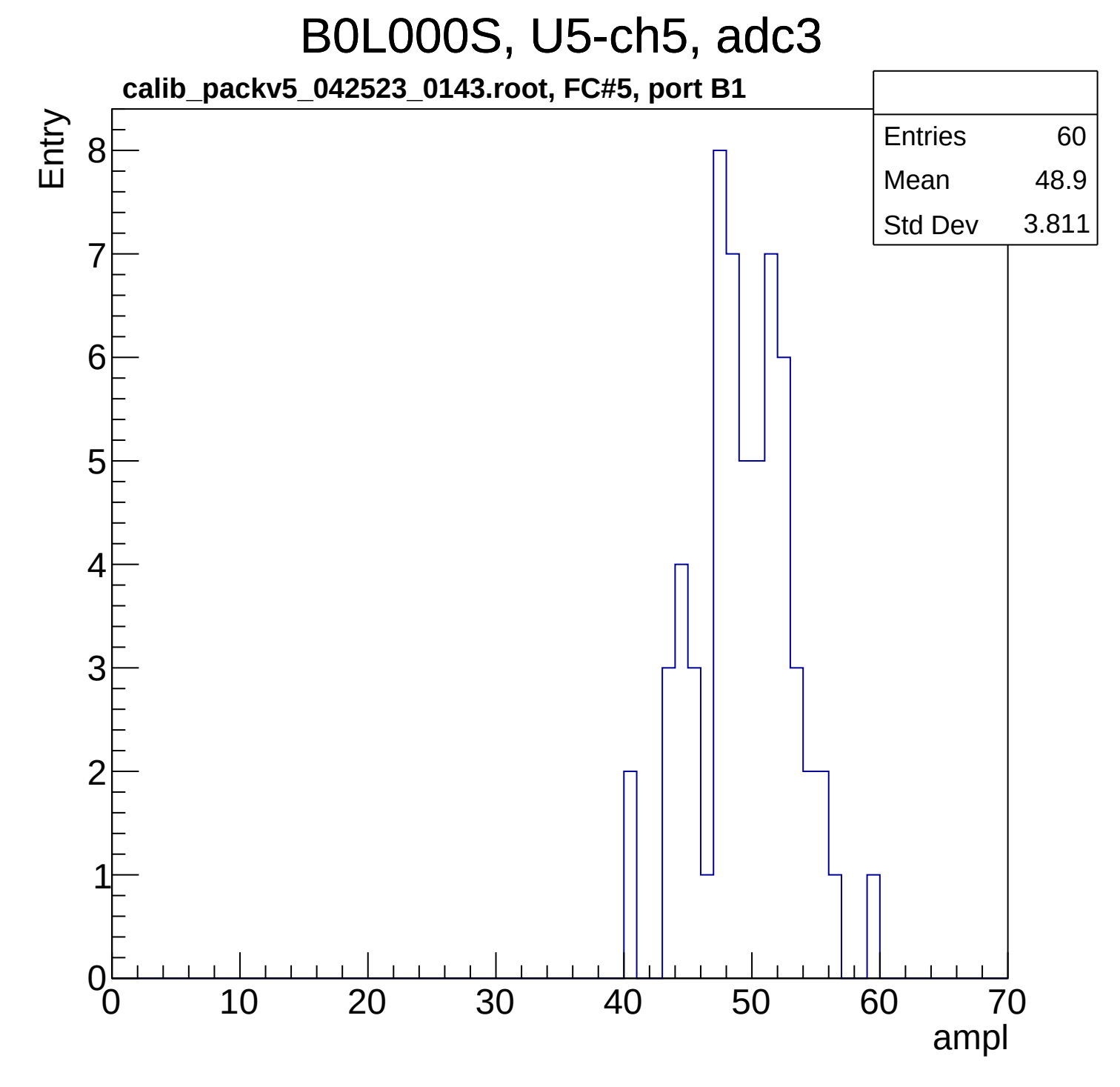
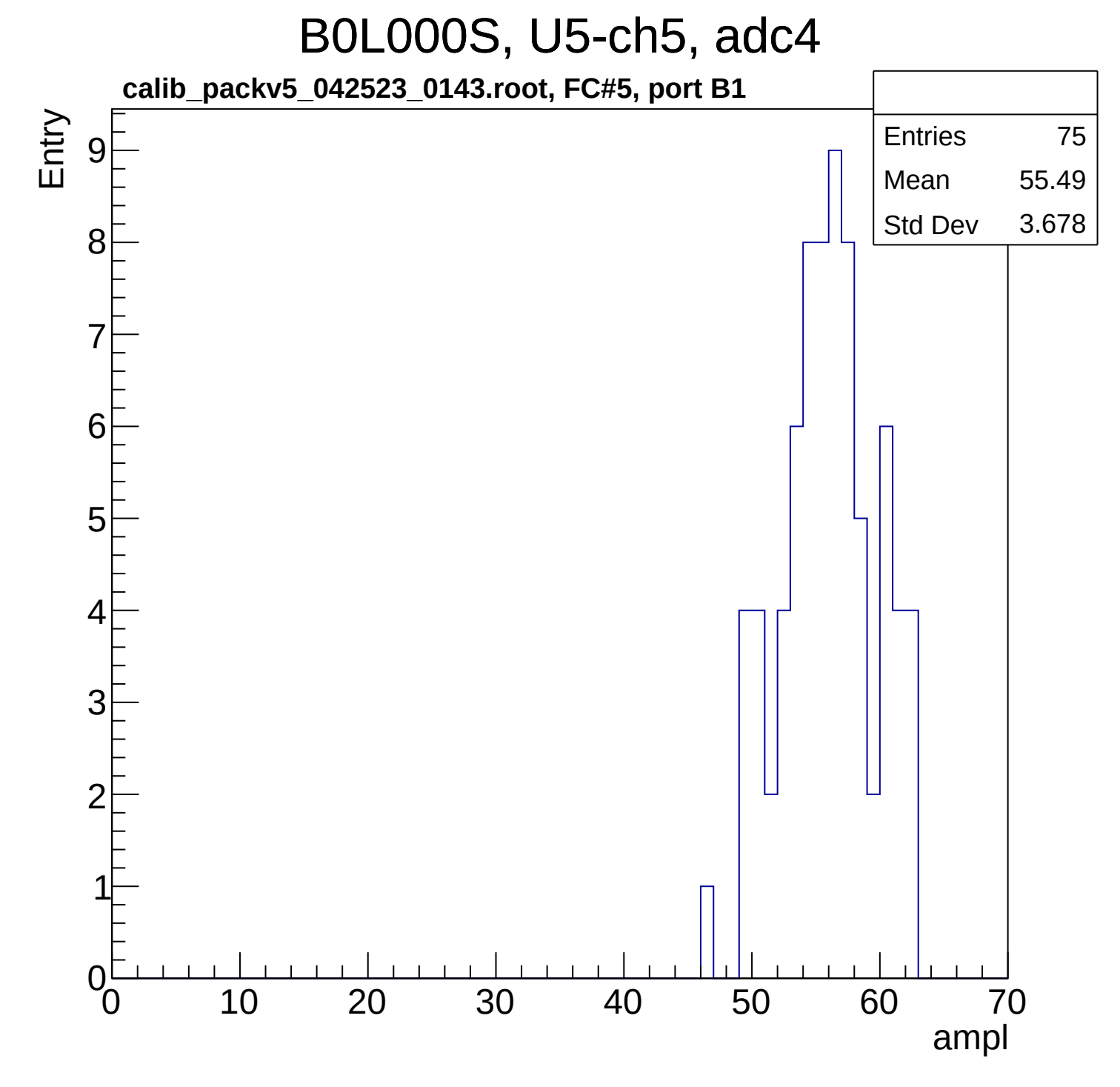
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

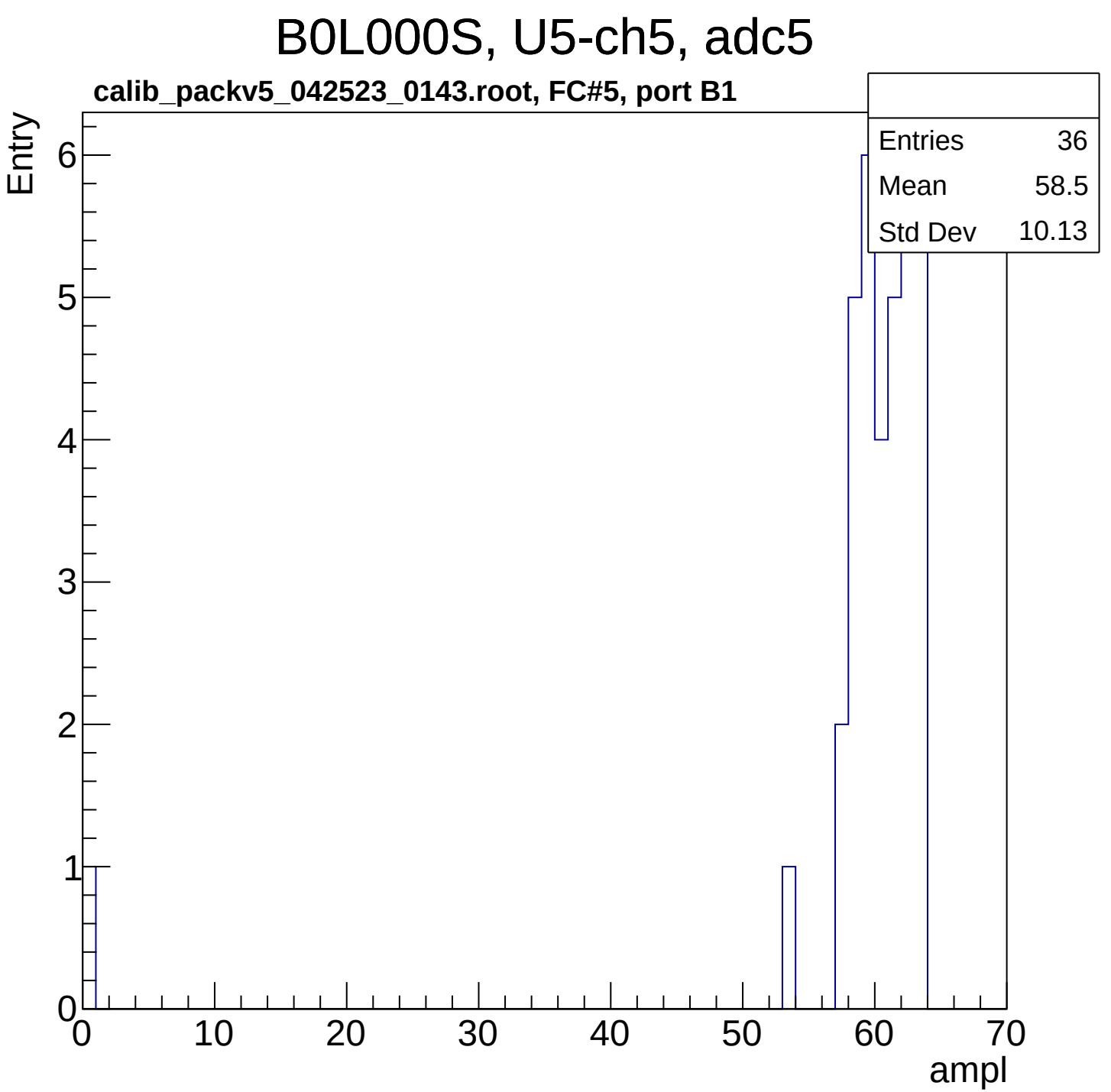

B0L000S, U5-ch5, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 30.33
Std Dev 3.792
Gaus mean : 30.3965
Gaus Width: 4.4030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

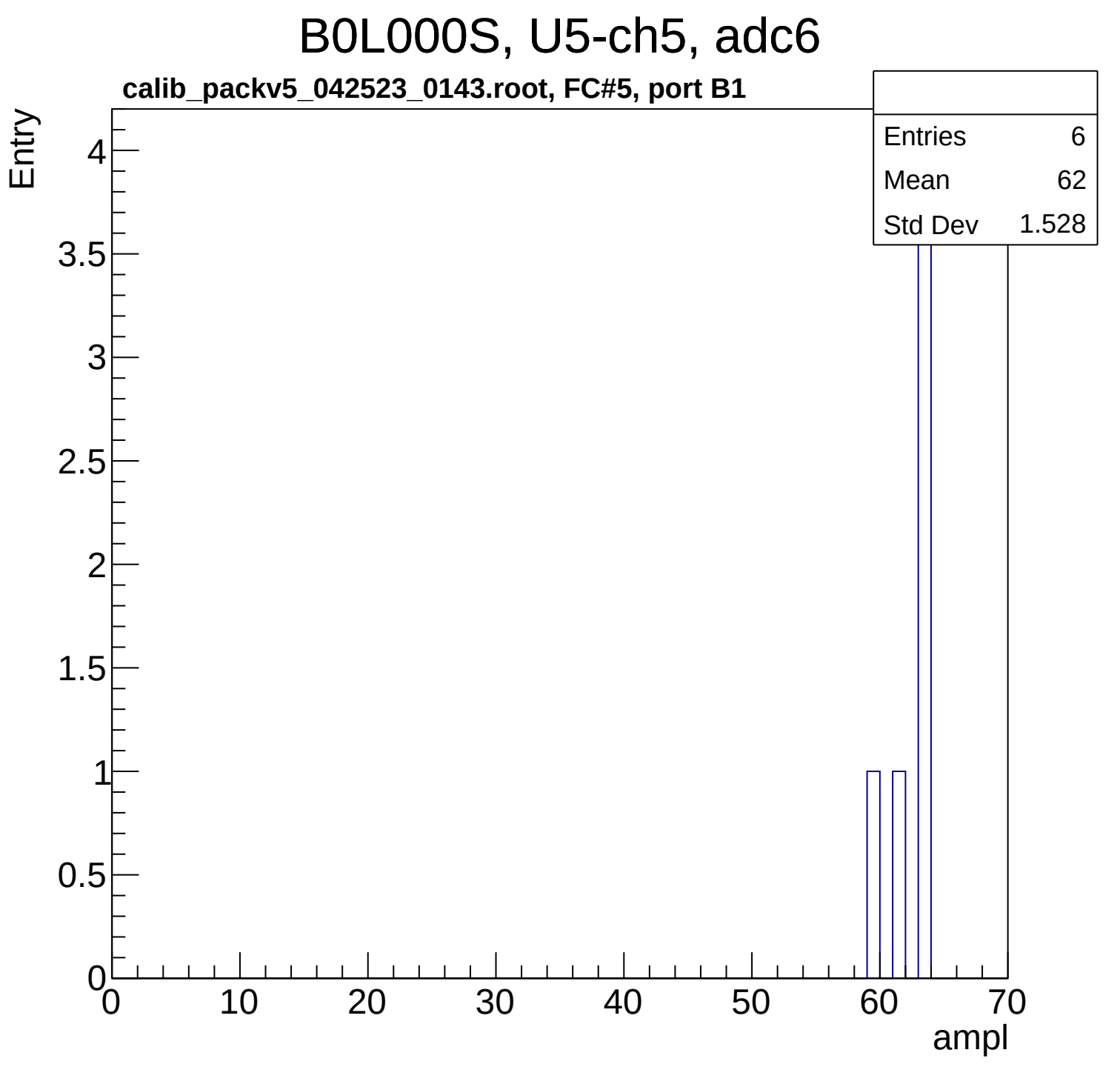

B0L000S, U5-ch5, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 36.98
Std Dev 3.34
Gaus mean : 36.9068
Gaus Width: 3.1138
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

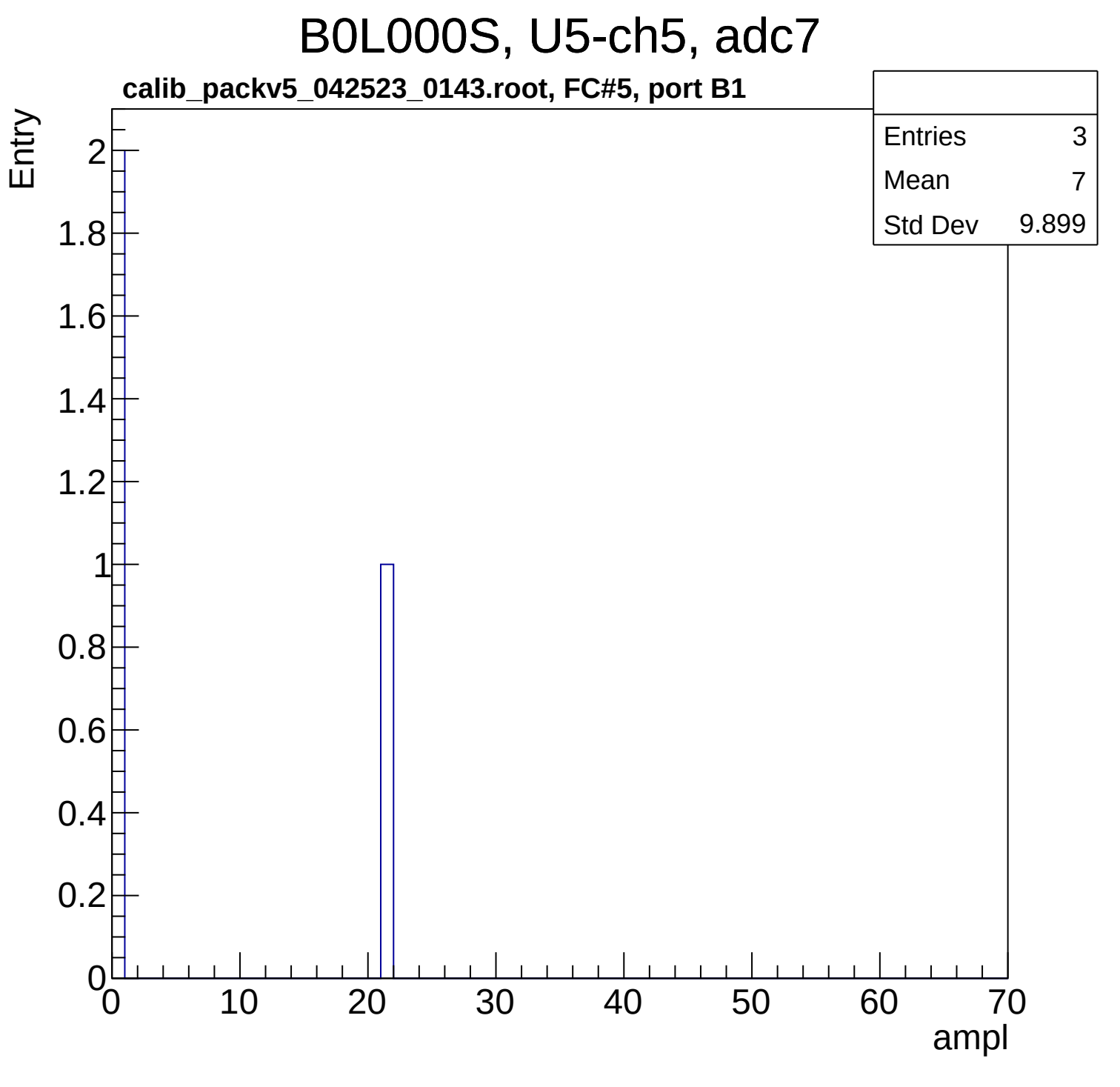

B0L000S, U5-ch5, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.45
Std Dev 3.274
Gaus mean : 43.9369
Gaus Width: 3.3912
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

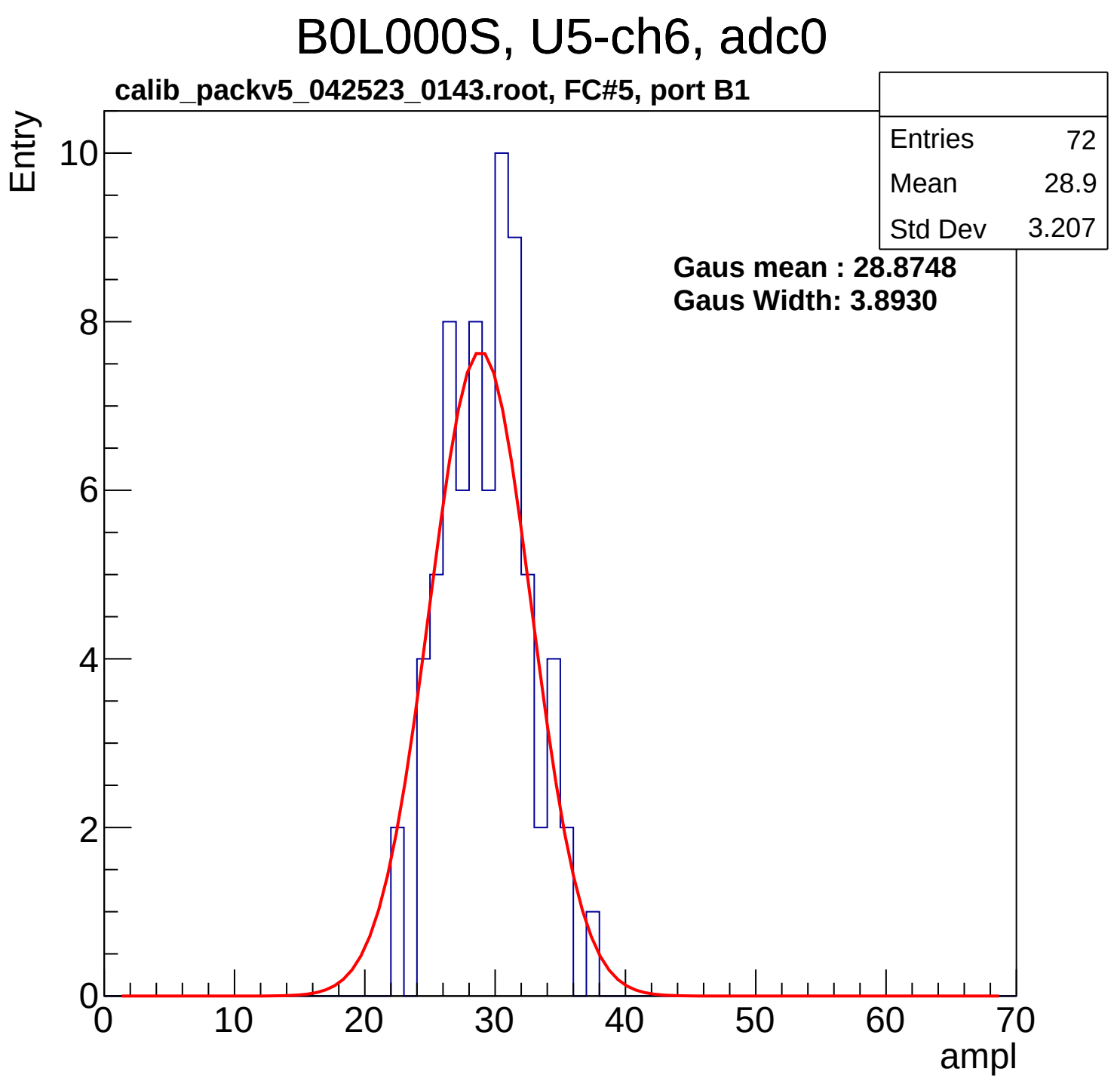

B0L000S, U5-ch5, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 48.9
Std Dev 3.811
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

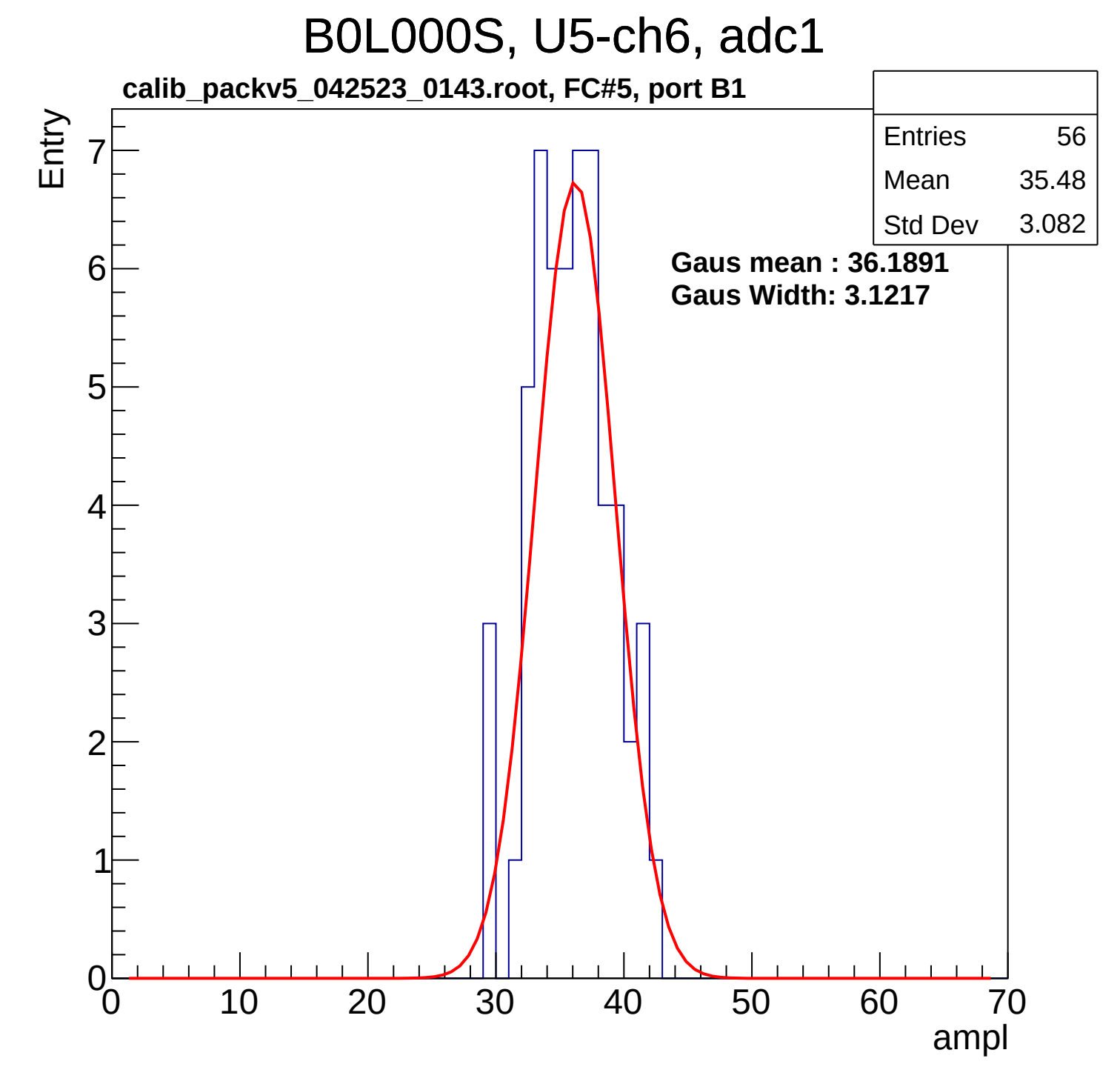

B0L000S, U5-ch5, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 55.49
Std Dev 3.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

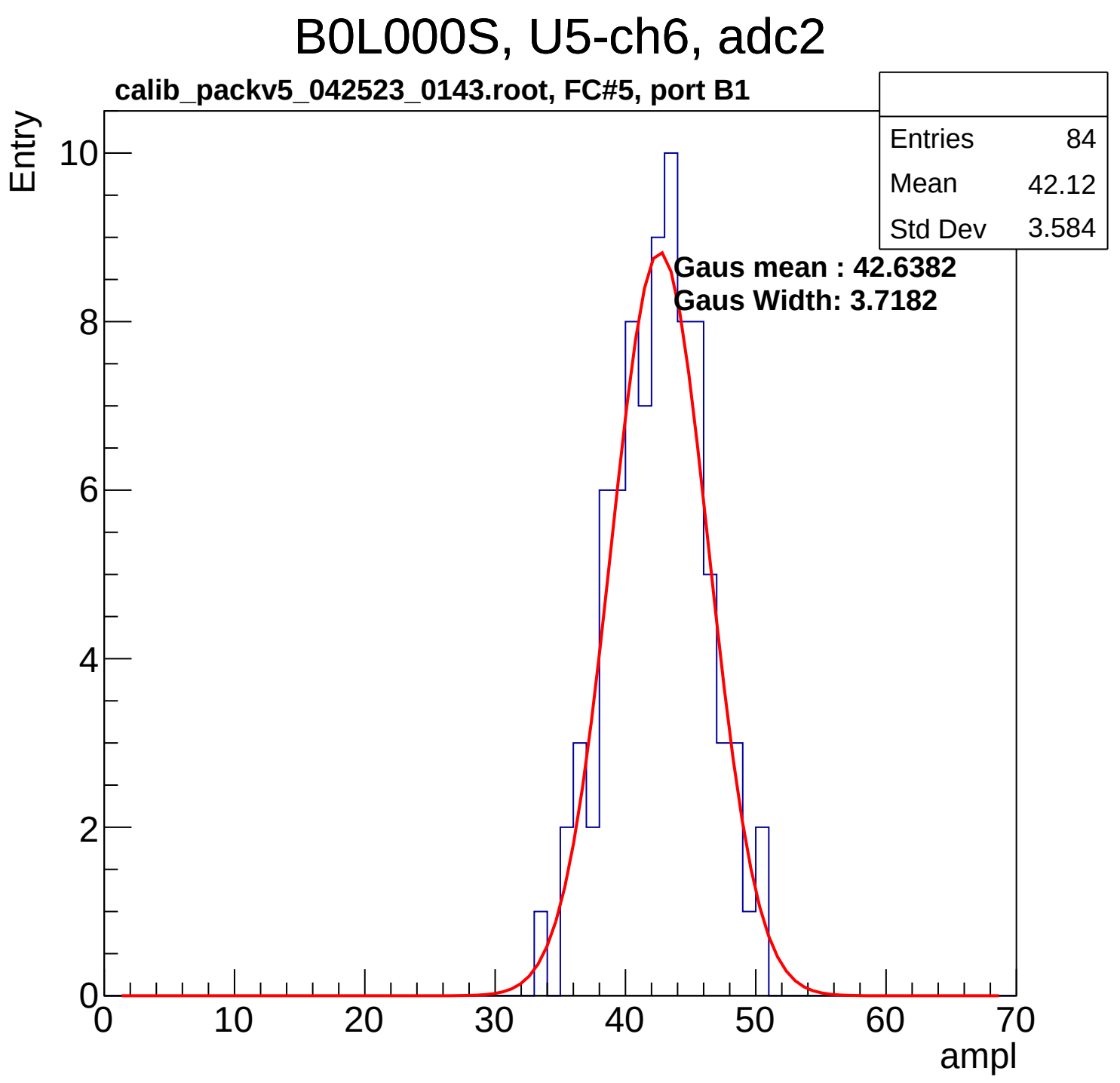

B0L000S, U5-ch5, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.5
Std Dev 10.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

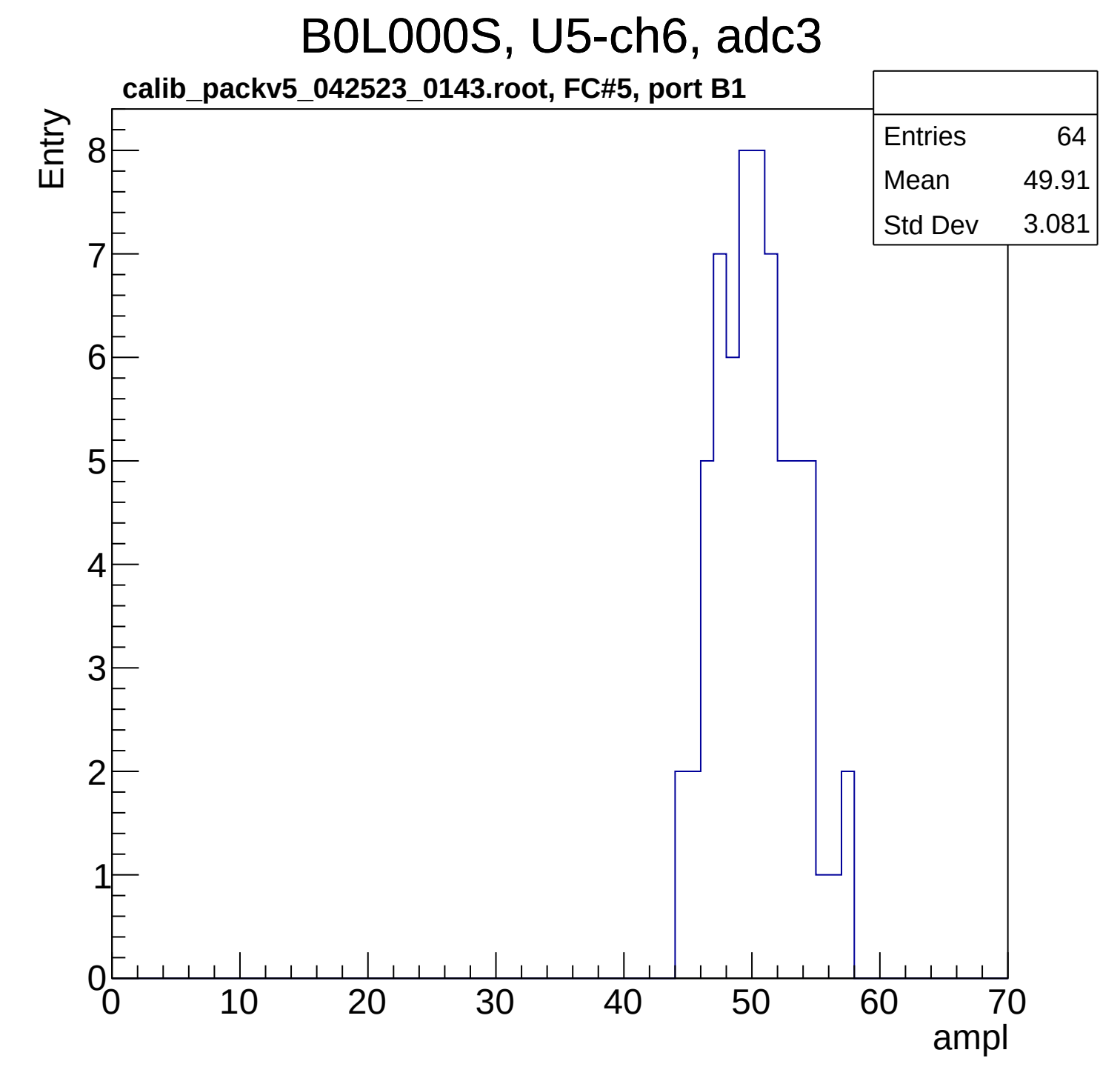

B0L000S, U5-ch5, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 1.528
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

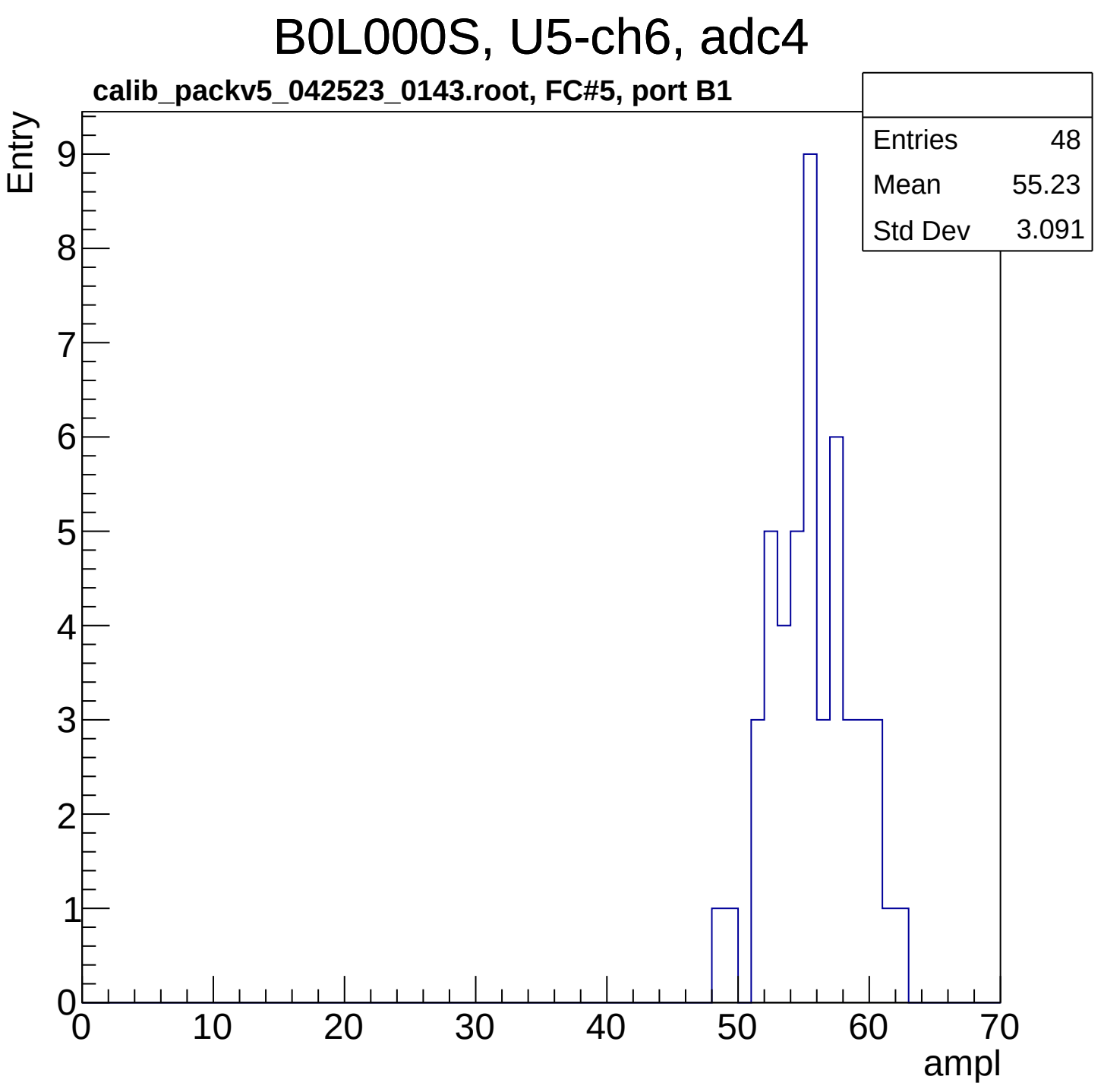

B0L000S, U5-ch5, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 28.9
Std Dev 3.207
Gaus mean : 28.8748
Gaus Width: 3.8930
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 35.48
Std Dev 3.082
Gaus mean : 36.1891
Gaus Width: 3.1217
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 42.12
Std Dev 3.584
Gaus mean : 42.6382
Gaus Width: 3.7182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.91
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.23
Std Dev 3.091
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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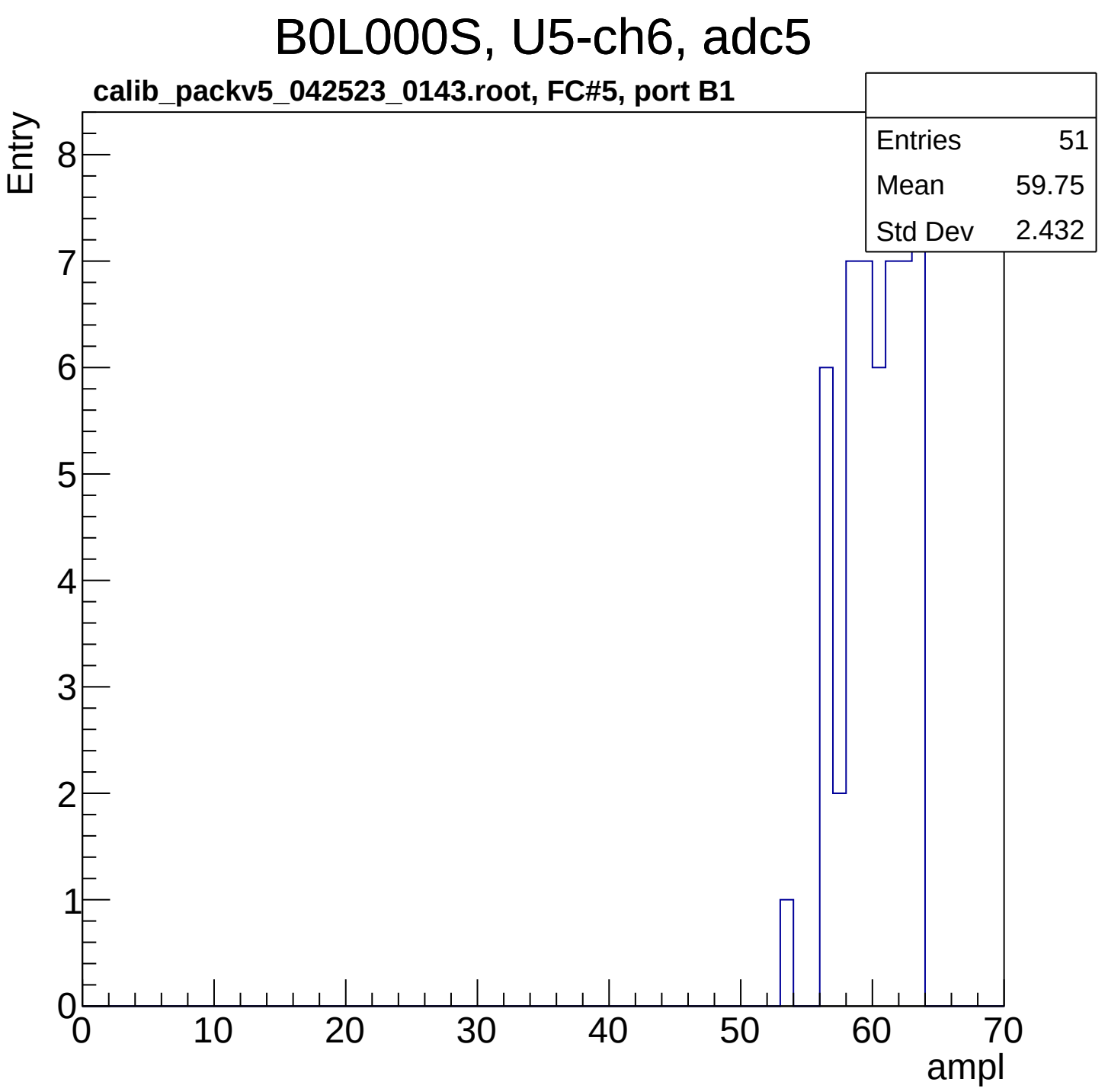
60
70


B0L000S, U5-ch6, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 59.75
Std Dev 2.432
Entry
ampl
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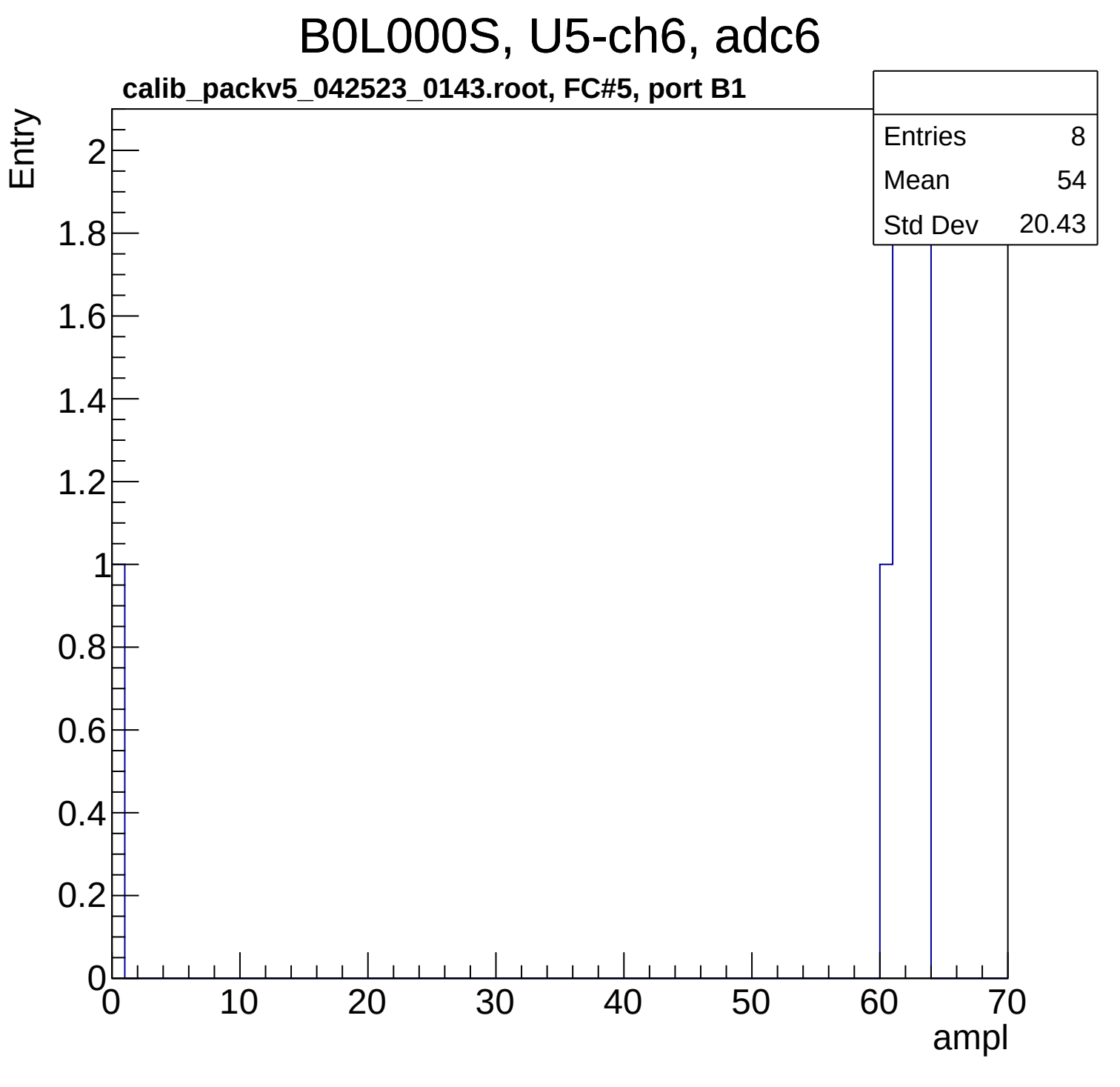
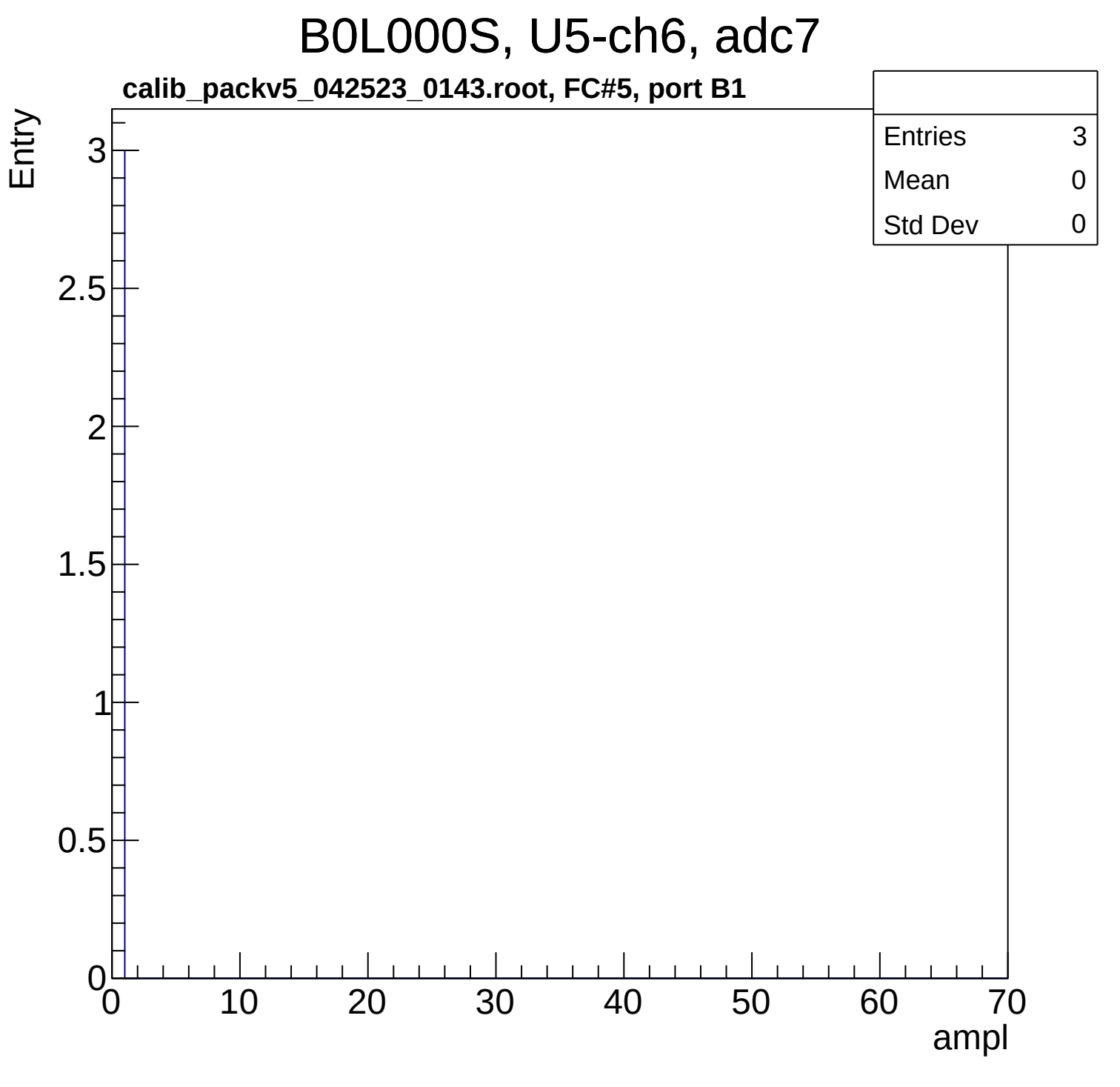
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54
Std Dev 20.43
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch6, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 0
Std Dev 0
Entry
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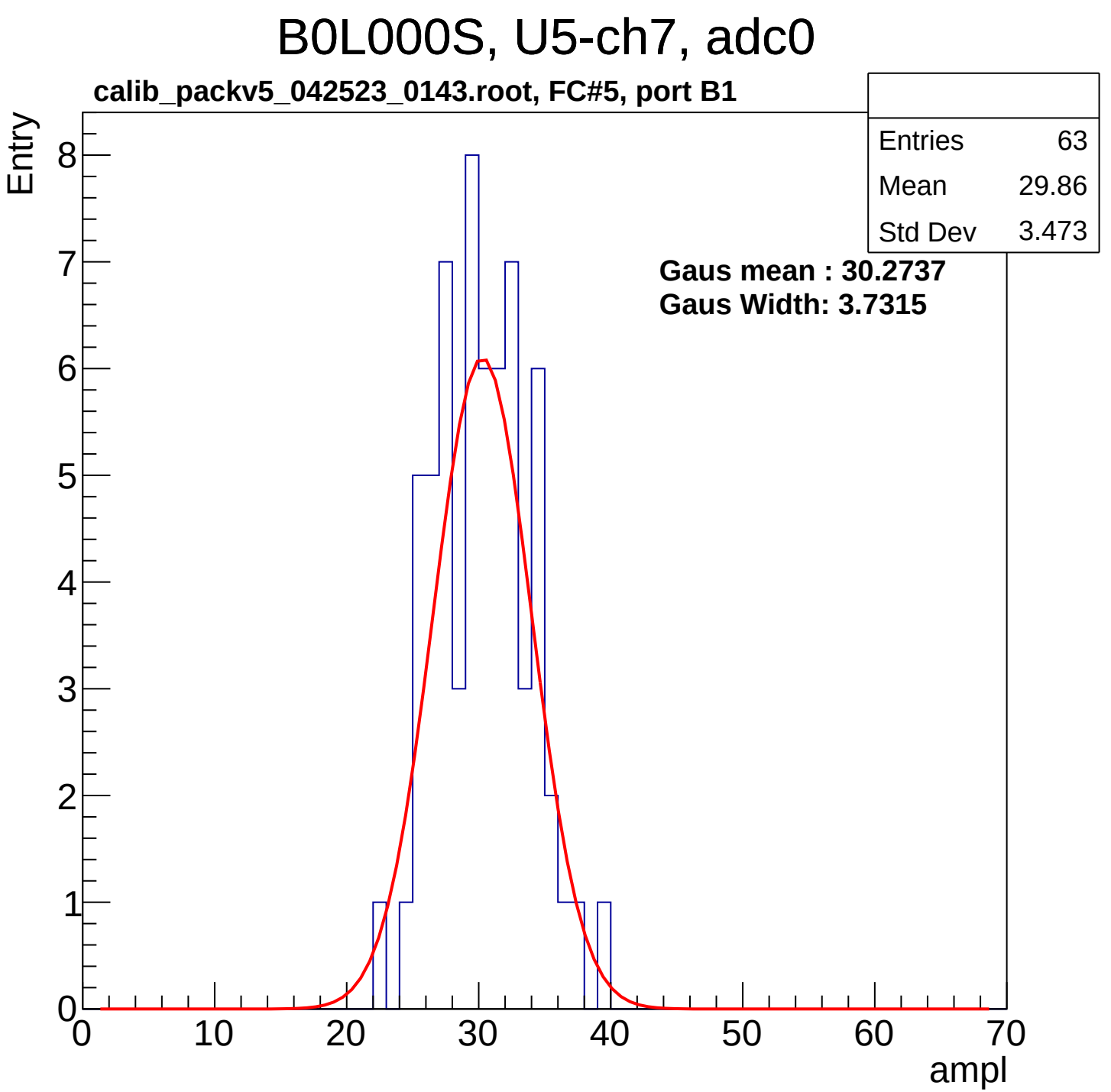
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch7, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.86
Std Dev 3.473
Gaus mean : 30.2737
Gaus Width: 3.7315
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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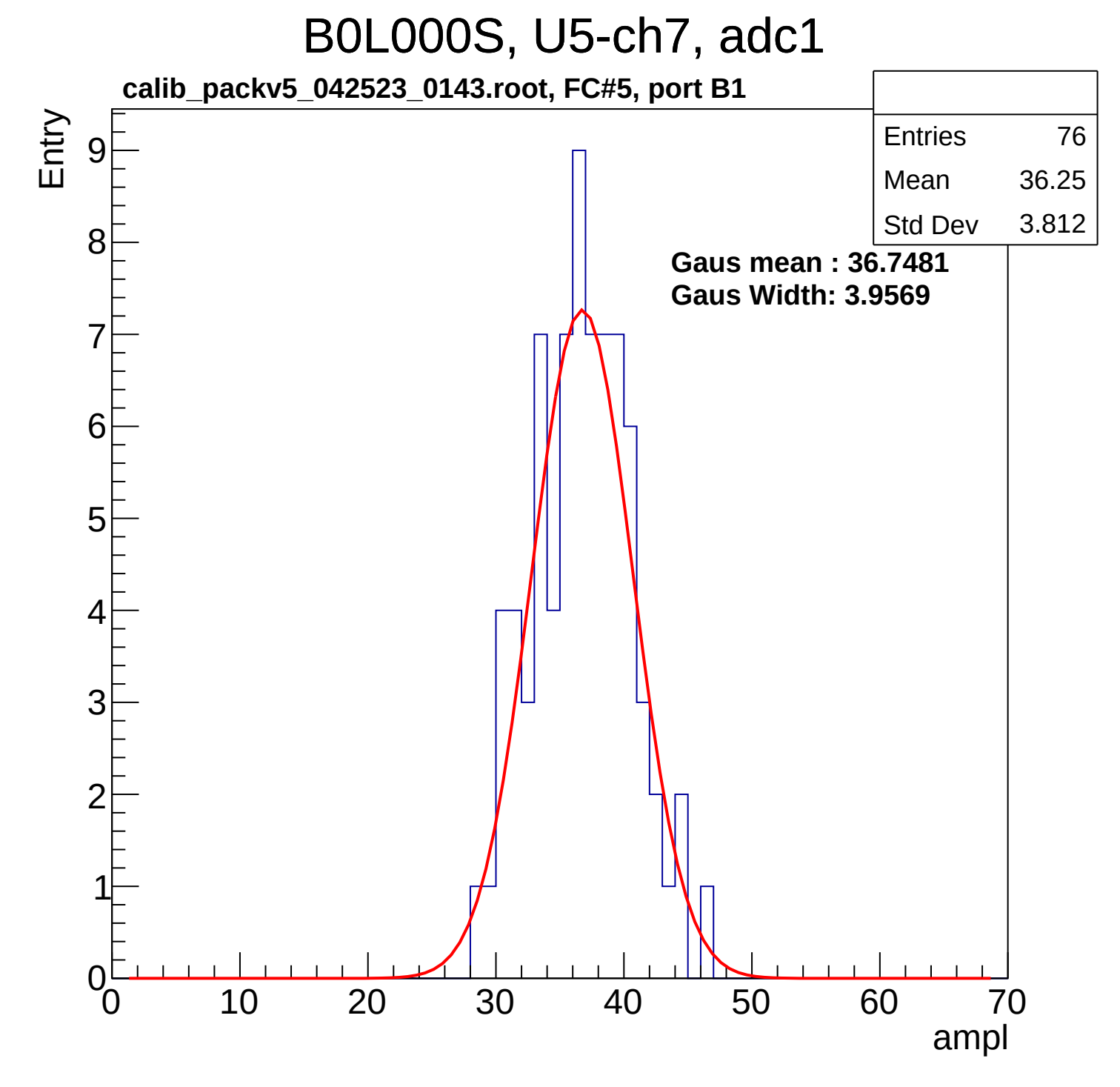
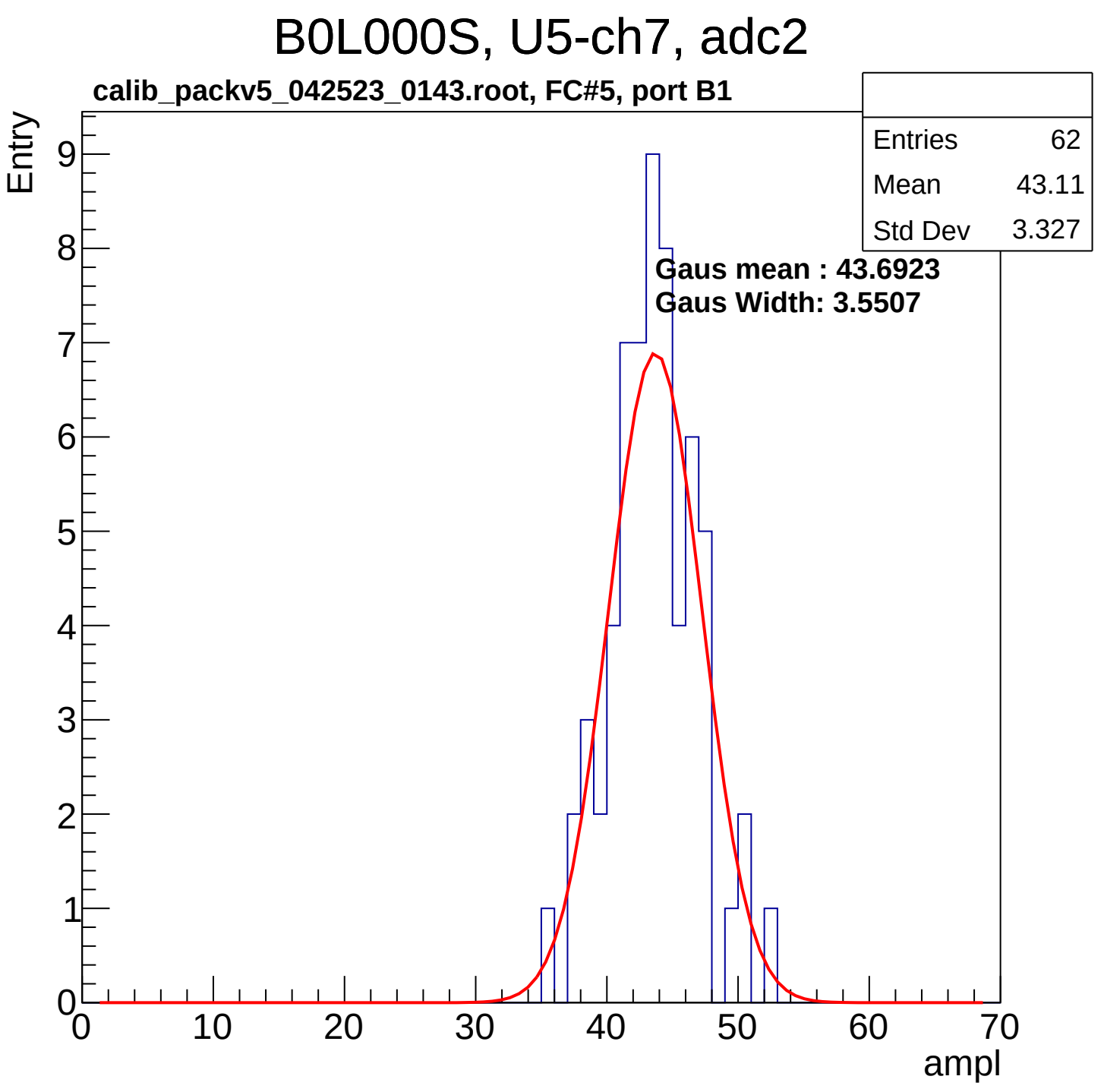
40
50
60
70


B0L000S, U5-ch7, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.25
Std Dev 3.812
Gaus mean : 36.7481
Gaus Width: 3.9569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch7, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.11
Std Dev 3.327
Gaus mean : 43.6923
Gaus Width: 3.5507
Entry
ampl
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2
3
4
5
6
7
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9
0
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60
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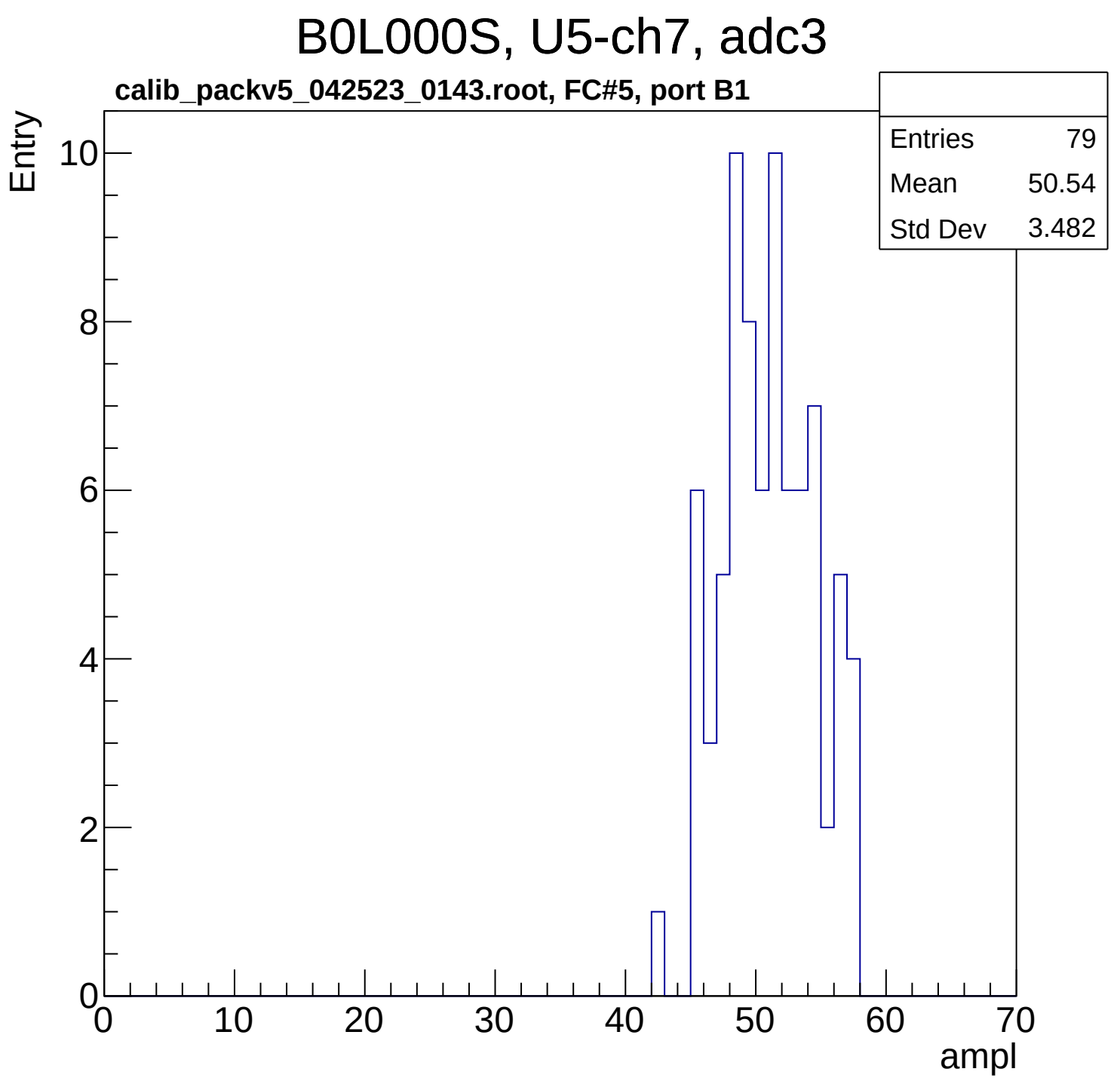
B0L000S, U5-ch7, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 50.54
Std Dev 3.482
Entry
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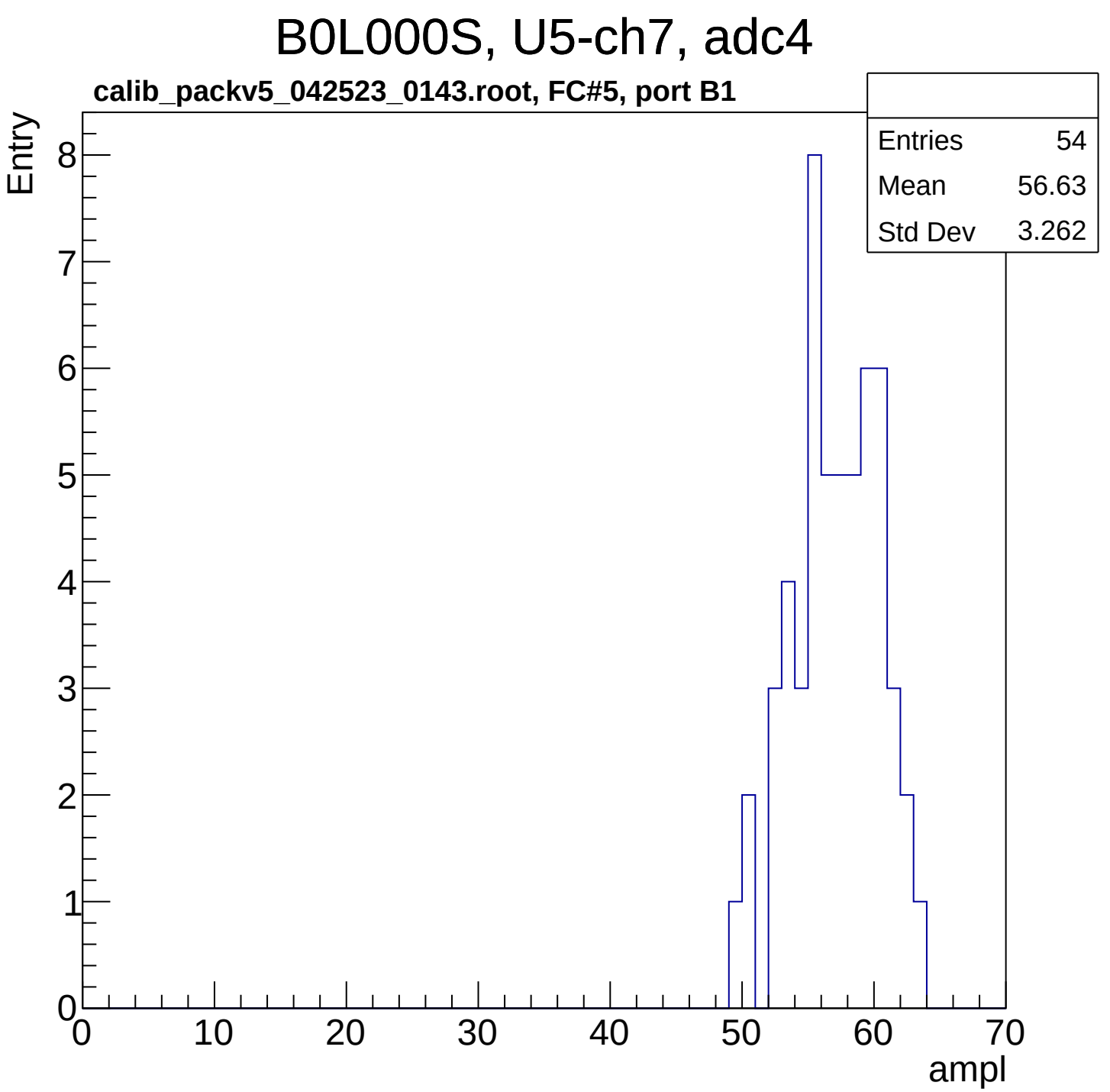
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch7, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.63
Std Dev 3.262
Entry
ampl
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1
2
3
4
5
6
7
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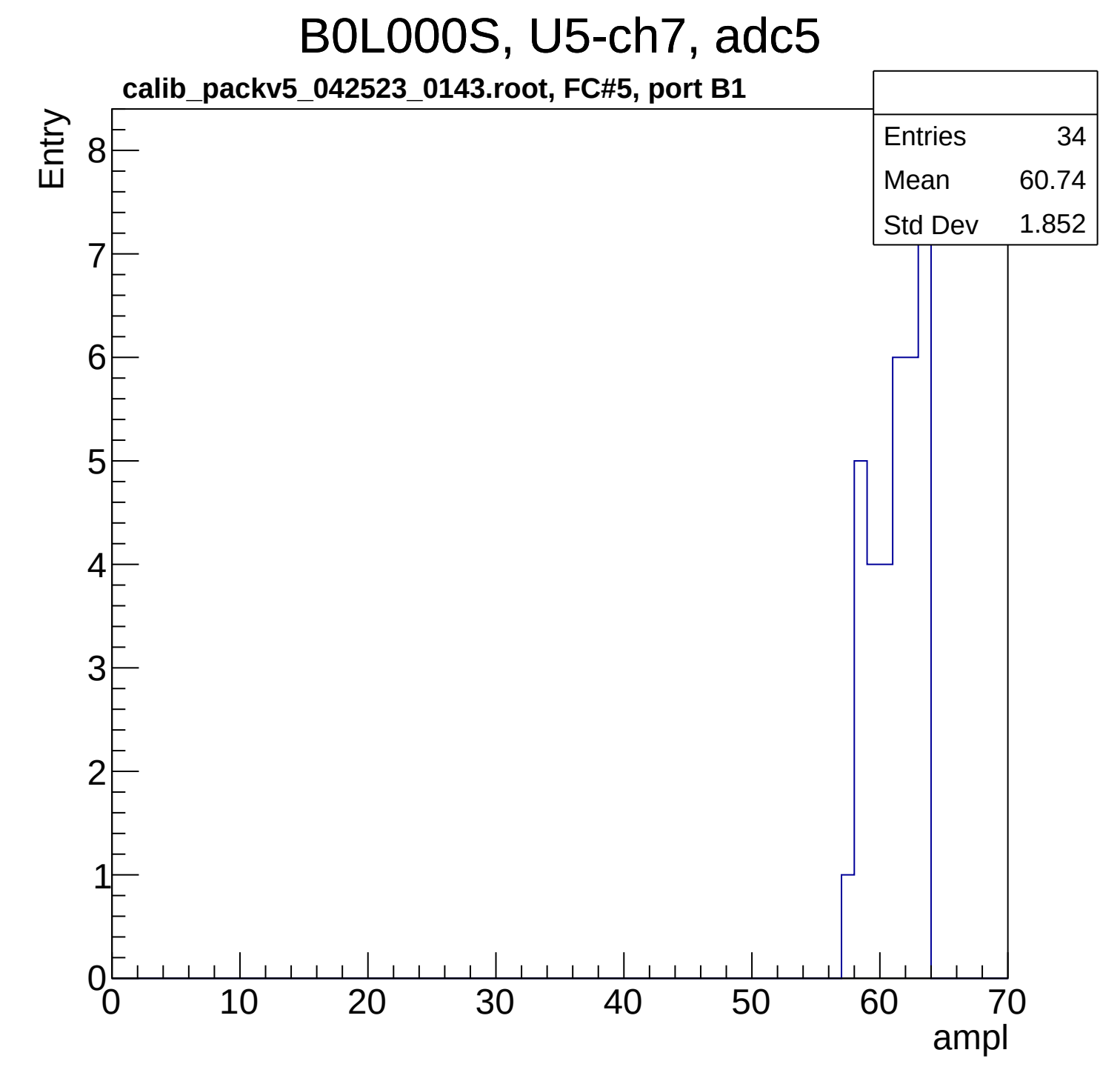
0
10
20
30
40
50
60
70


B0L000S, U5-ch7, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 60.74
Std Dev 1.852
Entry
ampl
0
1
2
3
4
5
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7
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0
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40
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60
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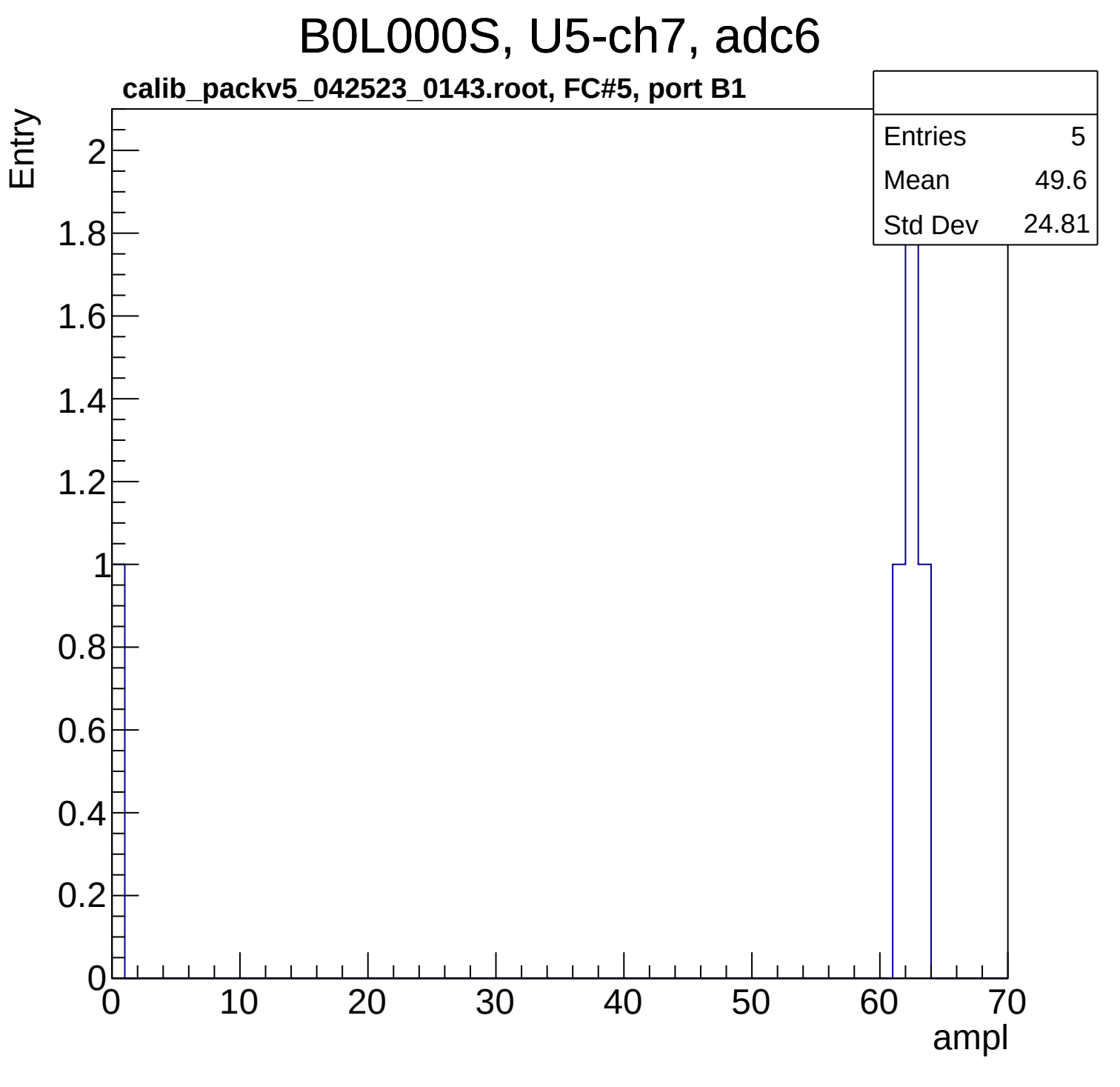
B0L000S, U5-ch7, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 49.6
Std Dev 24.81
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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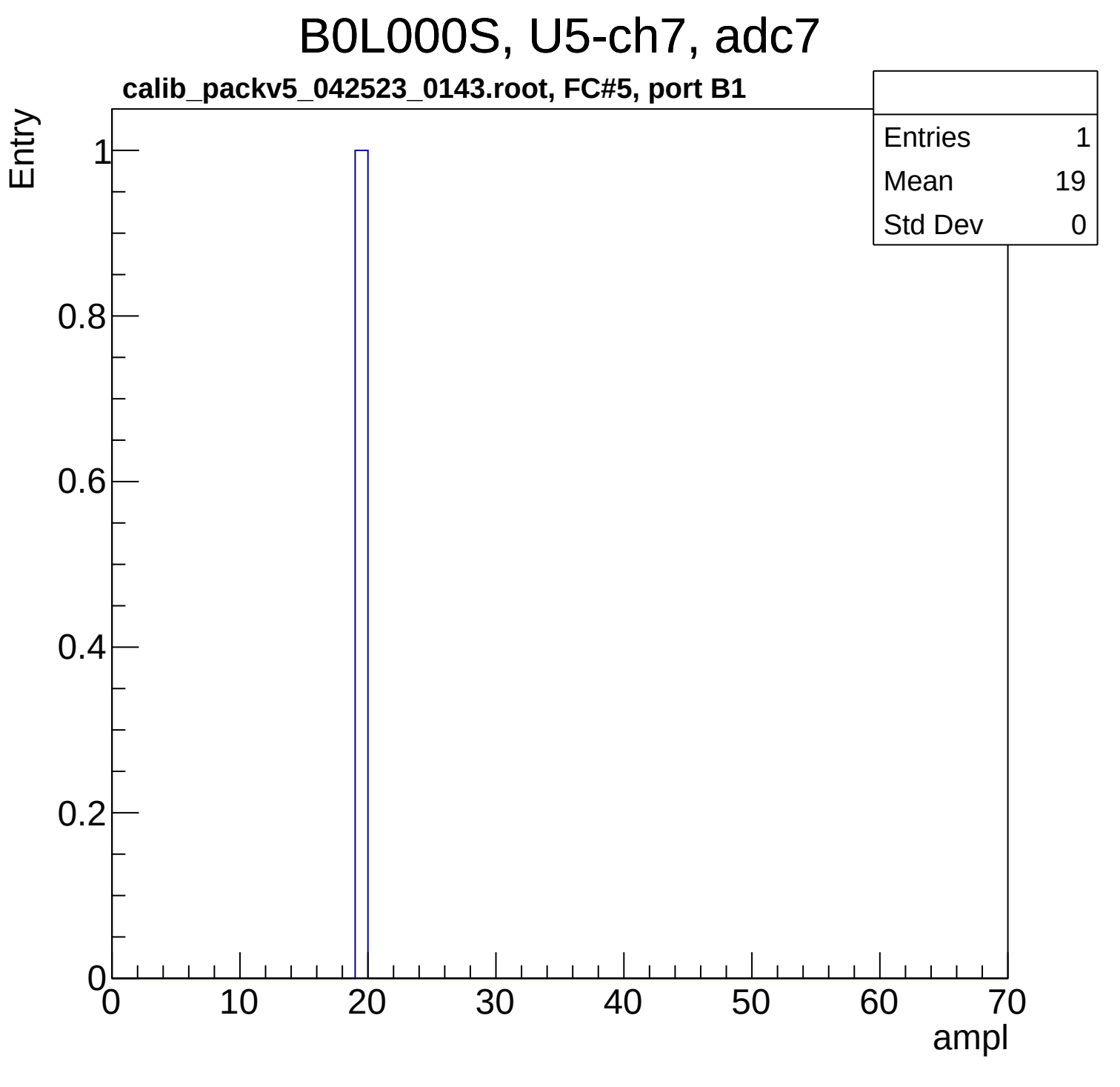
B0L000S, U5-ch7, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
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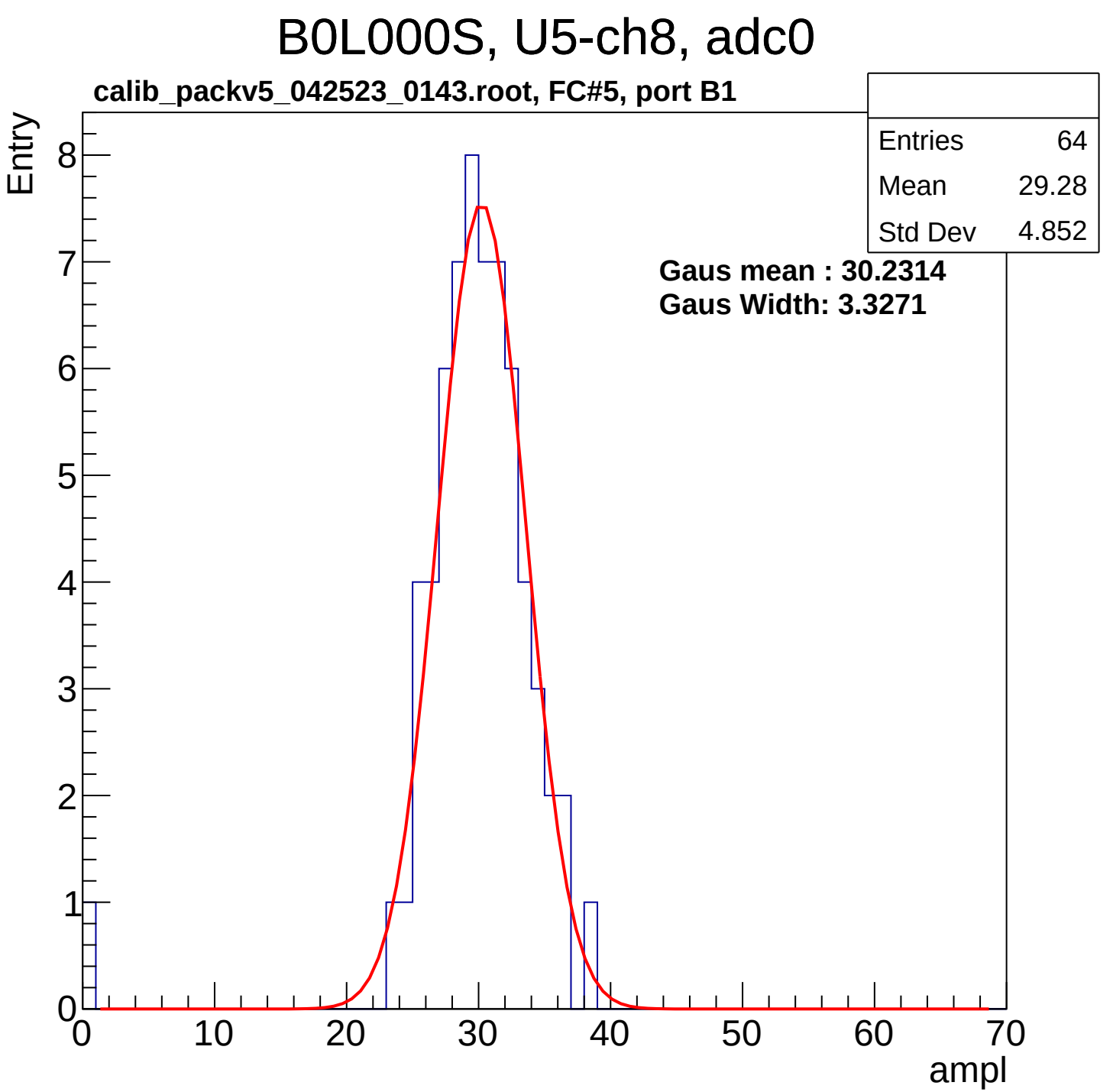
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch8, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.28
Std Dev 4.852
Gaus mean : 30.2314
Gaus Width: 3.3271
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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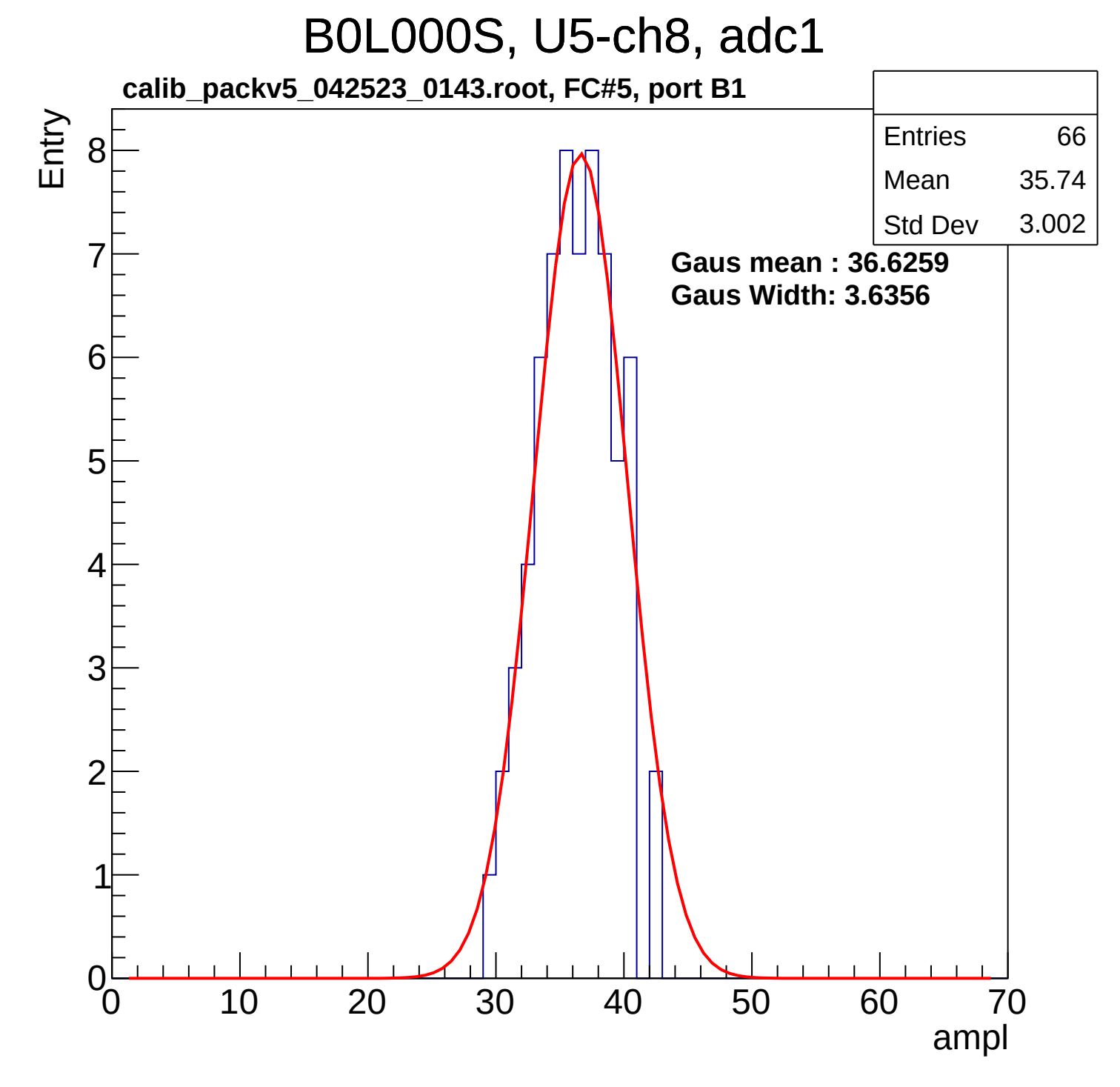
B0L000S, U5-ch8, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.74
Std Dev 3.002
Gaus mean : 36.6259
Gaus Width: 3.6356
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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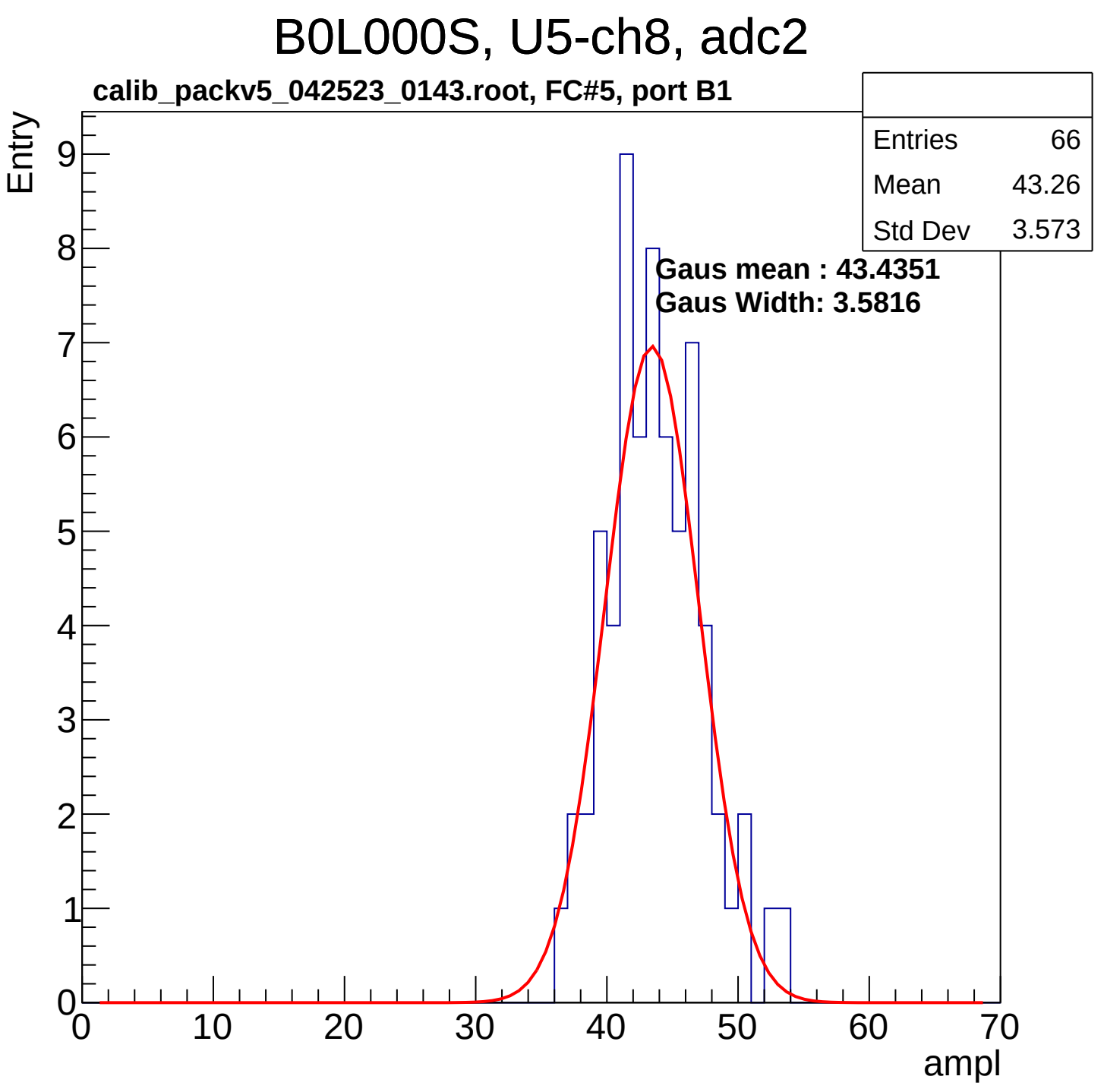
B0L000S, U5-ch8, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.26
Std Dev 3.573
Gaus mean : 43.4351
Gaus Width: 3.5816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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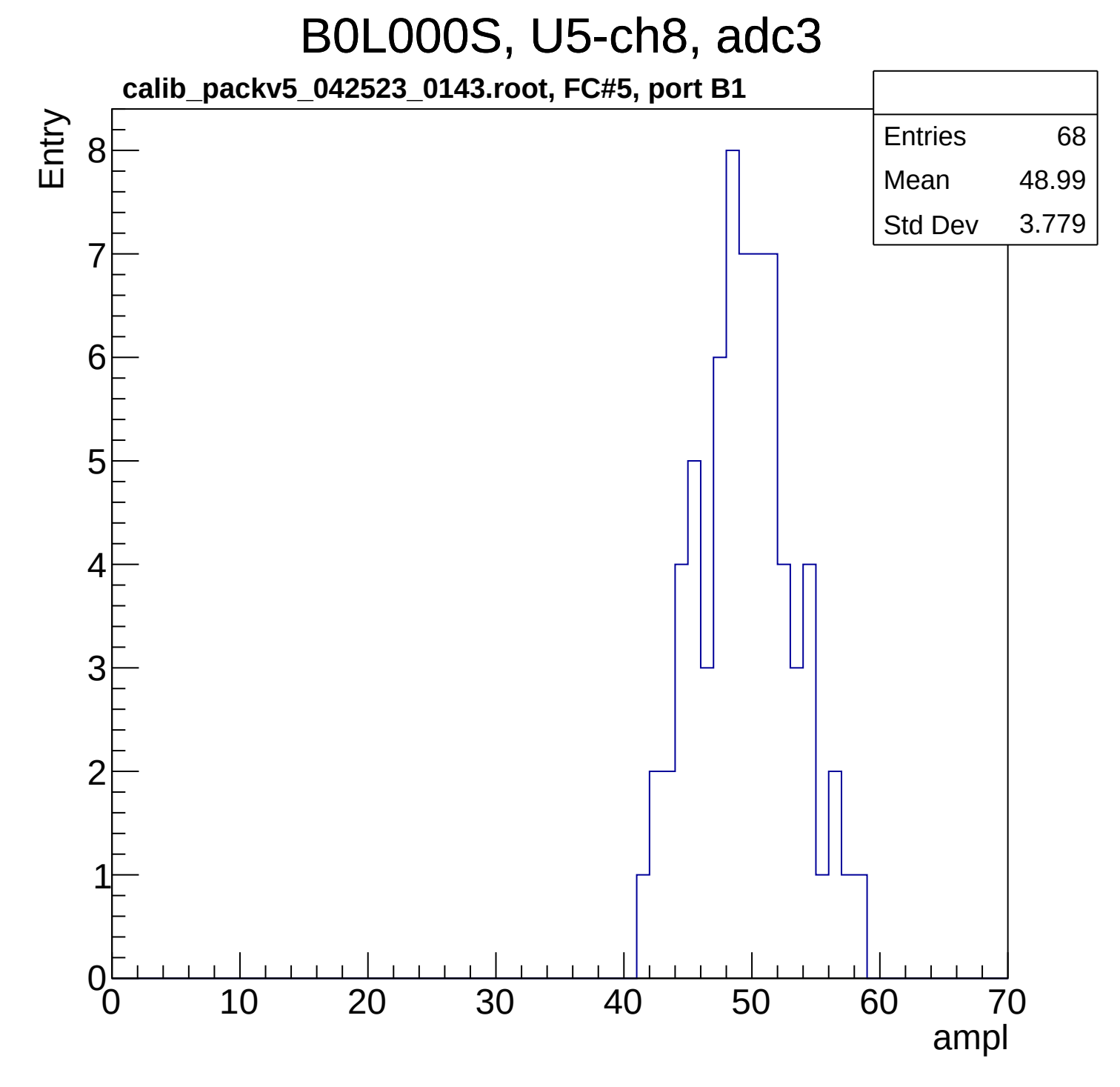
B0L000S, U5-ch8, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.99
Std Dev 3.779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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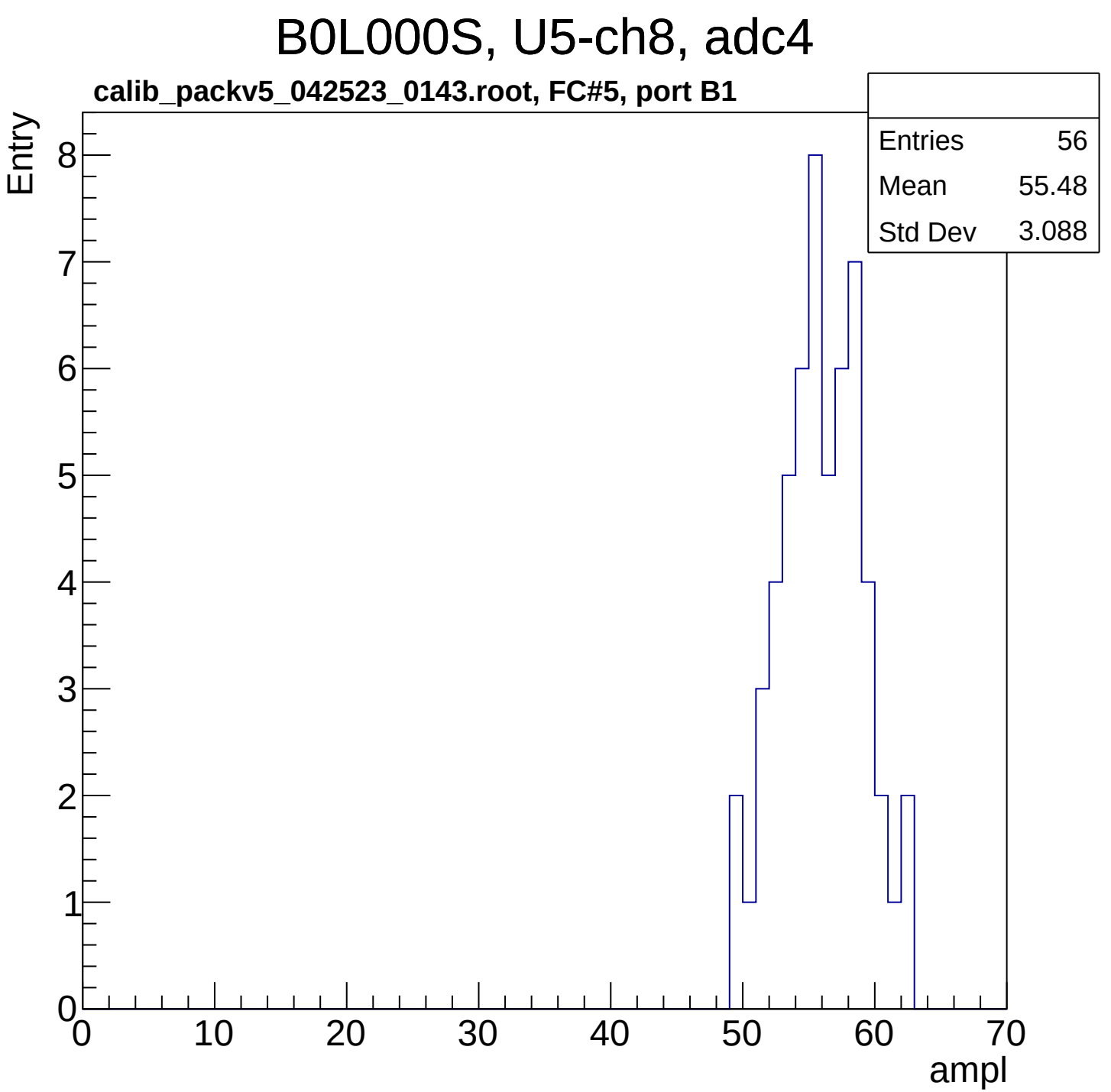
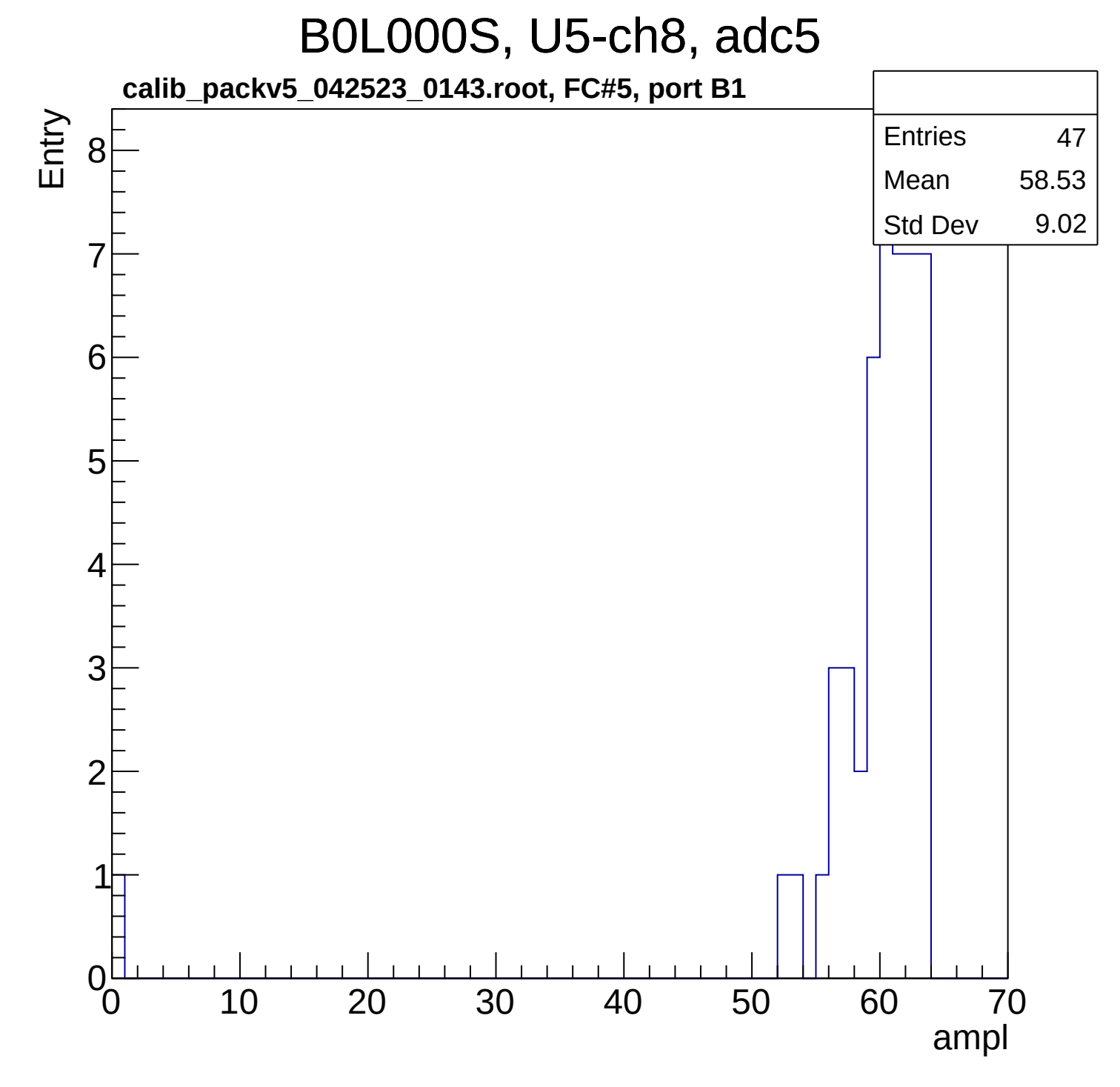

B0L000S, U5-ch8, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.48
Std Dev 3.088
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch8, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.53
Std Dev 9.02
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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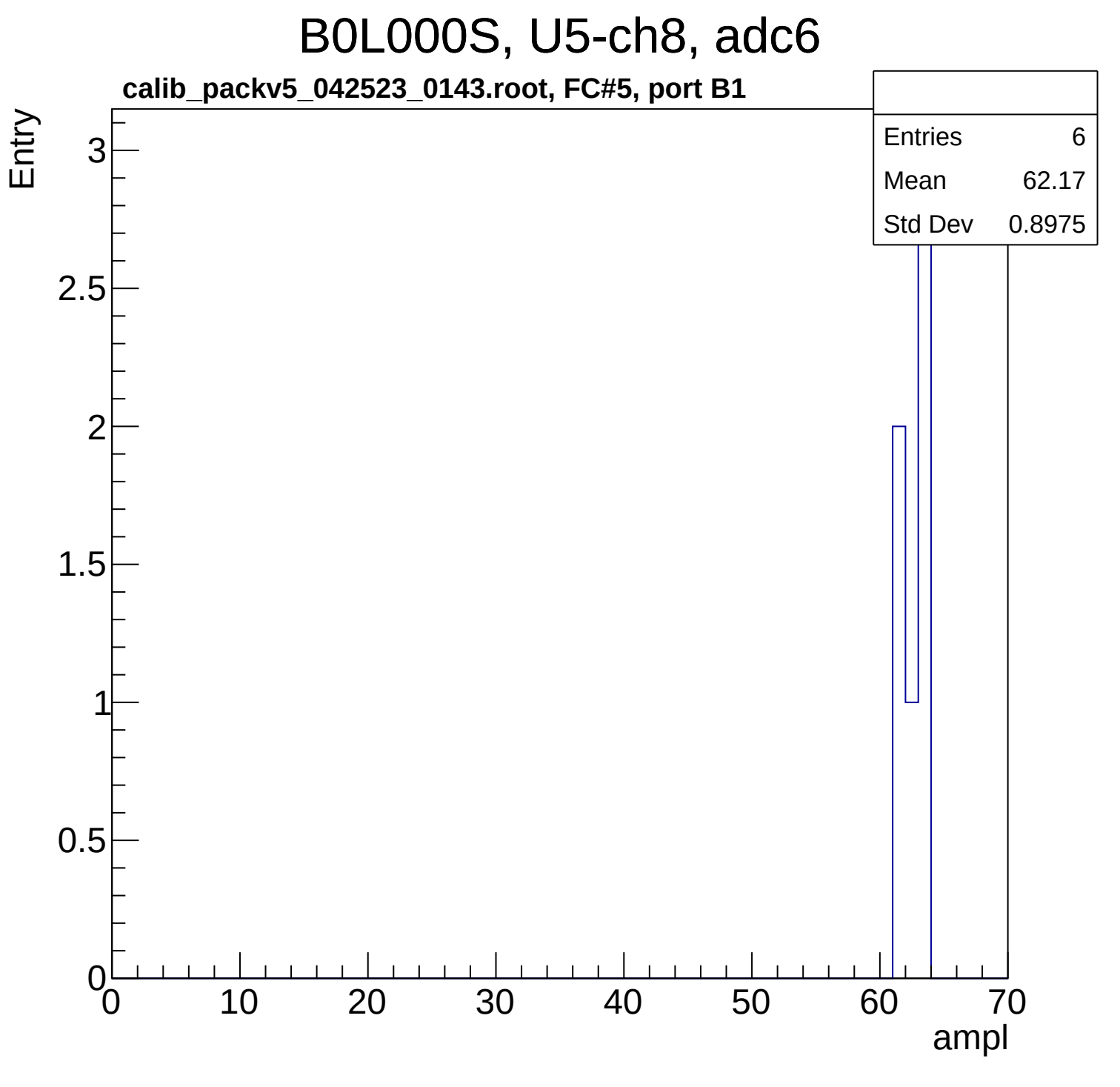
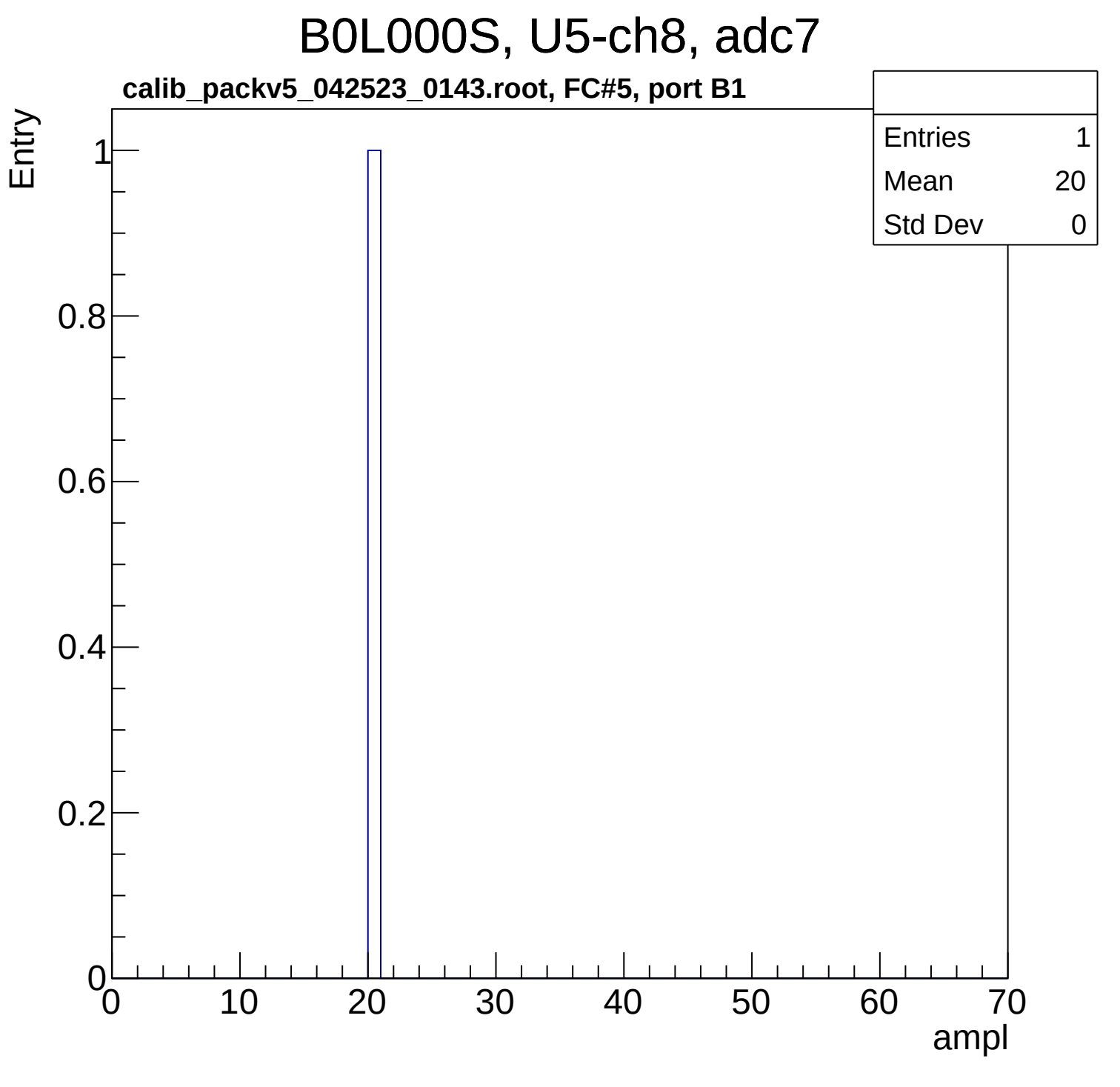
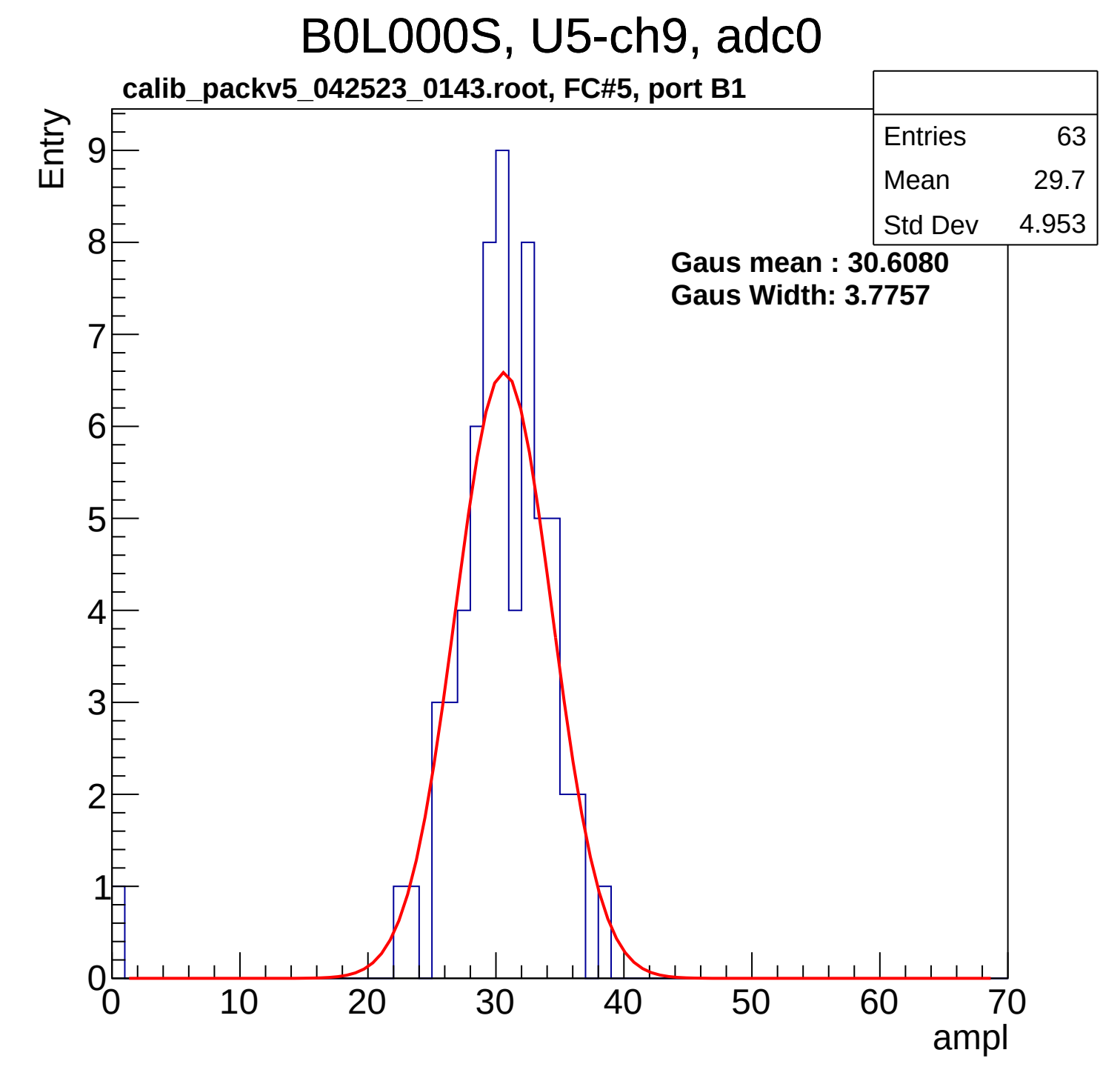

B0L000S, U5-ch8, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch8, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch9, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 29.7
Std Dev 4.953
Gaus mean : 30.6080
Gaus Width: 3.7757
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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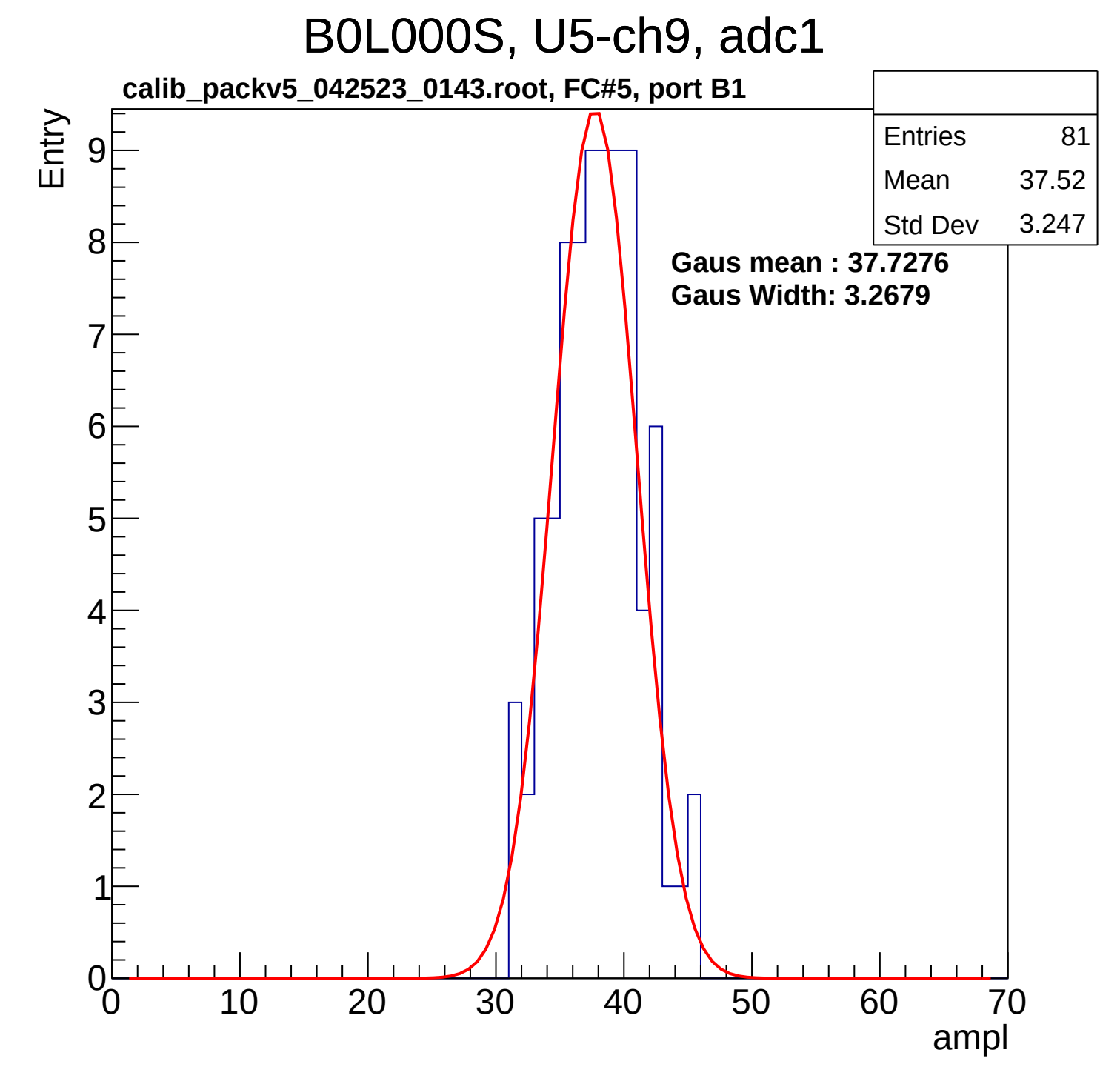
B0L000S, U5-ch9, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.52
Std Dev 3.247
Gaus mean : 37.7276
Gaus Width: 3.2679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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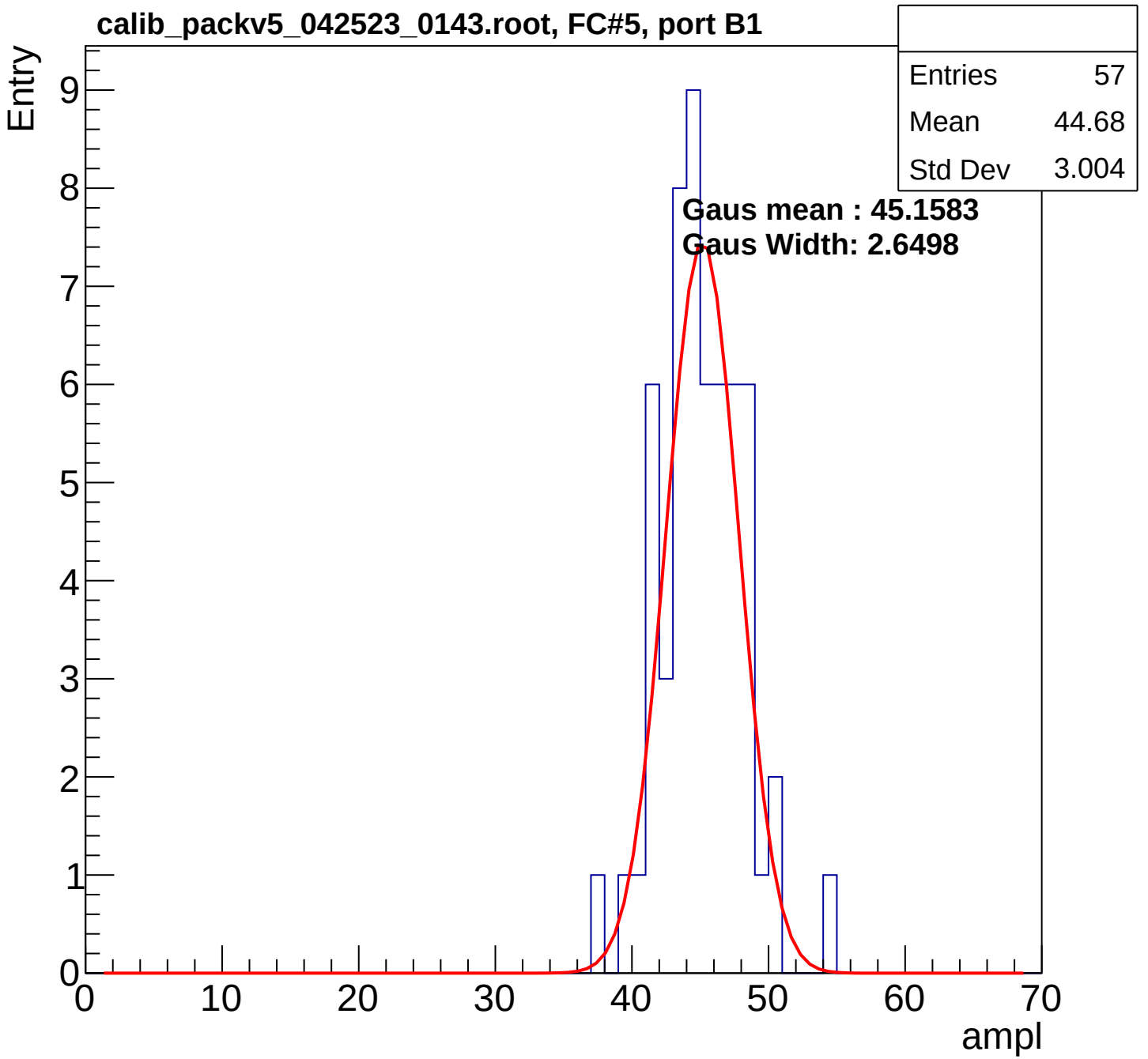
B0L000S, U5-ch9, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 44.68
Std Dev 3.004
Gaus mean : 45.1583
Gaus Width: 2.6498
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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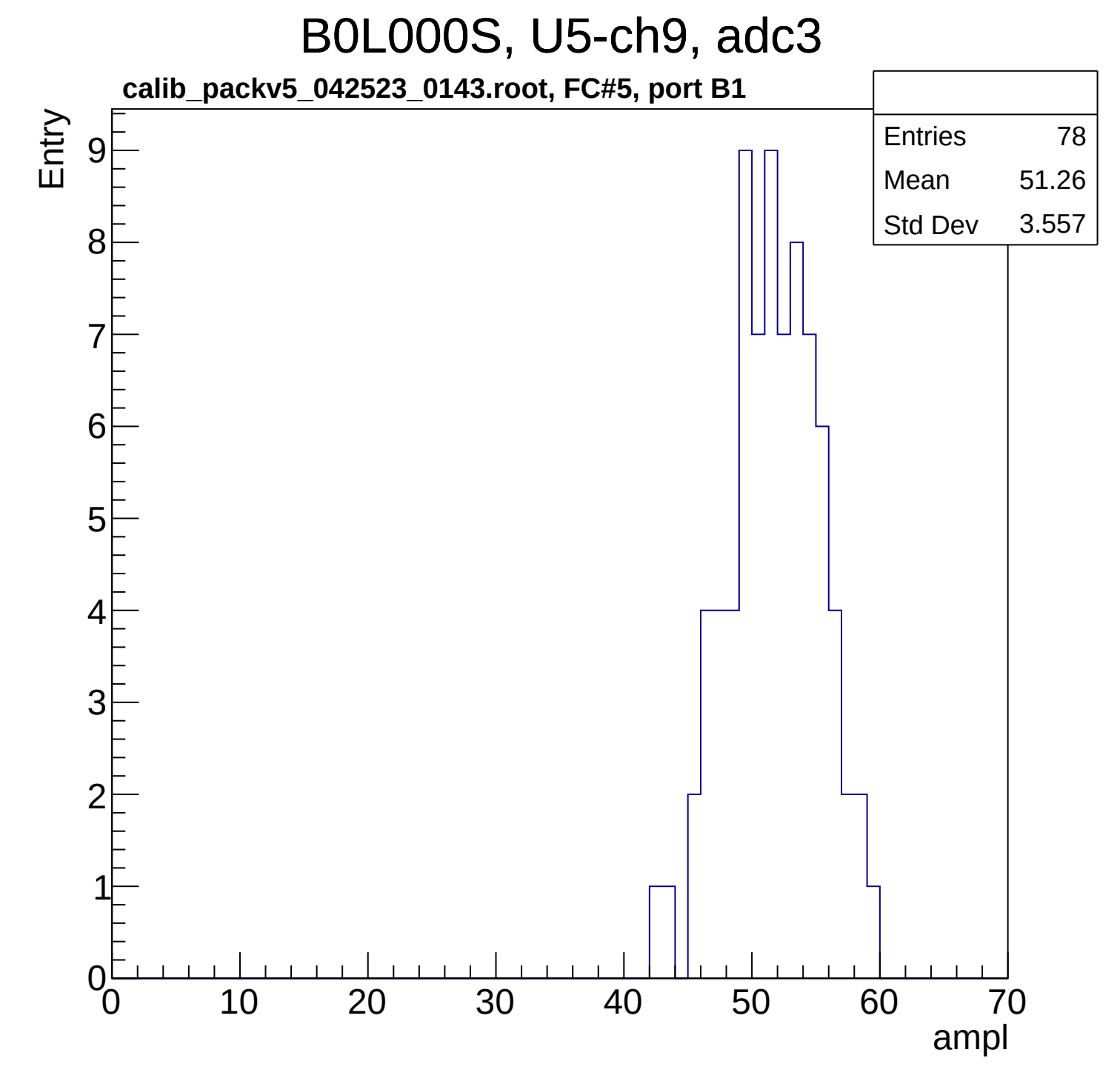
B0L000S, U5-ch9, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 51.26
Std Dev 3.557
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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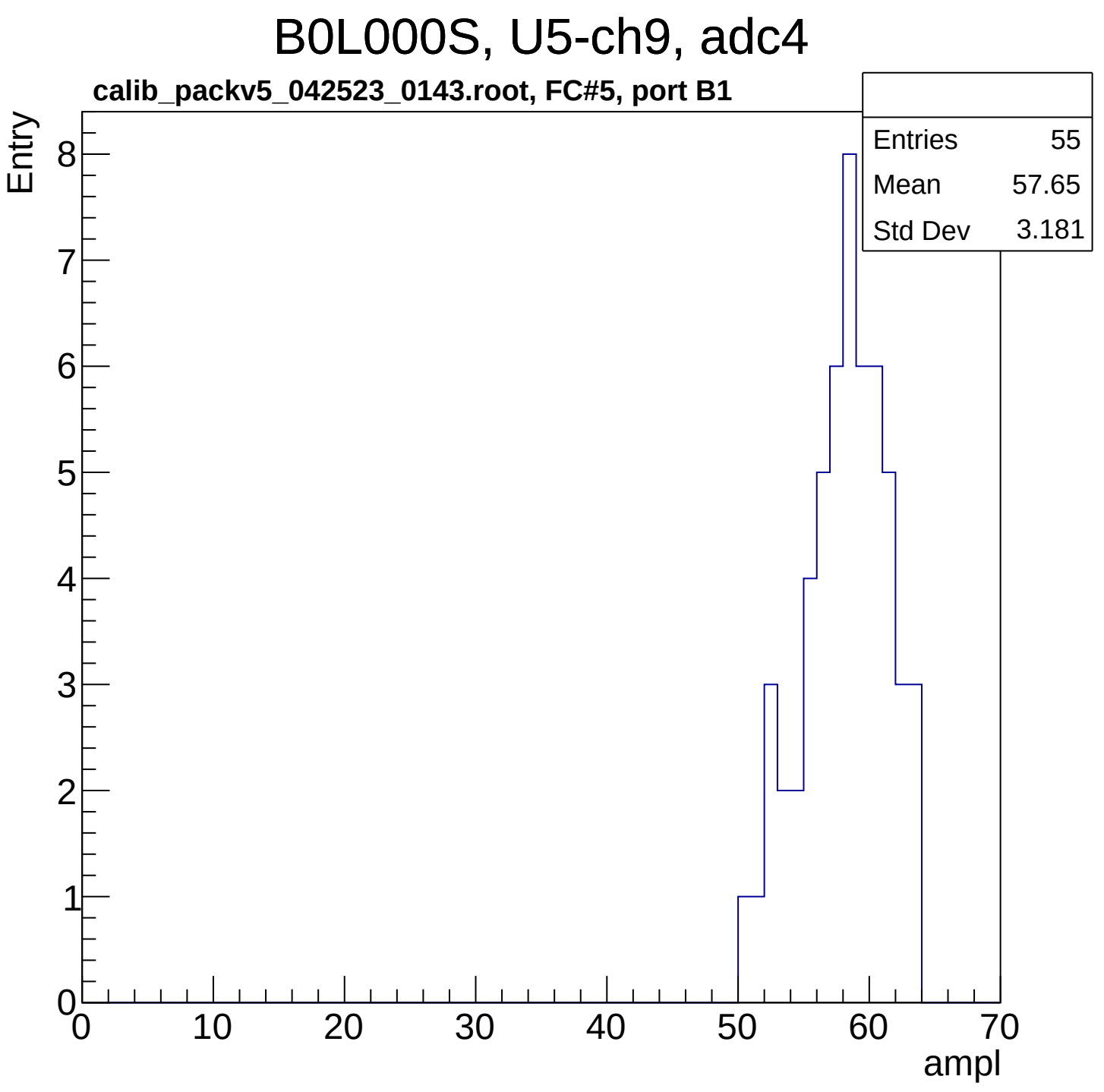
B0L000S, U5-ch9, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 57.65
Std Dev 3.181
Entry
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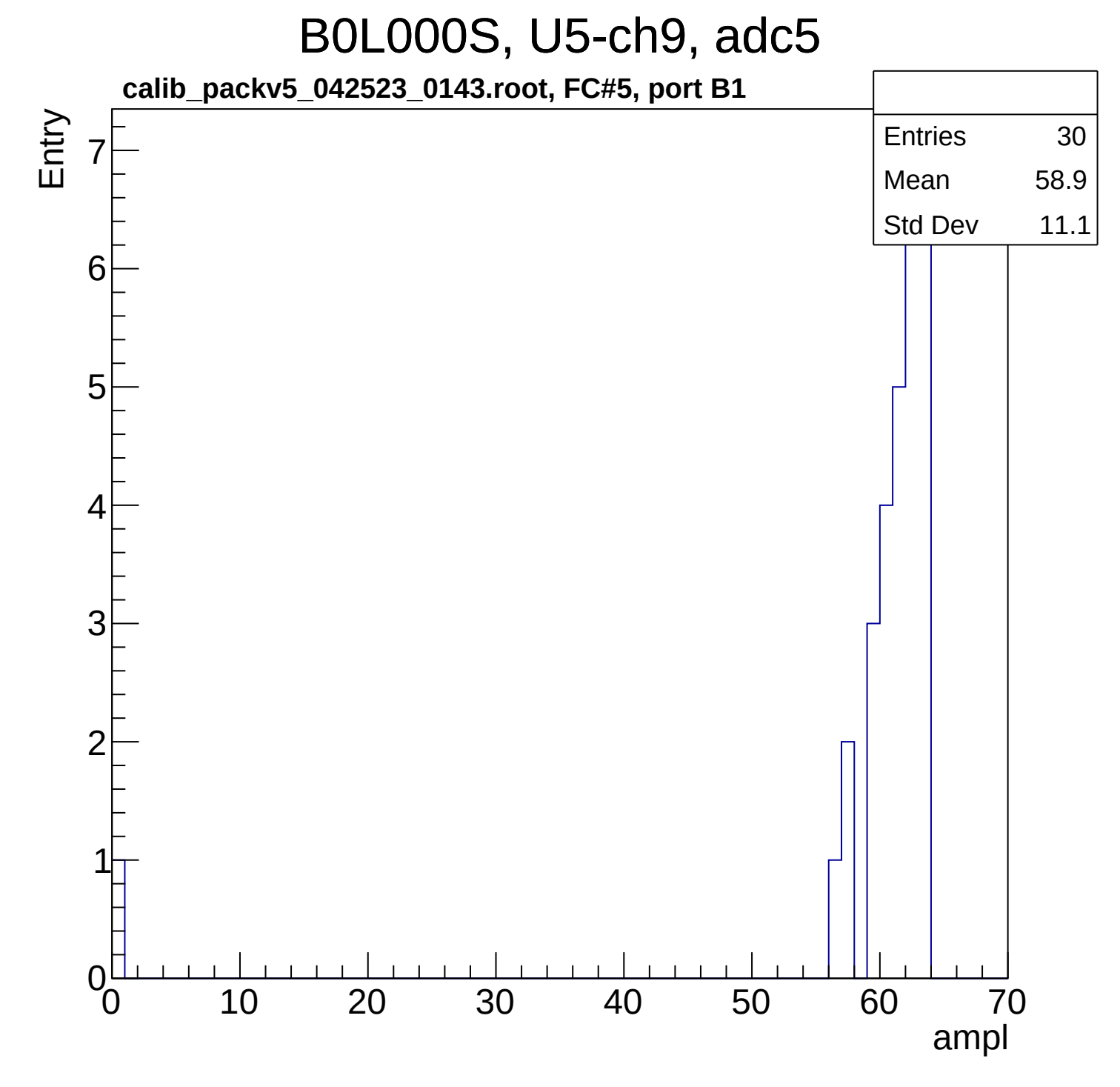
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch9, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.9
Std Dev 11.1
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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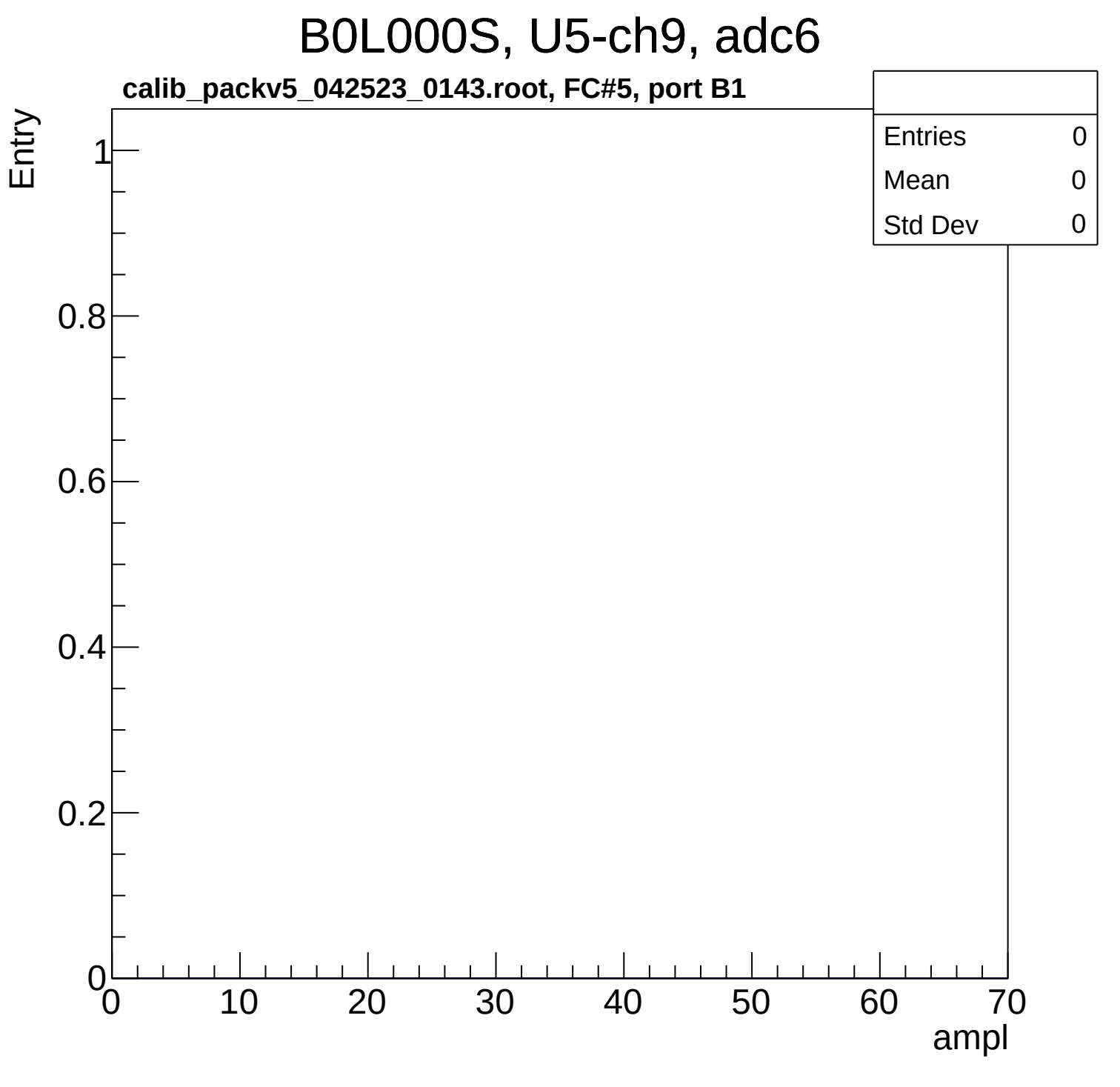
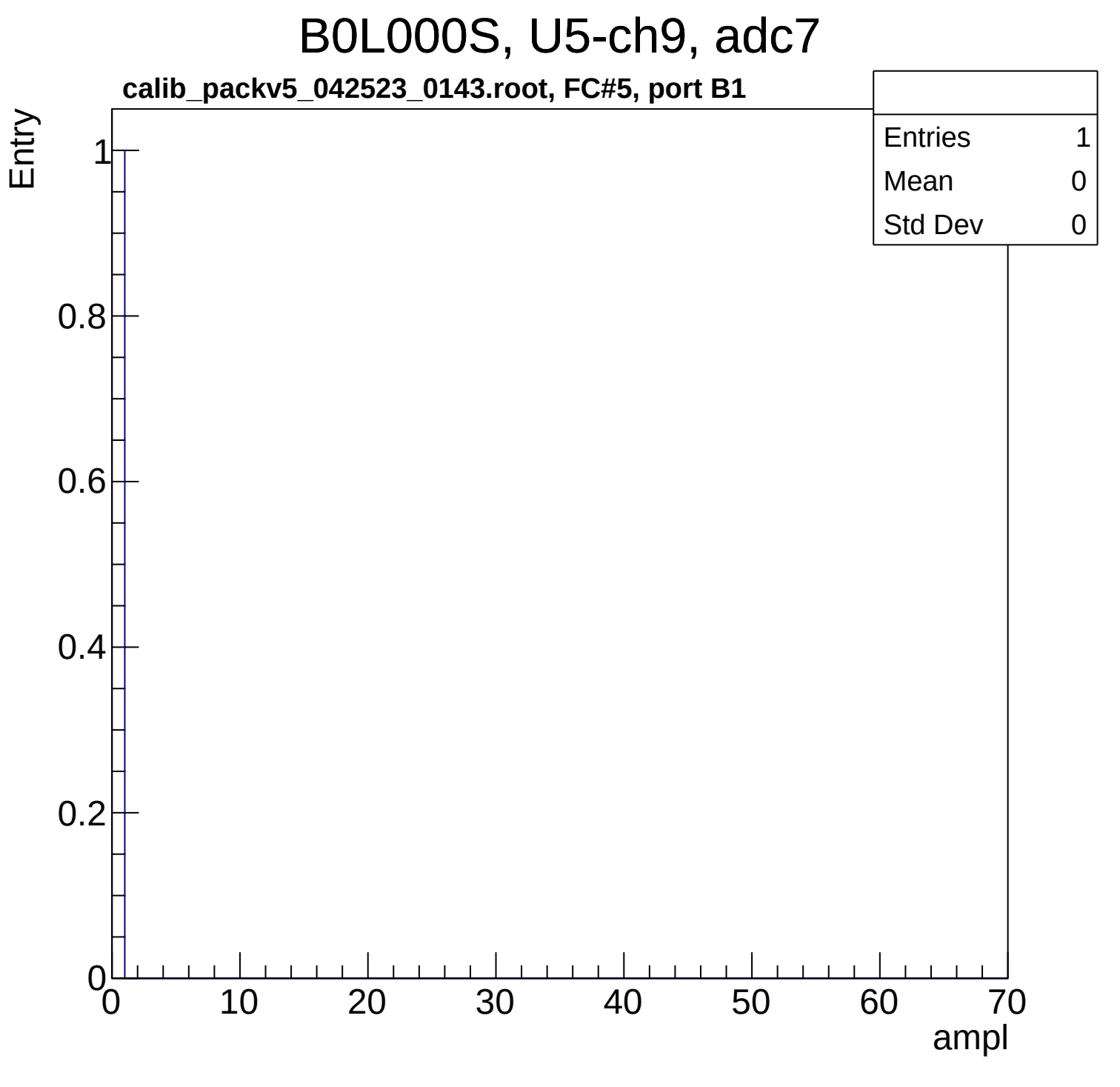

B0L000S, U5-ch9, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch9, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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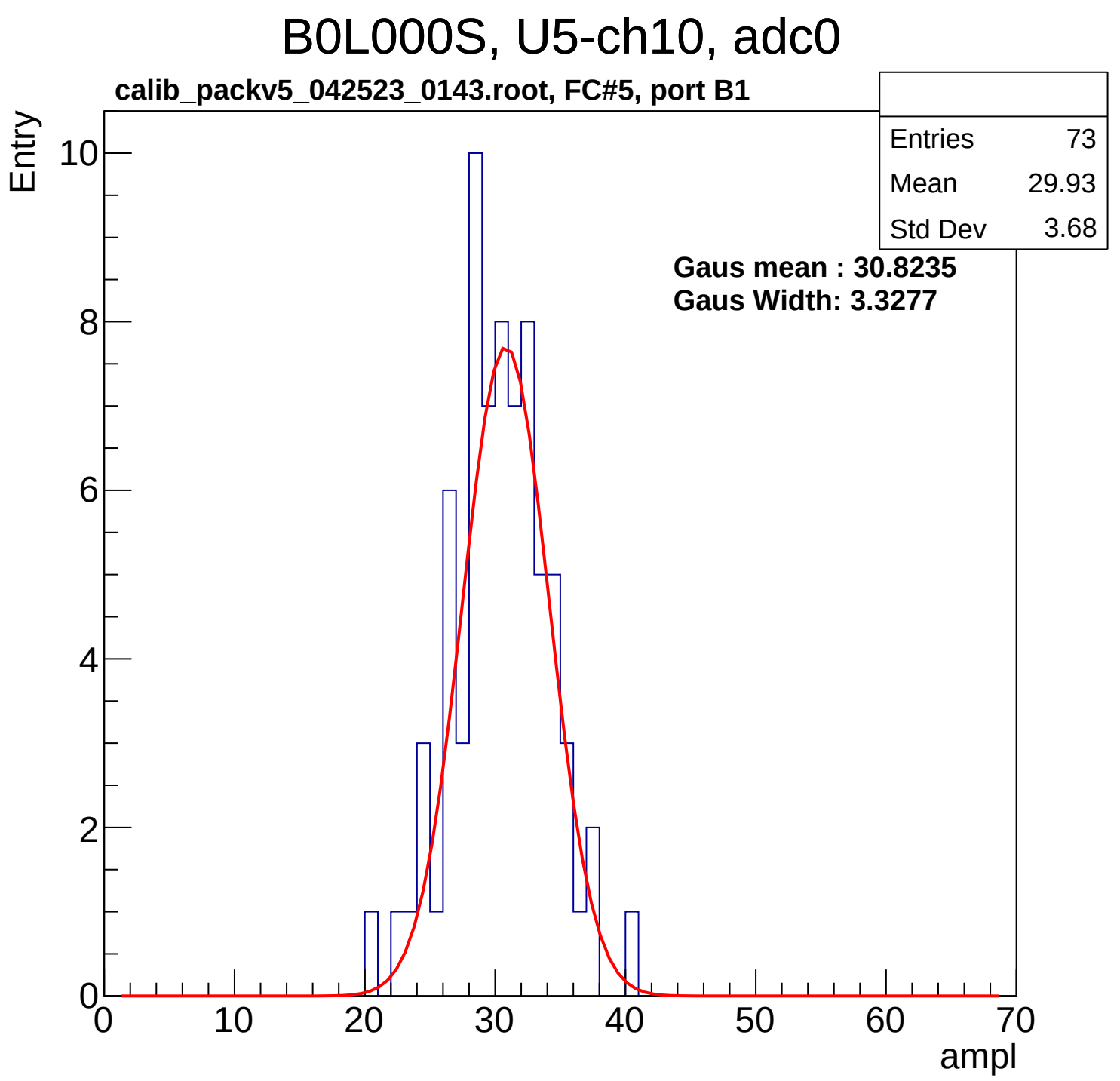
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch10, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.93
Std Dev 3.68
Gaus mean : 30.8235
Gaus Width: 3.3277
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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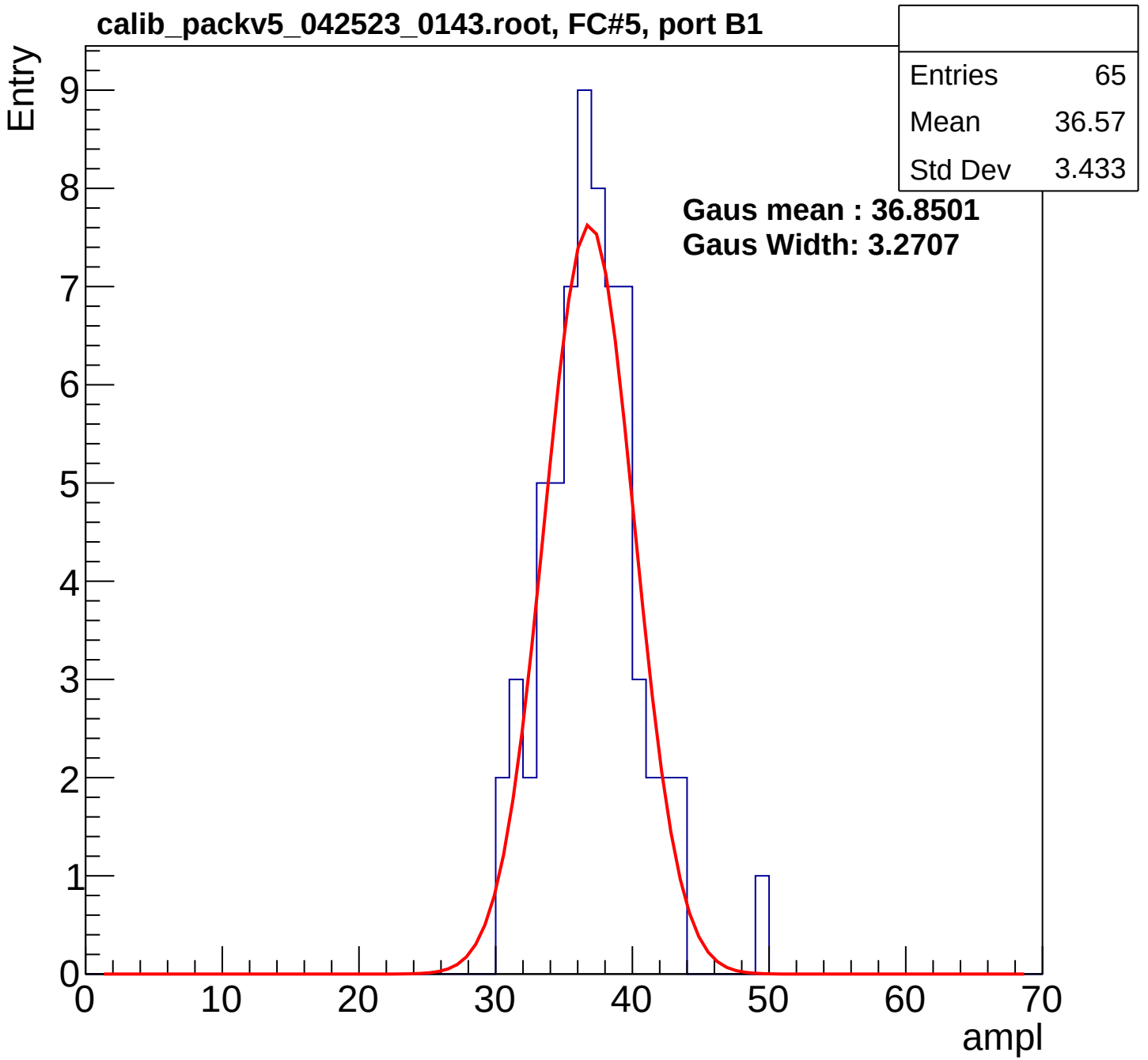
B0L000S, U5-ch10, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.57
Std Dev 3.433
Gaus mean : 36.8501
Gaus Width: 3.2707
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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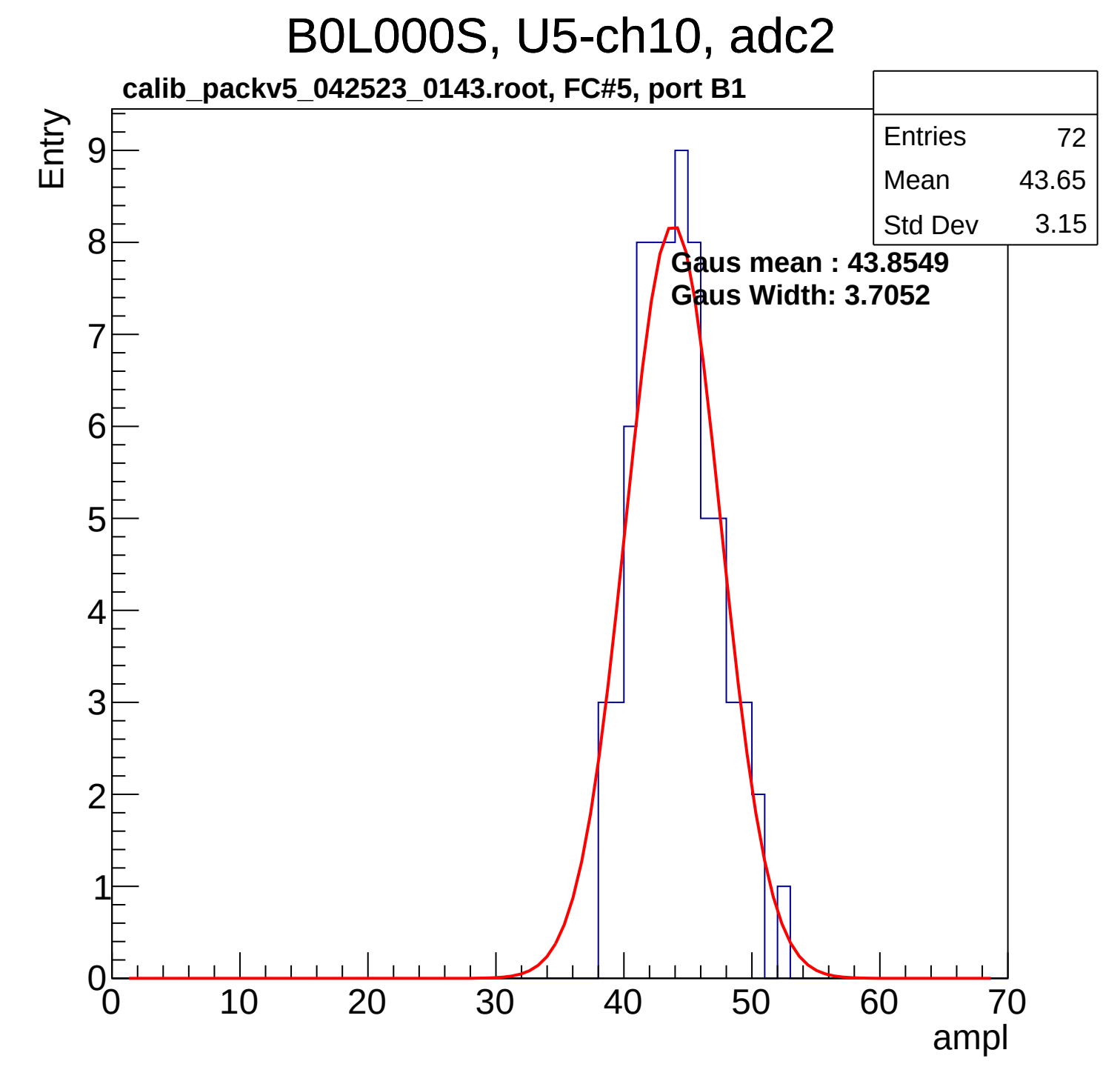
B0L000S, U5-ch10, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 43.65
Std Dev 3.15
Gaus mean : 43.8549
Gaus Width: 3.7052
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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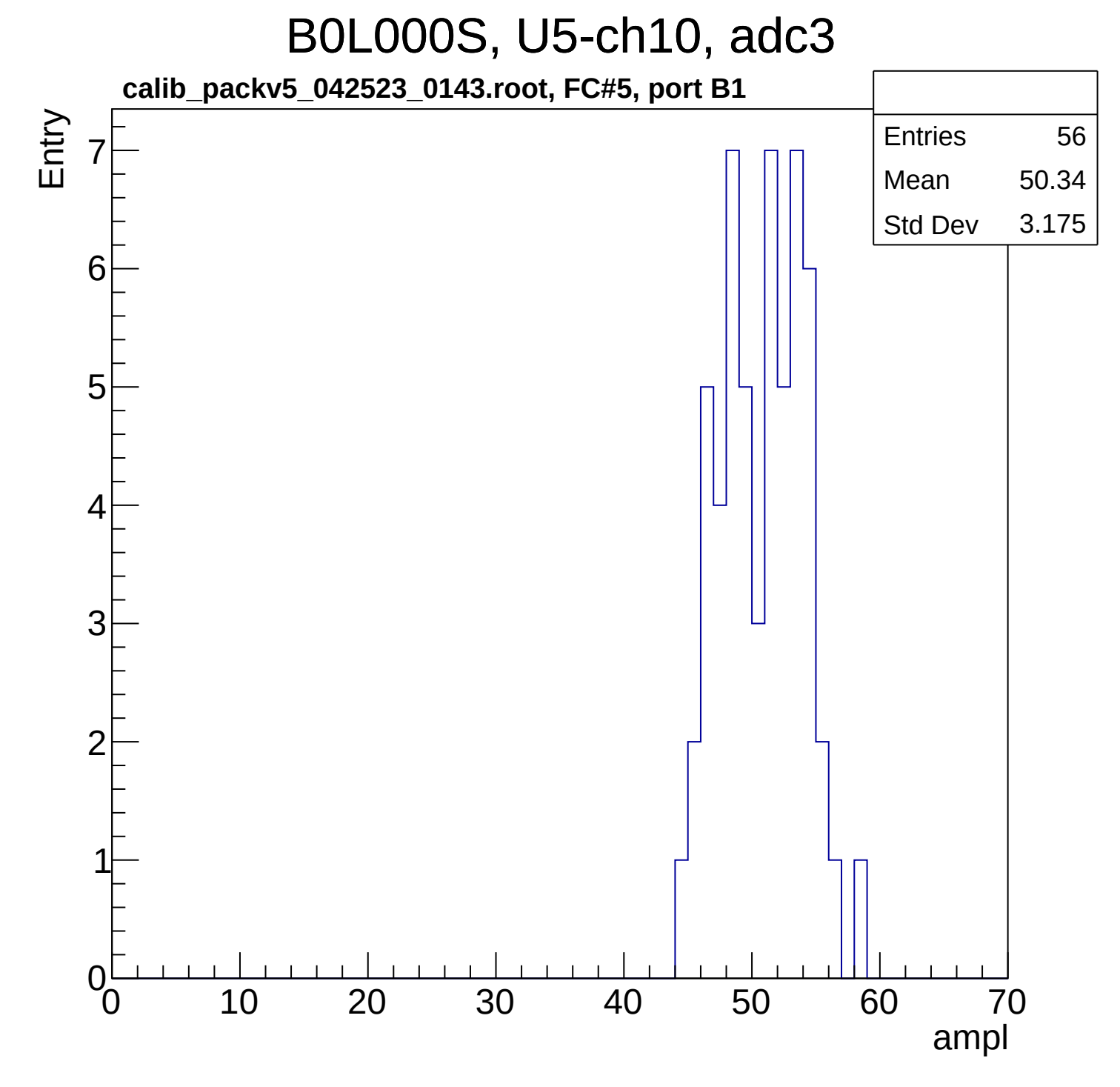
B0L000S, U5-ch10, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 50.34
Std Dev 3.175
Entry
ampl
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0
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60
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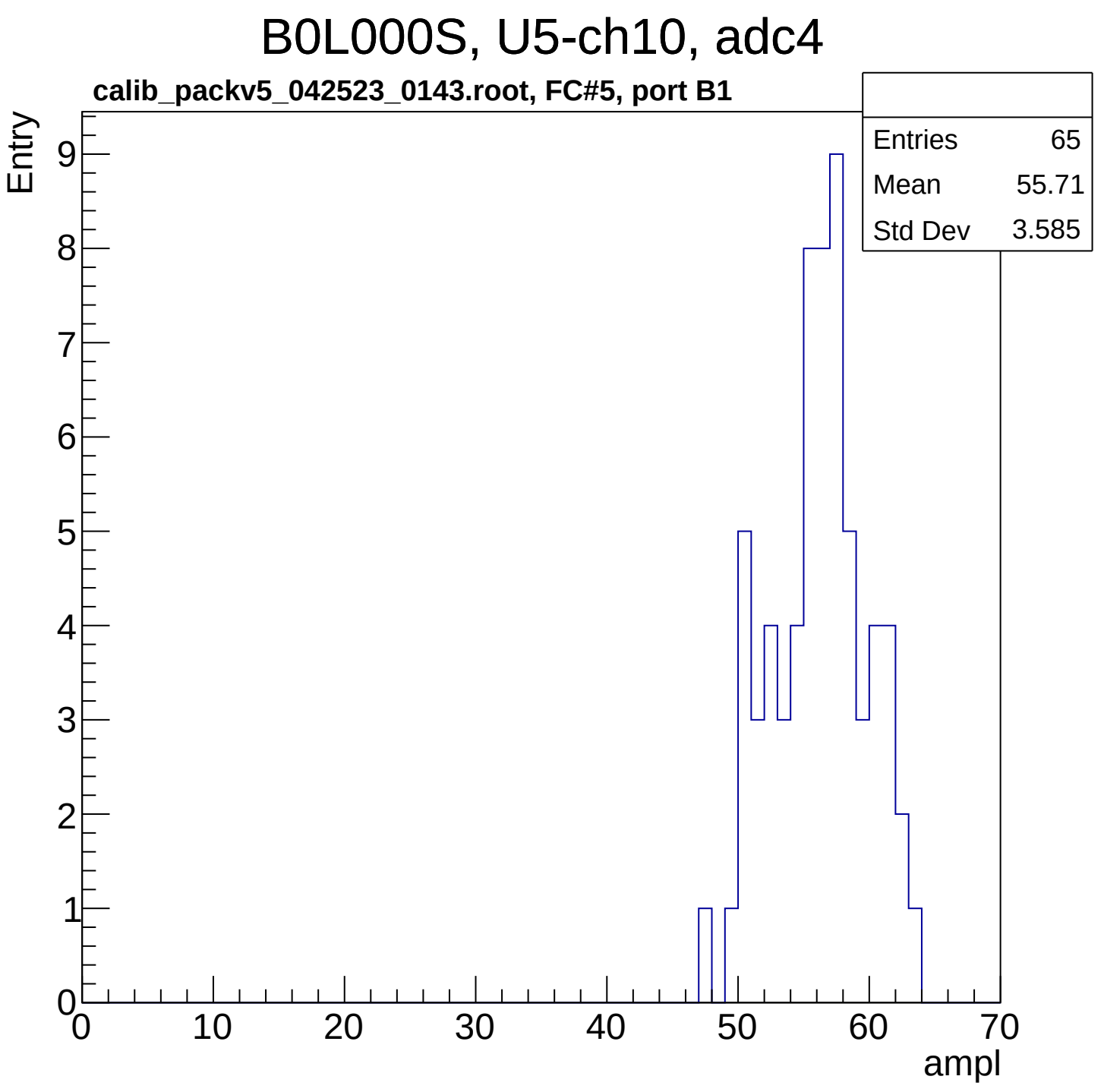
B0L000S, U5-ch10, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.71
Std Dev 3.585
Entry
ampl
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4
5
6
7
8
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0
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60
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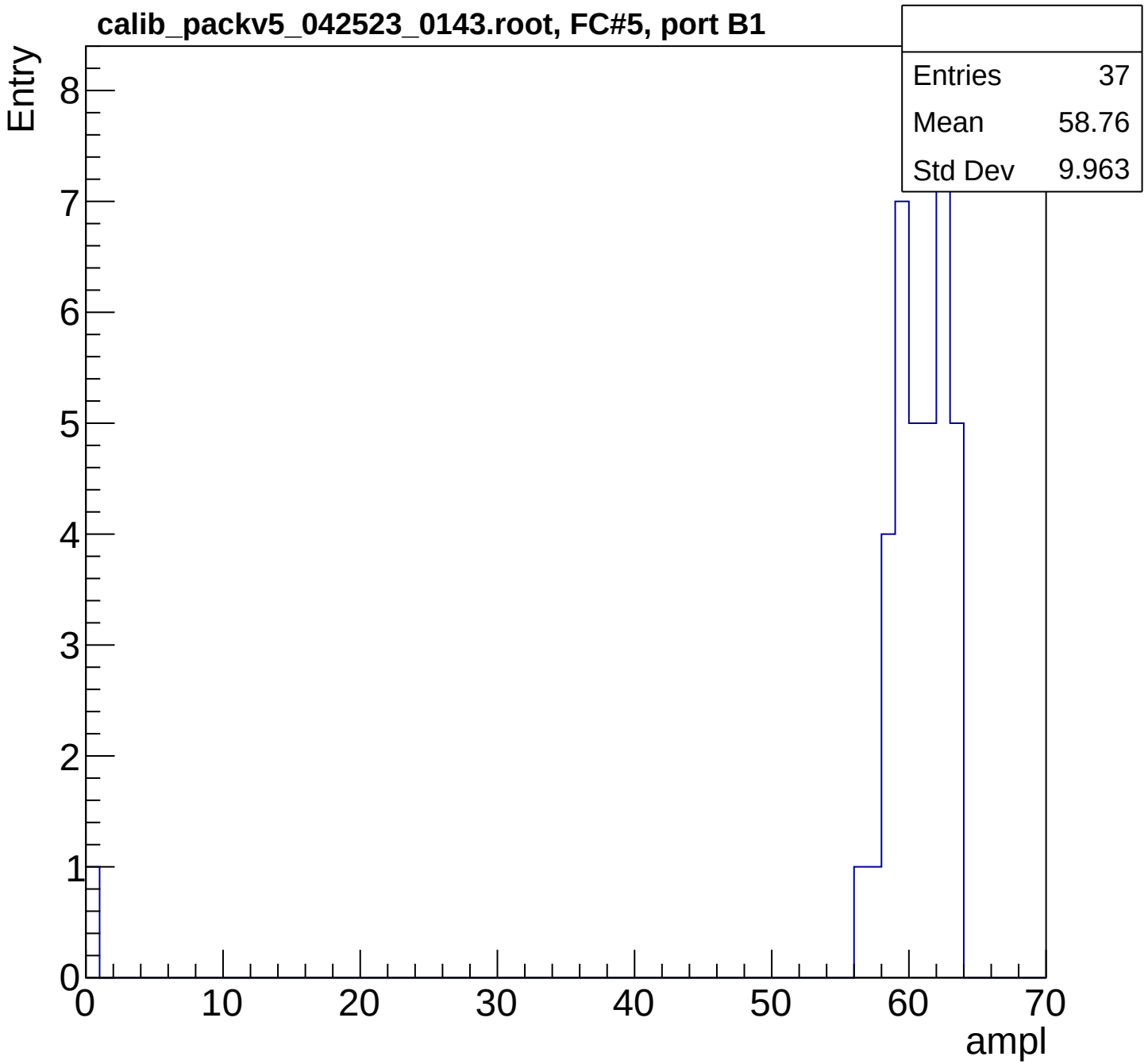
B0L000S, U5-ch10, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.76
Std Dev 9.963
Entry
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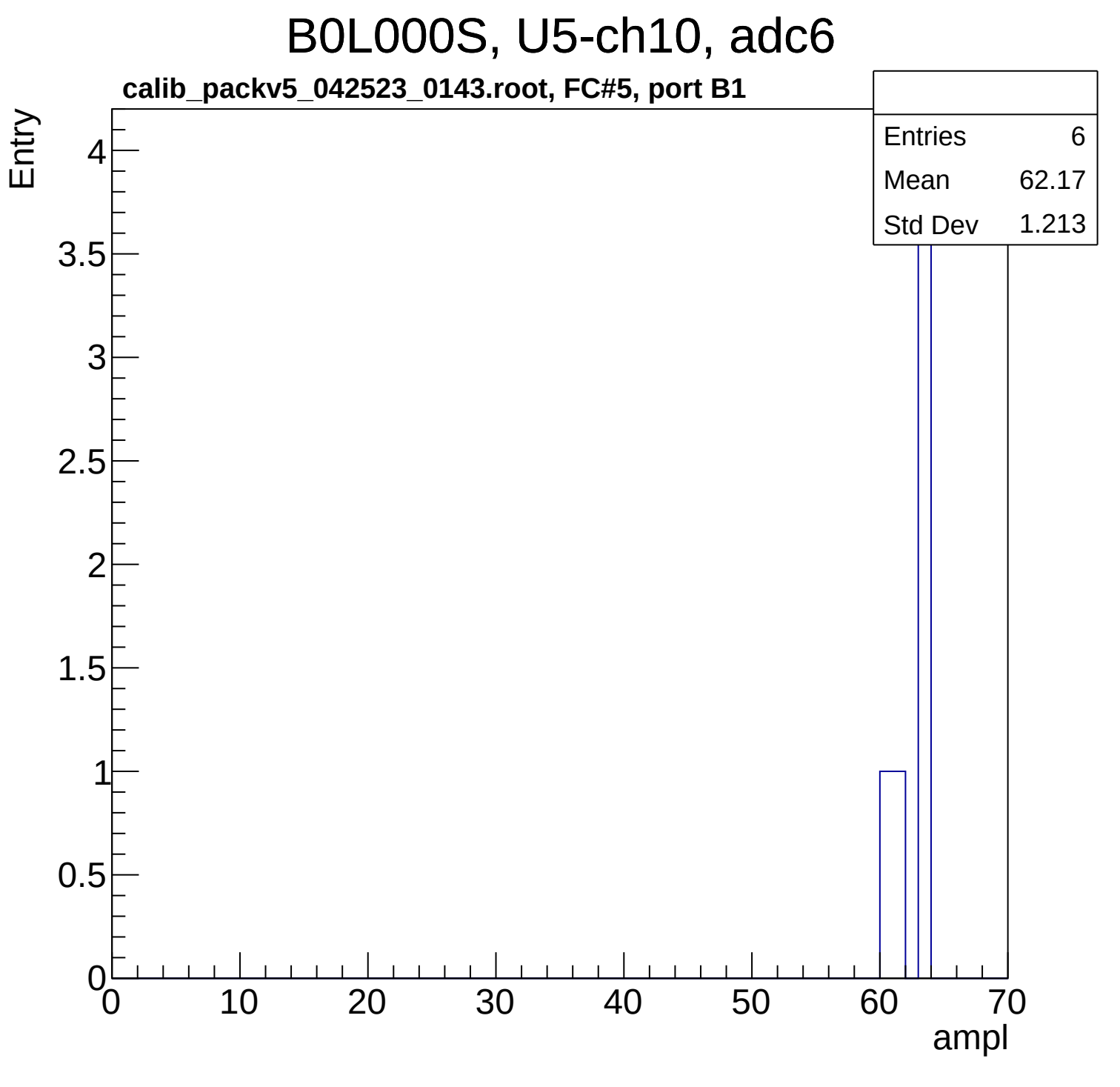
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch10, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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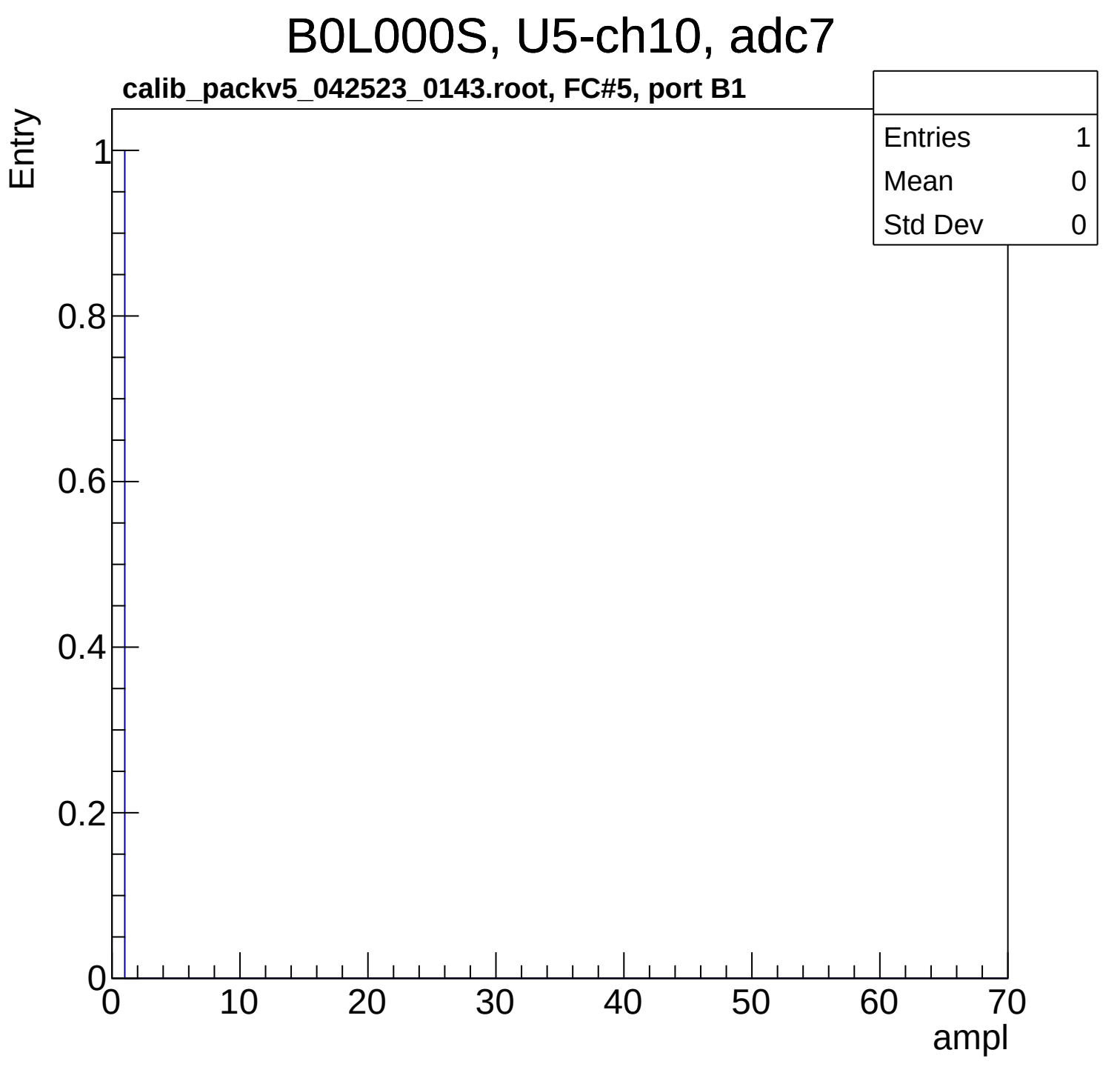
B0L000S, U5-ch10, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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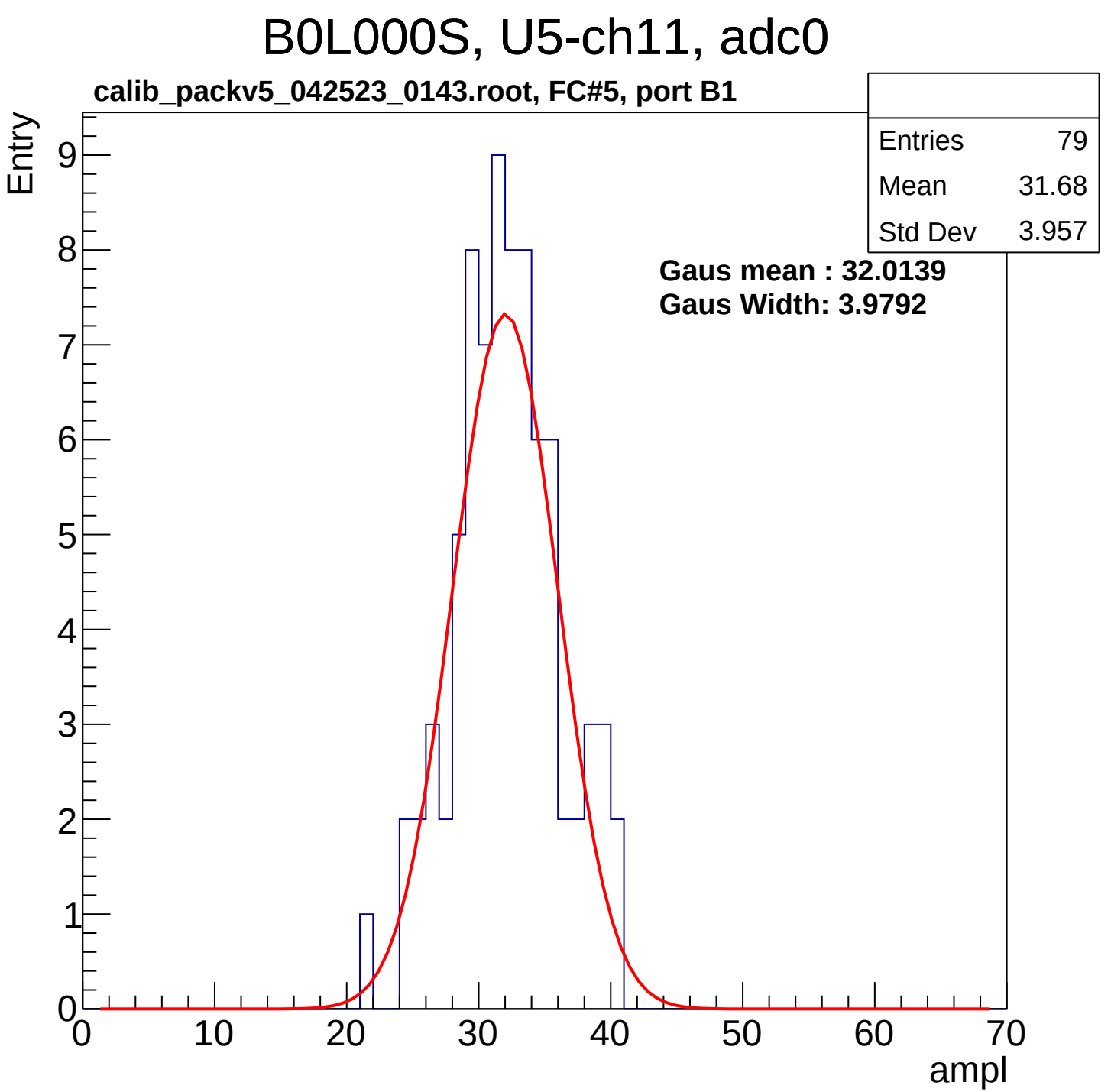
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch11, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 31.68
Std Dev 3.957
Gaus mean : 32.0139
Gaus Width: 3.9792
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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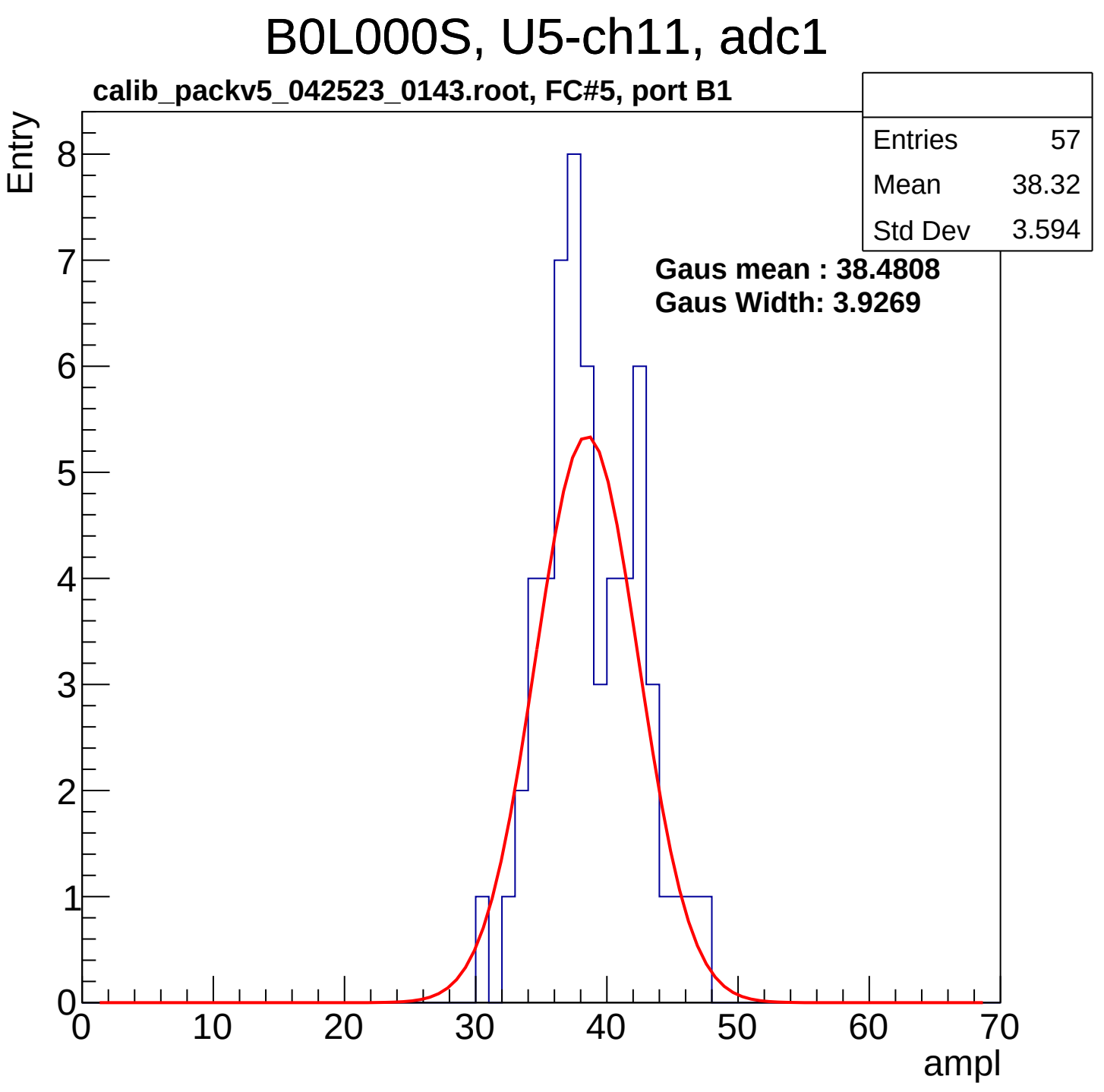
B0L000S, U5-ch11, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 38.32
Std Dev 3.594
Gaus mean : 38.4808
Gaus Width: 3.9269
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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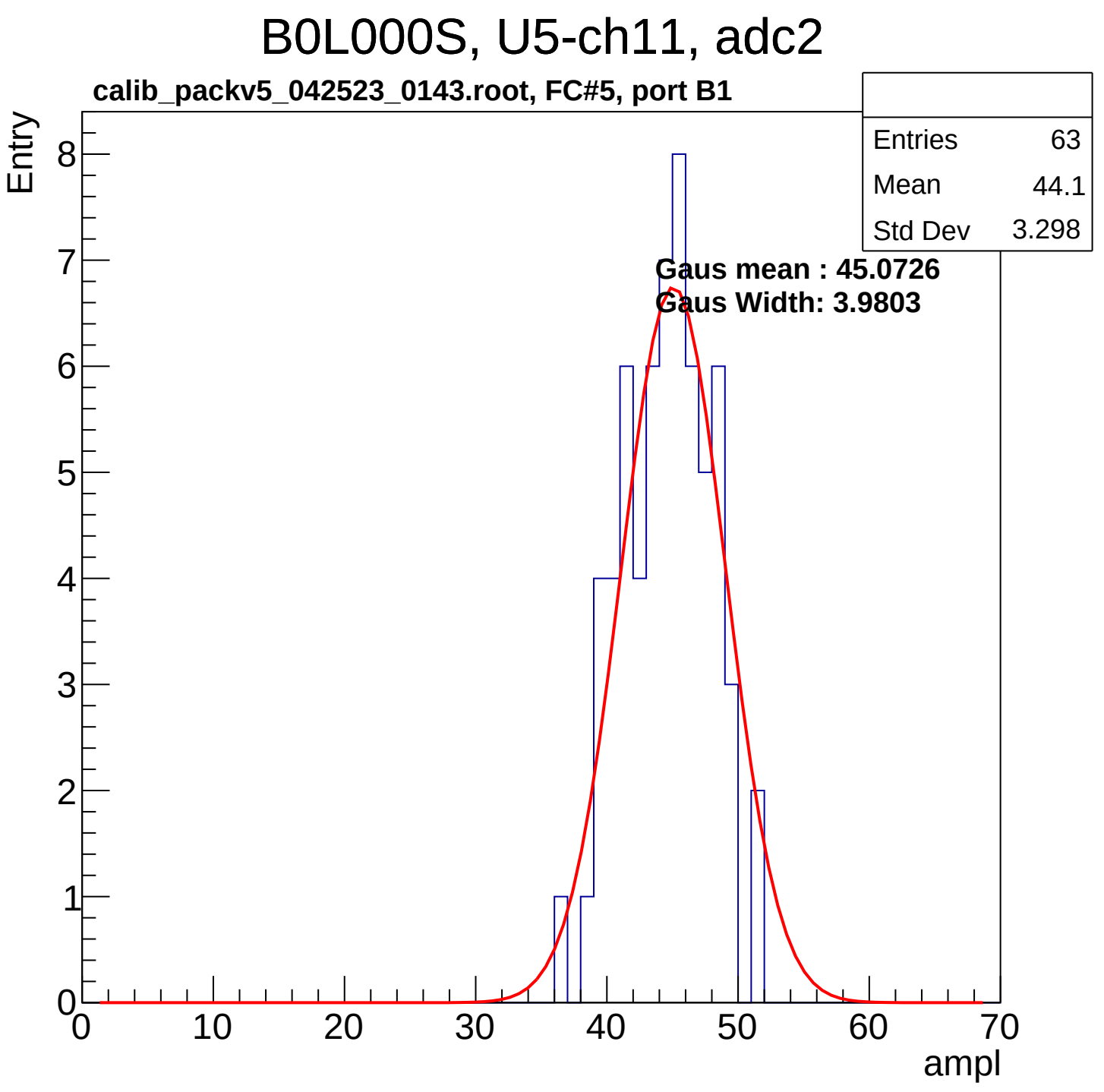
B0L000S, U5-ch11, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 44.1
Std Dev 3.298
Gaus mean : 45.0726
Gaus Width: 3.9803
Entry
ampl
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2
3
4
5
6
7
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40
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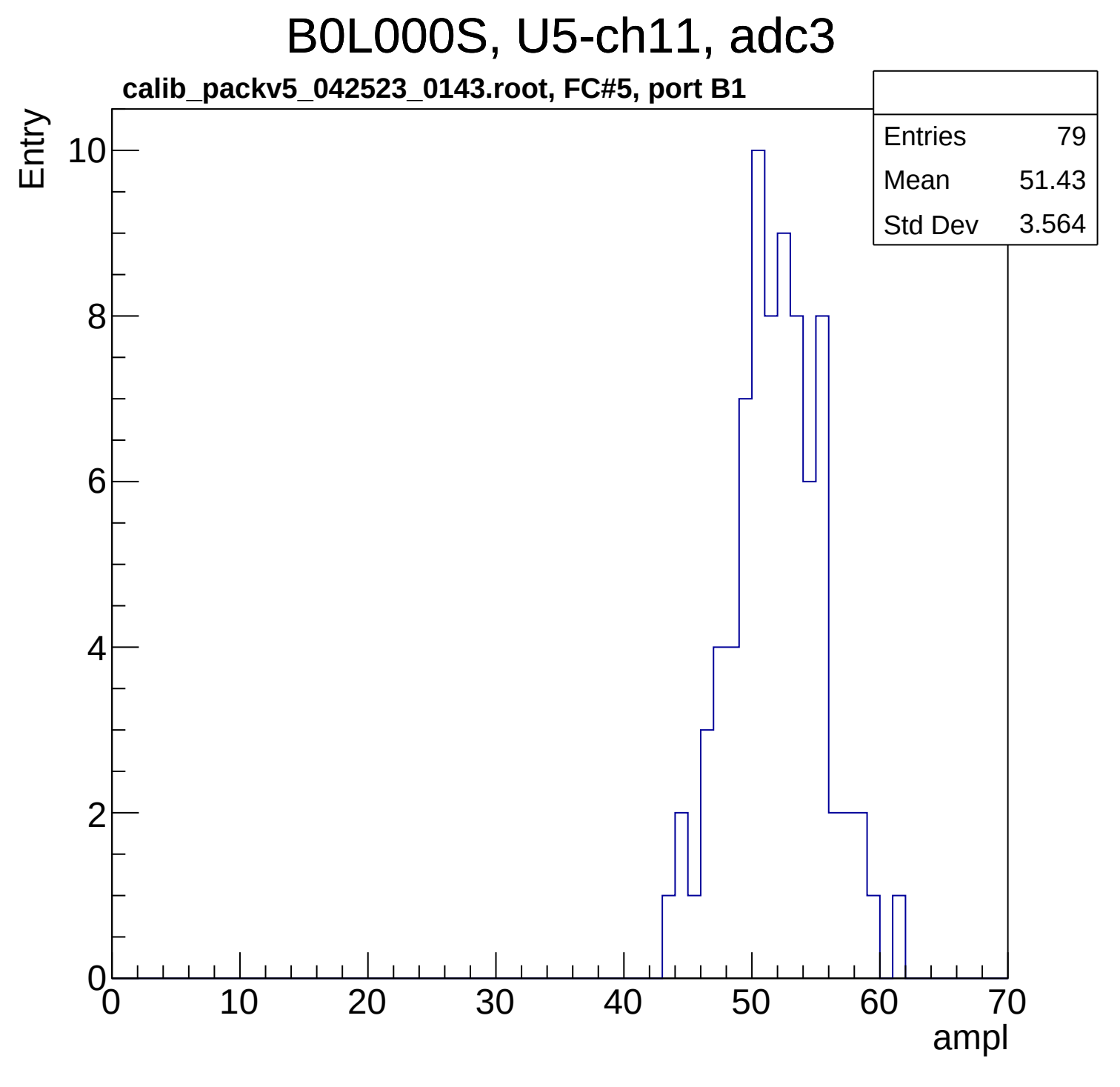
B0L000S, U5-ch11, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 51.43
Std Dev 3.564
Entry
ampl
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60
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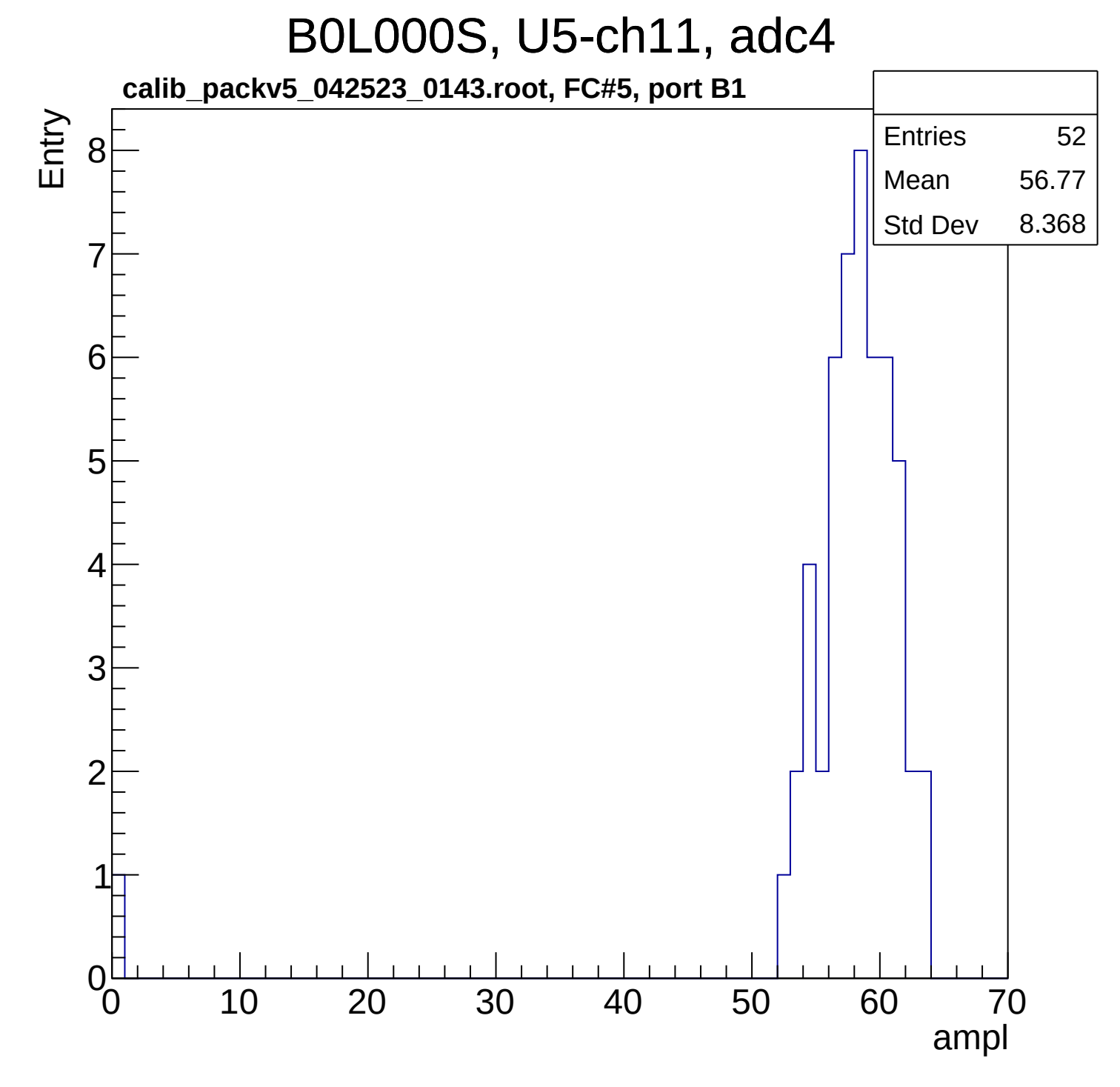
B0L000S, U5-ch11, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.77
Std Dev 8.368
Entry
ampl
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2
3
4
5
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7
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0
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50
60
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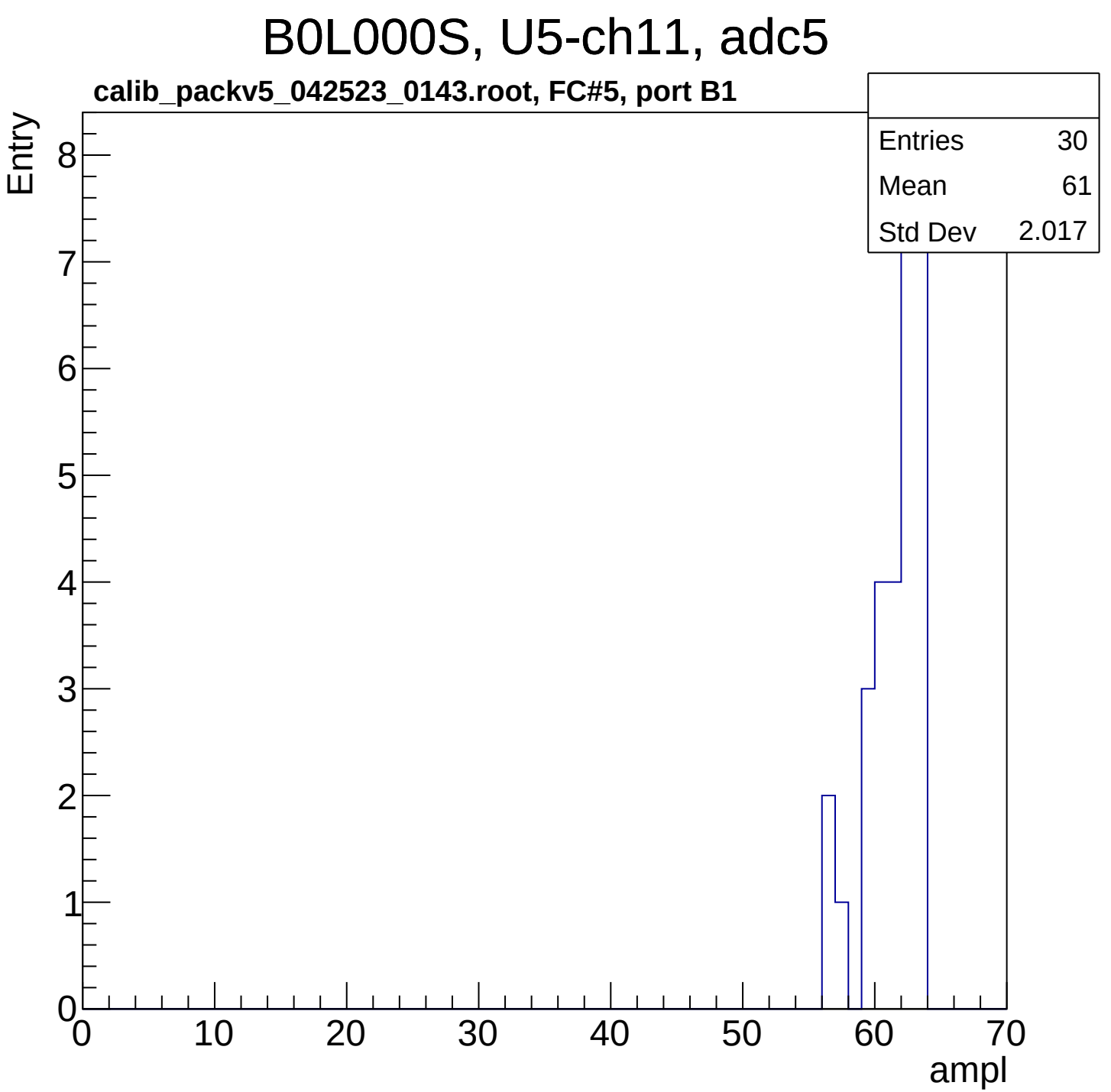
B0L000S, U5-ch11, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 61
Std Dev 2.017
Entry
ampl
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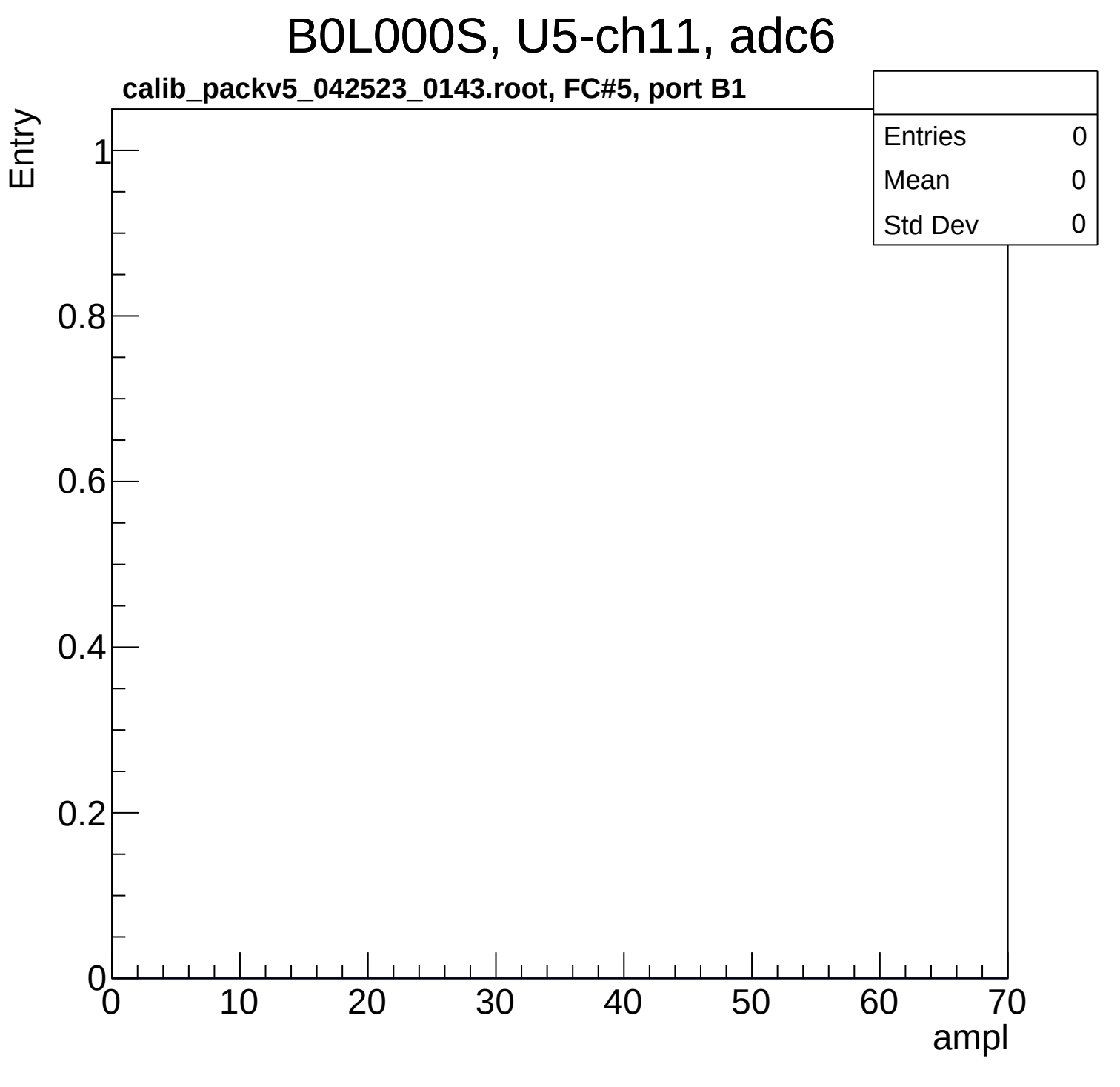
B0L000S, U5-ch11, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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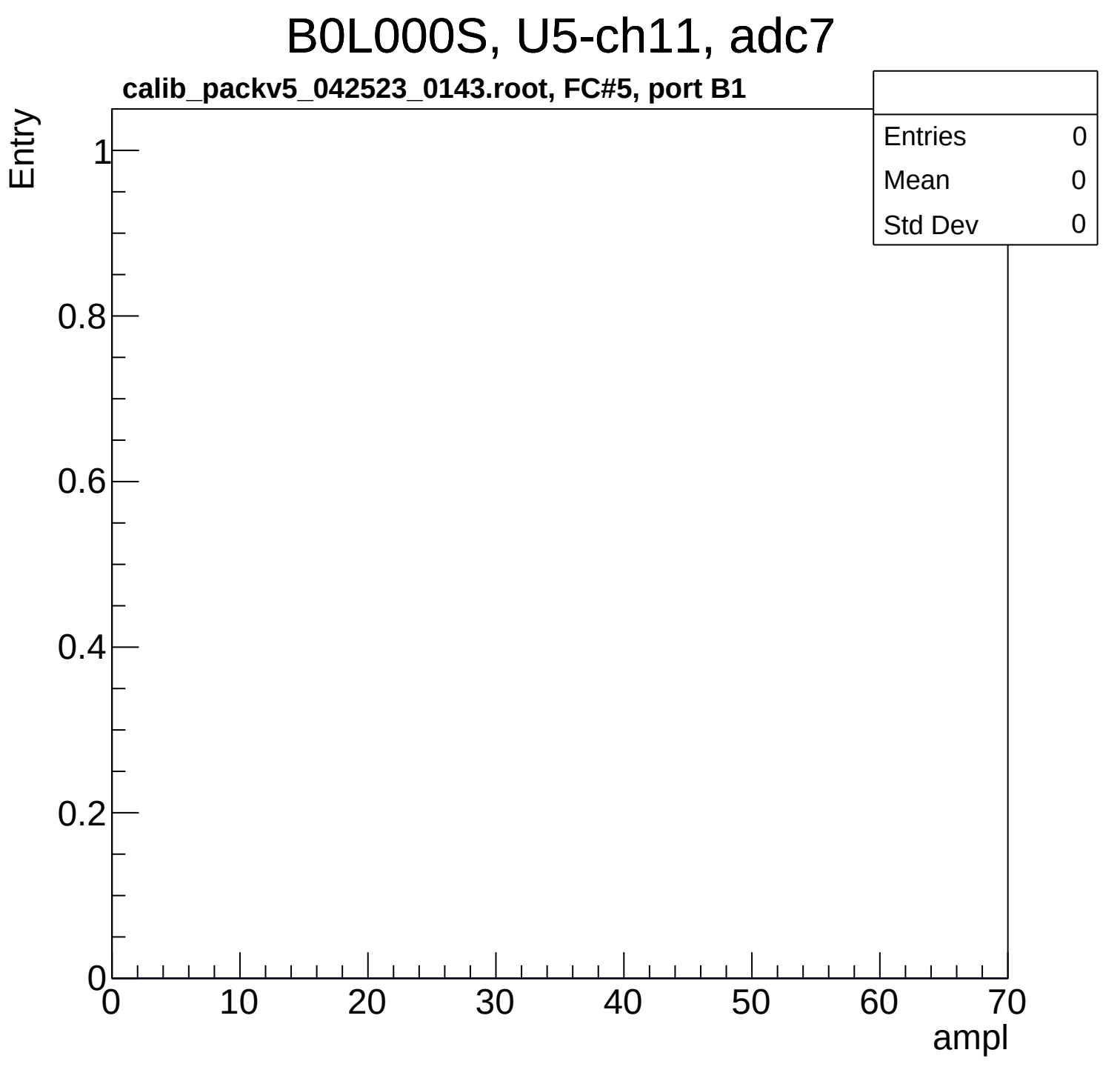
70
ampl


B0L000S, U5-ch11, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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50
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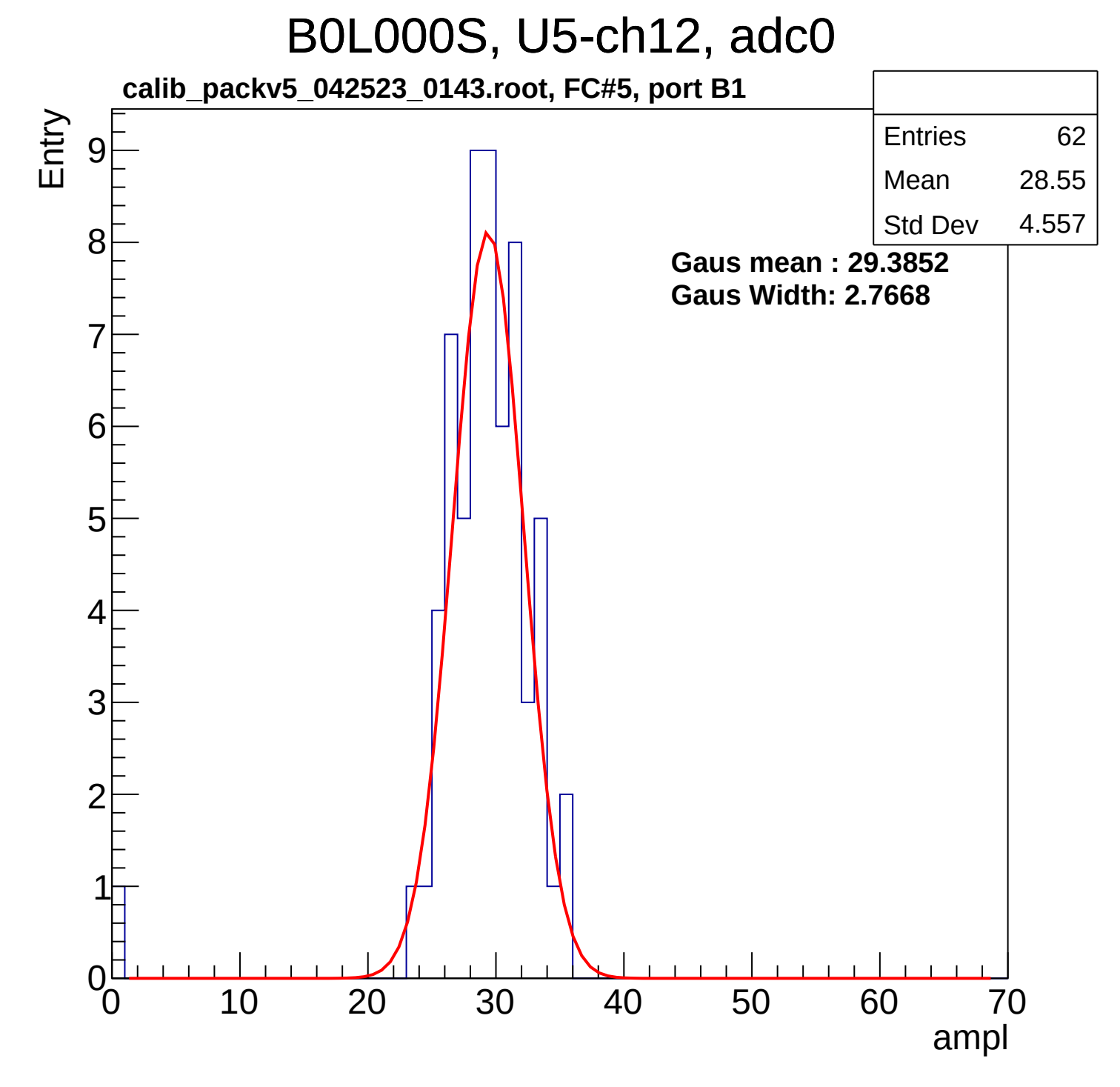
70
ampl


B0L000S, U5-ch12, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 28.55
Std Dev 4.557
Gaus mean : 29.3852
Gaus Width: 2.7668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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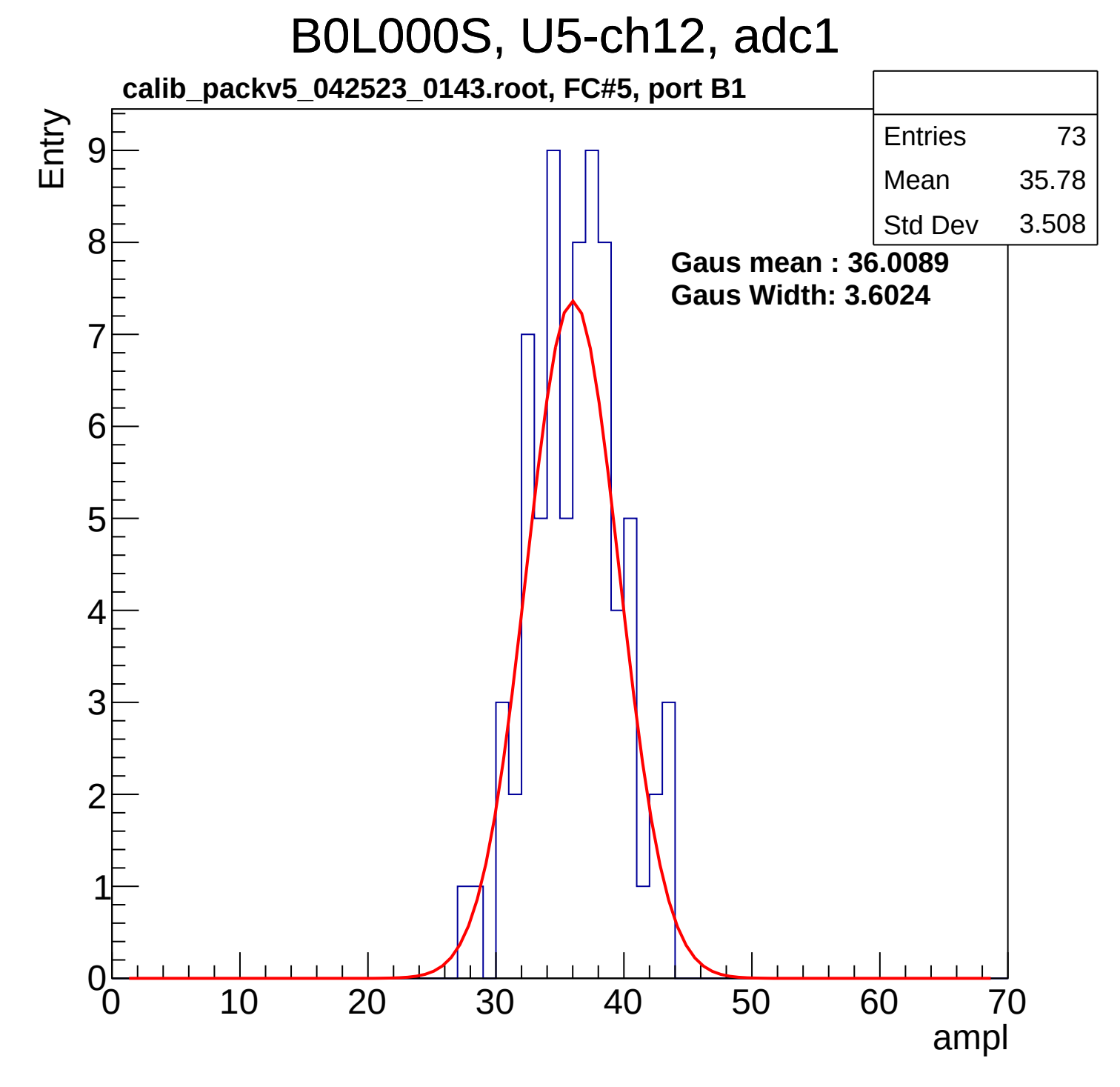
B0L000S, U5-ch12, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 35.78
Std Dev 3.508
Gaus mean : 36.0089
Gaus Width: 3.6024
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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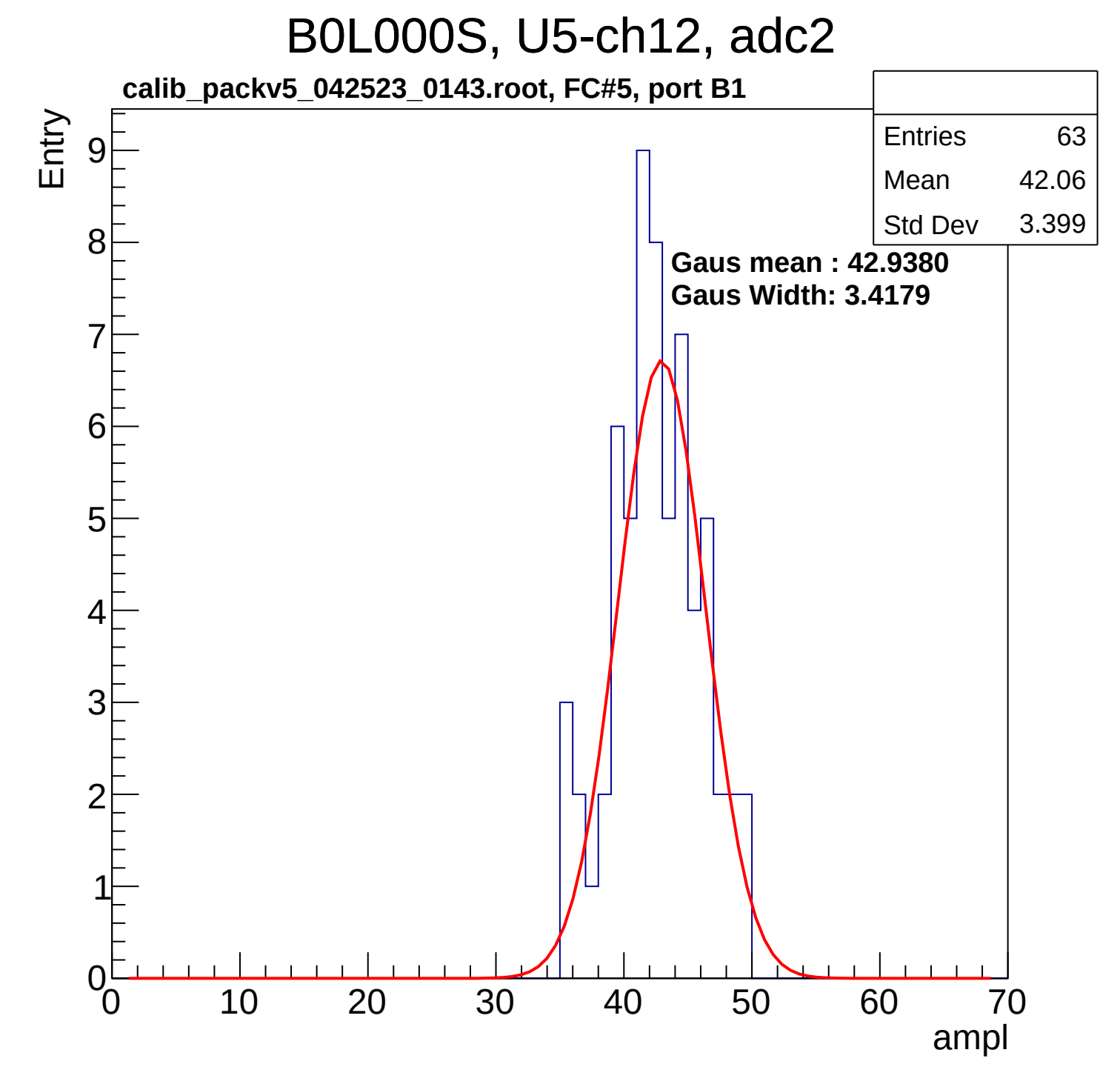
B0L000S, U5-ch12, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 42.06
Std Dev 3.399
Gaus mean : 42.9380
Gaus Width: 3.4179
Entry
ampl
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2
3
4
5
6
7
8
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0
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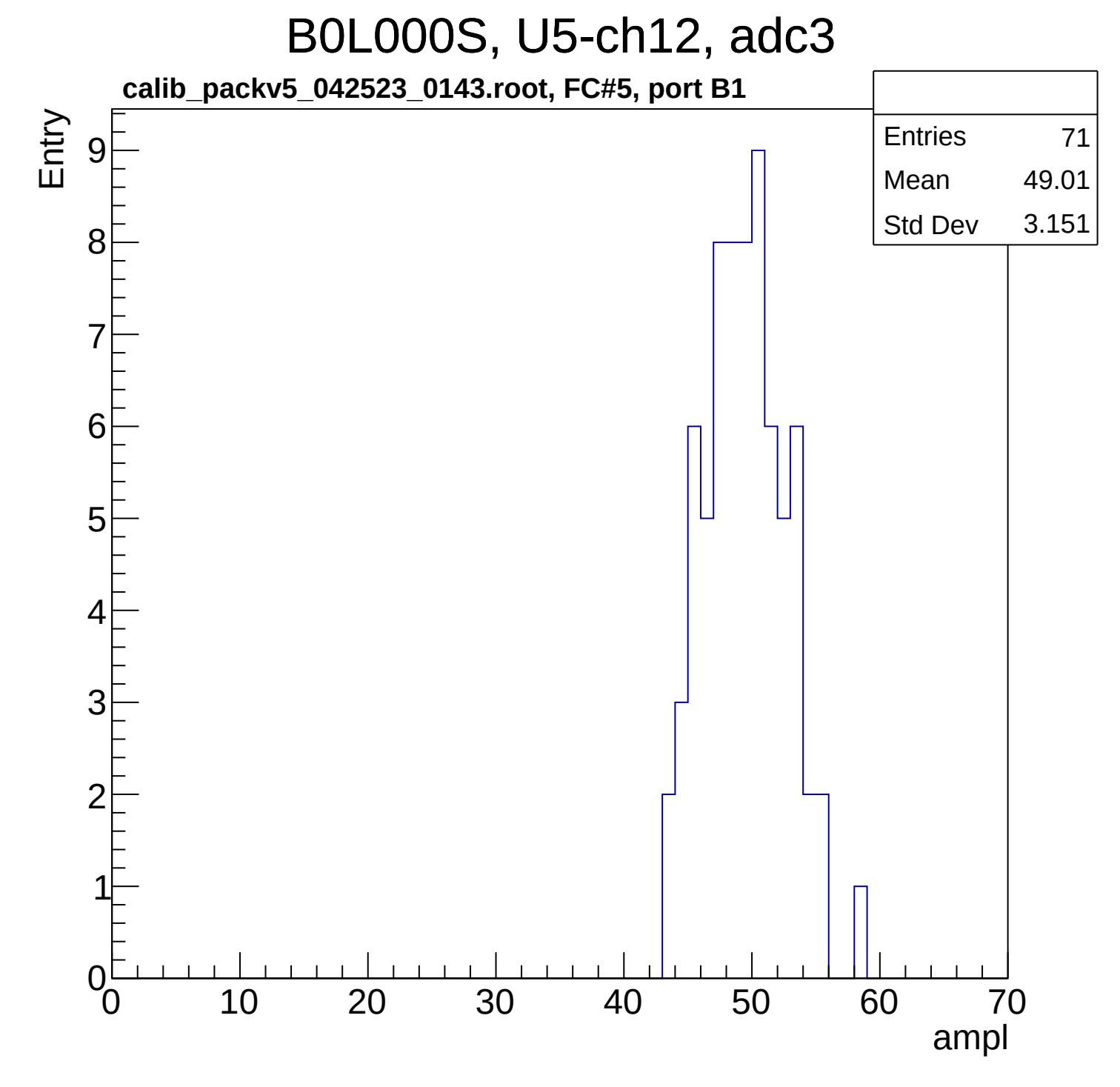
B0L000S, U5-ch12, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 49.01
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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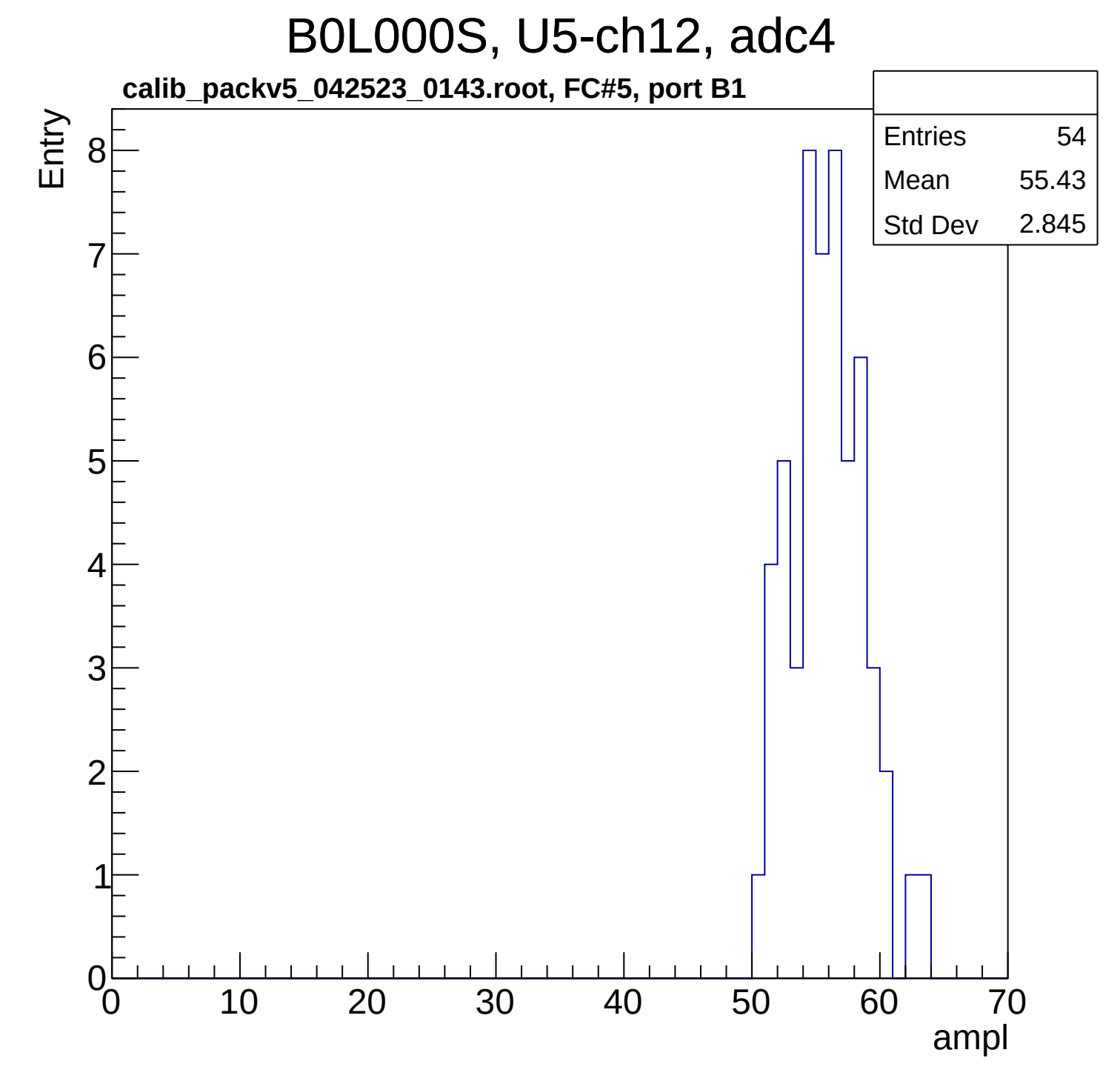
B0L000S, U5-ch12, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.43
Std Dev 2.845
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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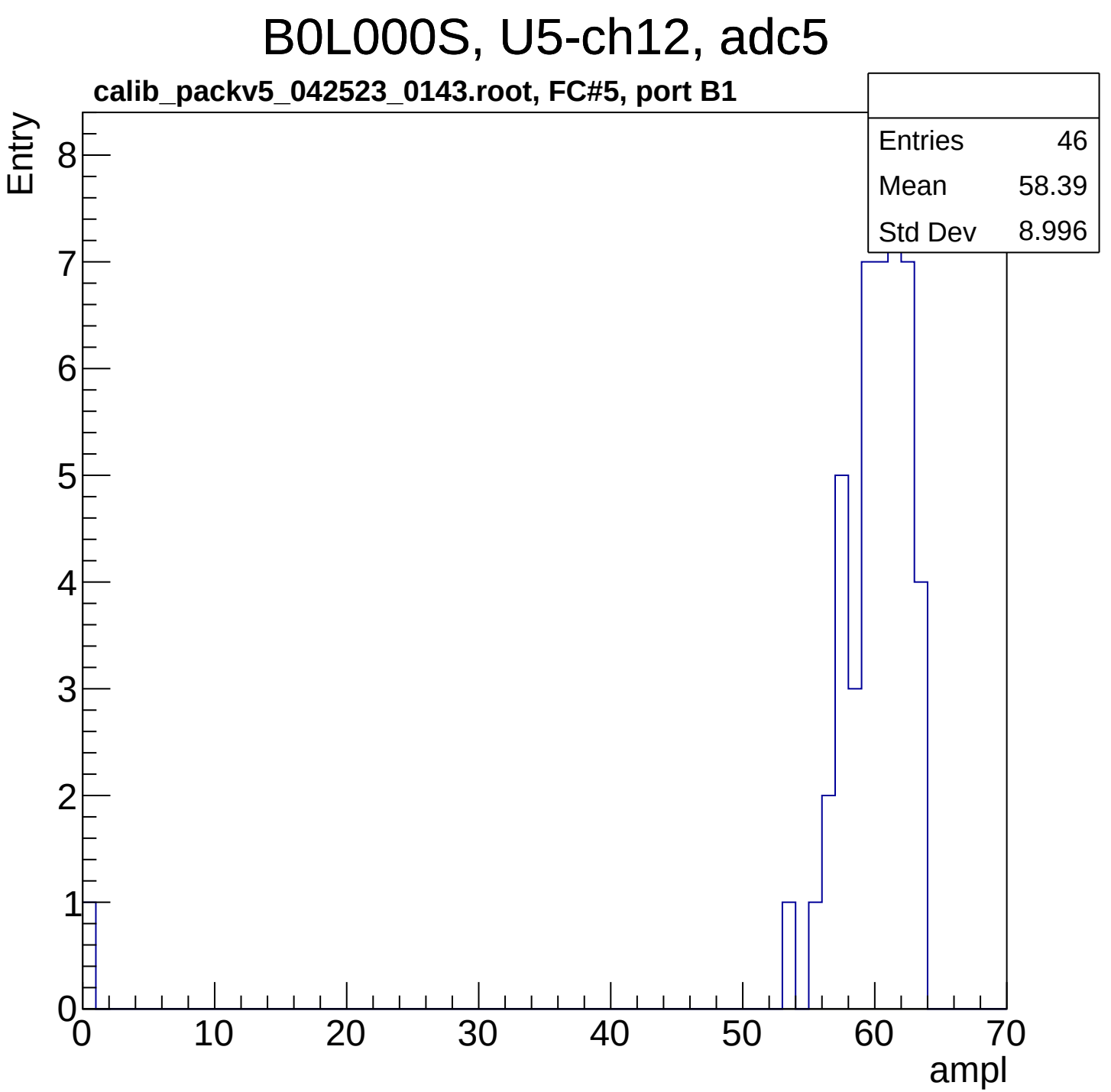
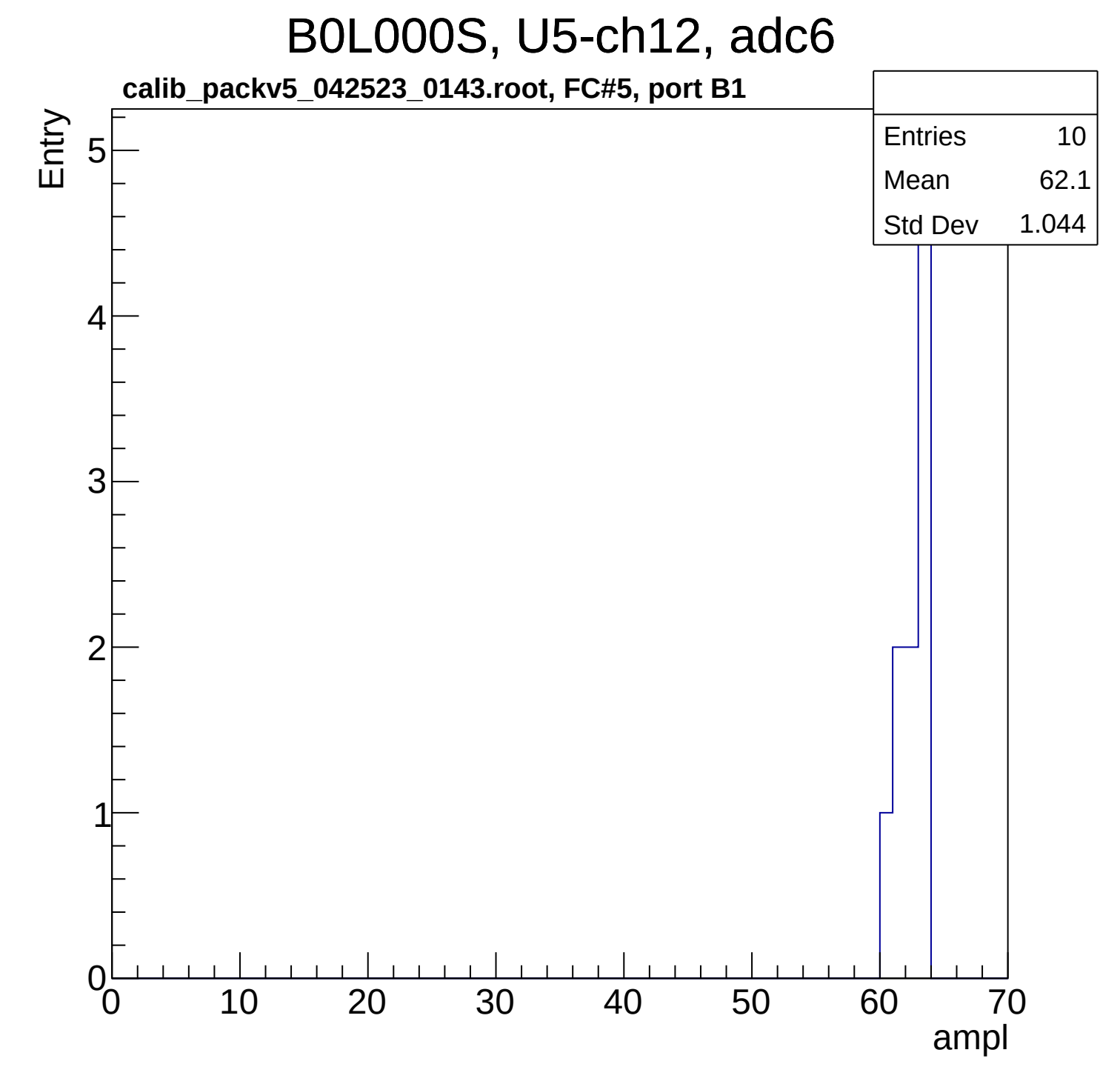
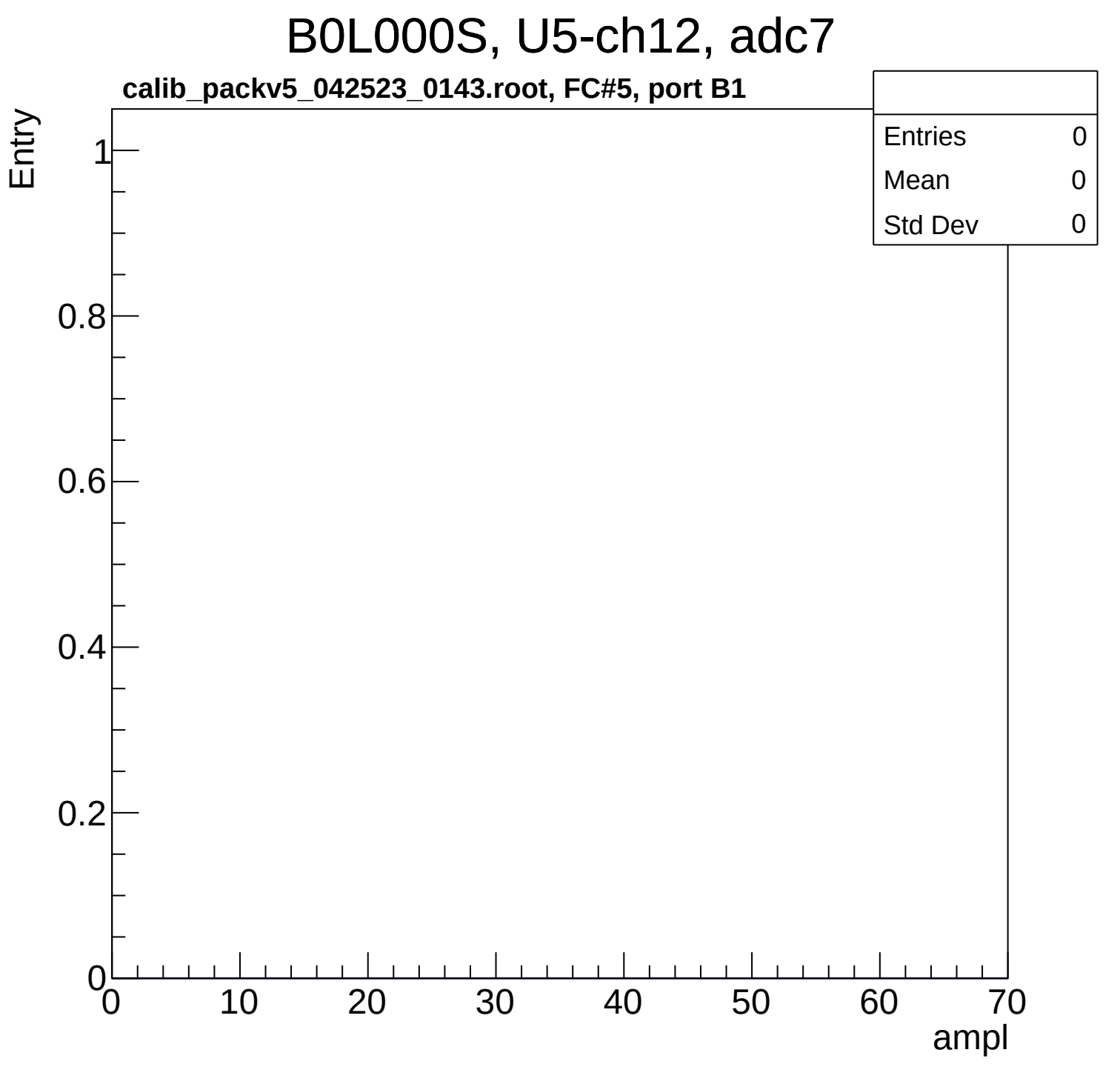
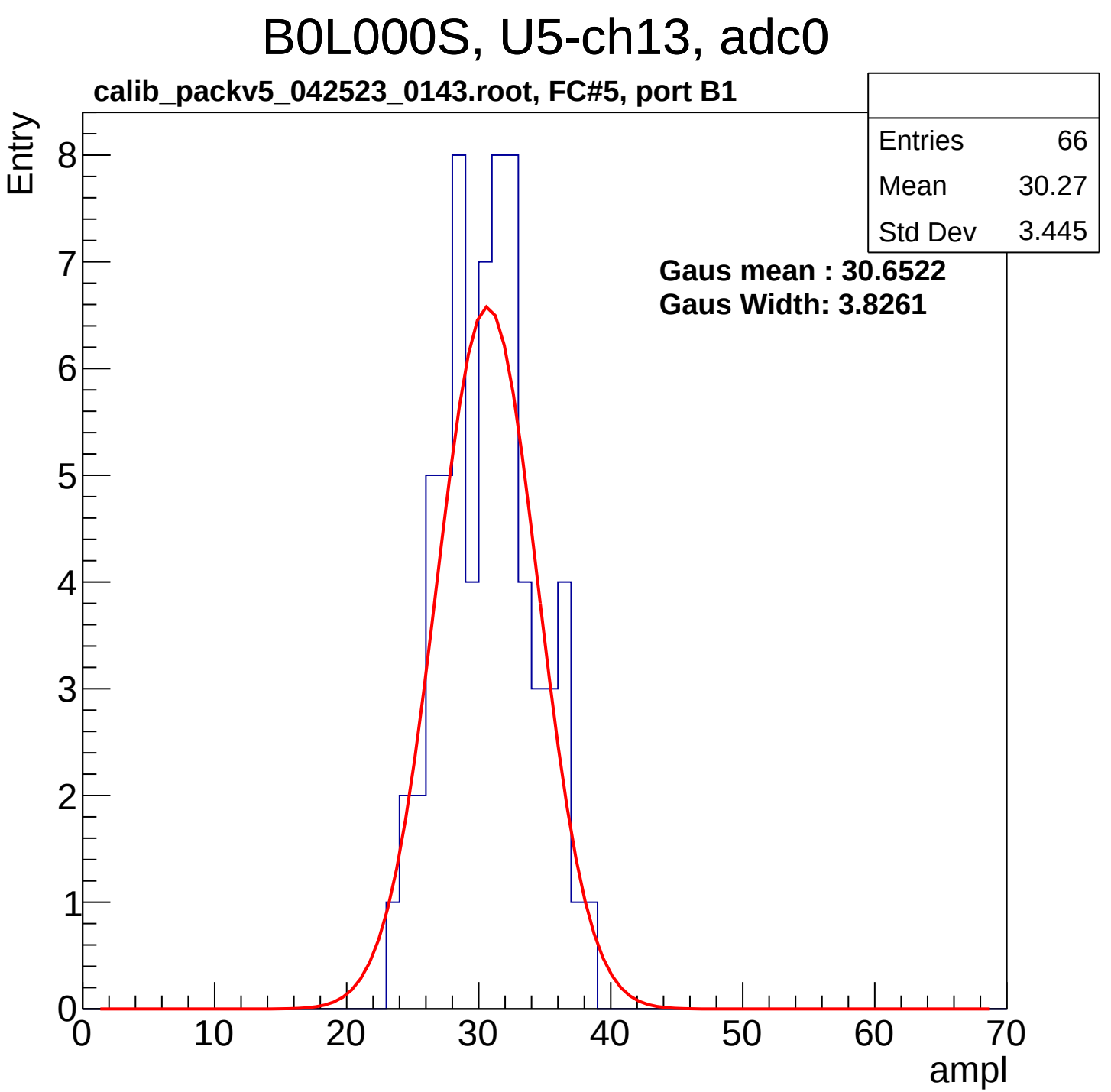

B0L000S, U5-ch12, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.39
Std Dev 8.996
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch12, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 62.1
Std Dev 1.044
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch12, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch13, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.27
Std Dev 3.445
Gaus mean : 30.6522
Gaus Width: 3.8261
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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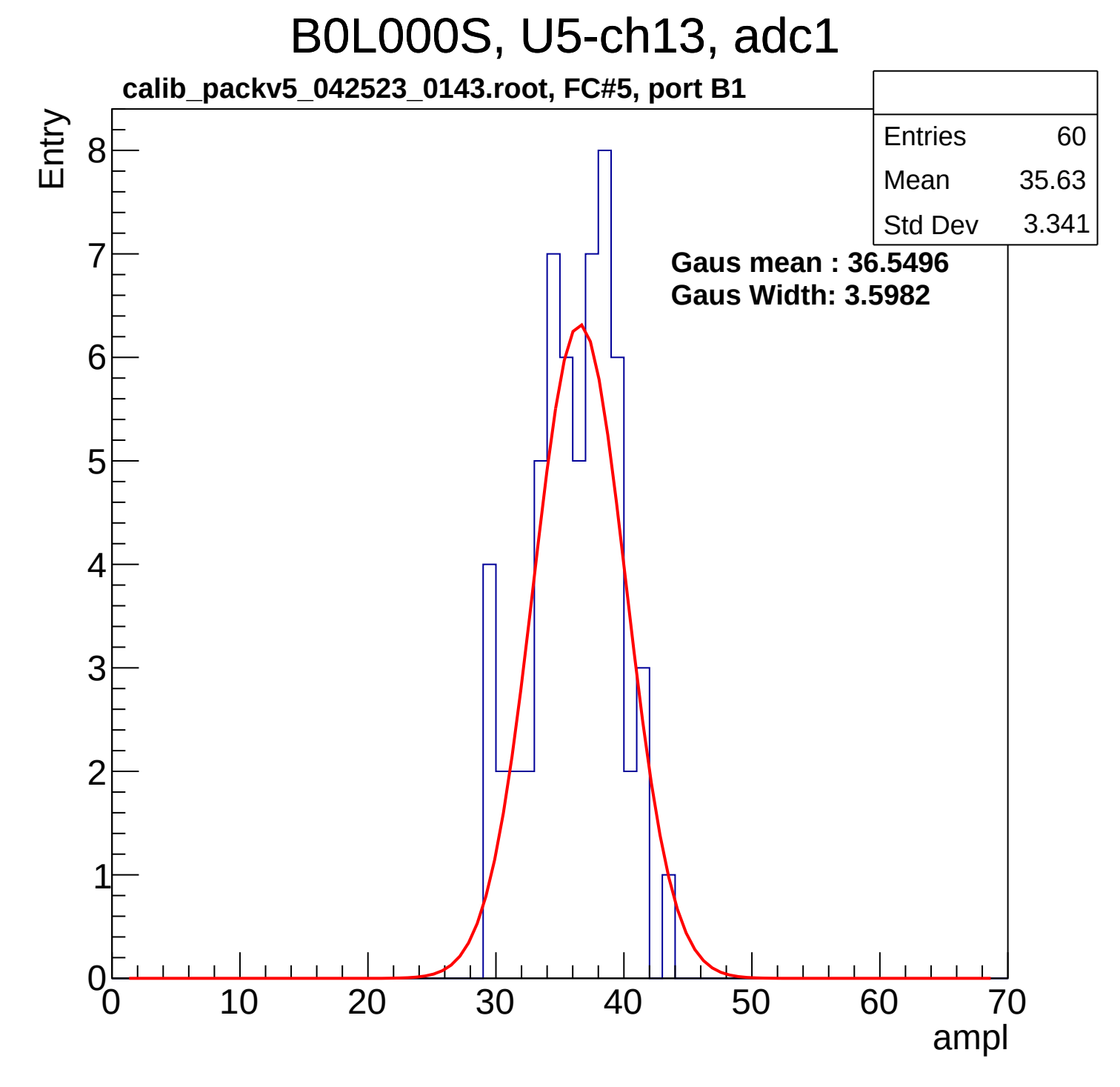
B0L000S, U5-ch13, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 35.63
Std Dev 3.341
Gaus mean : 36.5496
Gaus Width: 3.5982
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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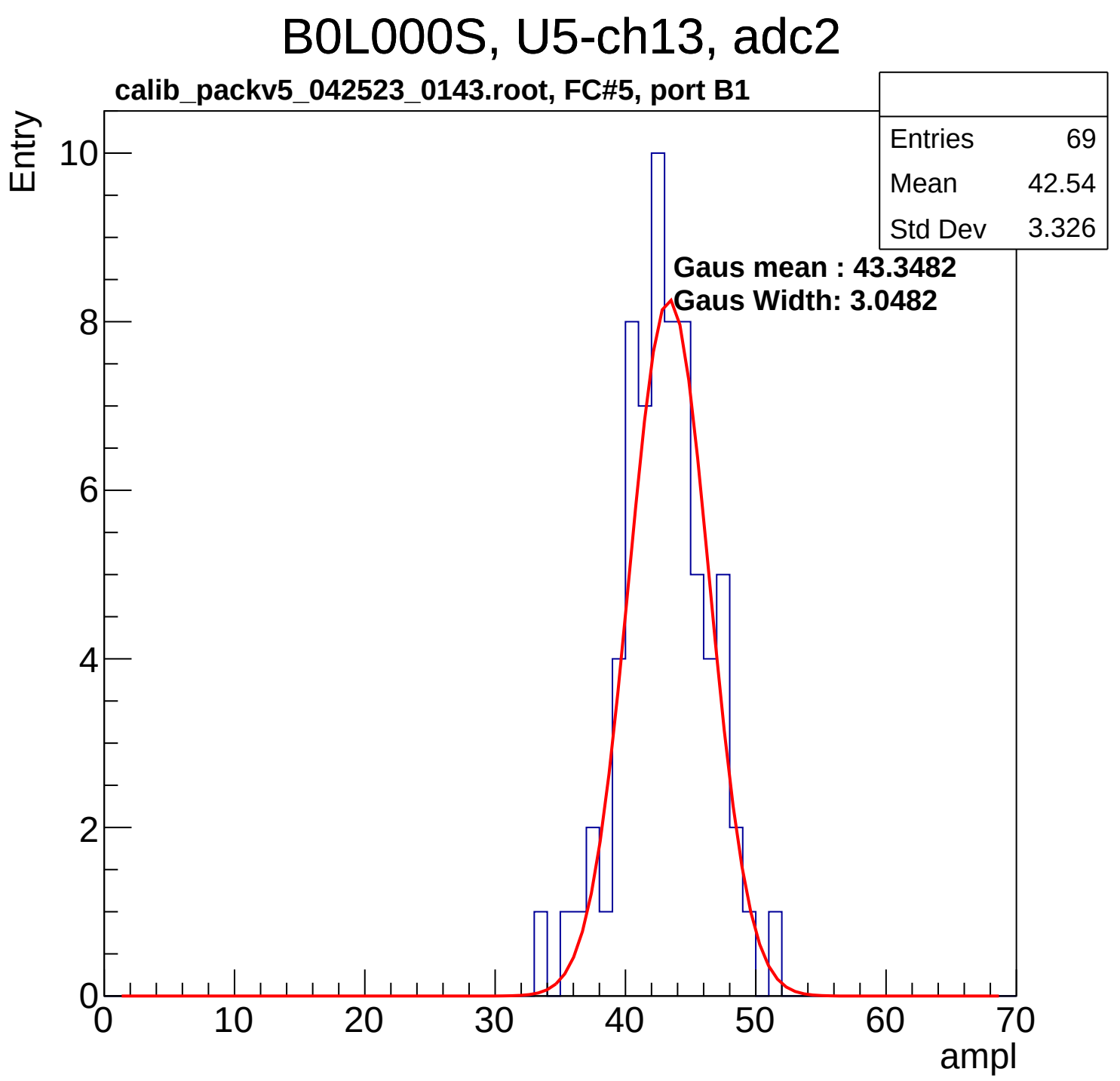
B0L000S, U5-ch13, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.54
Std Dev 3.326
Gaus mean : 43.3482
Gaus Width: 3.0482
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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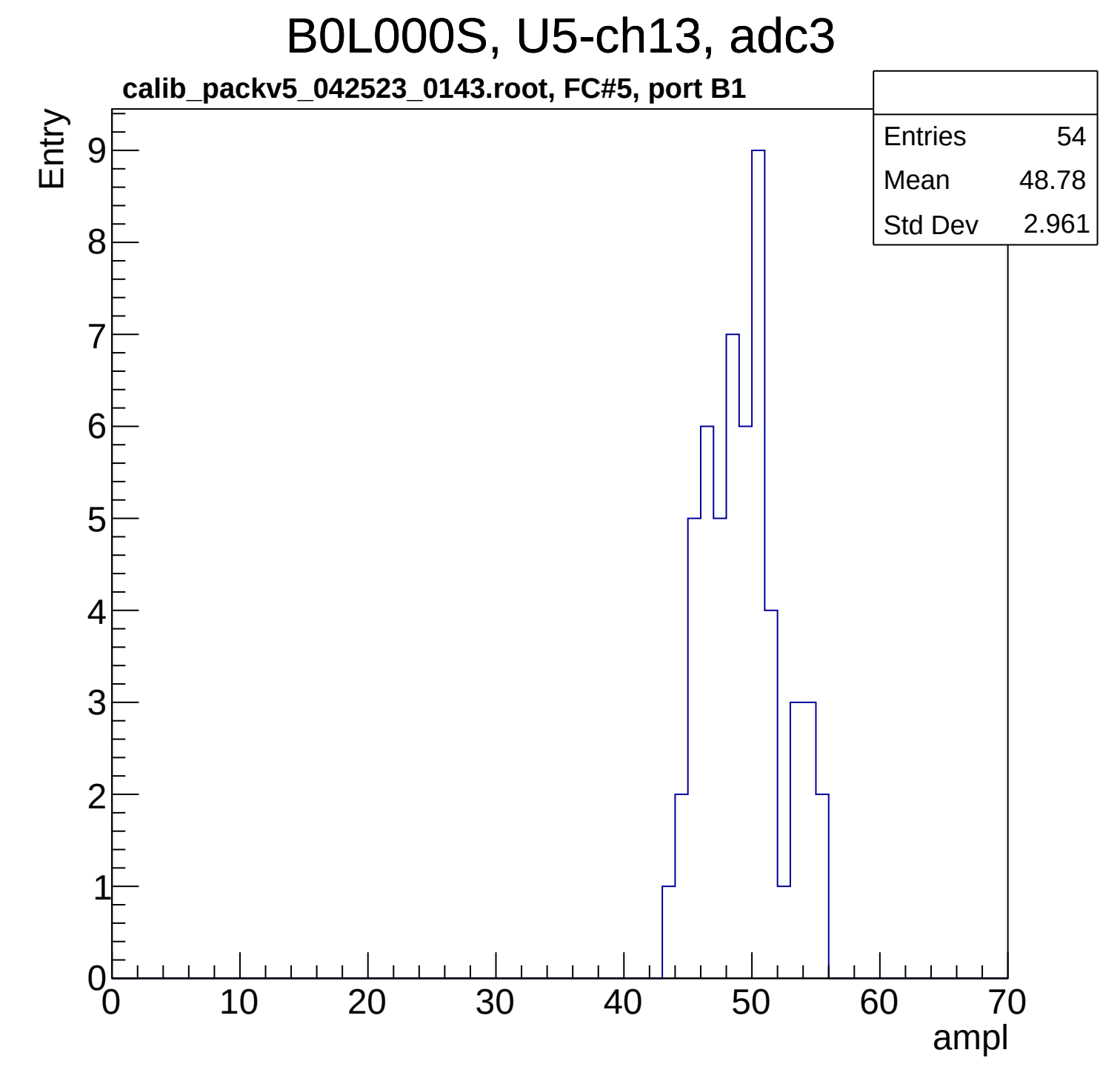
B0L000S, U5-ch13, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 48.78
Std Dev 2.961
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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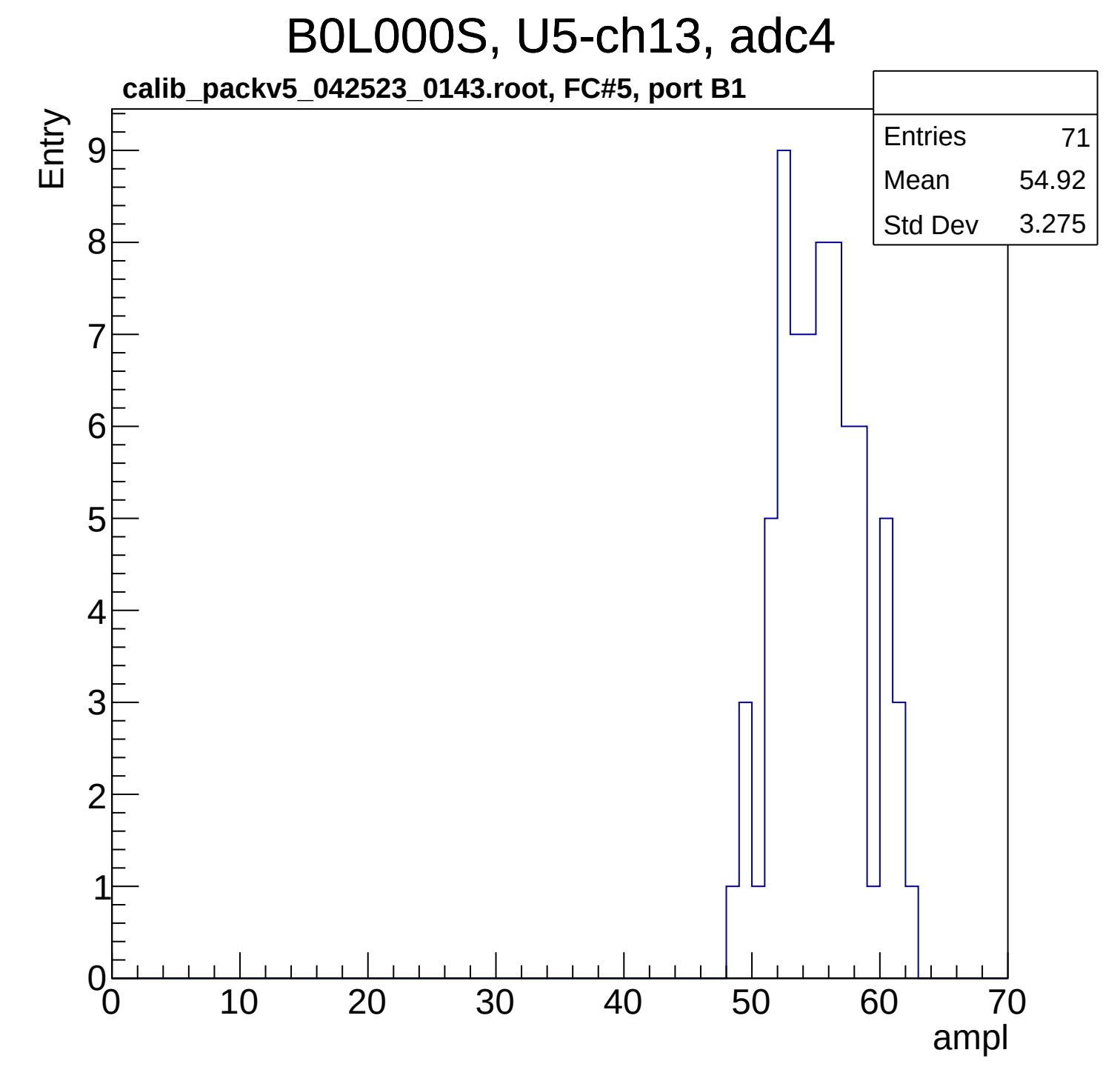
B0L000S, U5-ch13, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 54.92
Std Dev 3.275
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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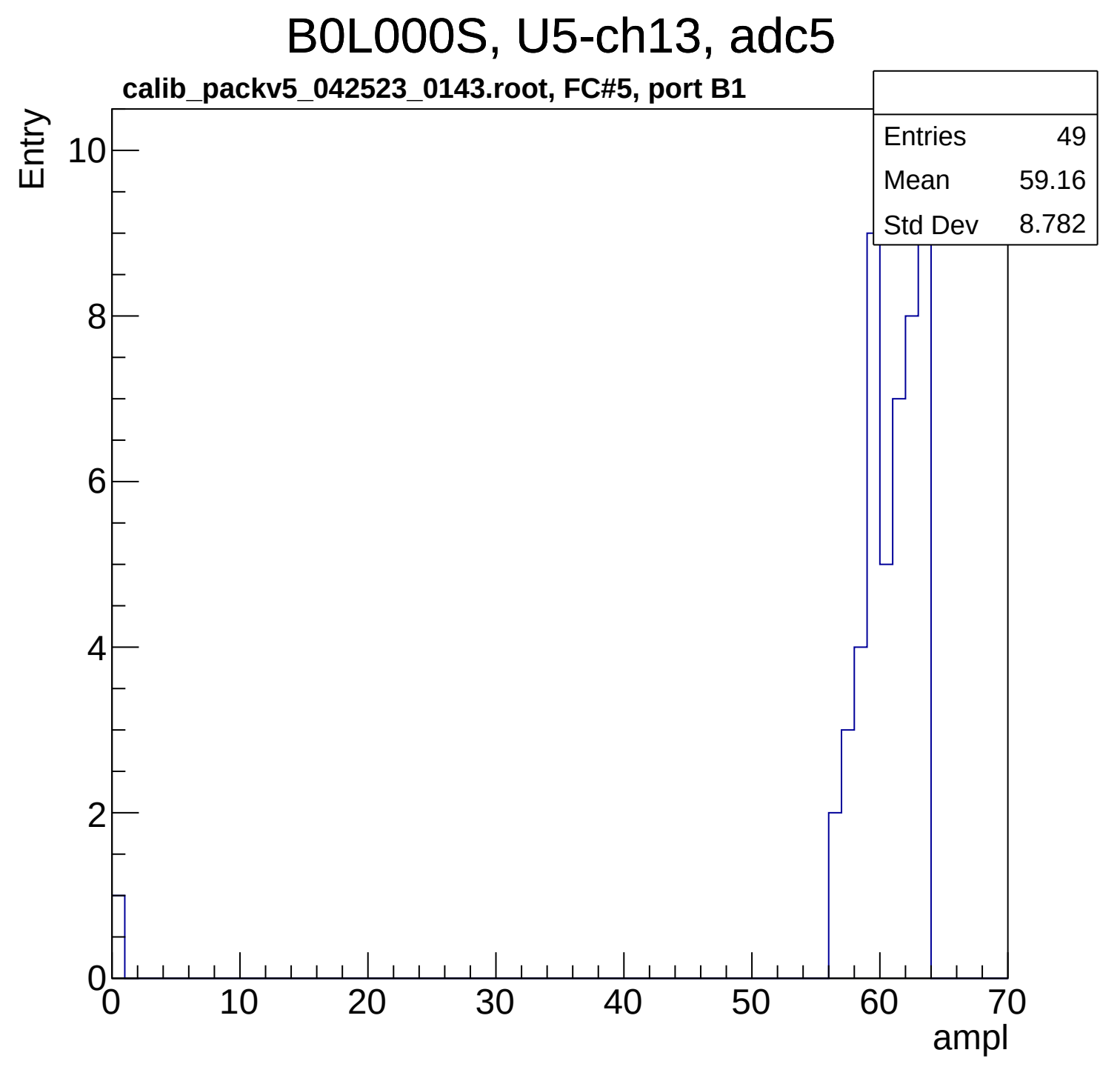
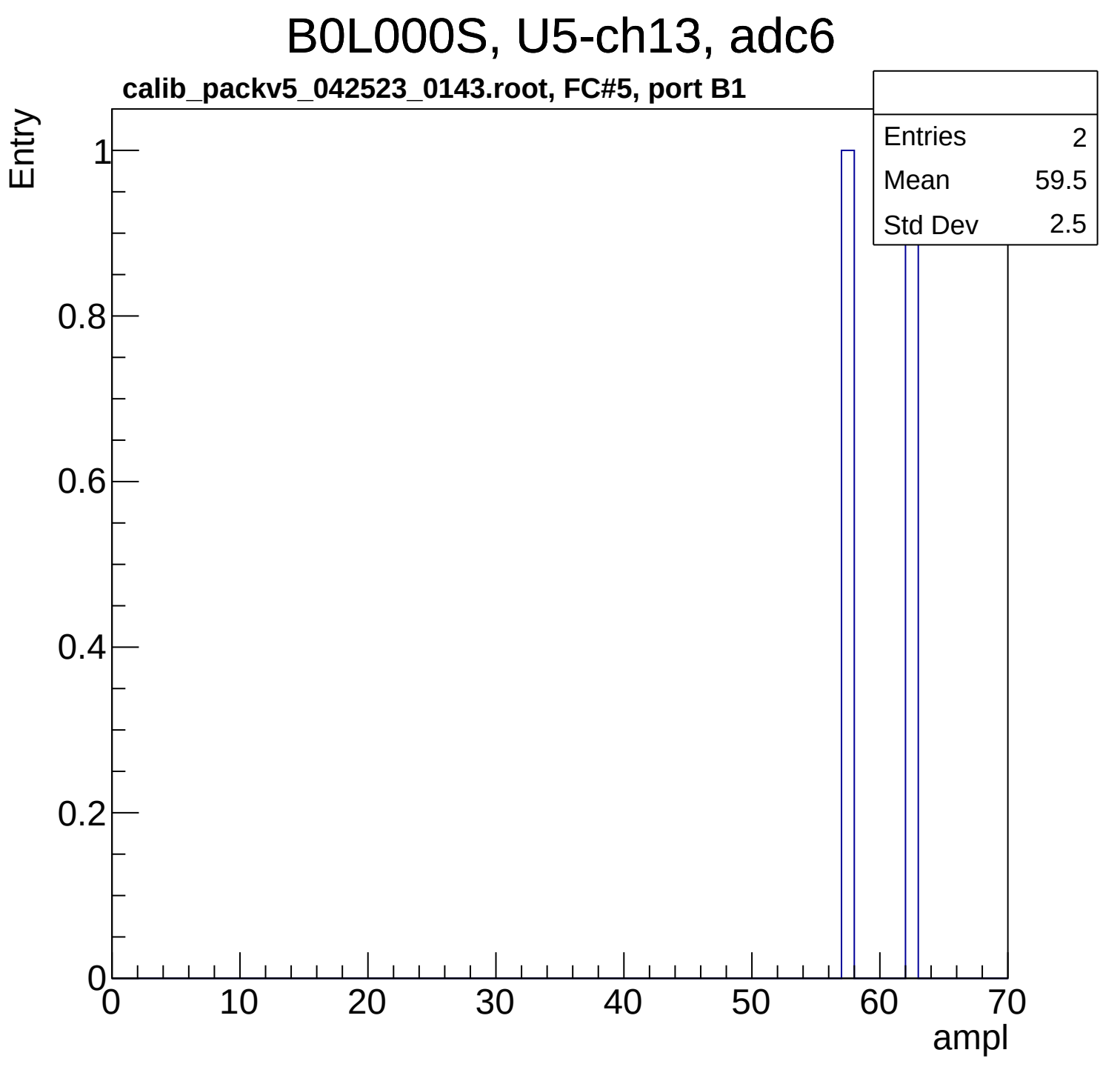

B0L000S, U5-ch13, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.16
Std Dev 8.782
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch13, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 59.5
Std Dev 2.5
Entry
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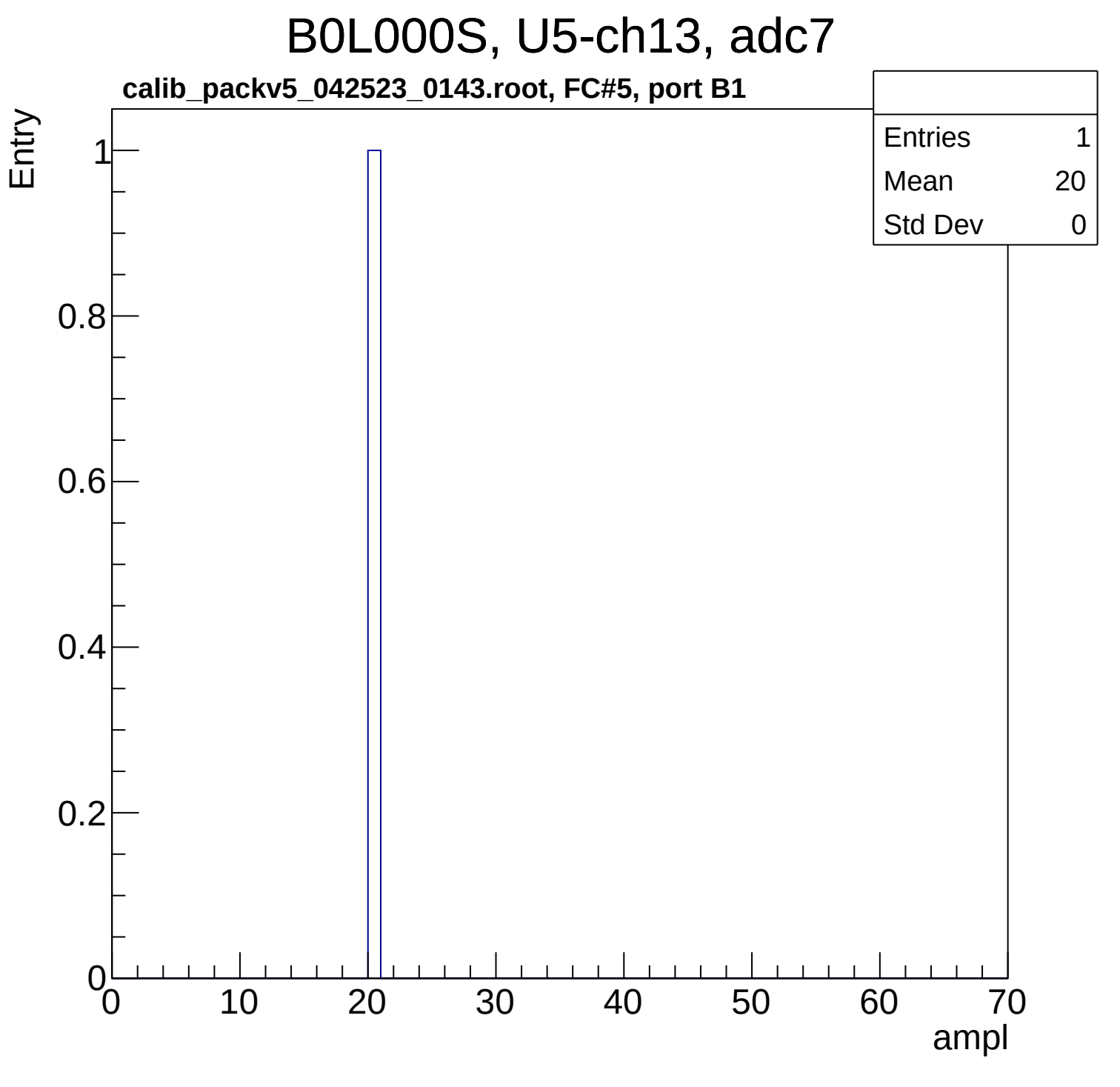
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch13, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 20
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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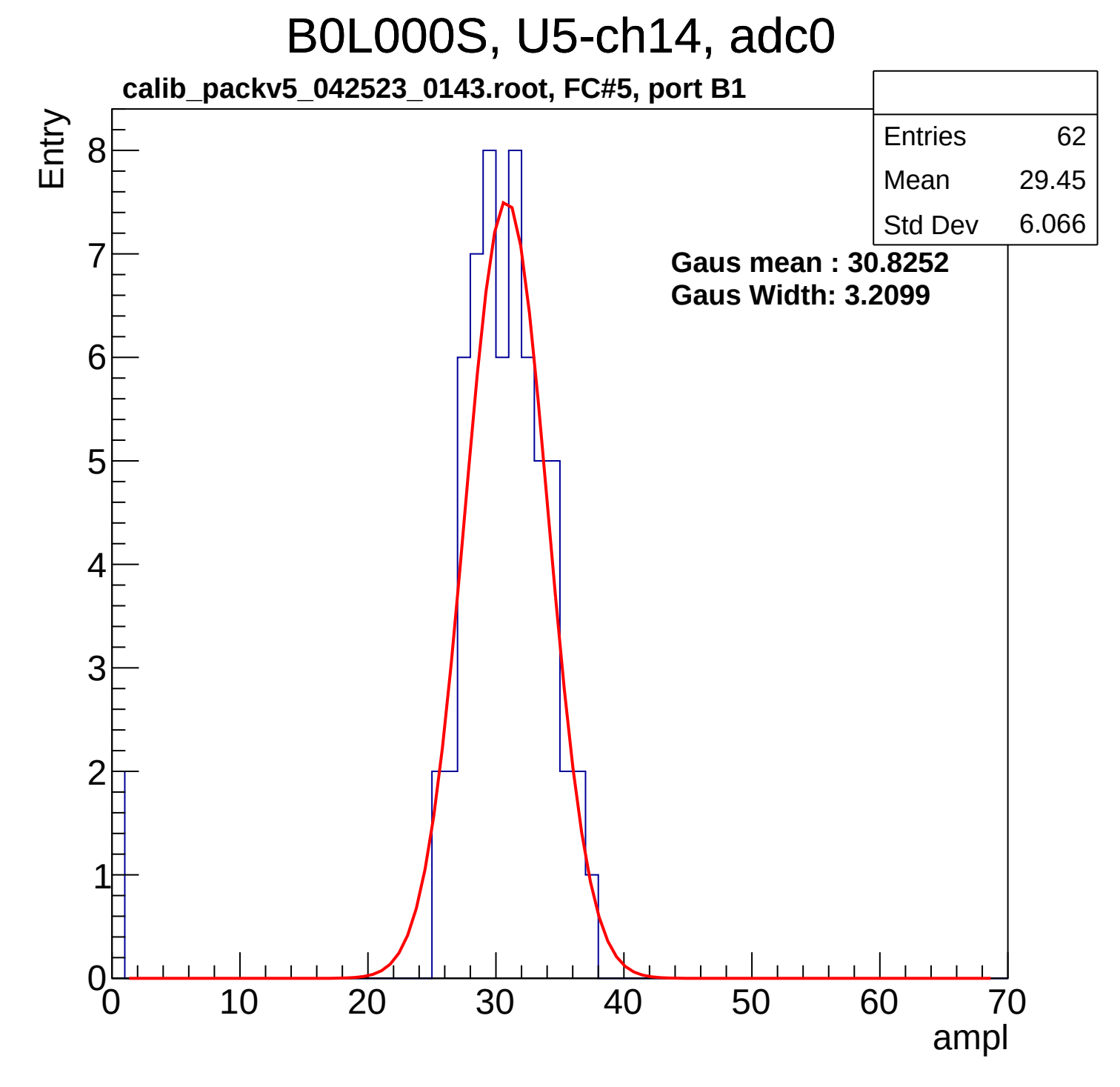
50
60
70
ampl


B0L000S, U5-ch14, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 29.45
Std Dev 6.066
Gaus mean : 30.8252
Gaus Width: 3.2099
Entry
ampl
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4
5
6
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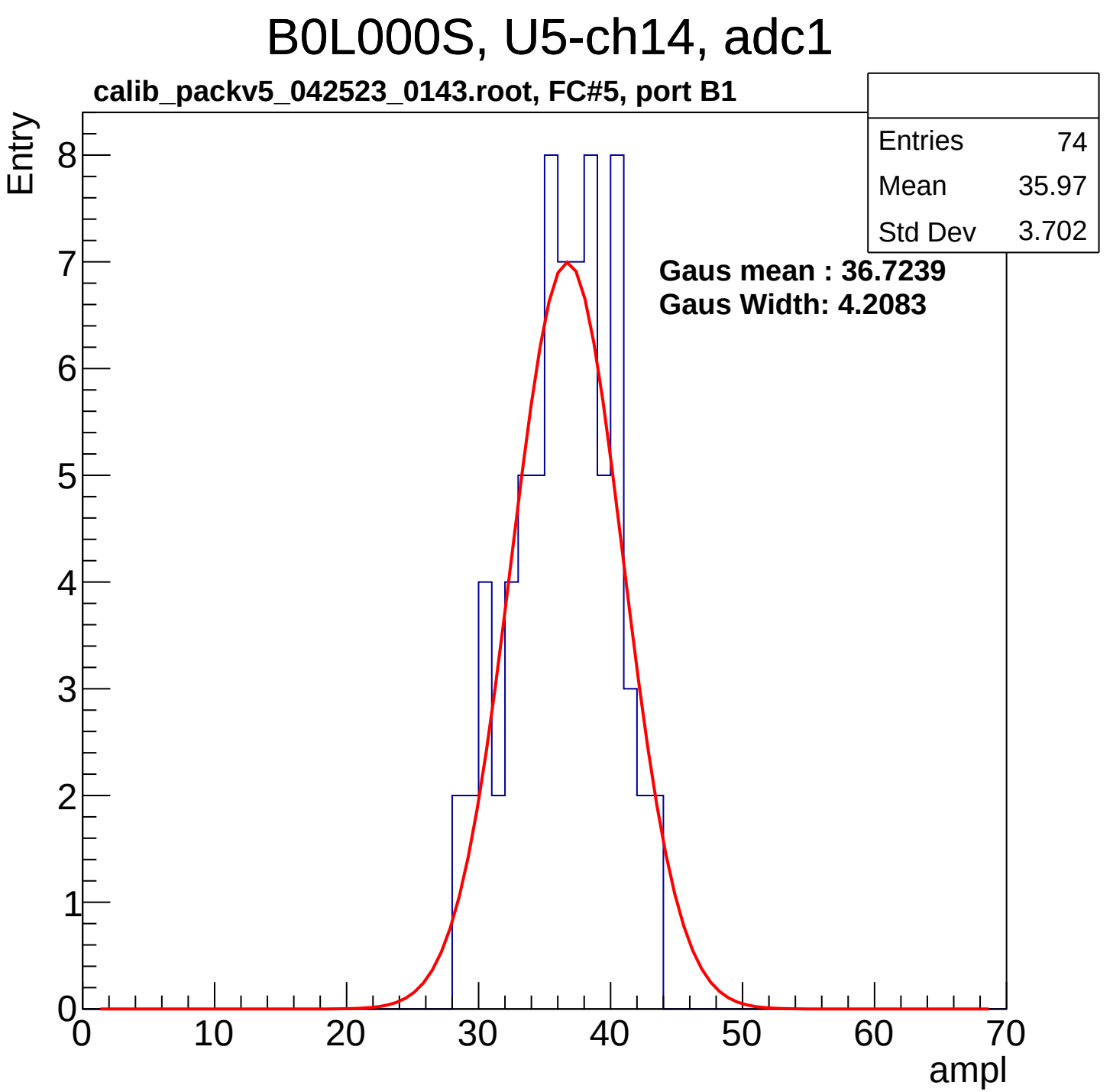
B0L000S, U5-ch14, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 35.97
Std Dev 3.702
Gaus mean : 36.7239
Gaus Width: 4.2083
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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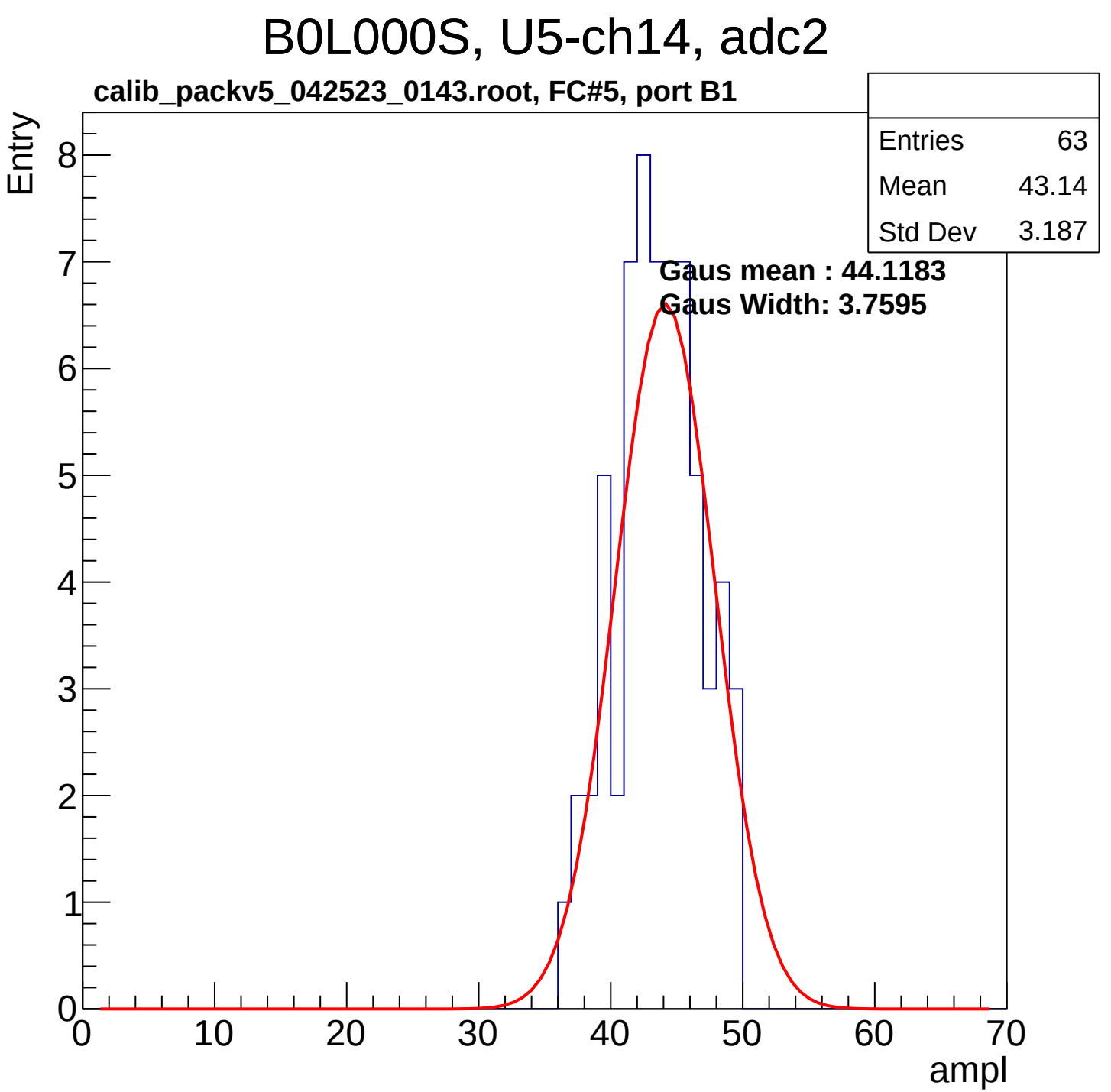
B0L000S, U5-ch14, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.14
Std Dev 3.187
Gaus mean : 44.1183
Gaus Width: 3.7595
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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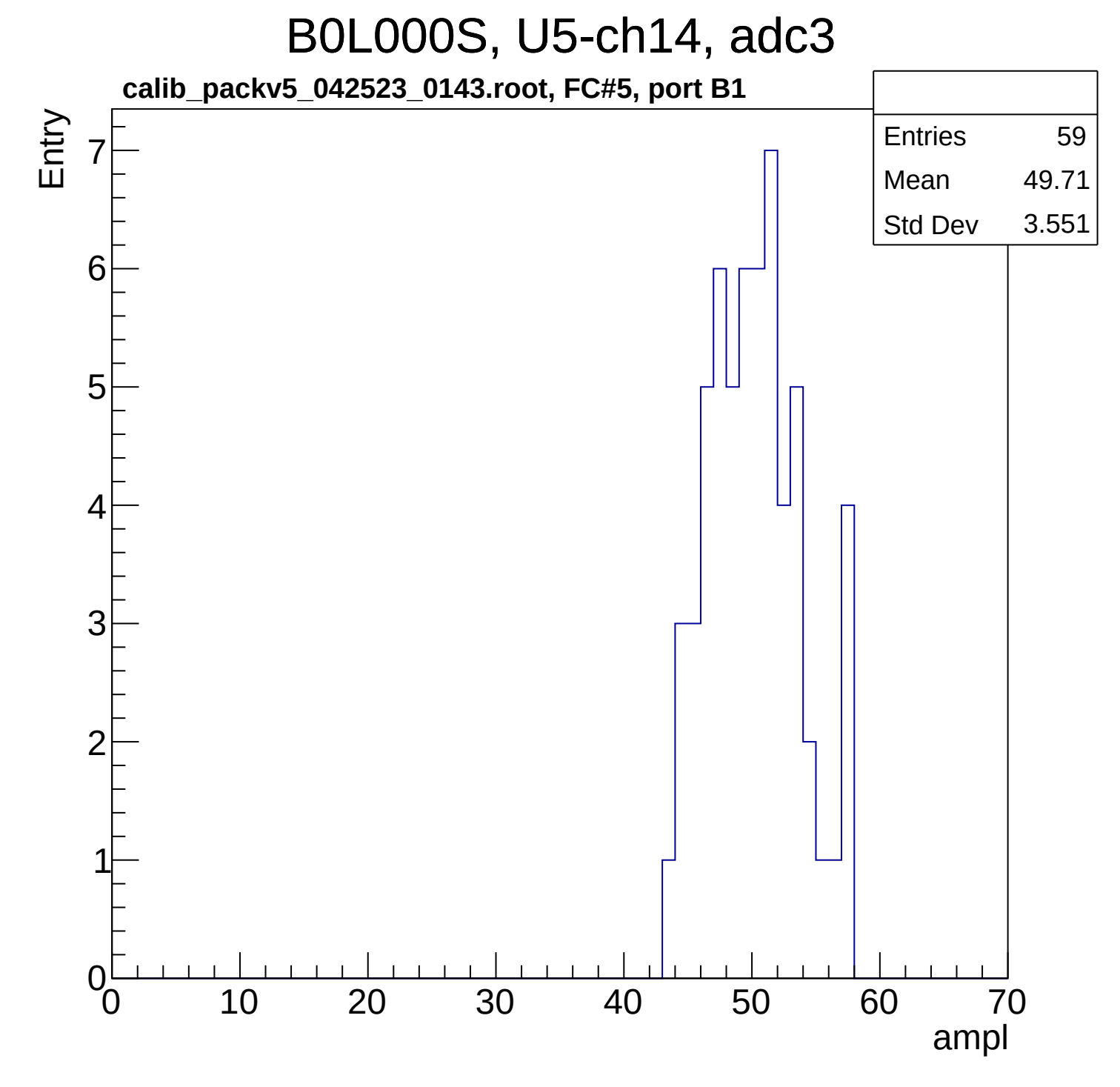
B0L000S, U5-ch14, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 49.71
Std Dev 3.551
Entry
ampl
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60
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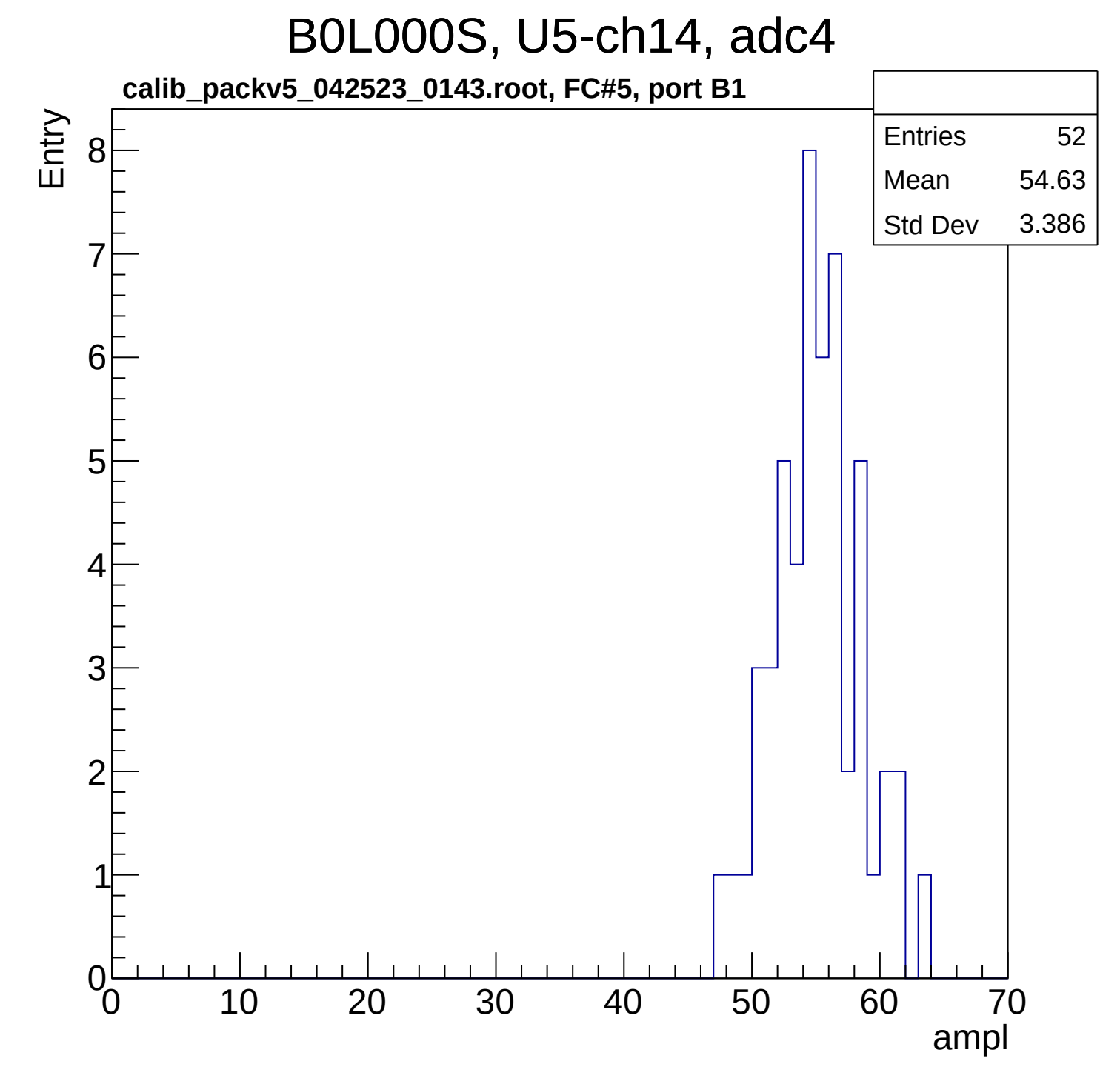
B0L000S, U5-ch14, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 54.63
Std Dev 3.386
Entry
ampl
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3
4
5
6
7
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0
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60
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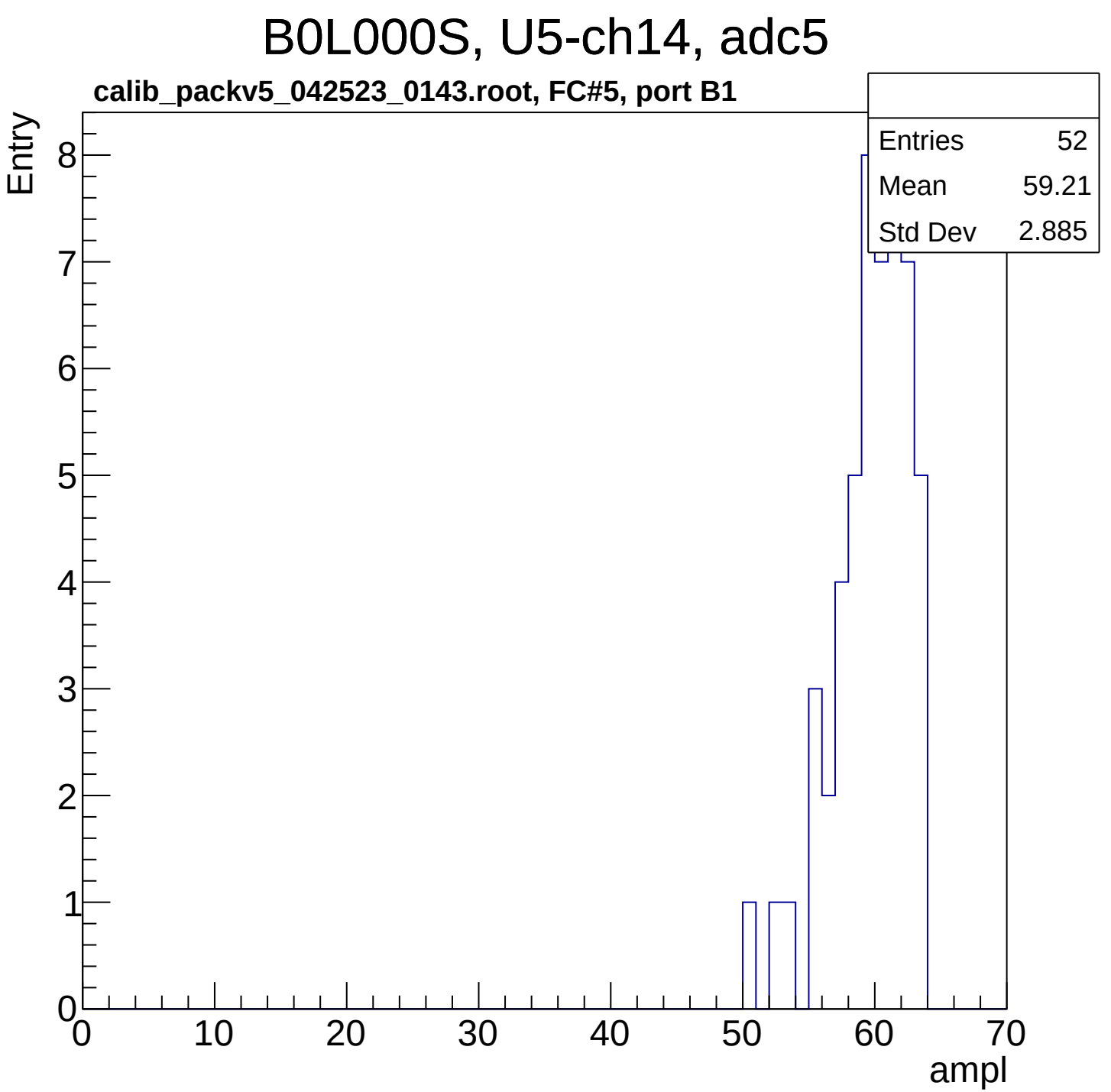
B0L000S, U5-ch14, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.21
Std Dev 2.885
Entry
ampl
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2
3
4
5
6
7
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0
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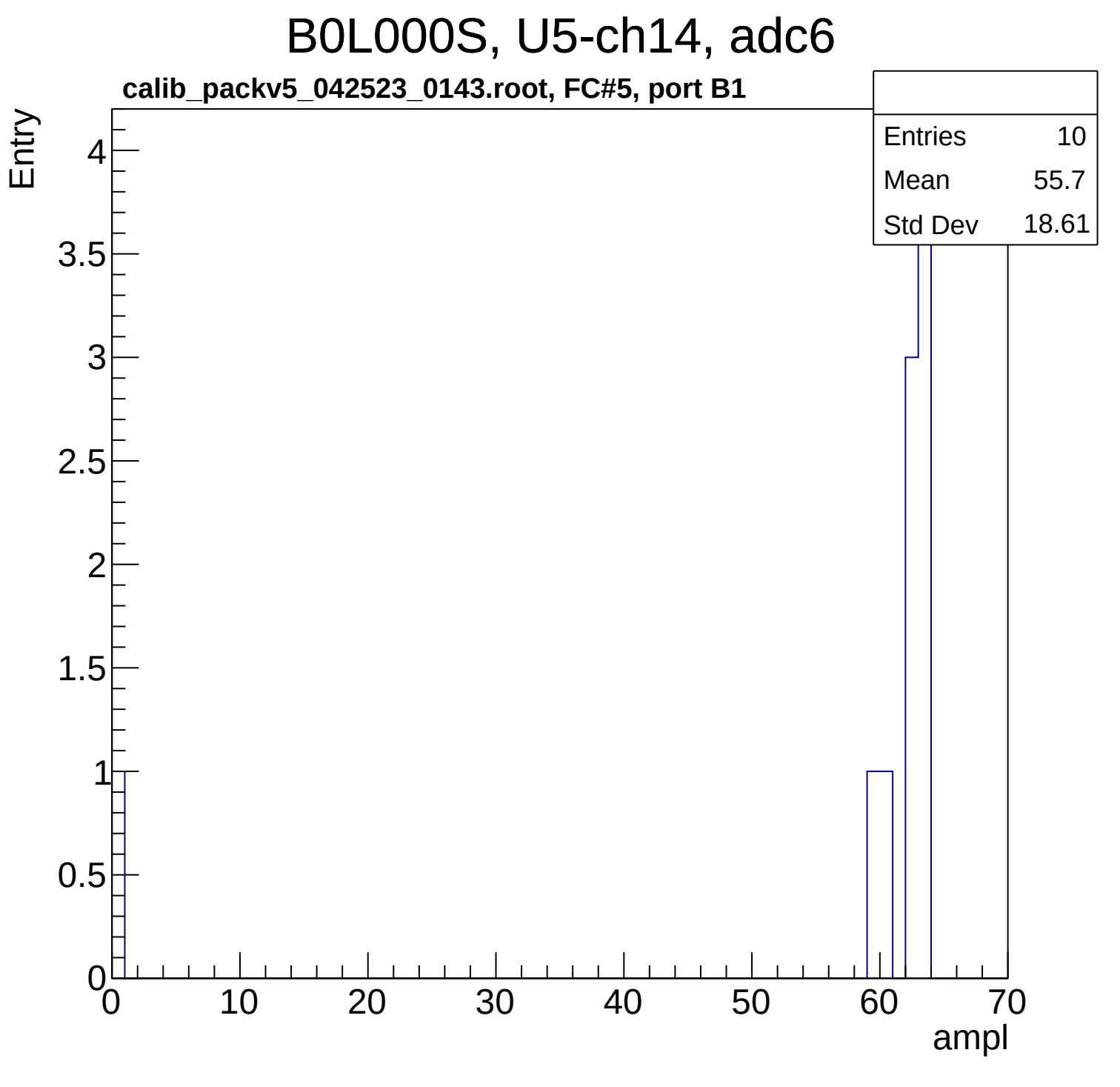
B0L000S, U5-ch14, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.7
Std Dev 18.61
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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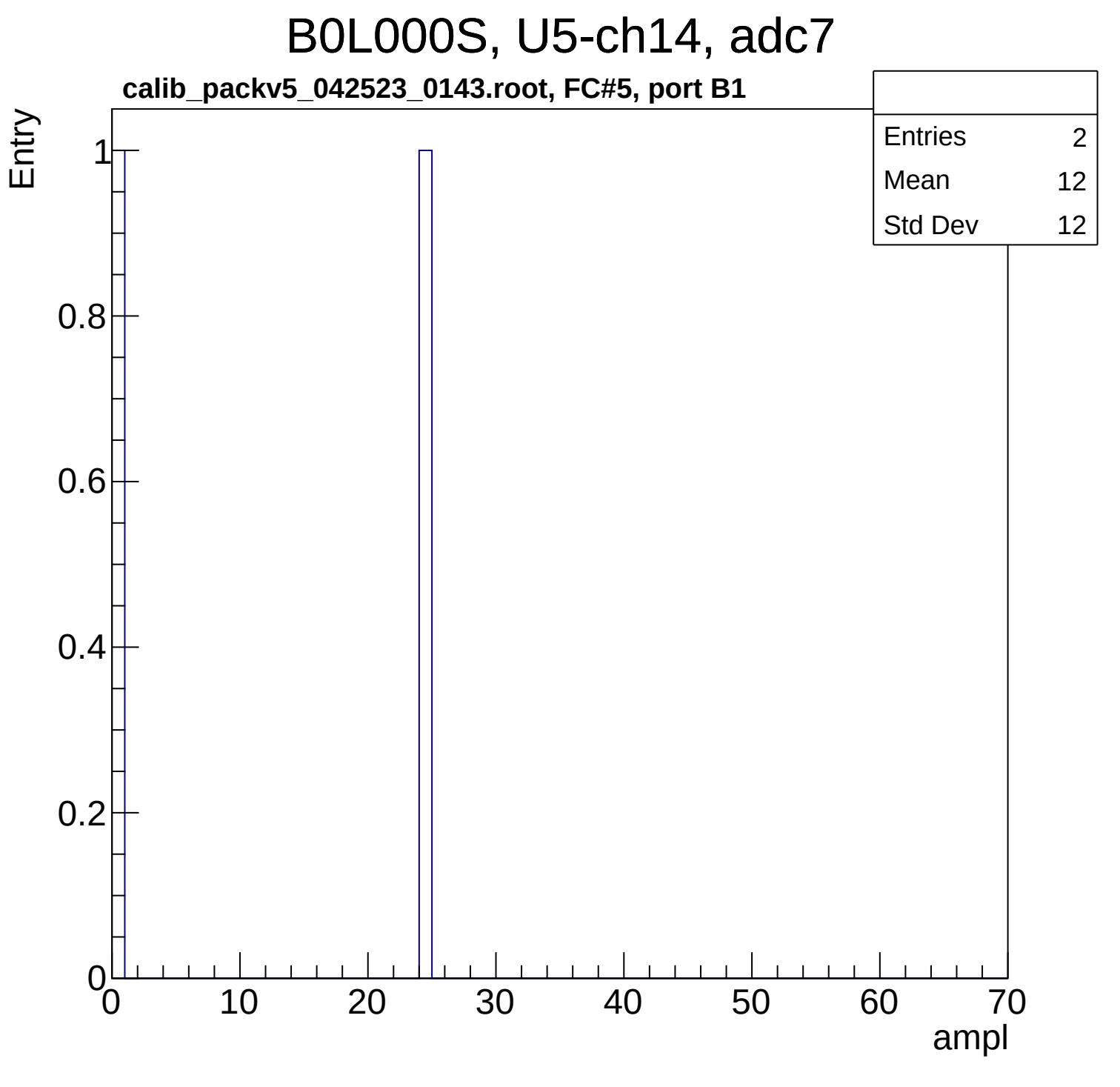
B0L000S, U5-ch14, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
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10
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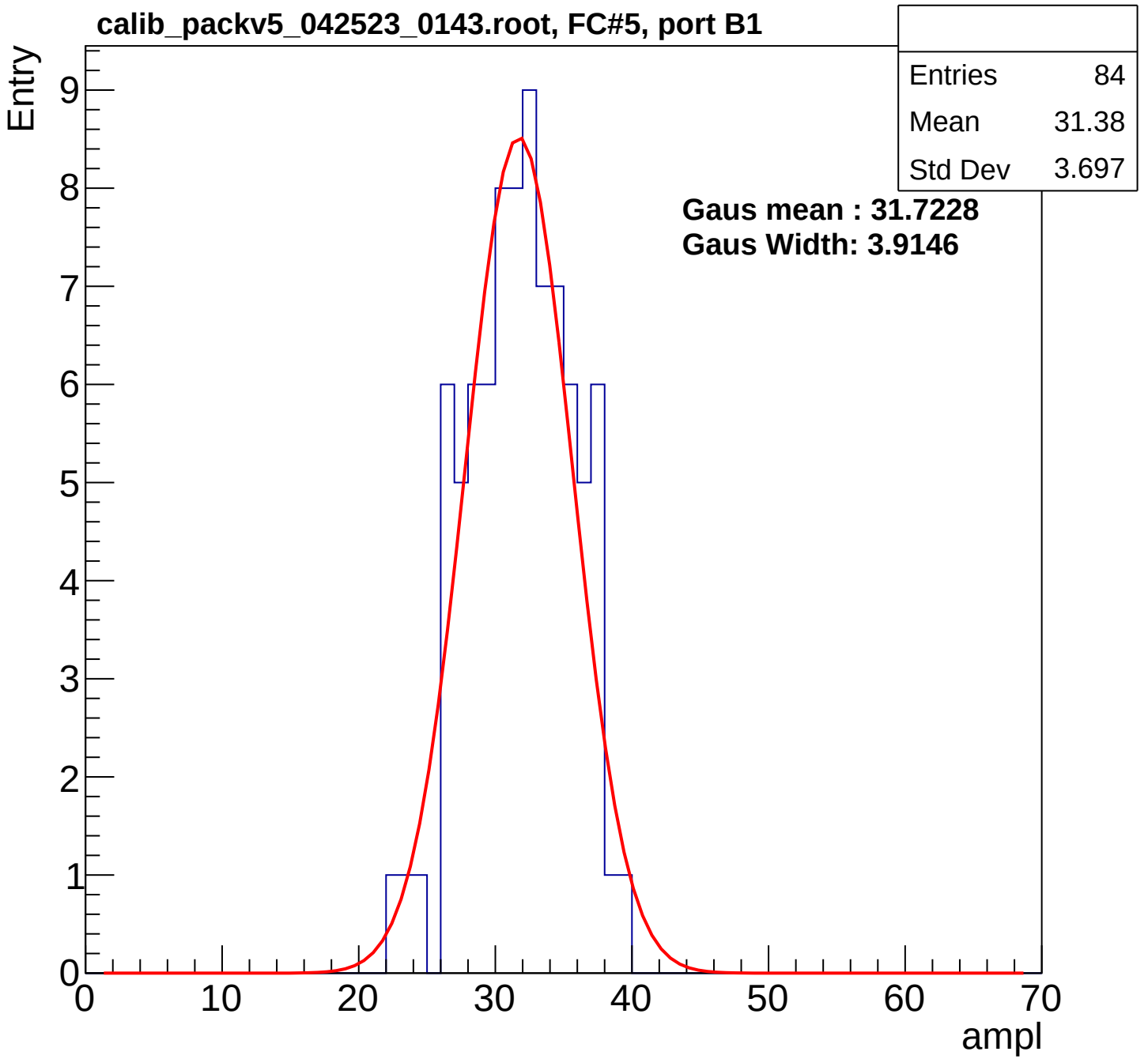
50
60
70
ampl


B0L000S, U5-ch15, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 31.38
Std Dev 3.697
Gaus mean : 31.7228
Gaus Width: 3.9146
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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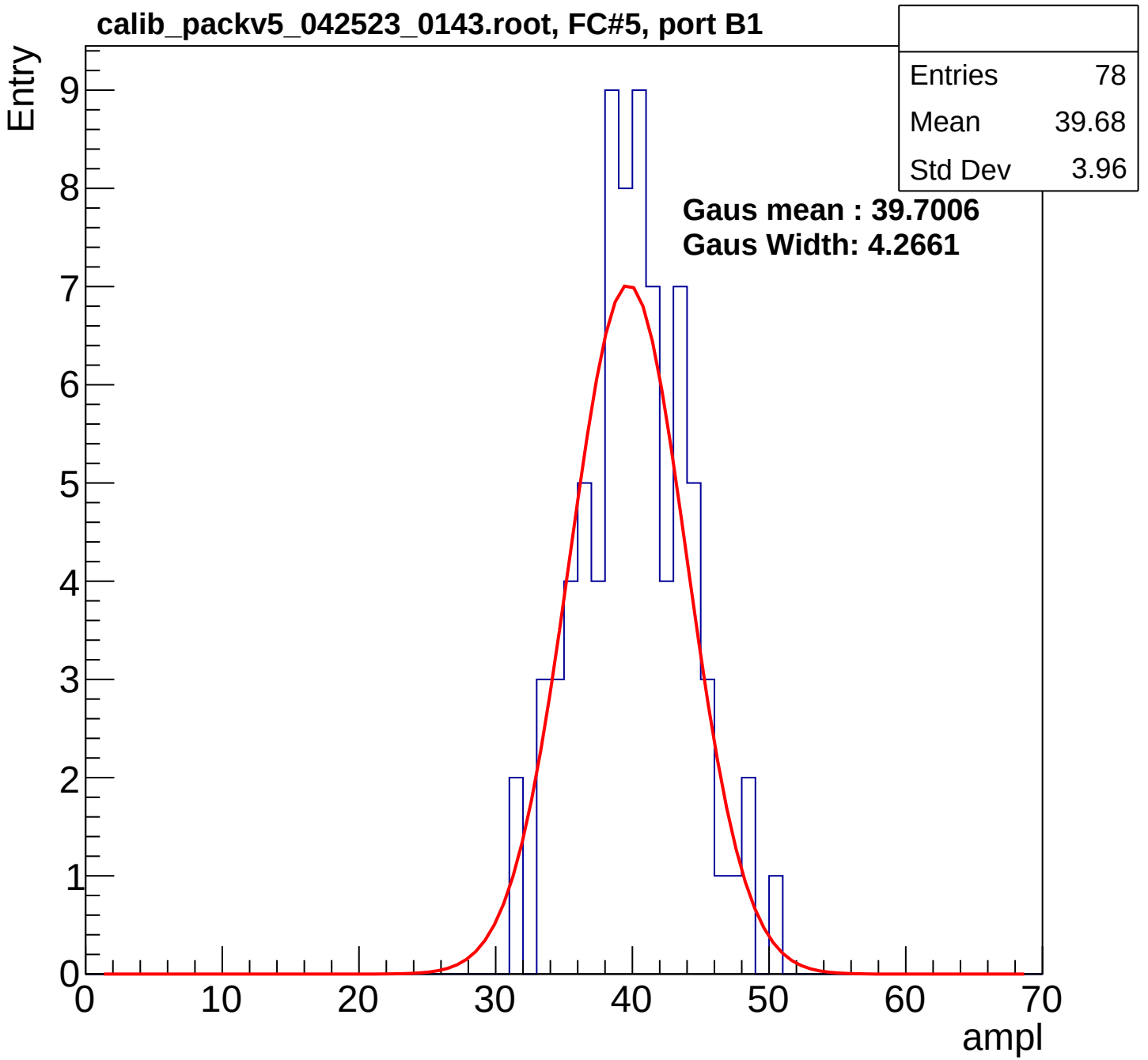
B0L000S, U5-ch15, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 39.68
Std Dev 3.96
Gaus mean : 39.7006
Gaus Width: 4.2661
Entry
ampl
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2
3
4
5
6
7
8
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0
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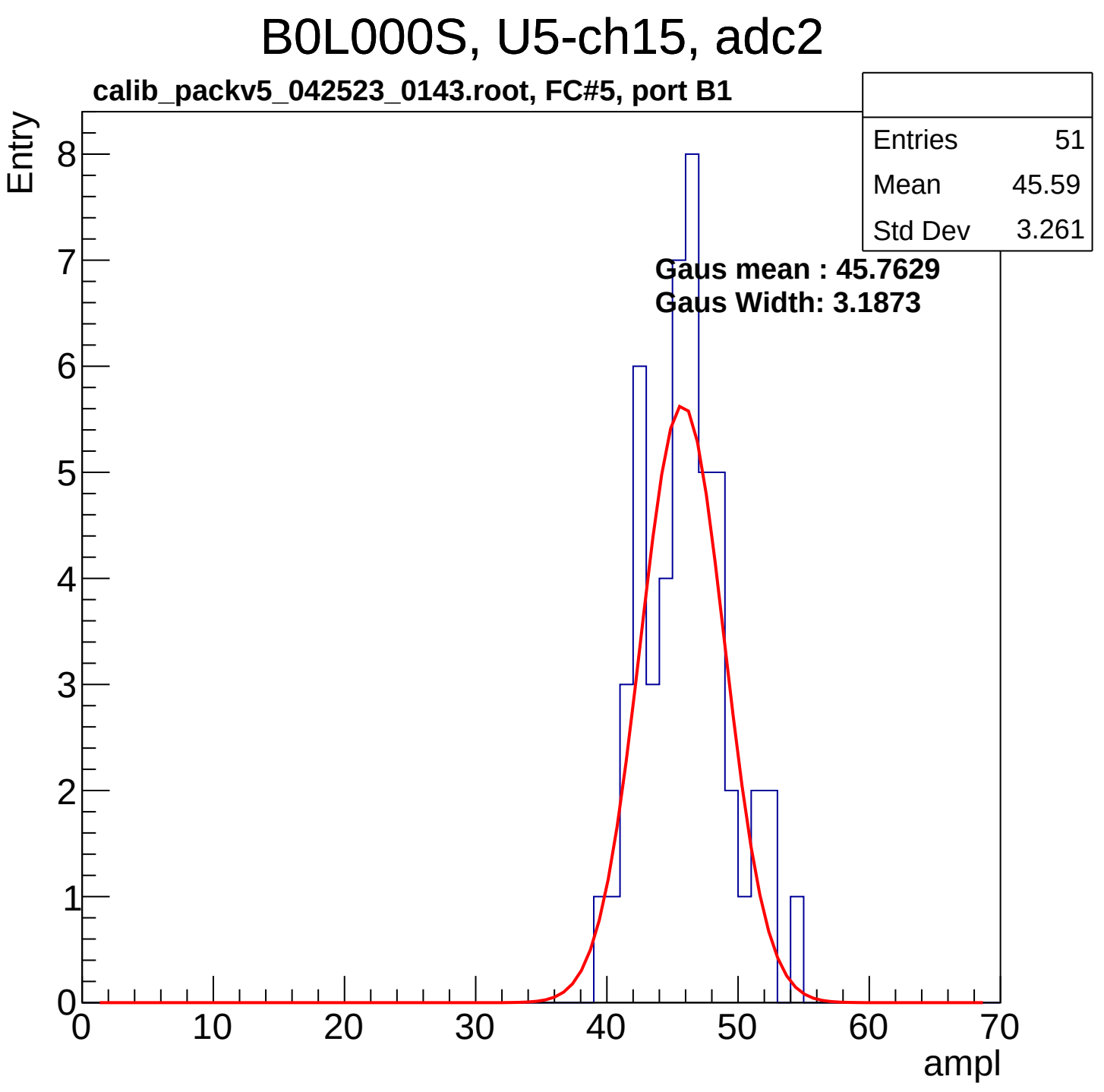
B0L000S, U5-ch15, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 45.59
Std Dev 3.261
Gaus mean : 45.7629
Gaus Width: 3.1873
Entry
ampl
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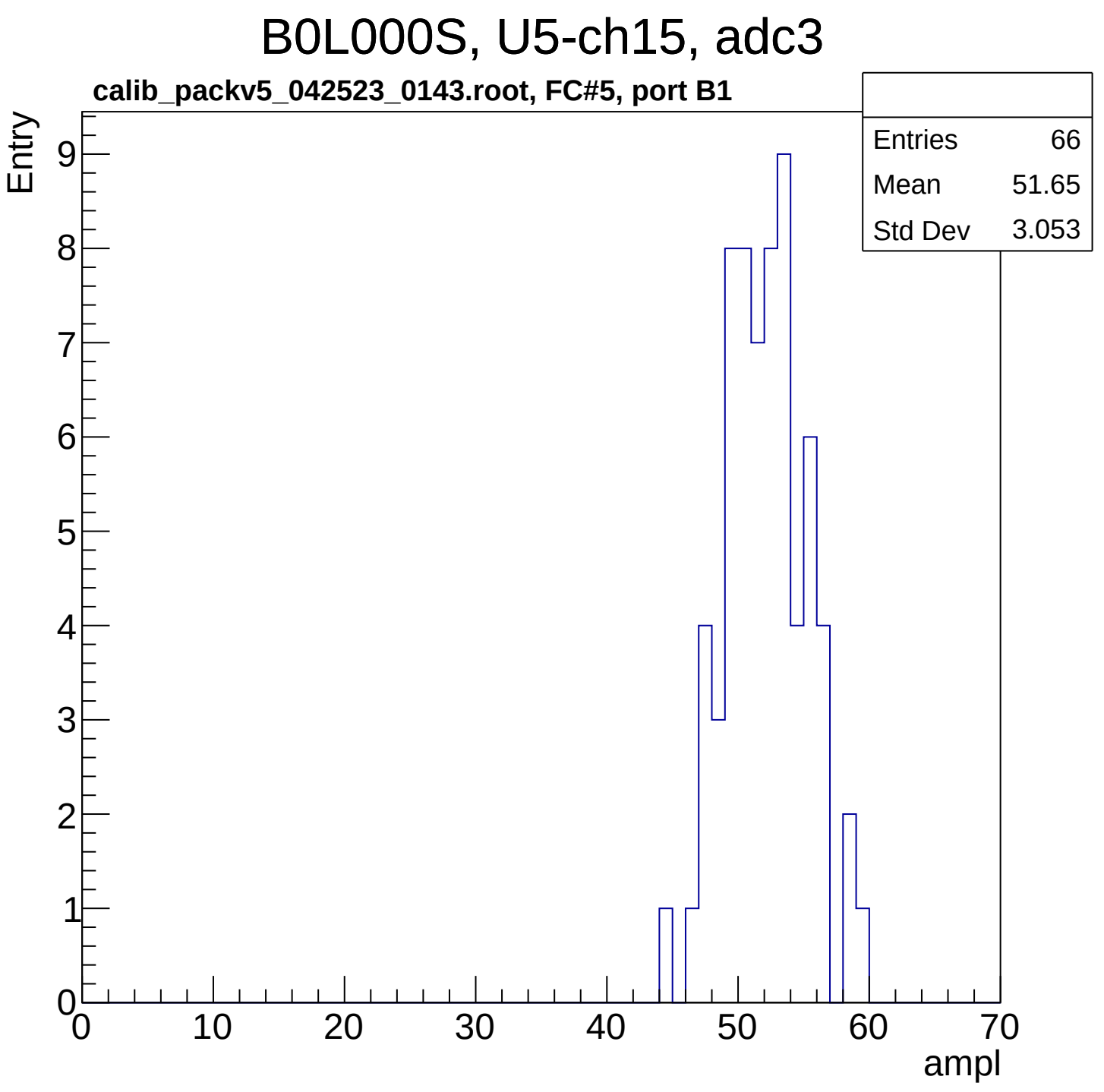
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch15, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.65
Std Dev 3.053
Entry
ampl
0
1
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0
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60
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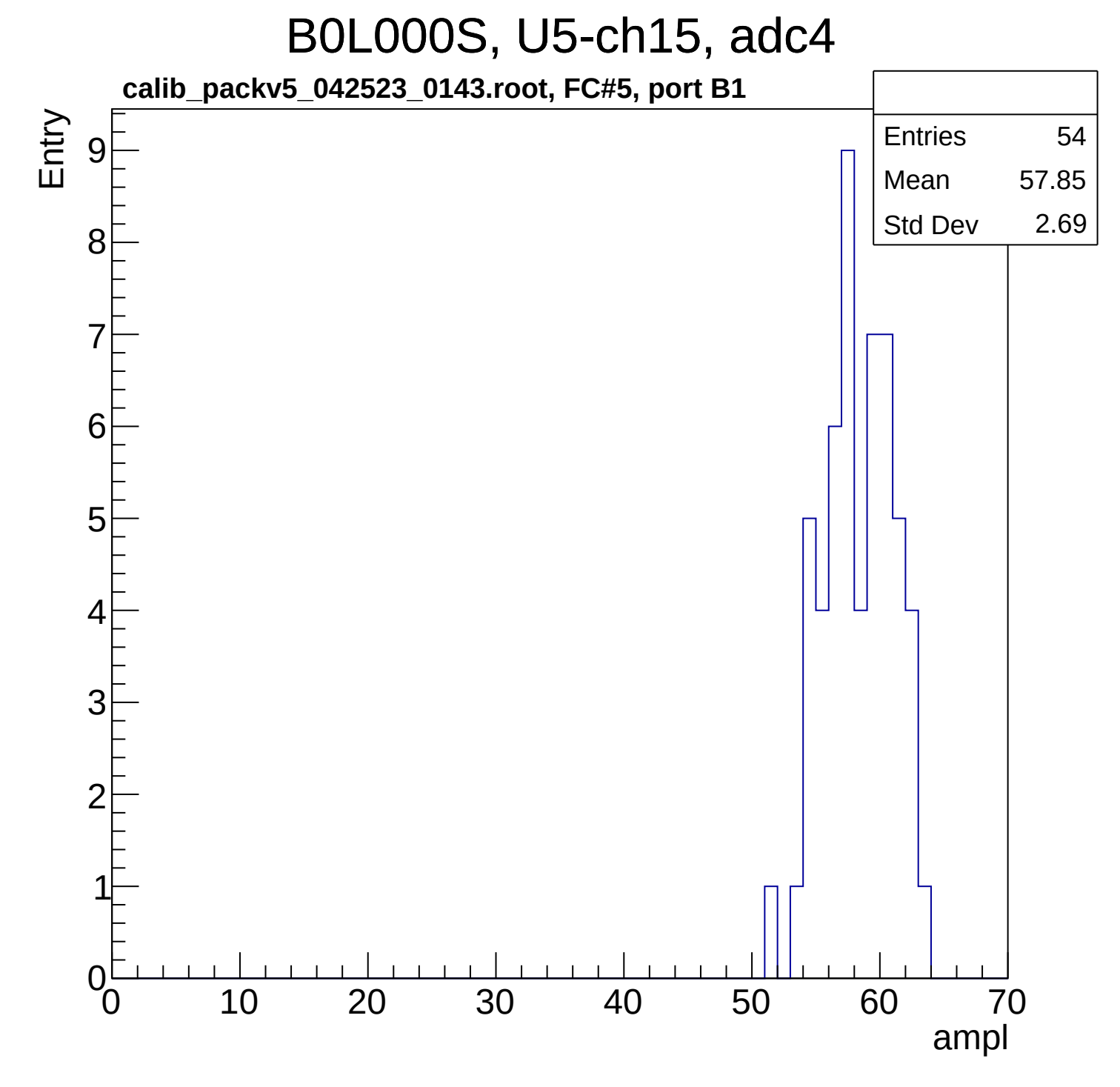
B0L000S, U5-ch15, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 57.85
Std Dev 2.69
Entry
ampl
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3
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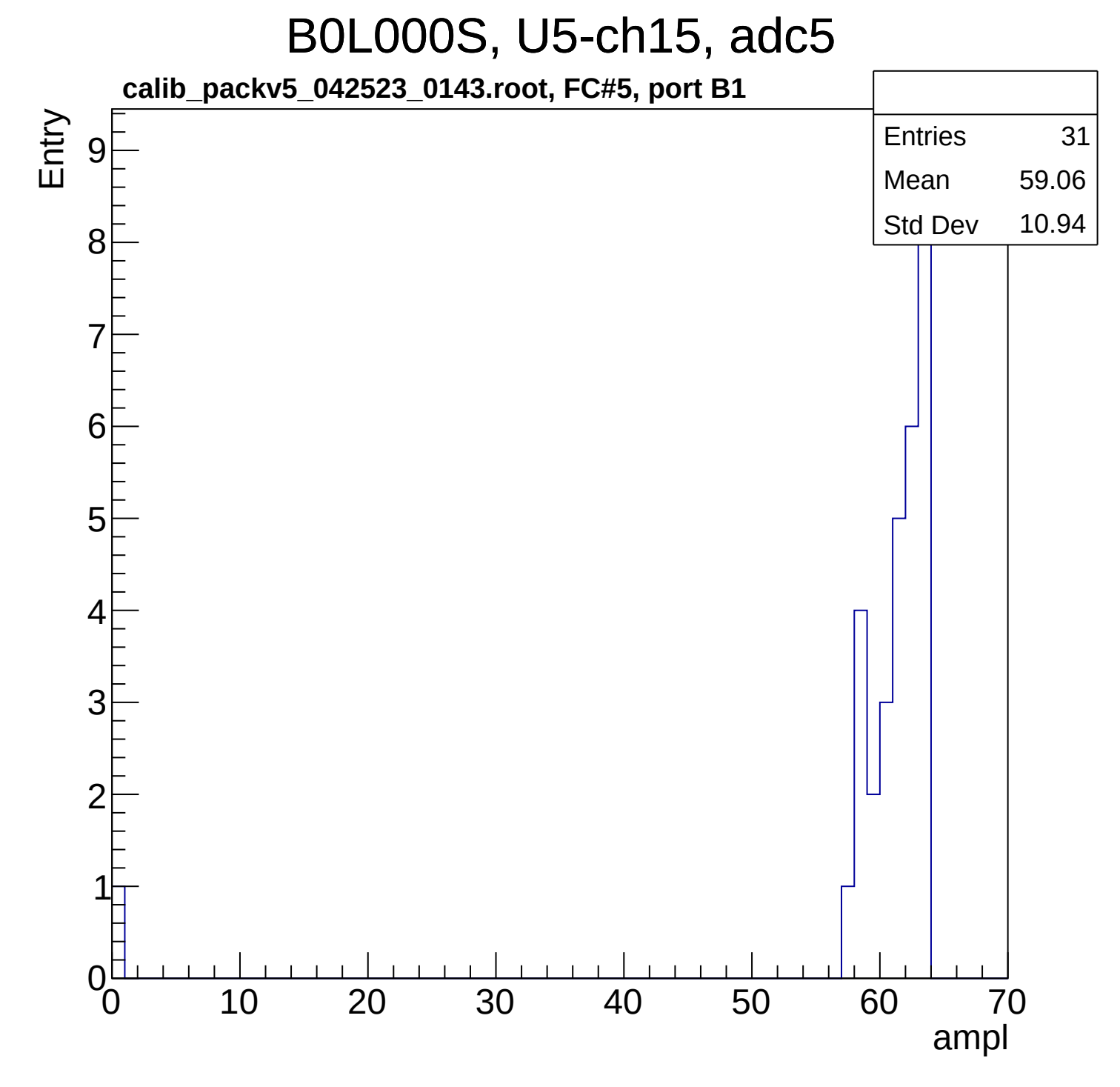
B0L000S, U5-ch15, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 59.06
Std Dev 10.94
Entry
0
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3
4
5
6
7
8
9
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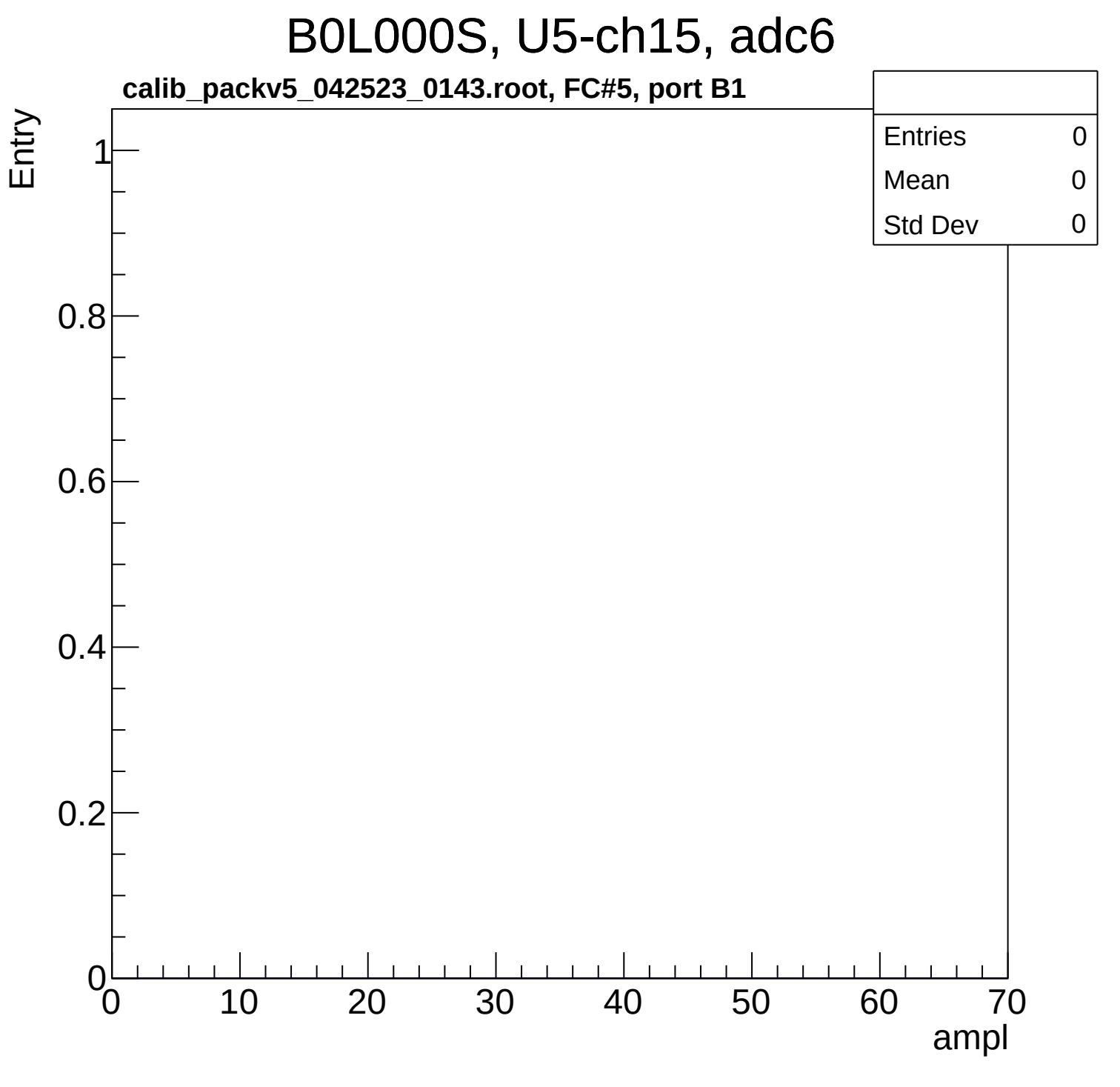
60
70
ampl


B0L000S, U5-ch15, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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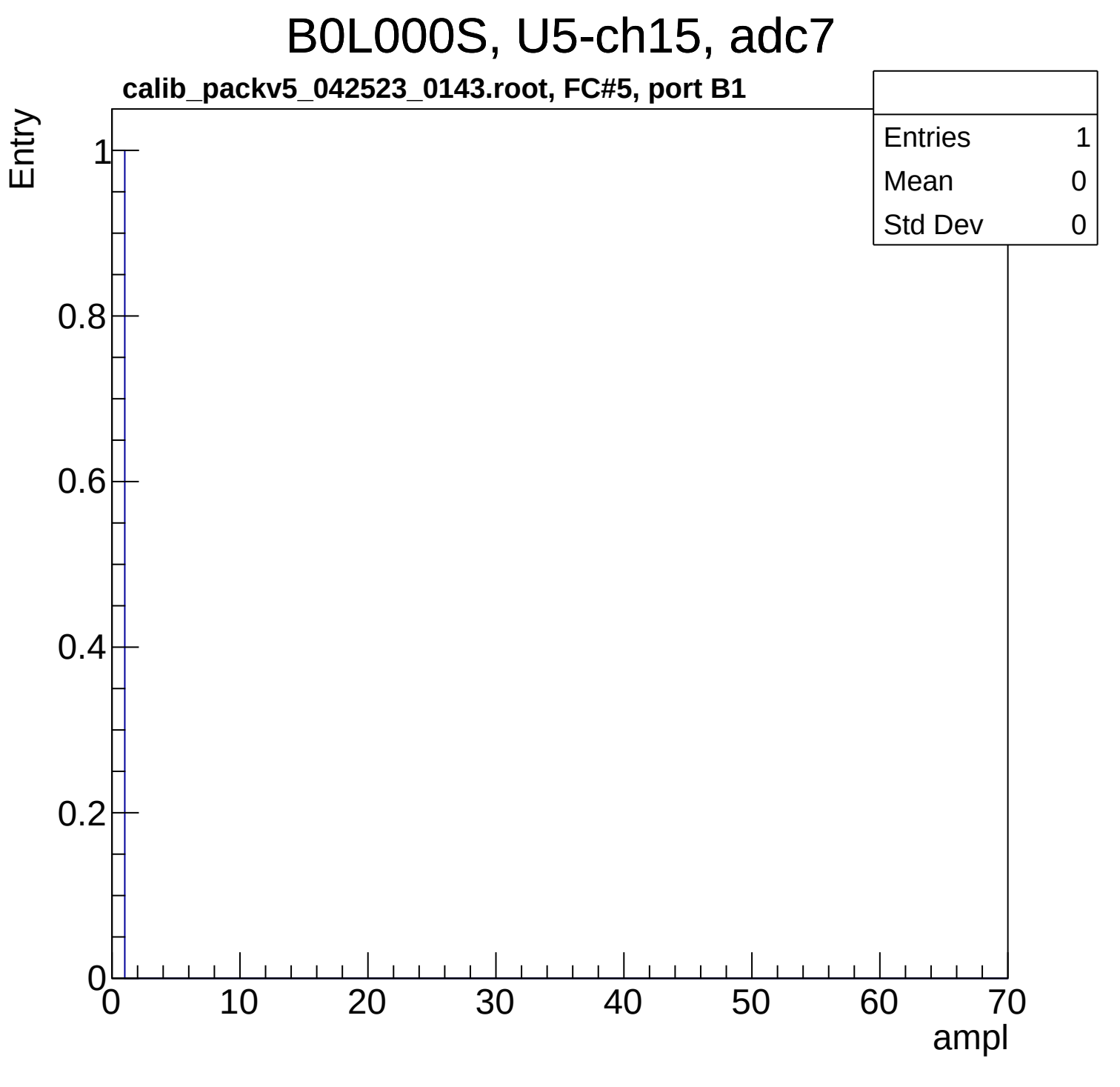
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch15, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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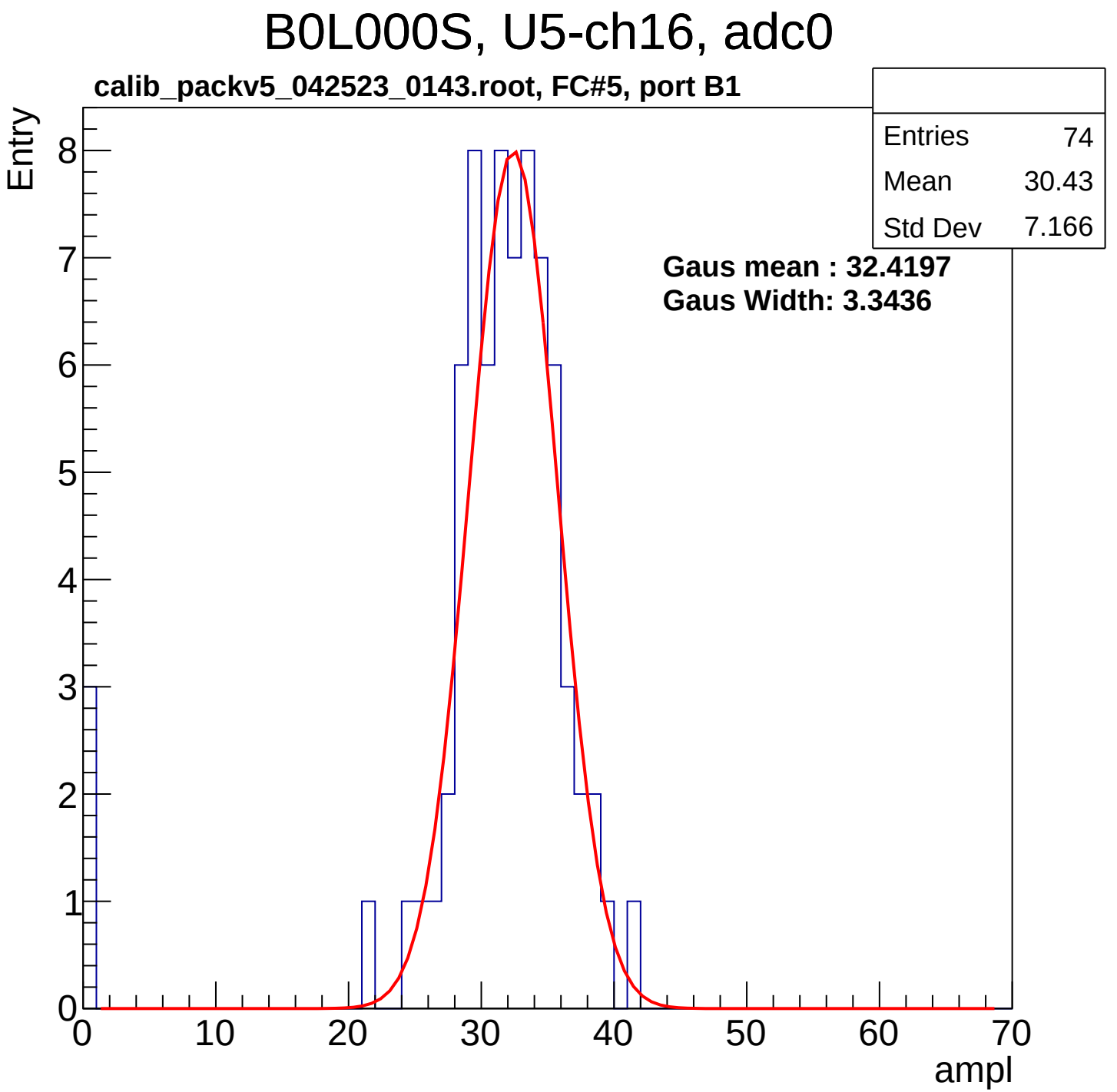
50
60
70
ampl


B0L000S, U5-ch16, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 30.43
Std Dev 7.166
Gaus mean : 32.4197
Gaus Width: 3.3436
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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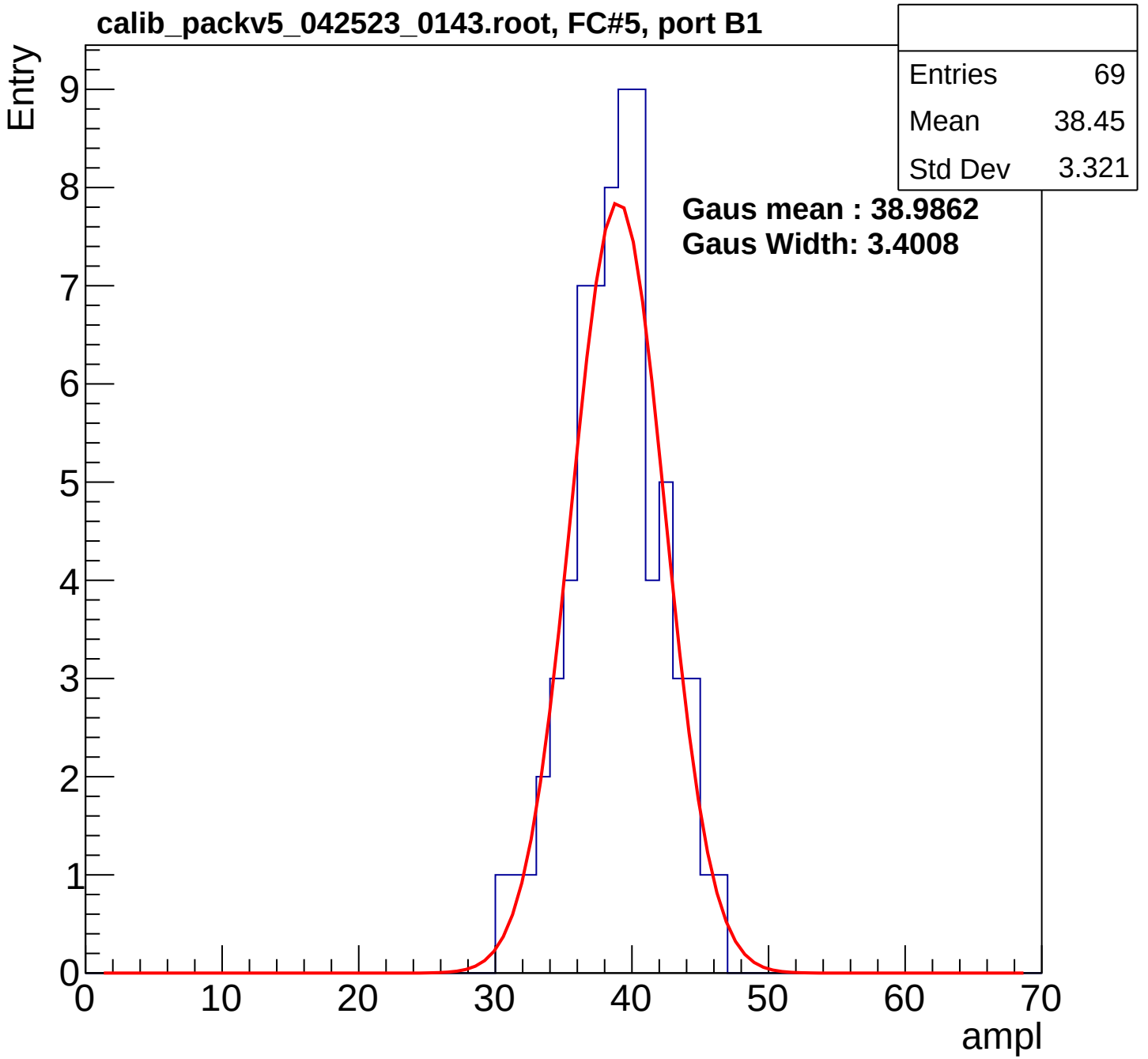
B0L000S, U5-ch16, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 38.45
Std Dev 3.321
Gaus mean : 38.9862
Gaus Width: 3.4008
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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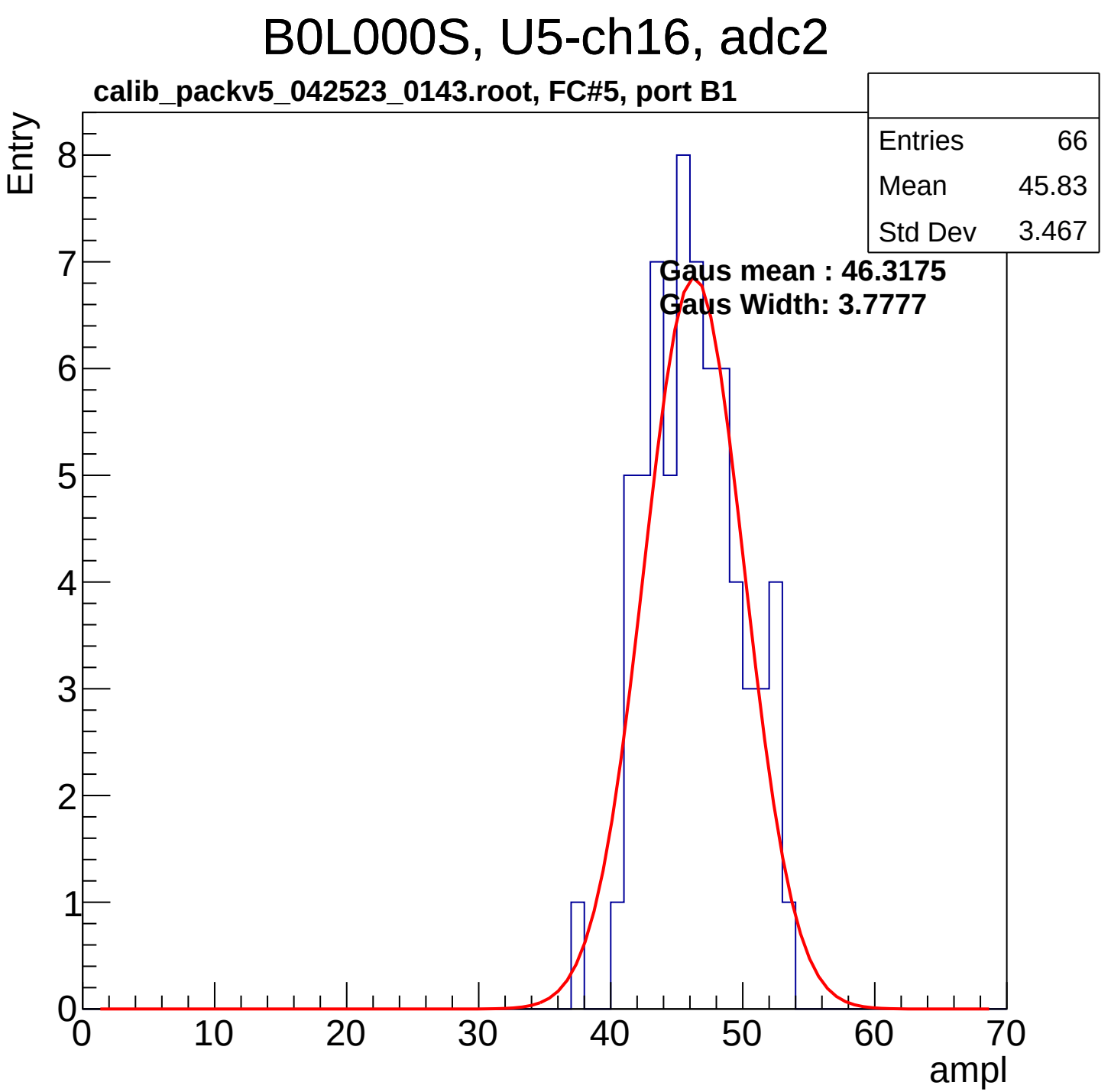
B0L000S, U5-ch16, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 45.83
Std Dev 3.467
Gaus mean : 46.3175
Gaus Width: 3.7777
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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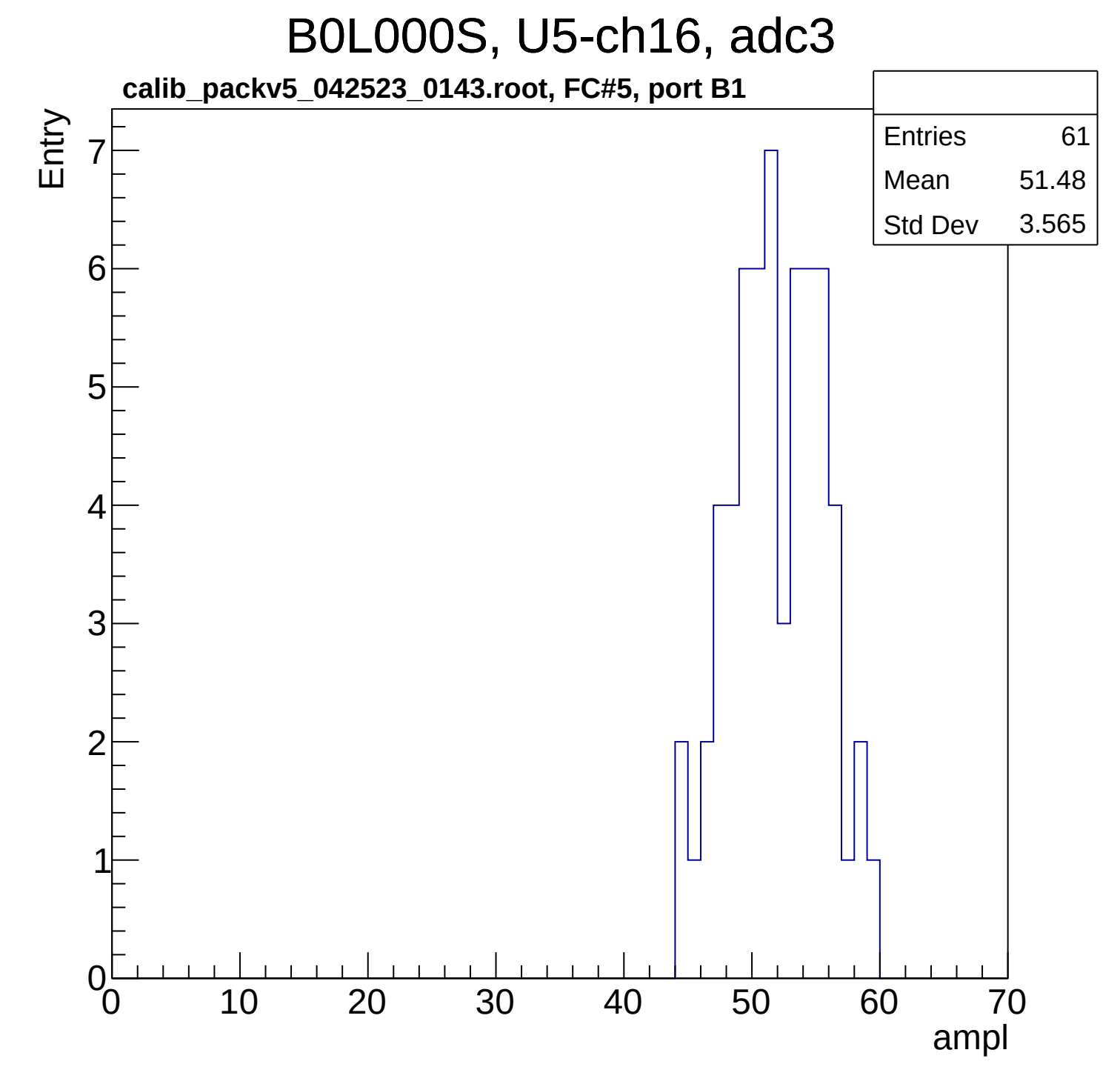
B0L000S, U5-ch16, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 51.48
Std Dev 3.565
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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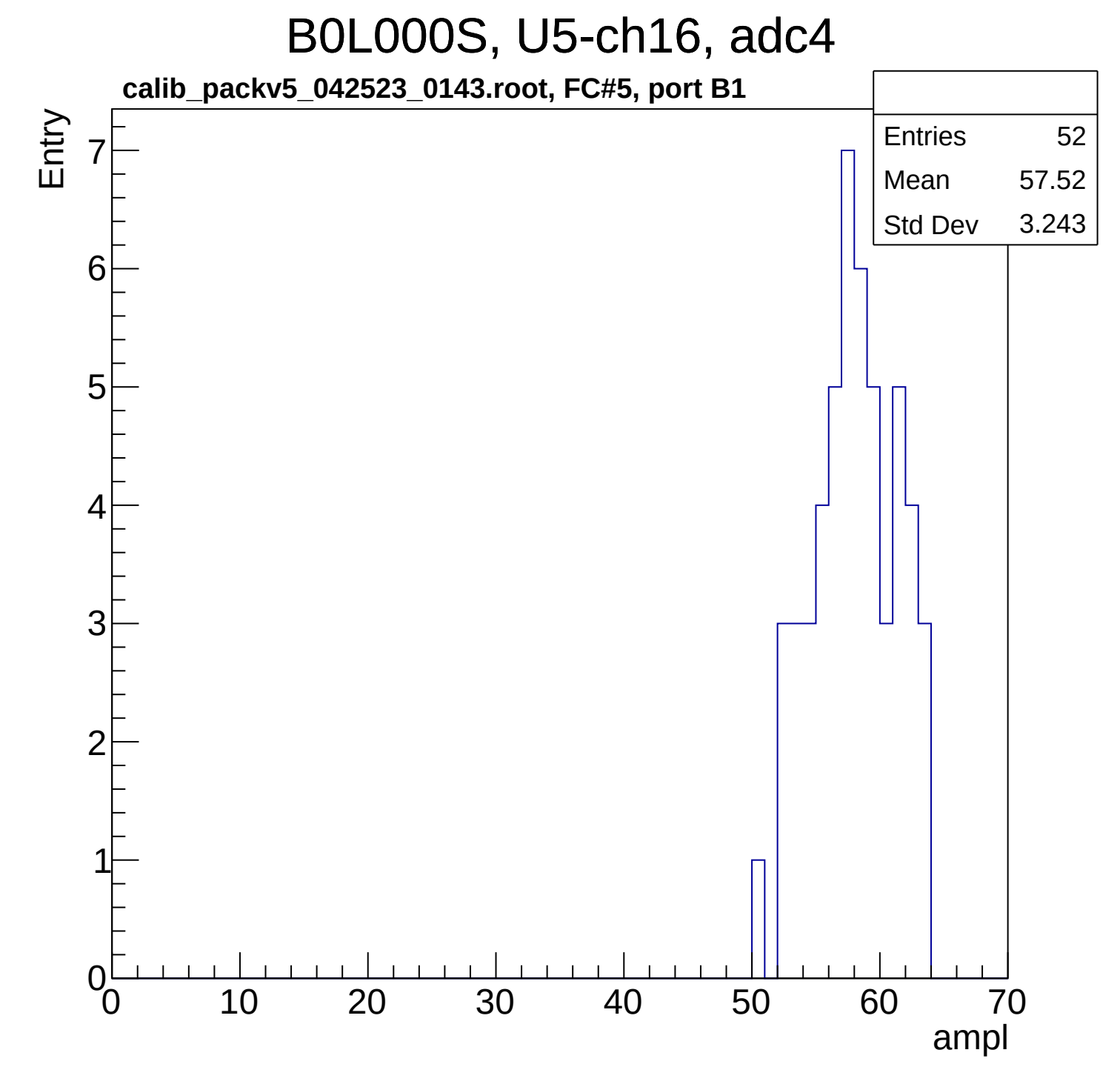
B0L000S, U5-ch16, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.52
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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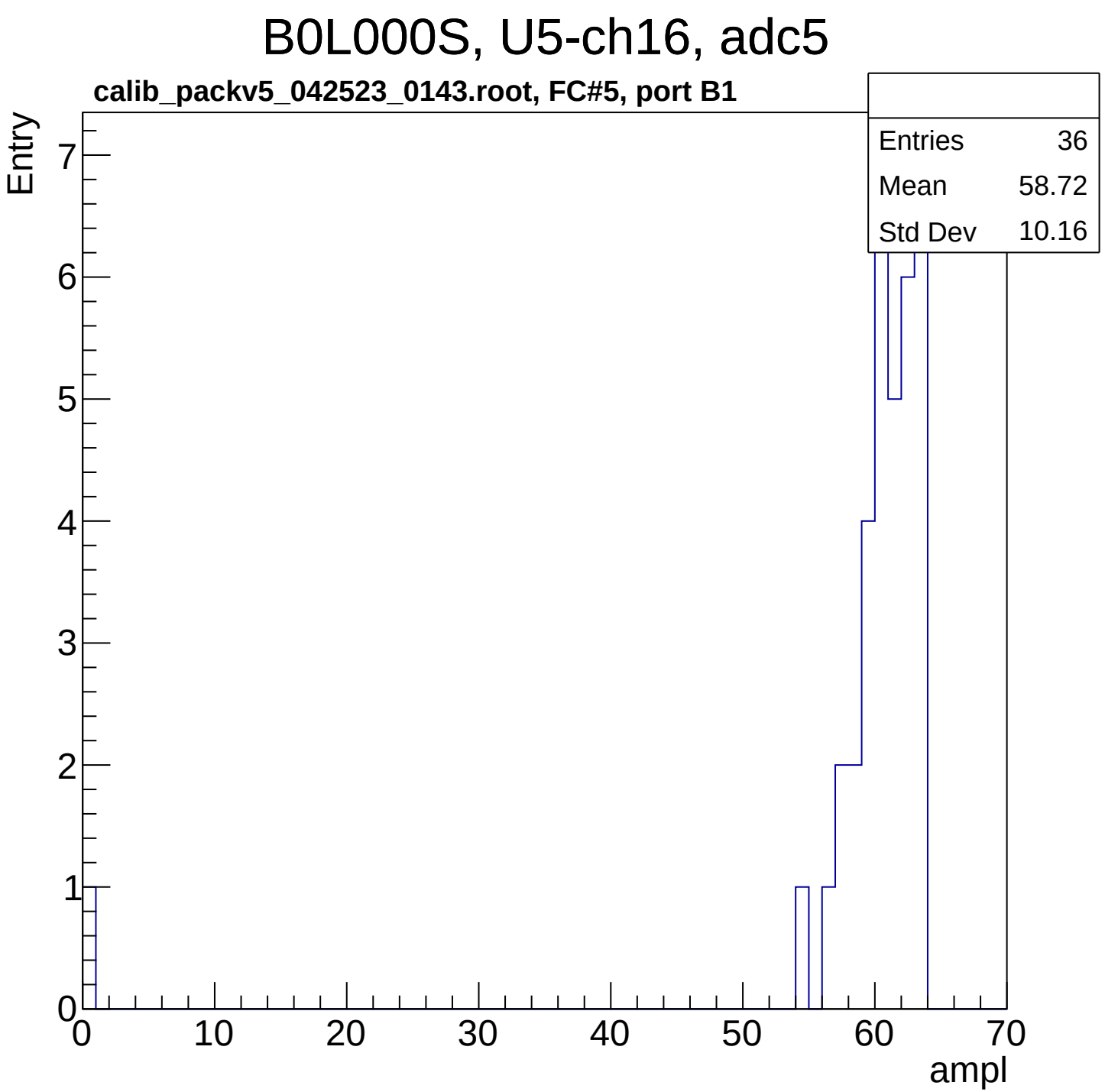
B0L000S, U5-ch16, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.72
Std Dev 10.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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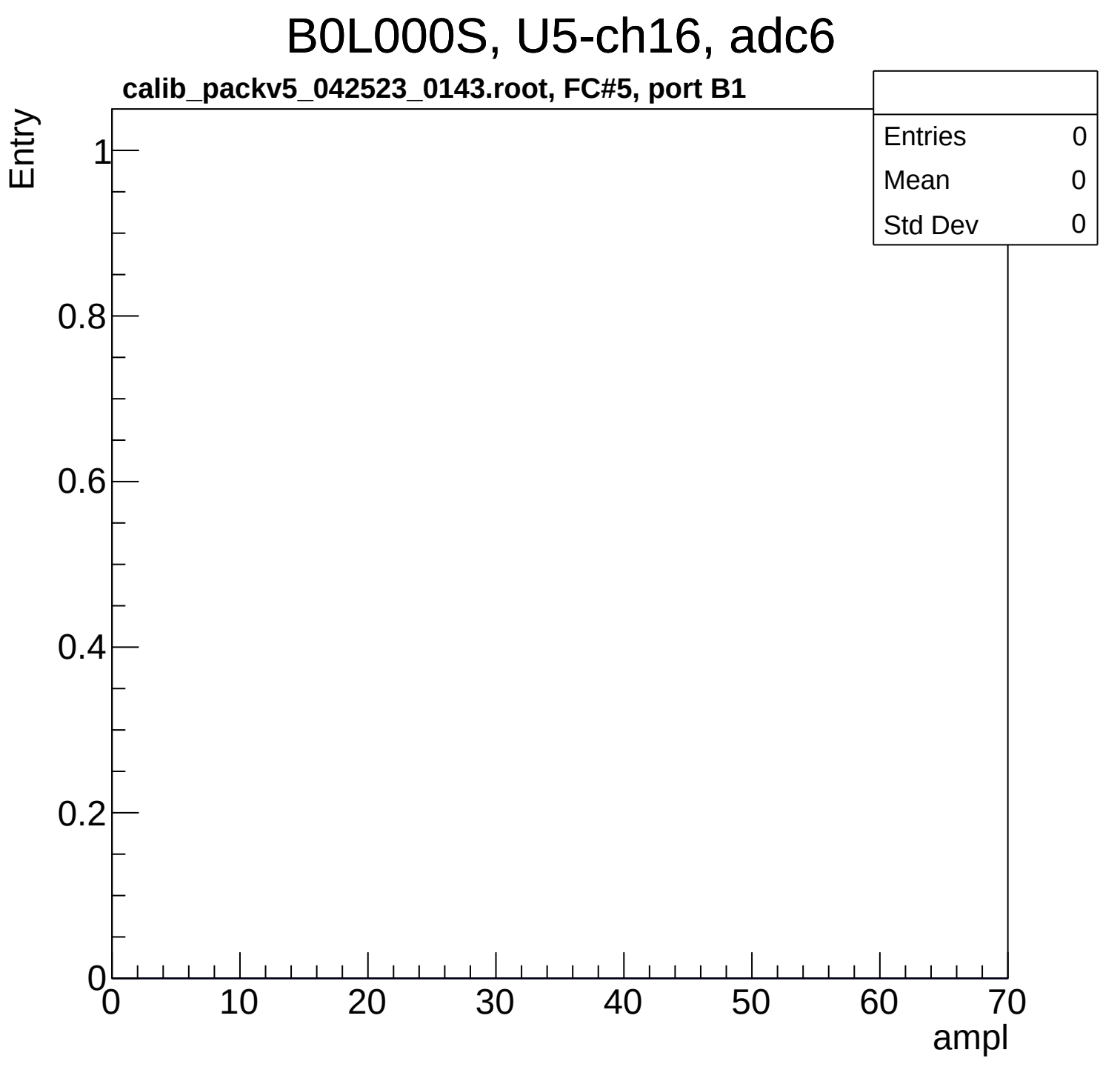
B0L000S, U5-ch16, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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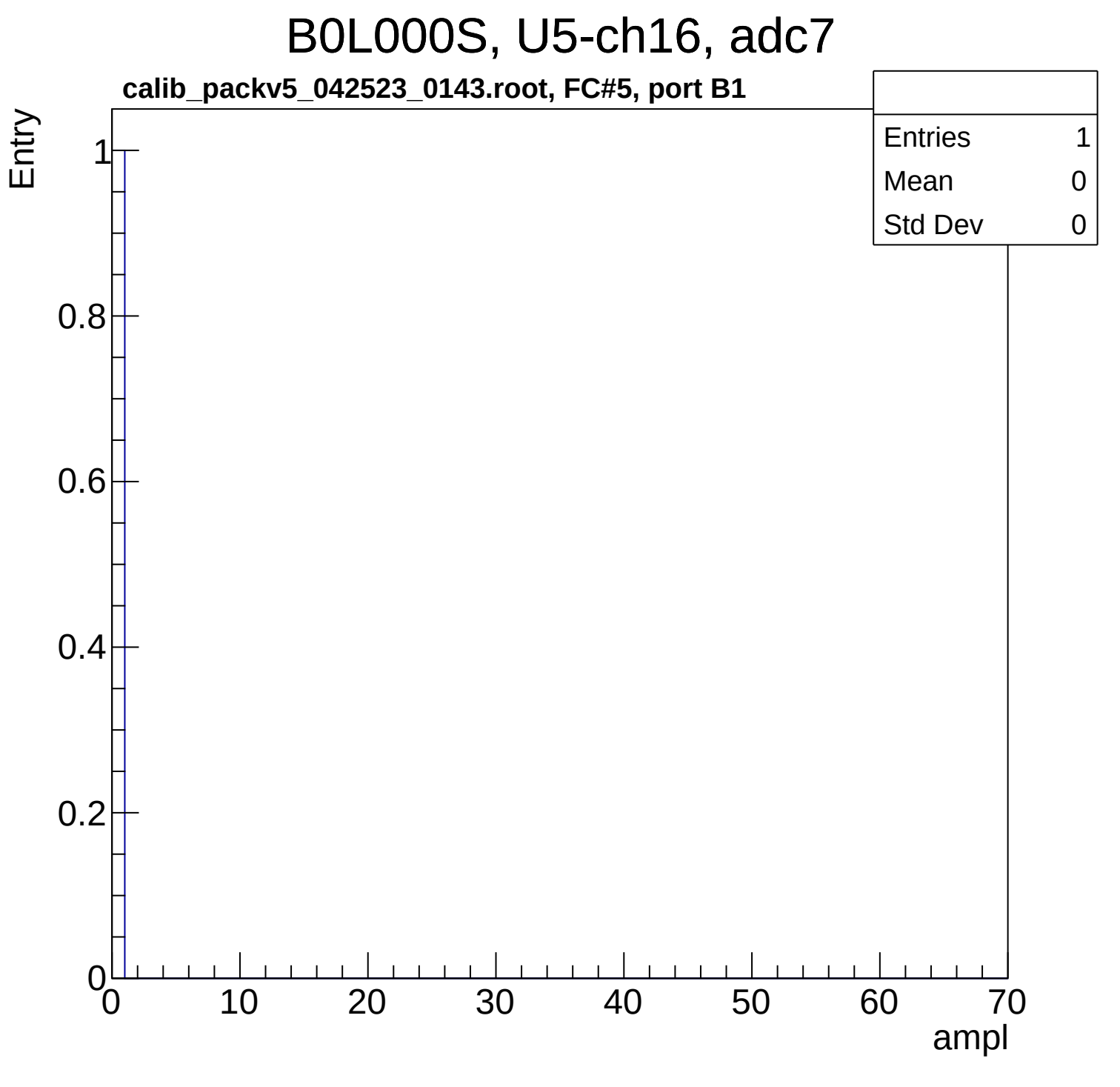
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch16, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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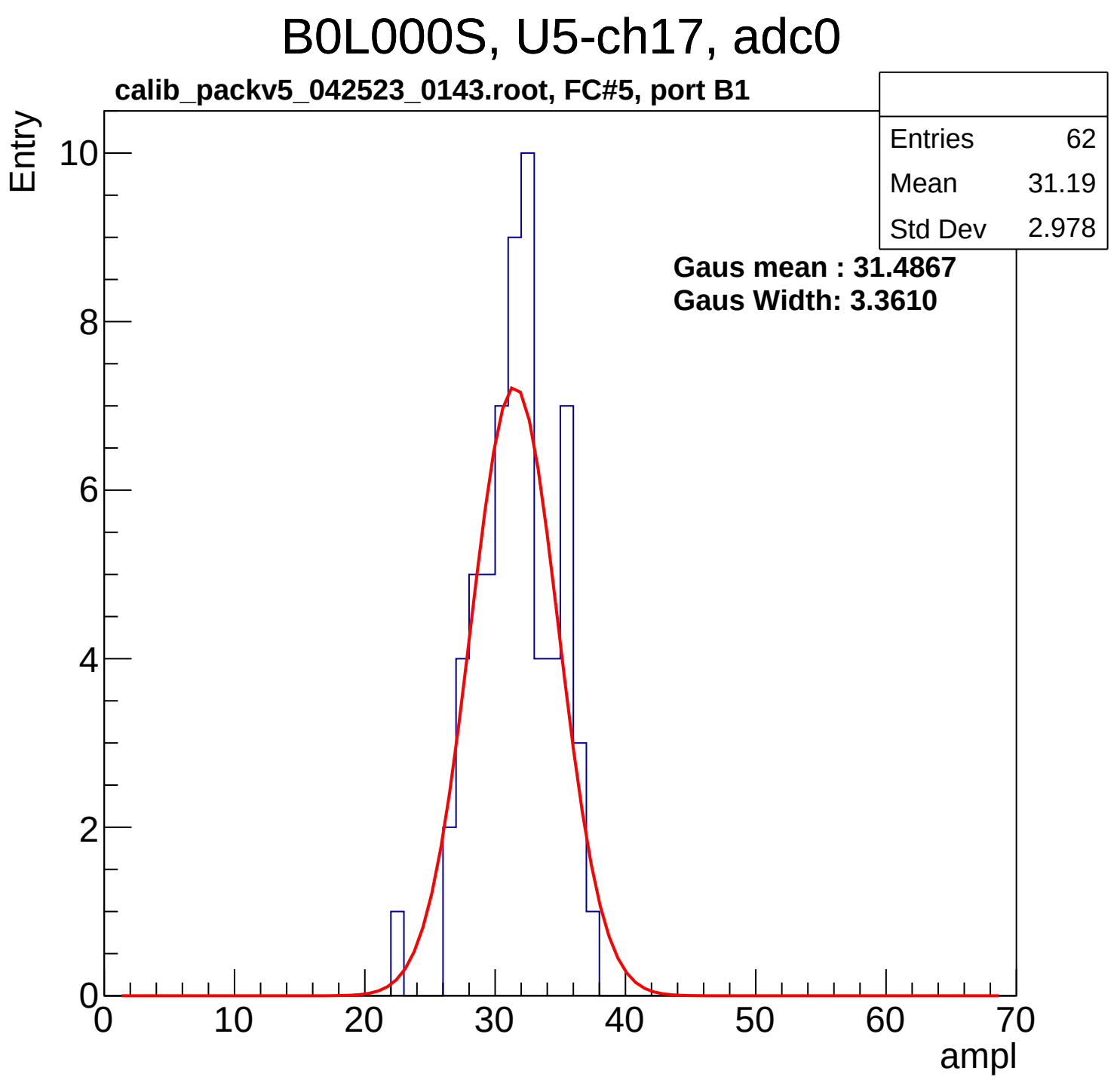
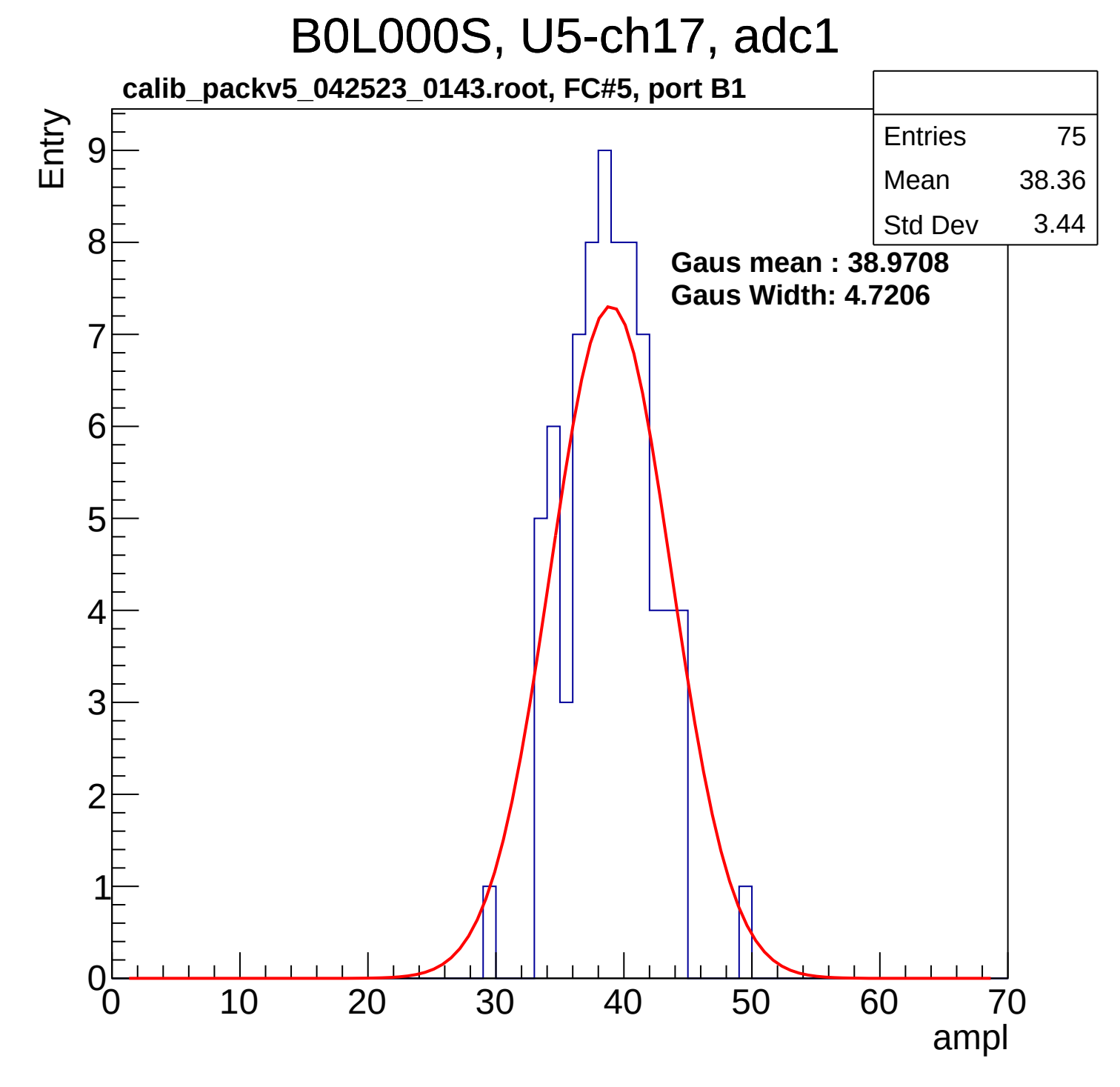
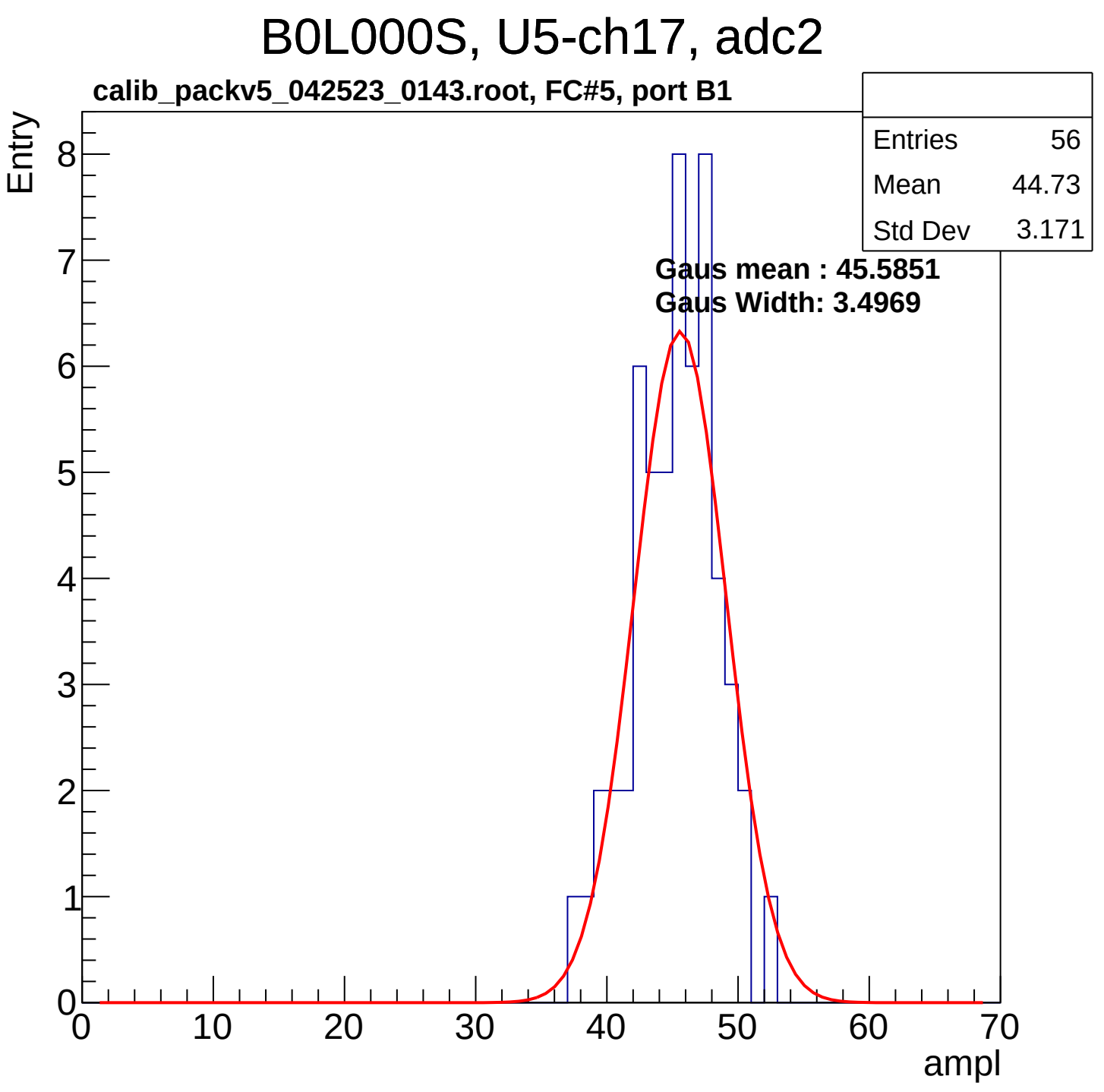
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch17, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 31.19
Std Dev 2.978
Gaus mean : 31.4867
Gaus Width: 3.3610
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch17, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 38.36
Std Dev 3.44
Gaus mean : 38.9708
Gaus Width: 4.7206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch17, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.73
Std Dev 3.171
Gaus mean : 45.5851
Gaus Width: 3.4969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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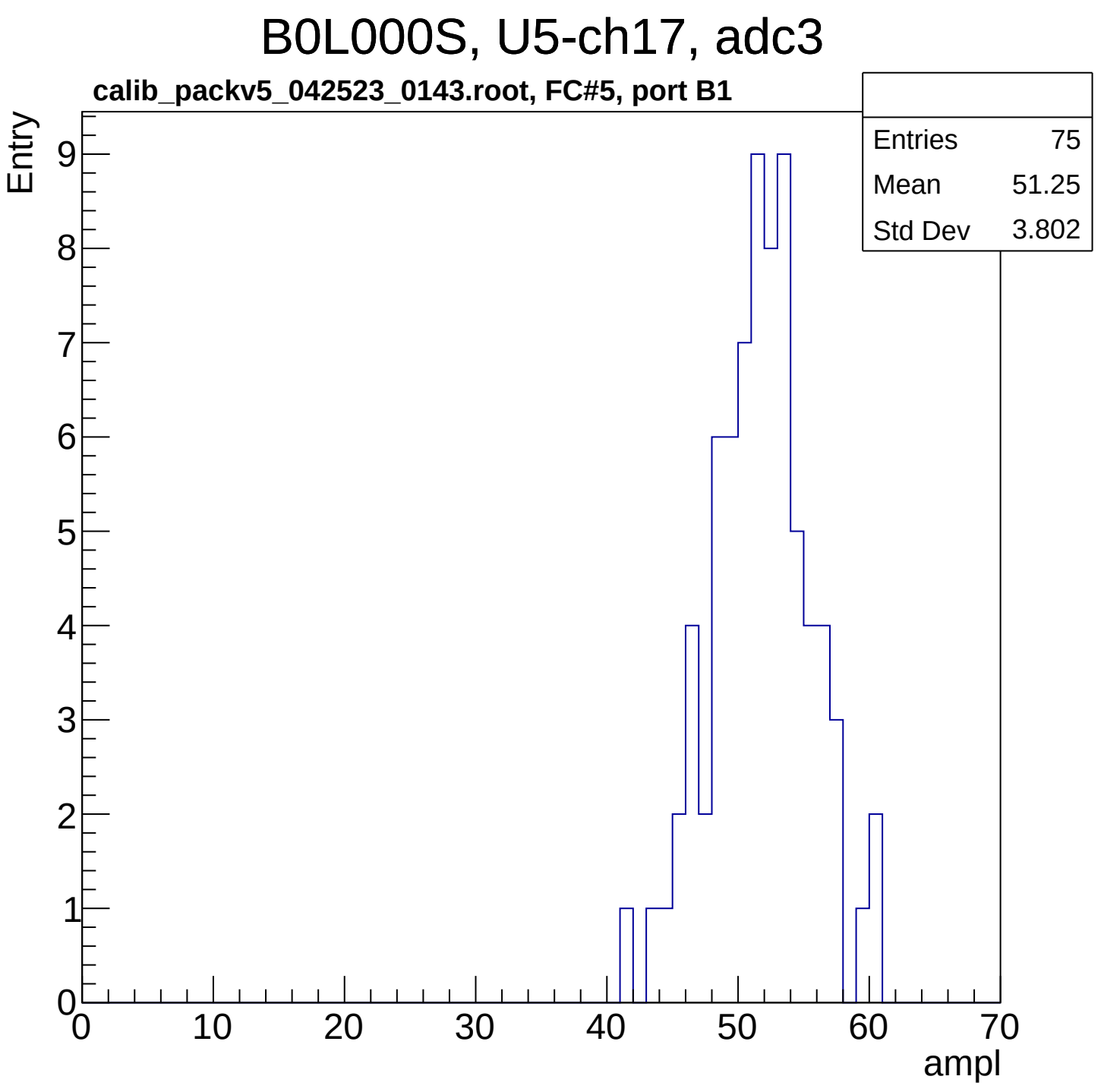
B0L000S, U5-ch17, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 51.25
Std Dev 3.802
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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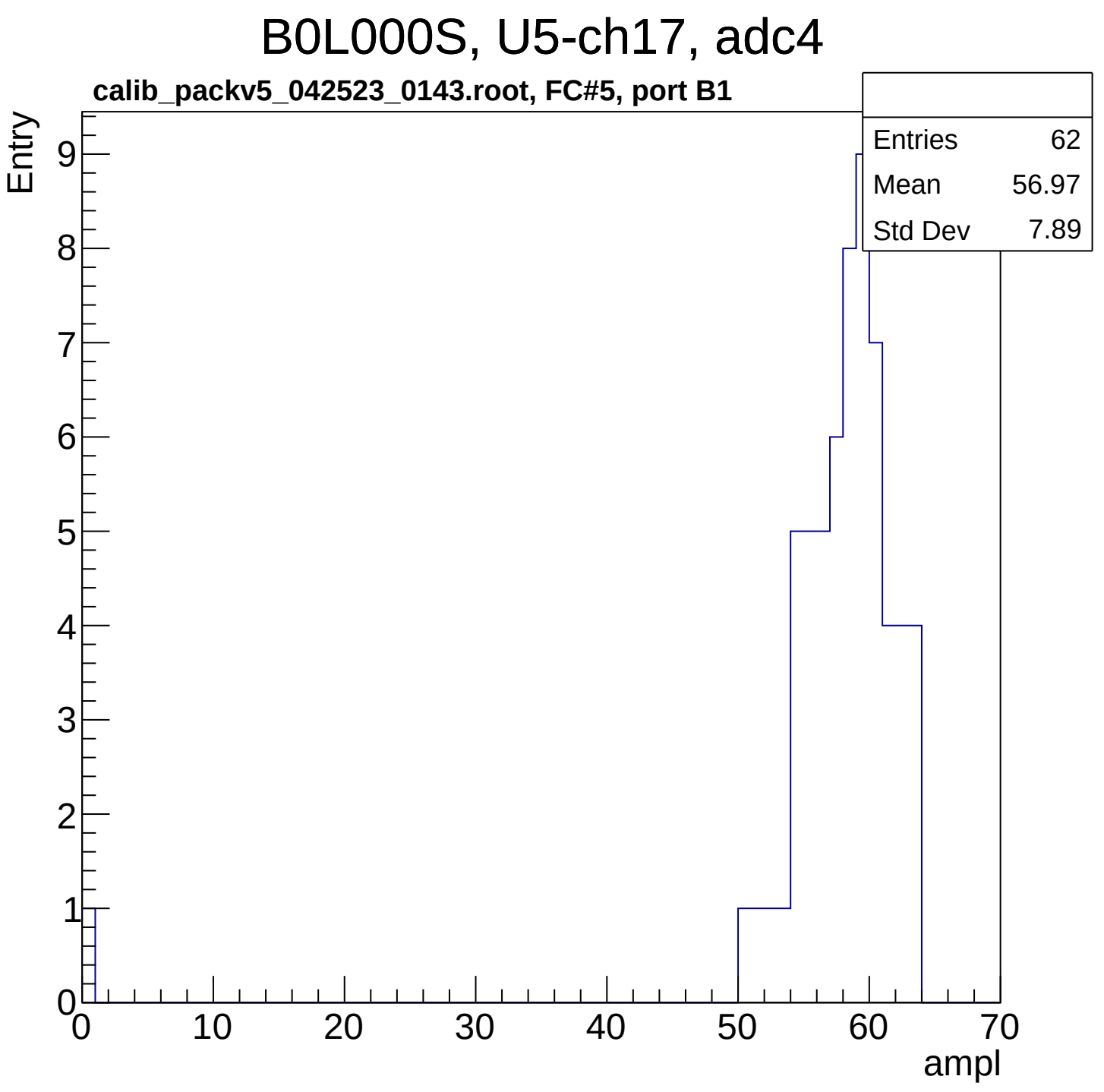
B0L000S, U5-ch17, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.97
Std Dev 7.89
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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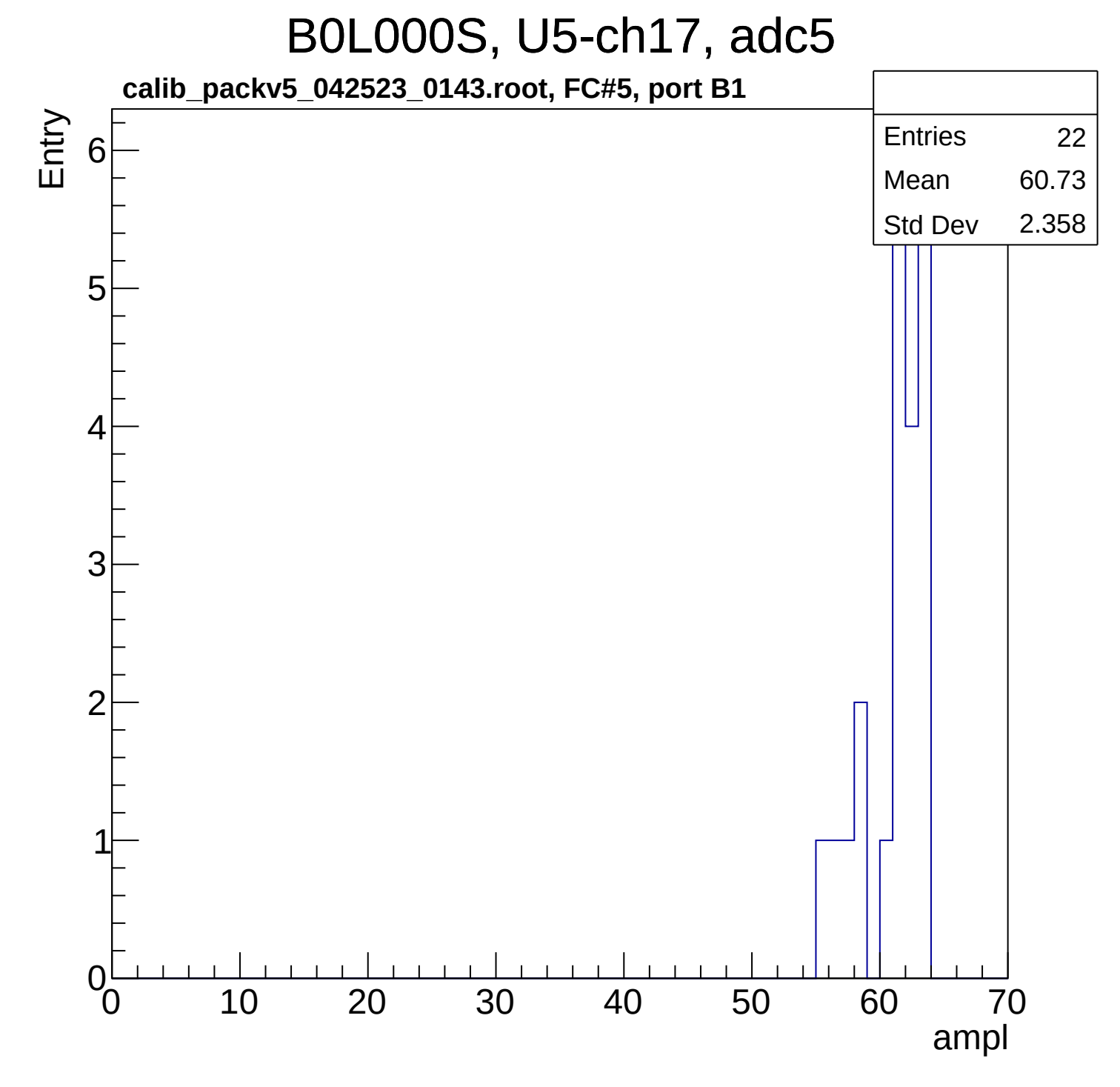
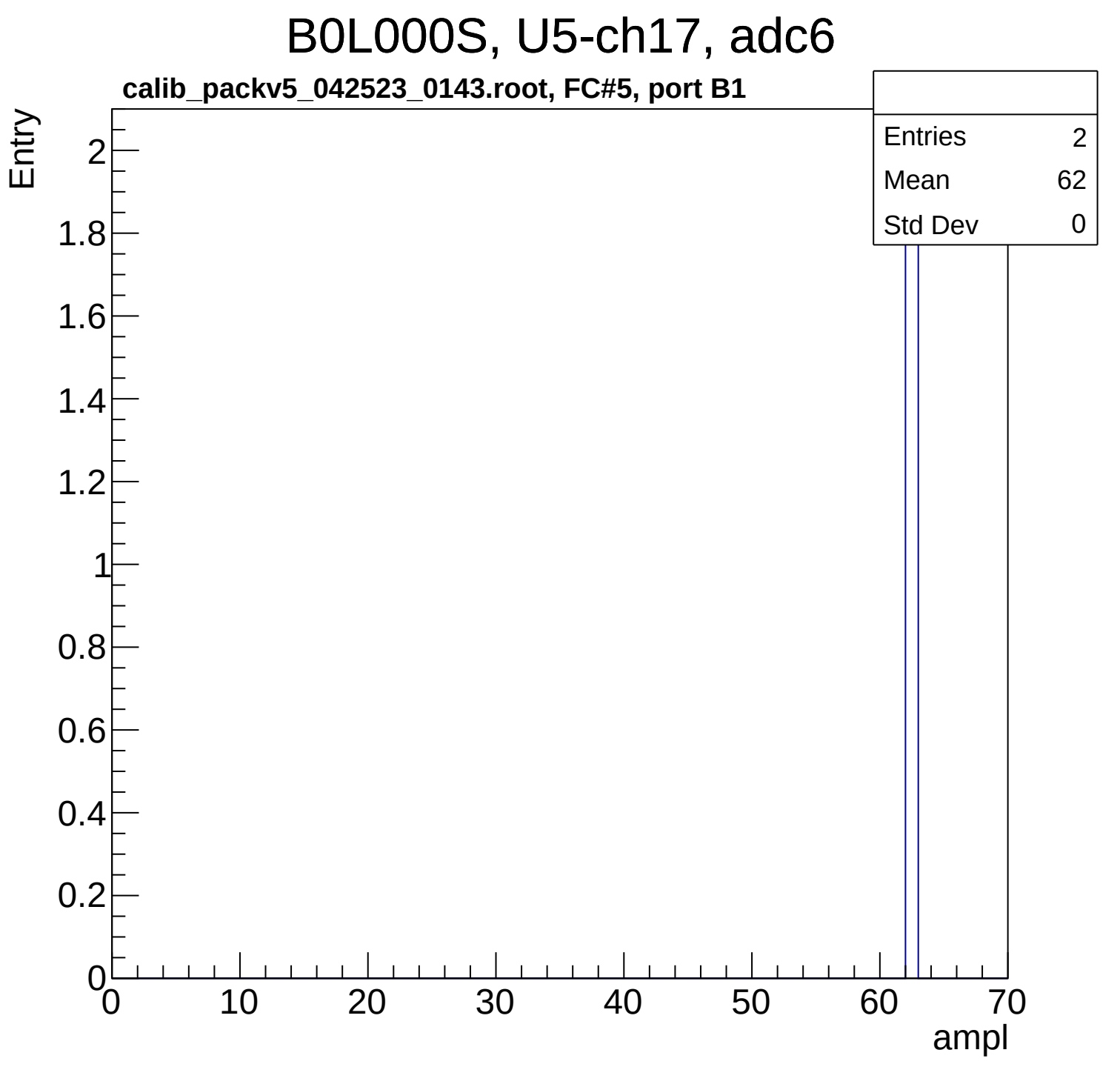
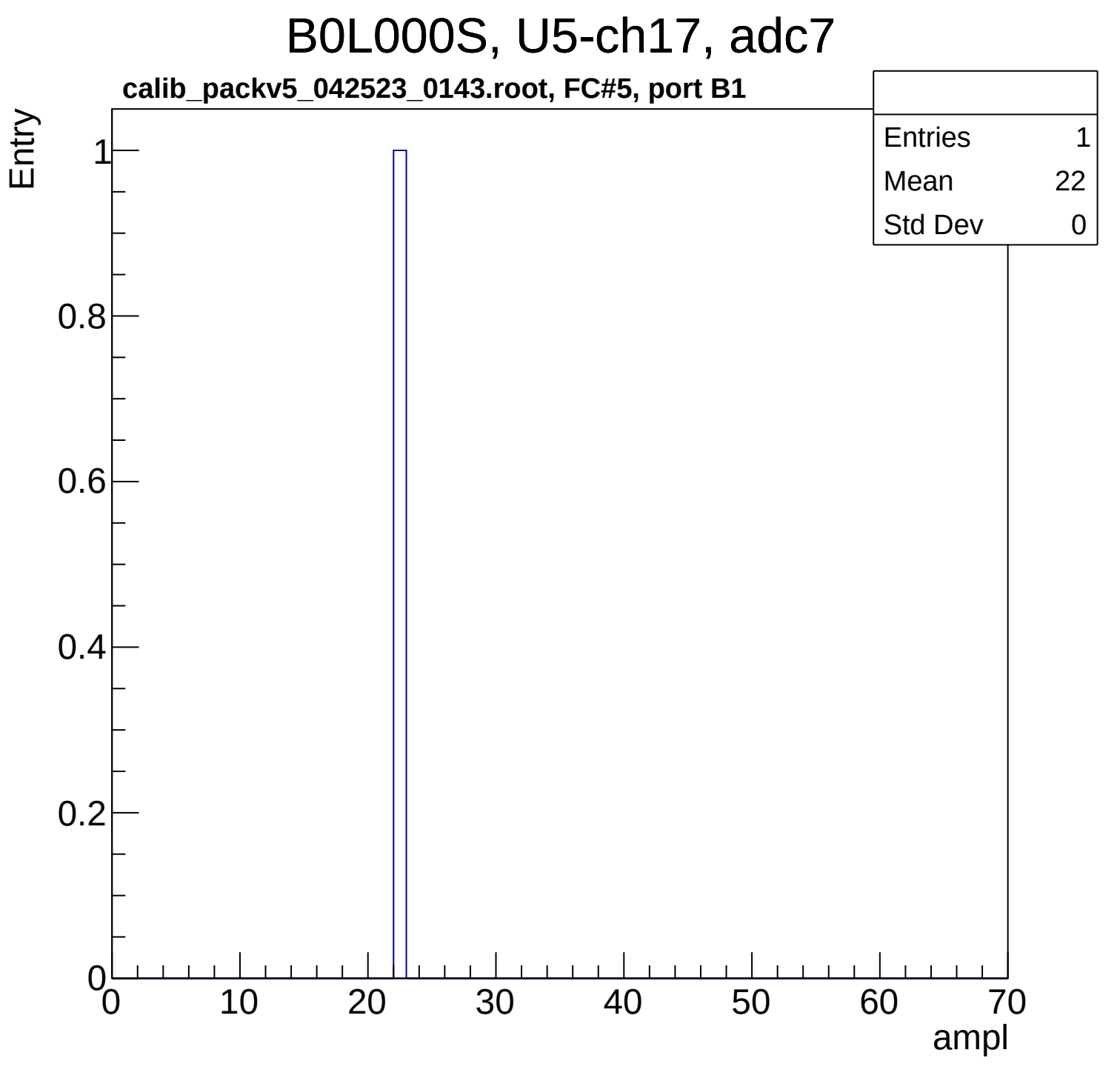

B0L000S, U5-ch17, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 60.73
Std Dev 2.358
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch17, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch17, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
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0
0
10
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30
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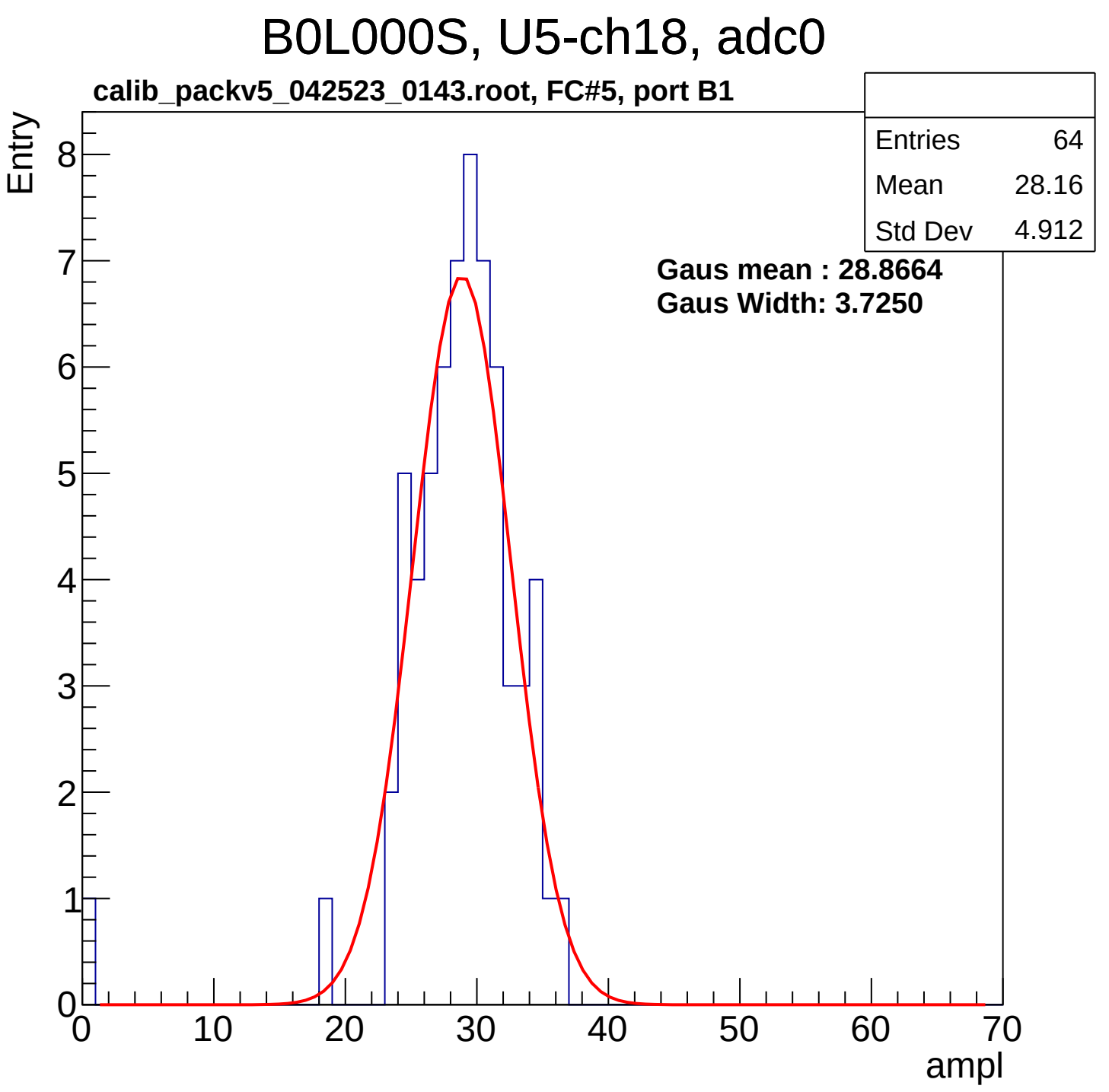
60
70
ampl


B0L000S, U5-ch18, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 28.16
Std Dev 4.912
Gaus mean : 28.8664
Gaus Width: 3.7250
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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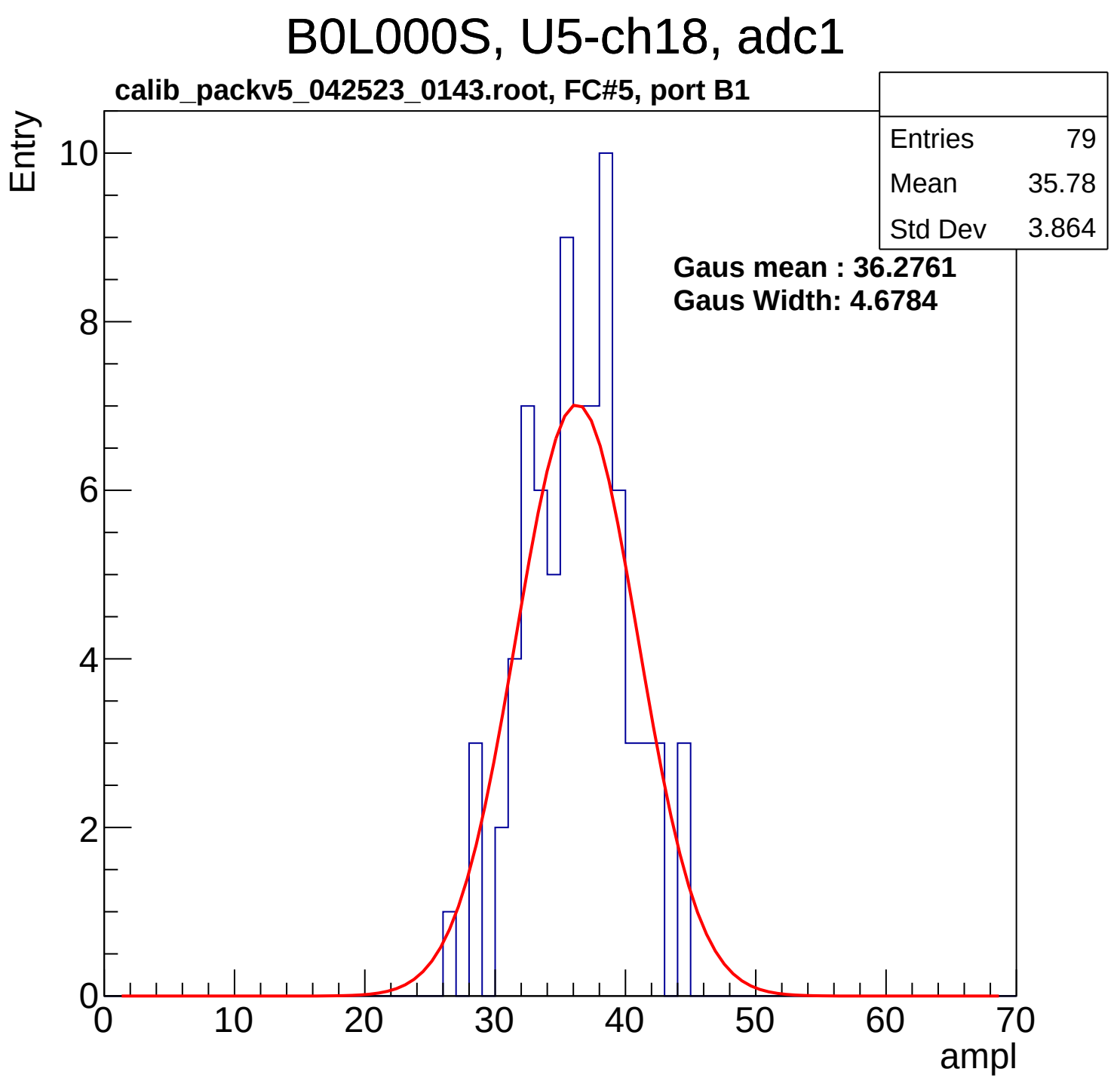
B0L000S, U5-ch18, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 35.78
Std Dev 3.864
Gaus mean : 36.2761
Gaus Width: 4.6784
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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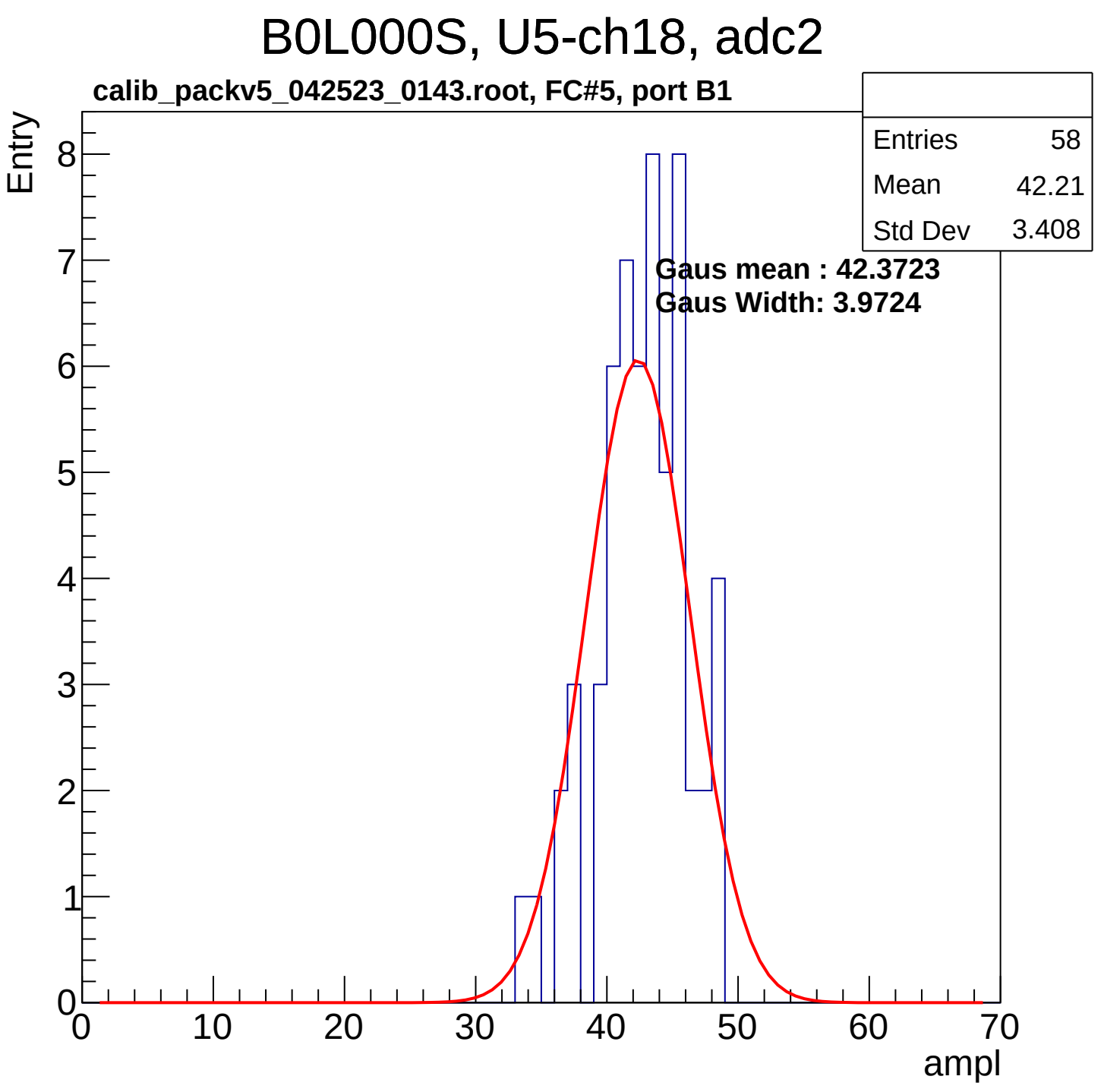
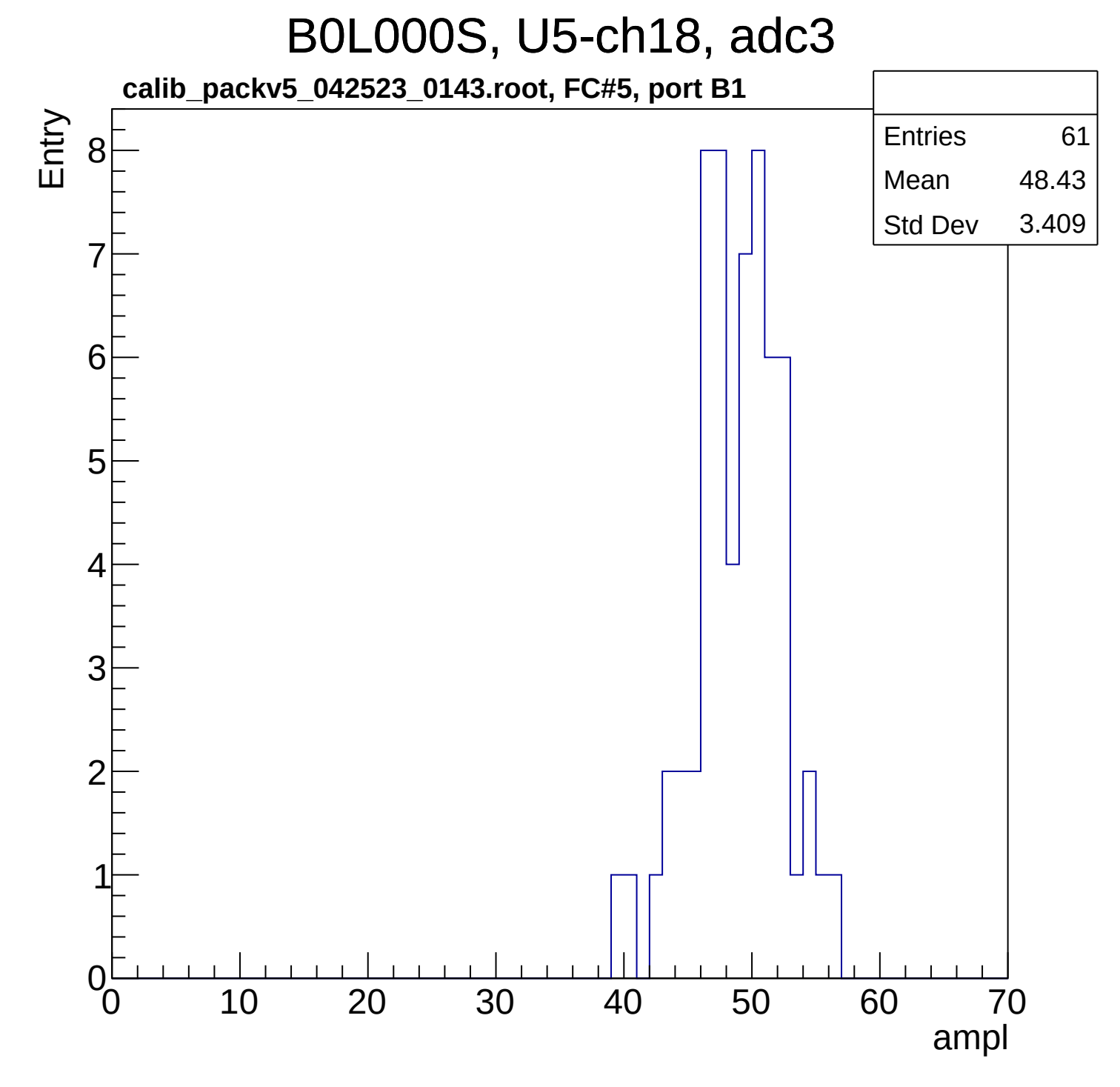

B0L000S, U5-ch18, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 42.21
Std Dev 3.408
Gaus mean : 42.3723
Gaus Width: 3.9724
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch18, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.43
Std Dev 3.409
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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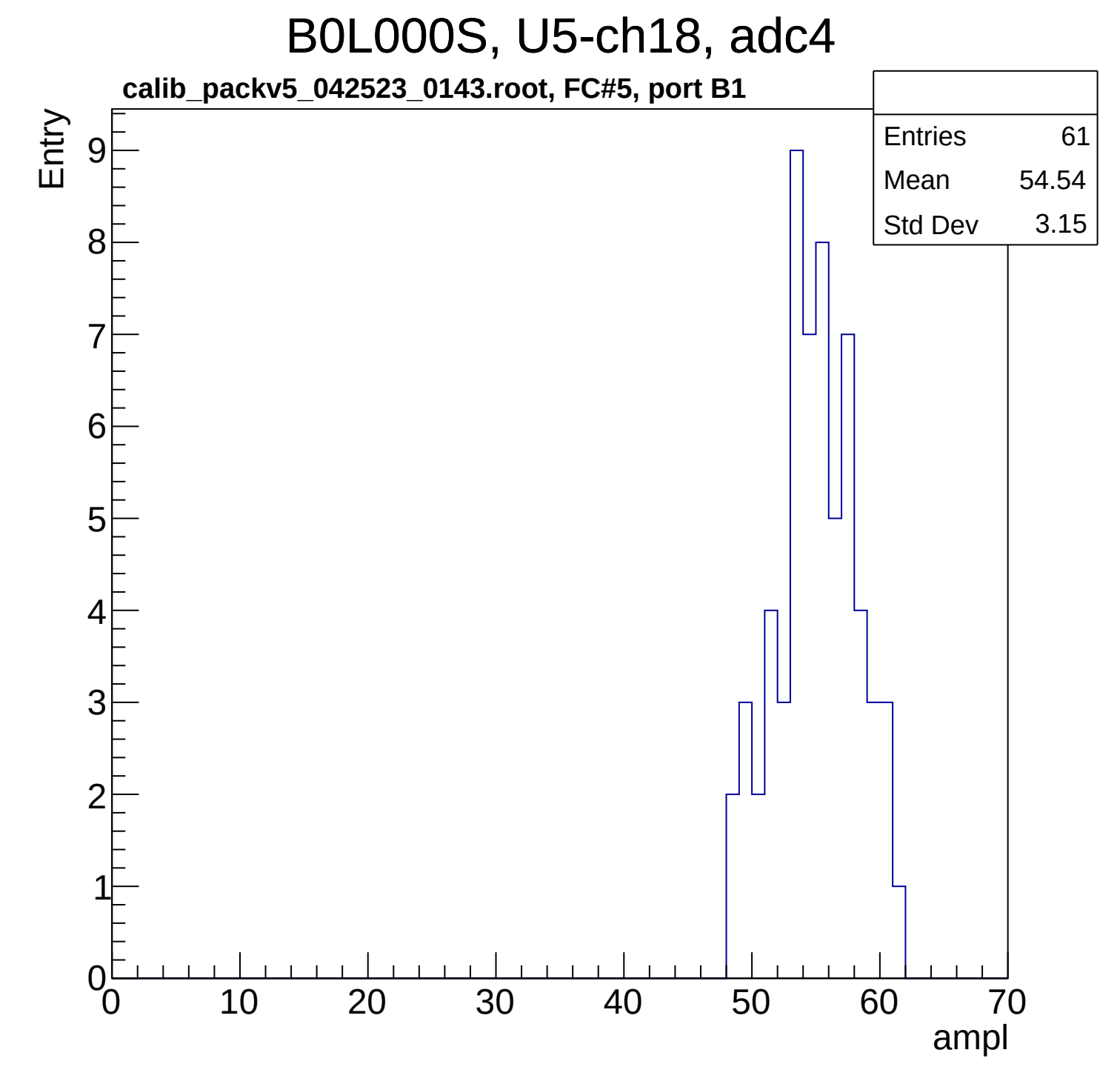
60
70


B0L000S, U5-ch18, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.54
Std Dev 3.15
Entry
ampl
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3
4
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6
7
8
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0
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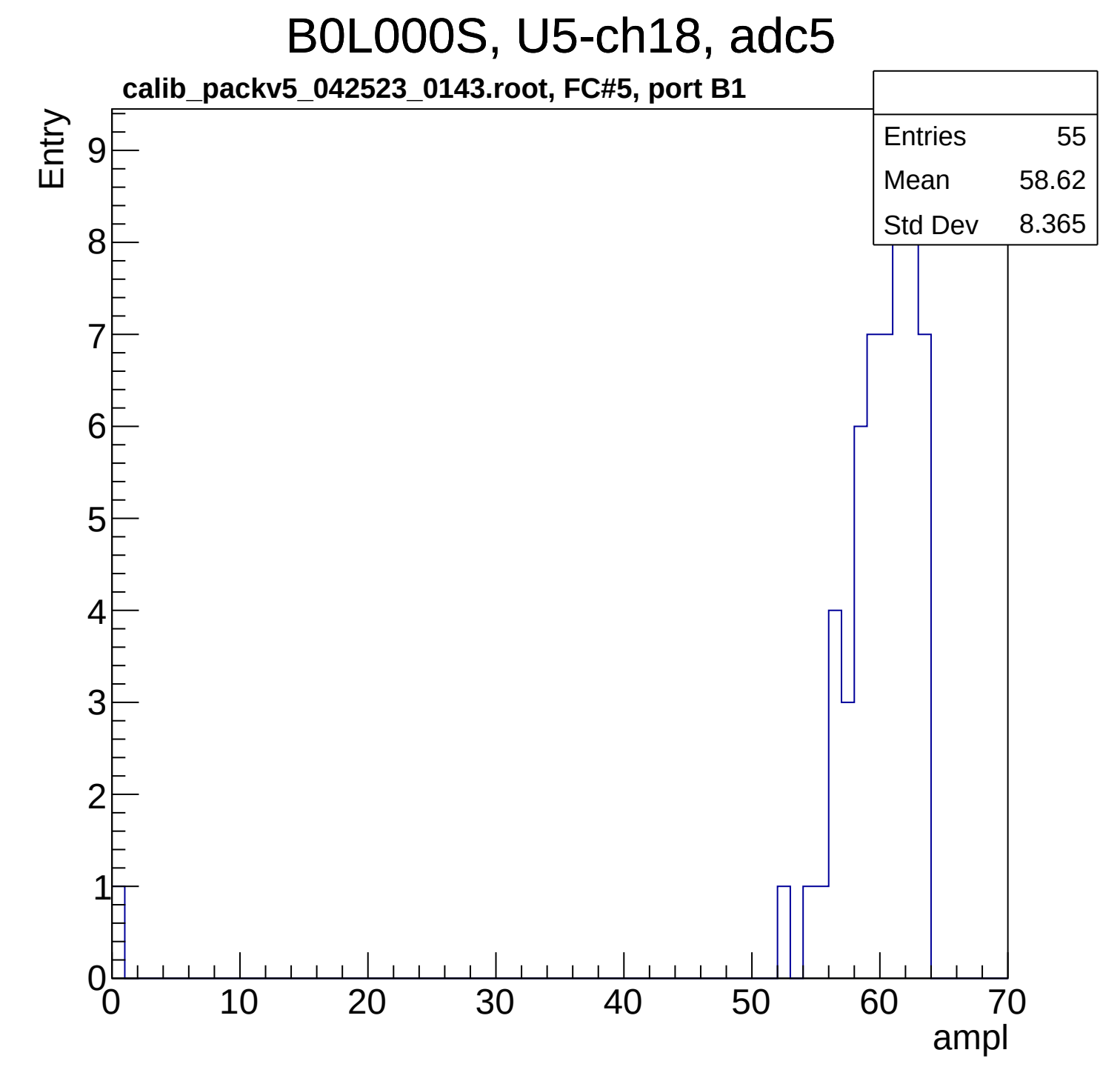
B0L000S, U5-ch18, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 58.62
Std Dev 8.365
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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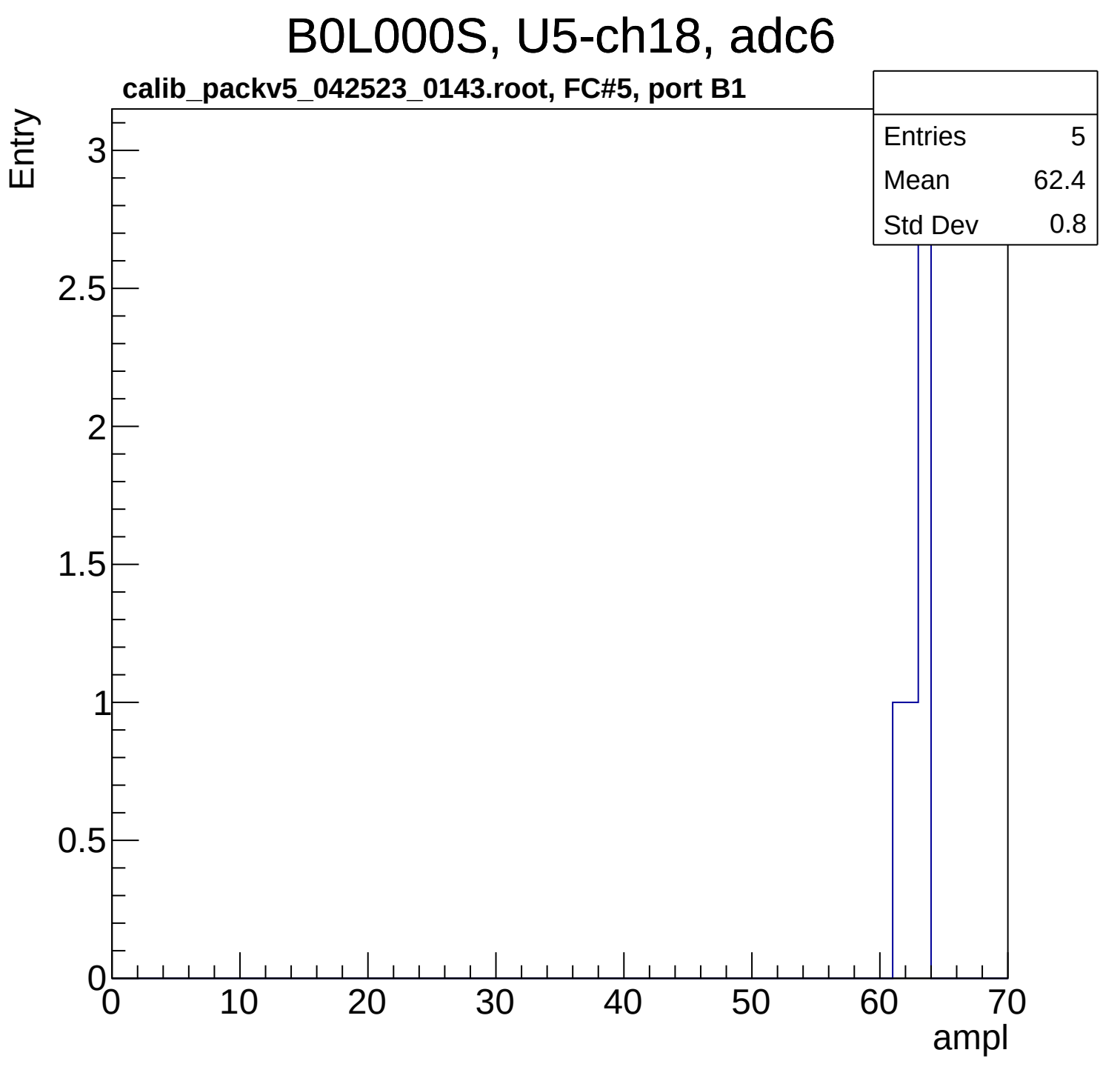
B0L000S, U5-ch18, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
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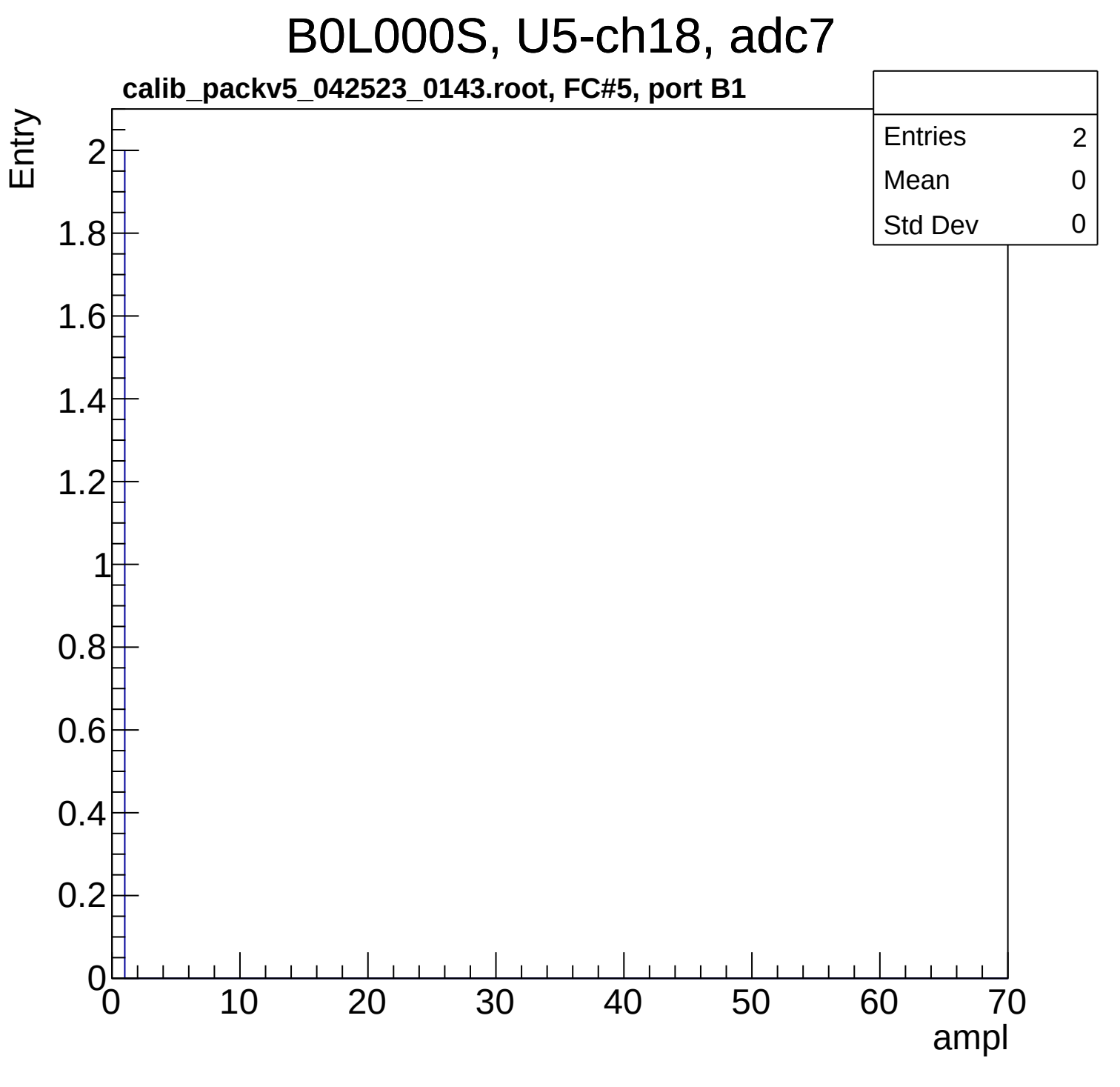
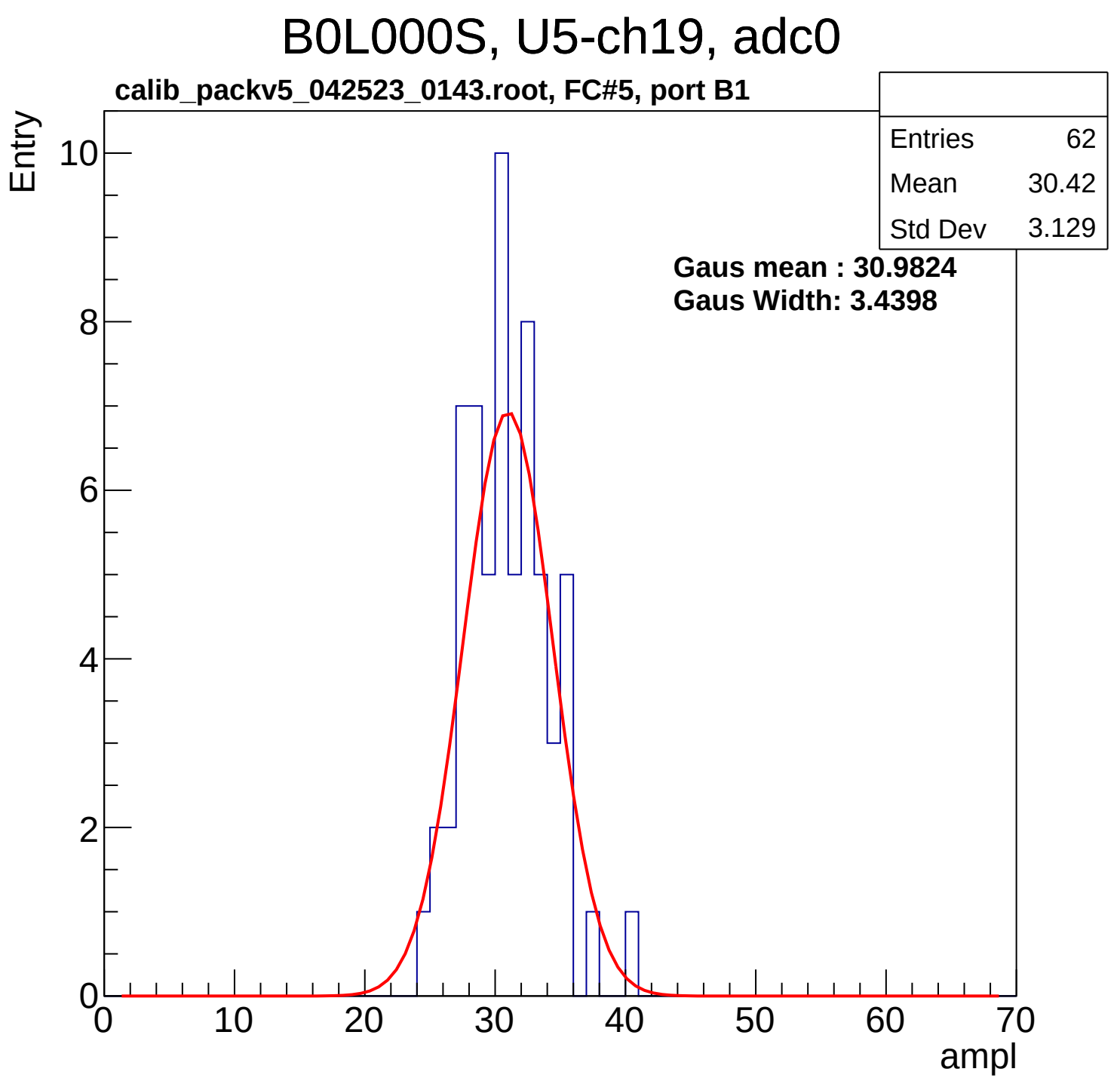
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch18, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch19, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 30.42
Std Dev 3.129
Gaus mean : 30.9824
Gaus Width: 3.4398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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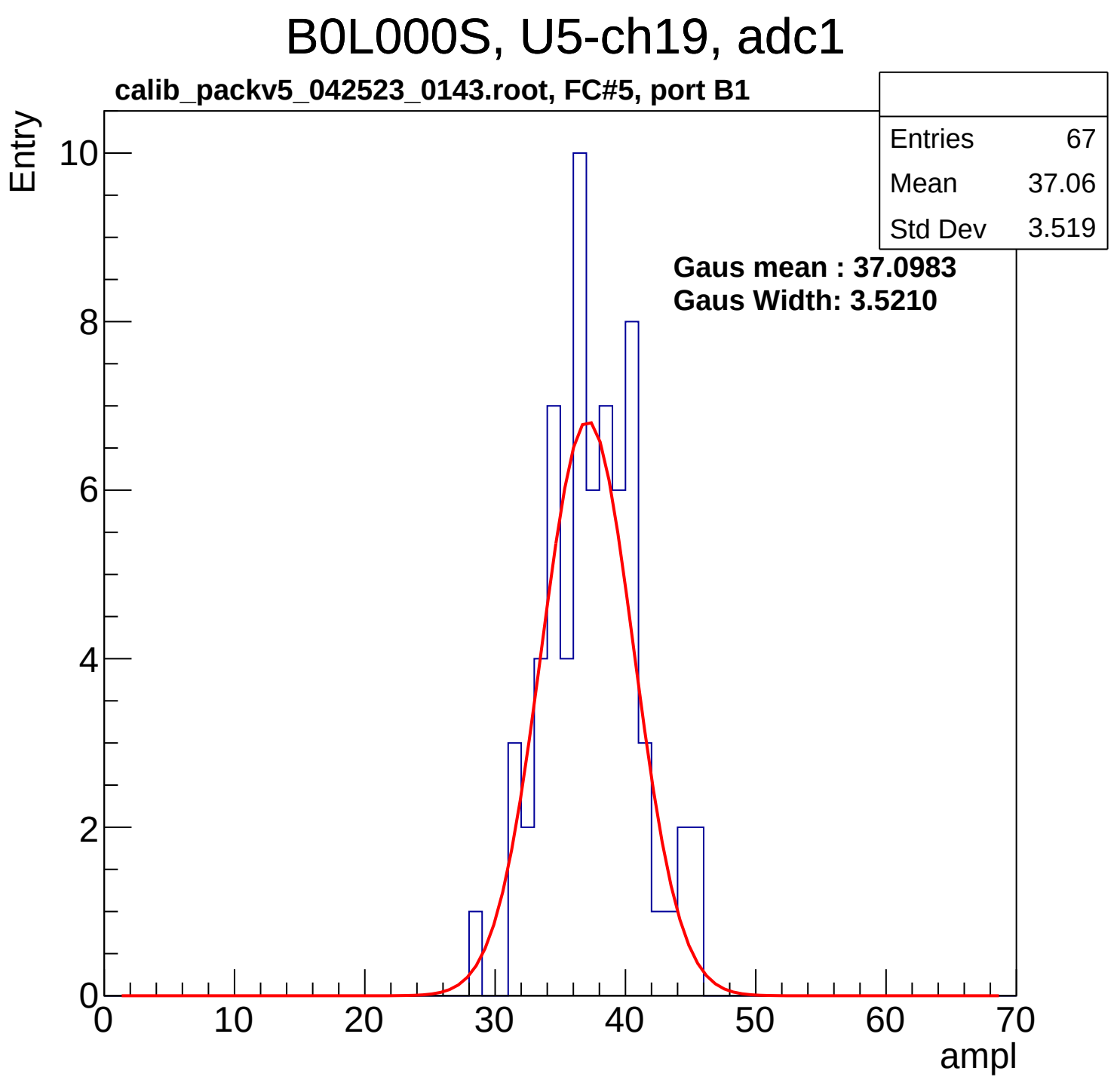
B0L000S, U5-ch19, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.06
Std Dev 3.519
Gaus mean : 37.0983
Gaus Width: 3.5210
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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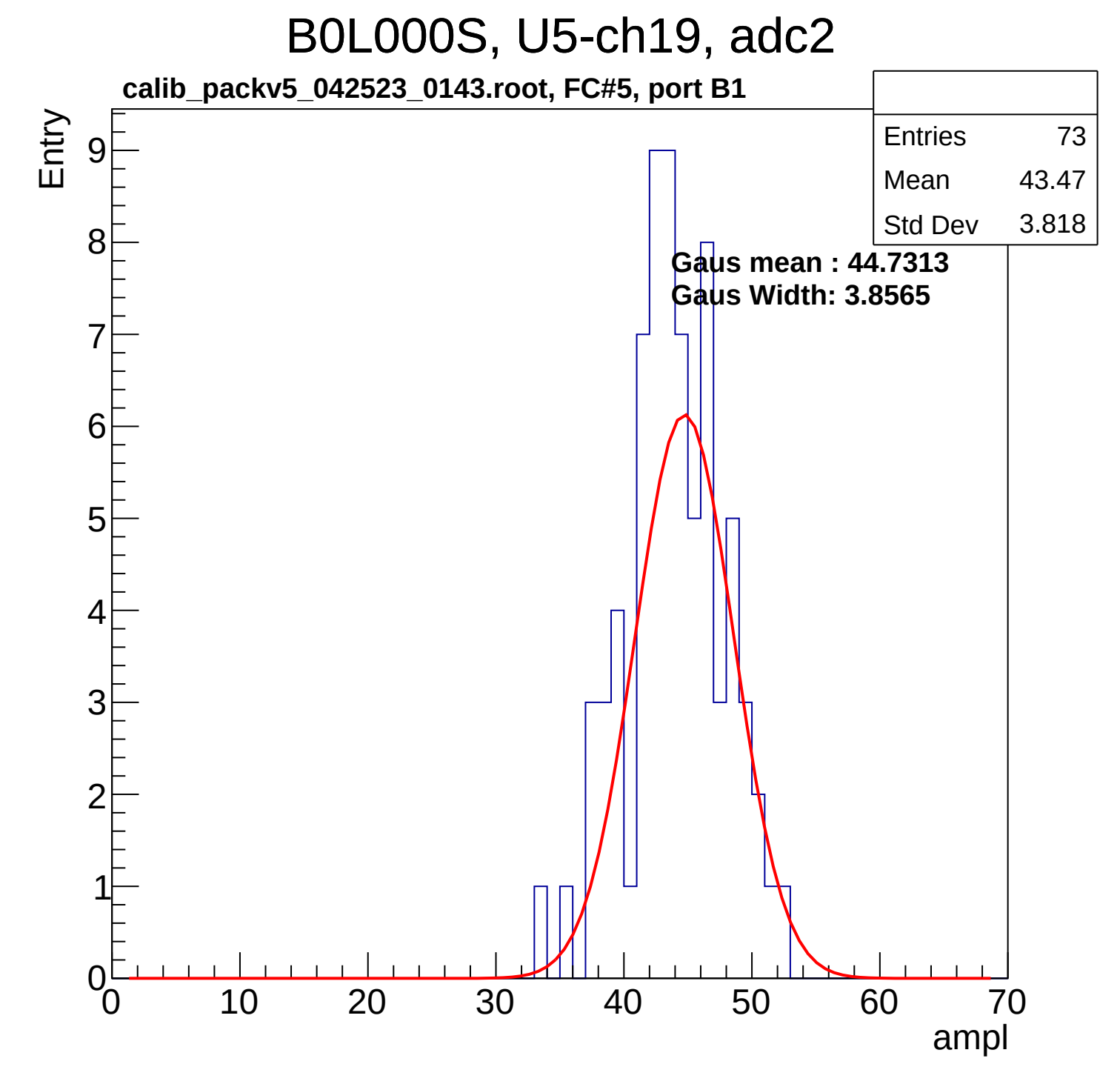
B0L000S, U5-ch19, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.47
Std Dev 3.818
Gaus mean : 44.7313
Gaus Width: 3.8565
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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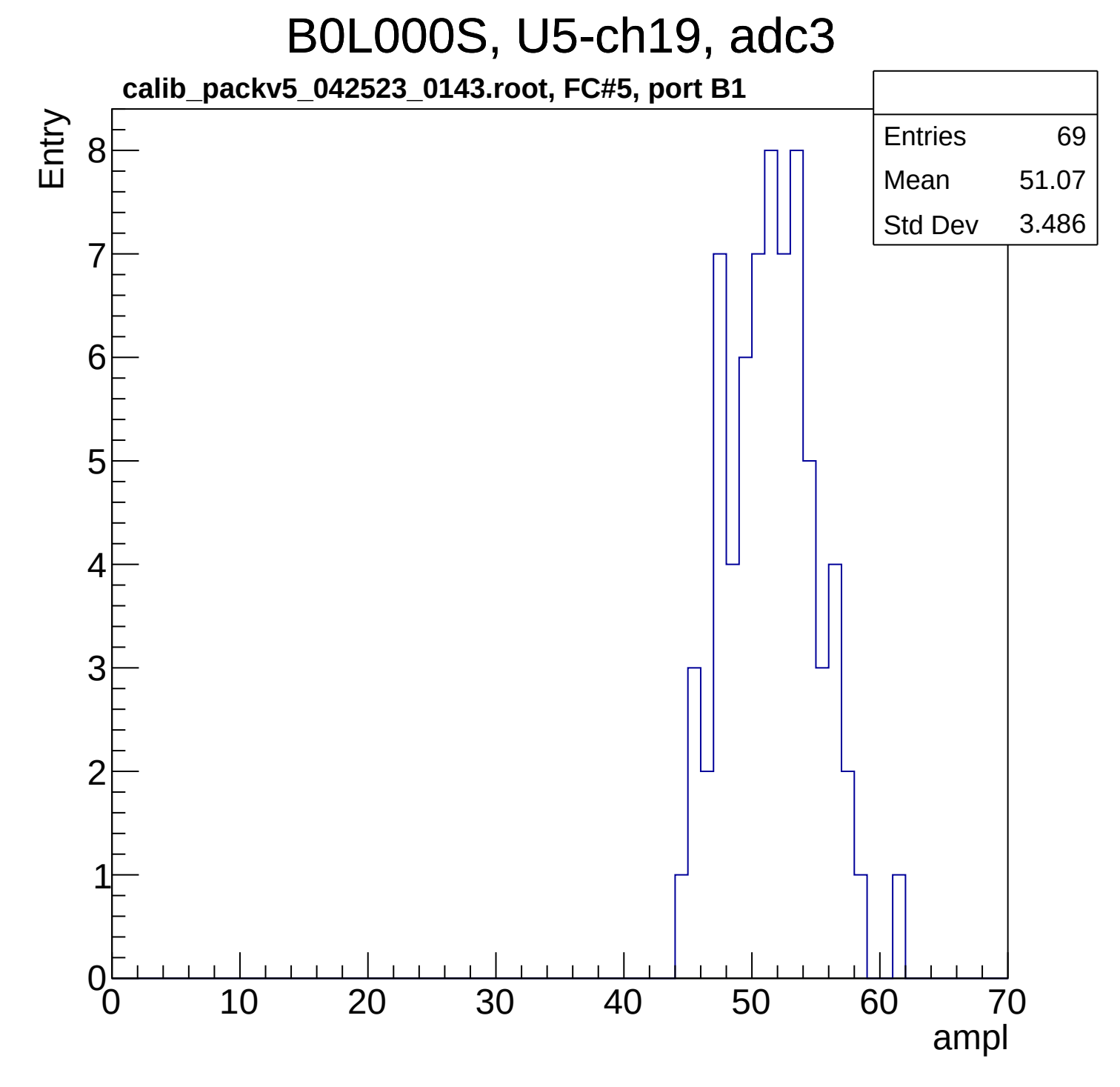
B0L000S, U5-ch19, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 51.07
Std Dev 3.486
Entry
ampl
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2
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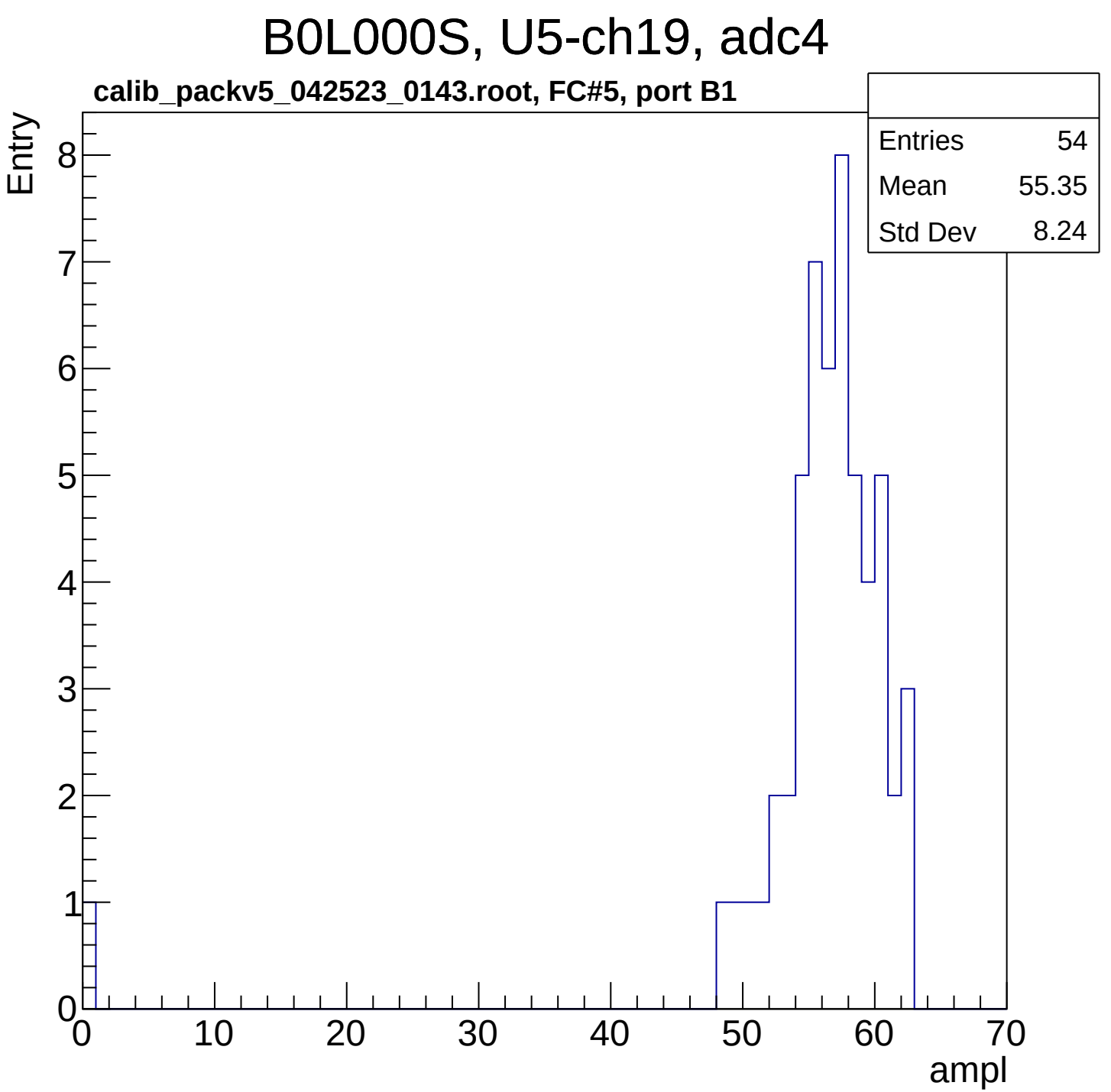
B0L000S, U5-ch19, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.35
Std Dev 8.24
Entry
0
1
2
3
4
5
6
7
8
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10
20
30
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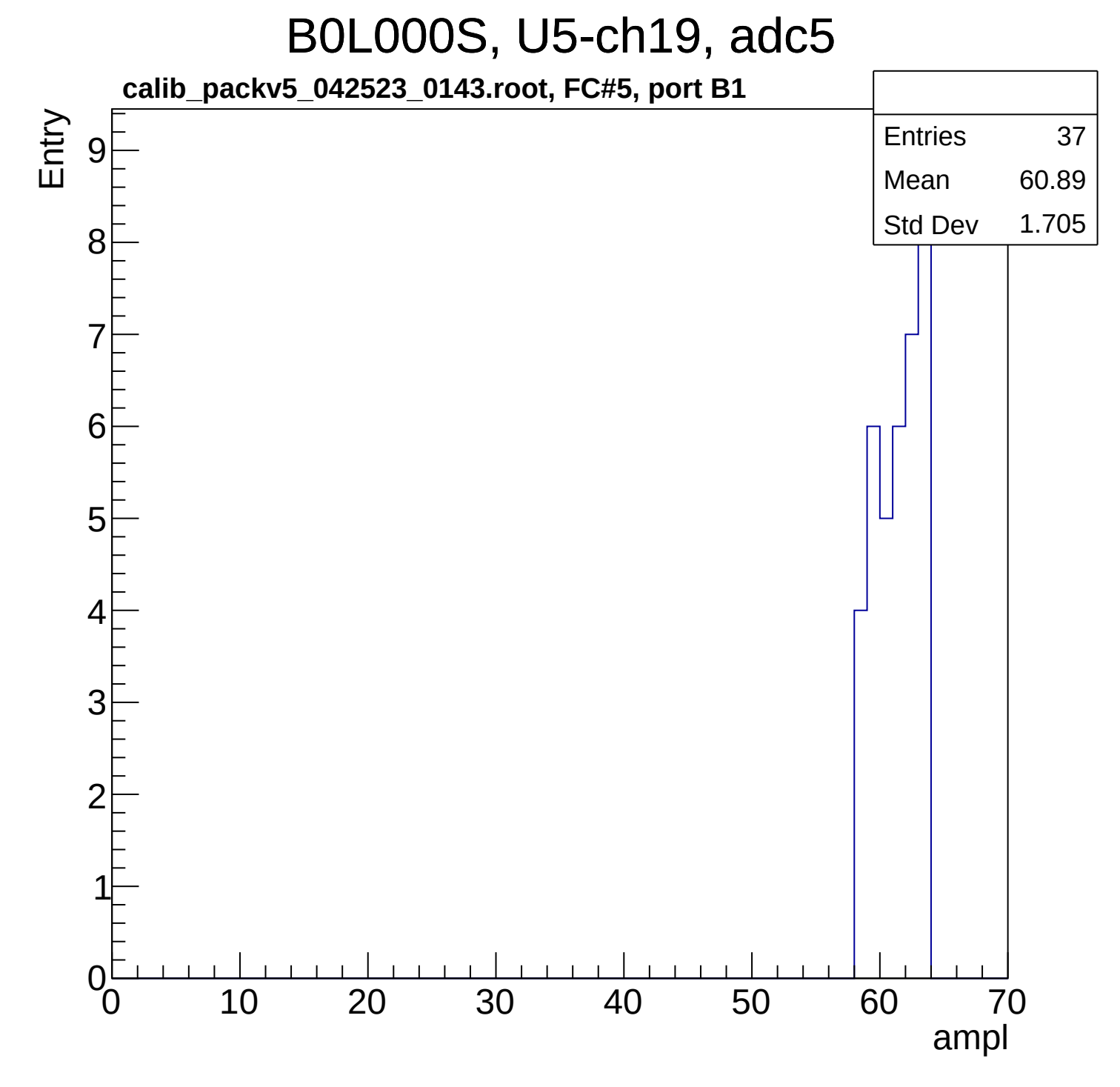
50
60
70
ampl


B0L000S, U5-ch19, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 60.89
Std Dev 1.705
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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30
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60
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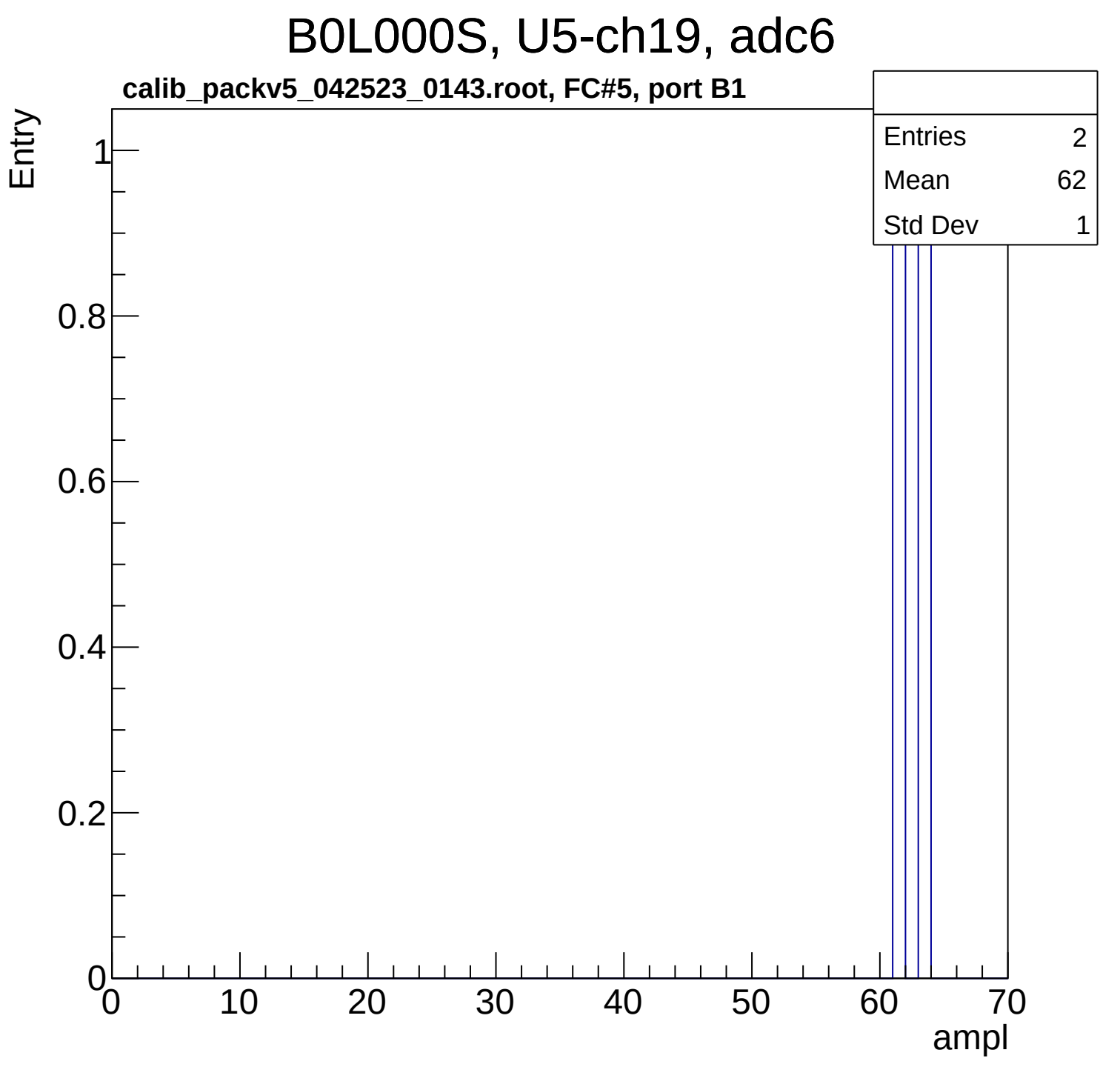
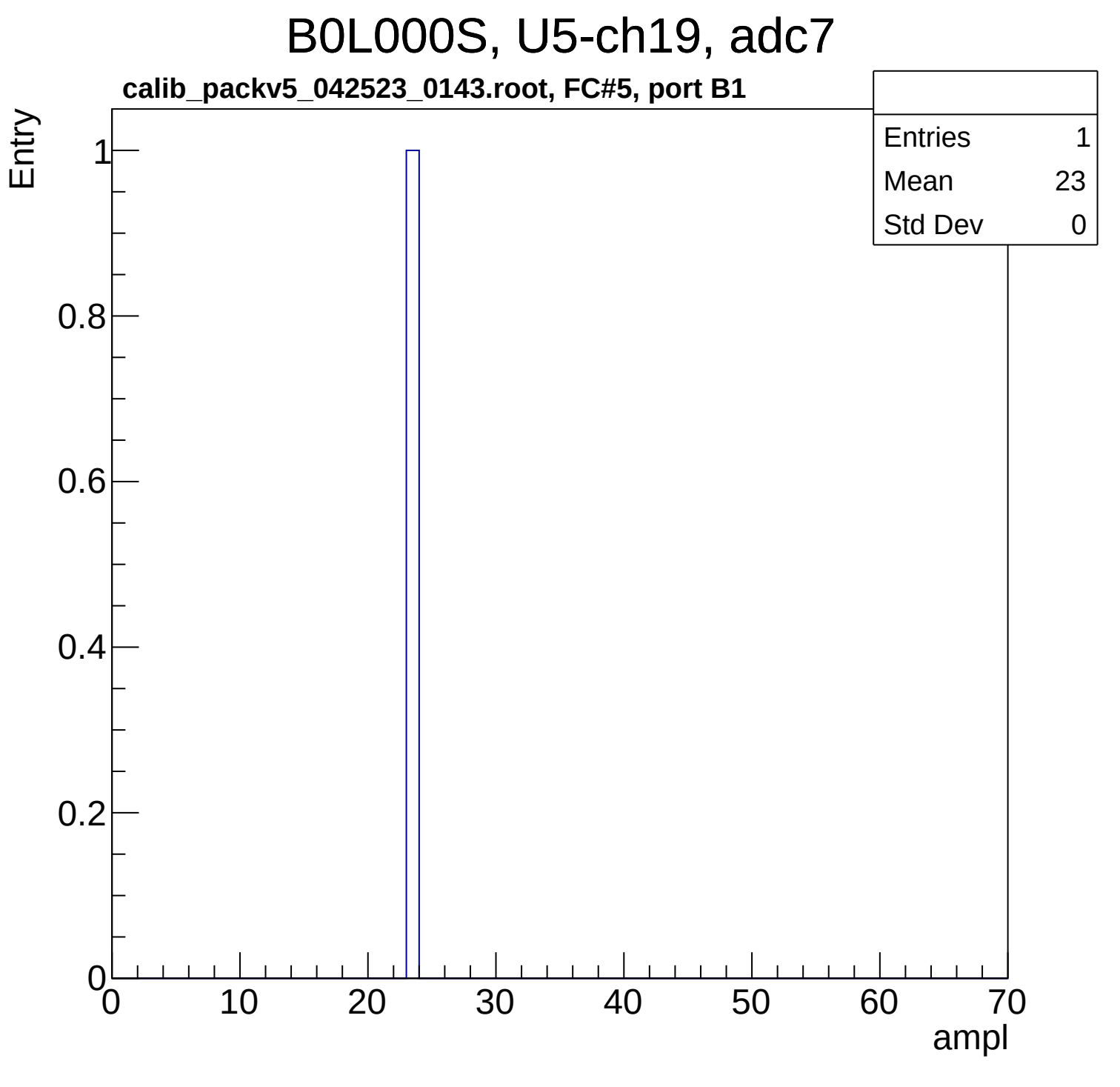

B0L000S, U5-ch19, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch19, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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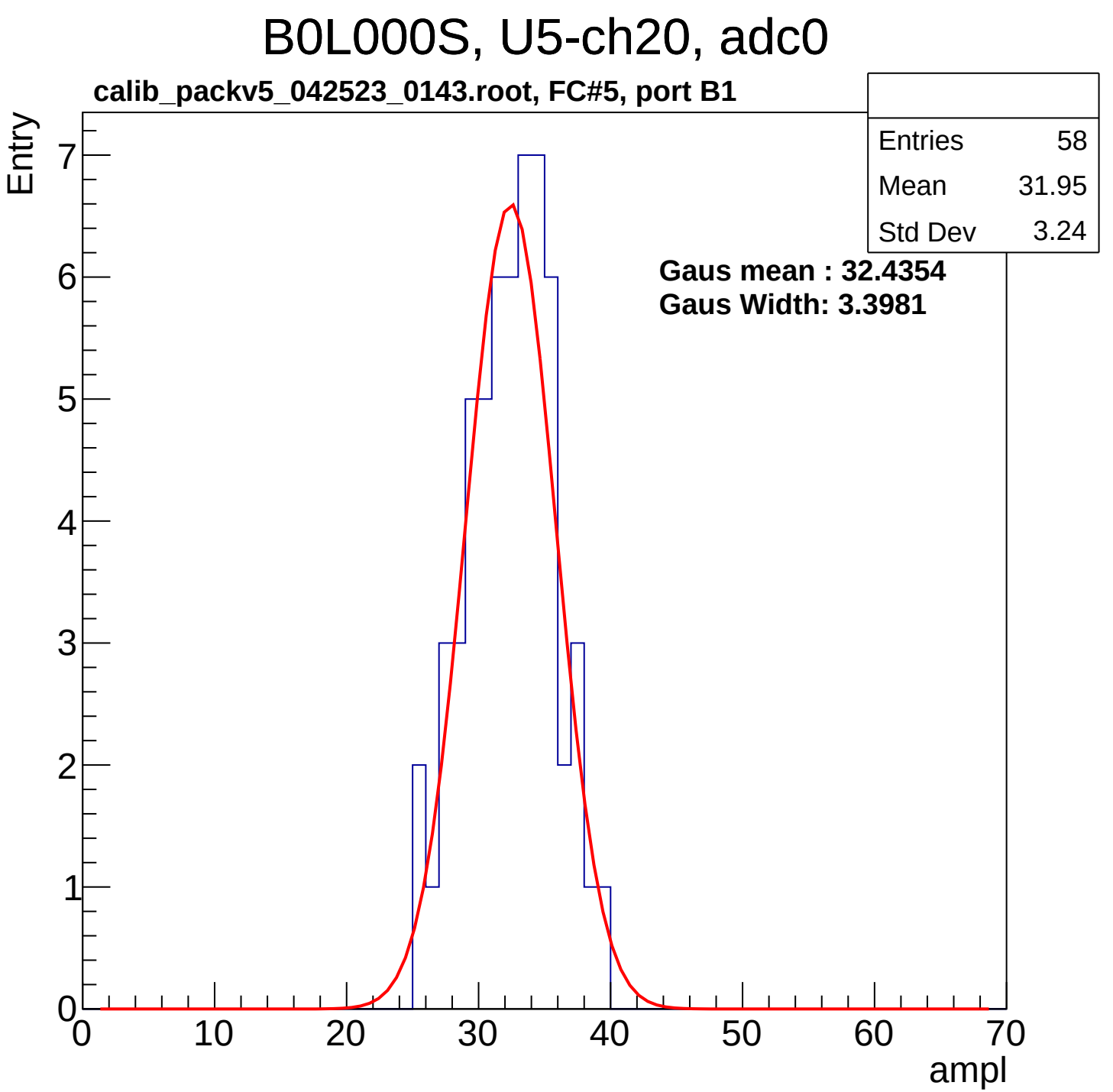
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch20, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 31.95
Std Dev 3.24
Gaus mean : 32.4354
Gaus Width: 3.3981
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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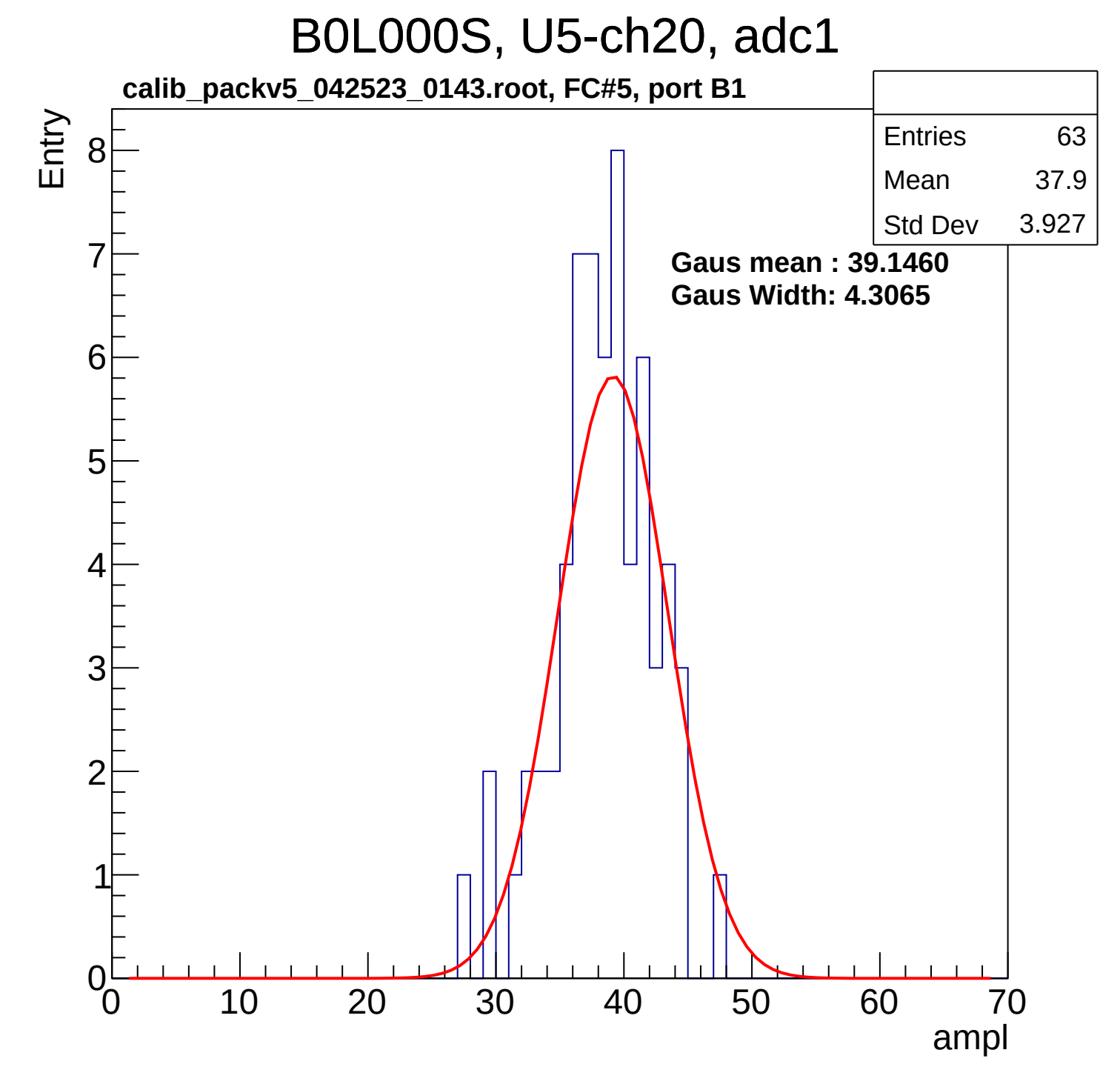
B0L000S, U5-ch20, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 37.9
Std Dev 3.927
Gaus mean : 39.1460
Gaus Width: 4.3065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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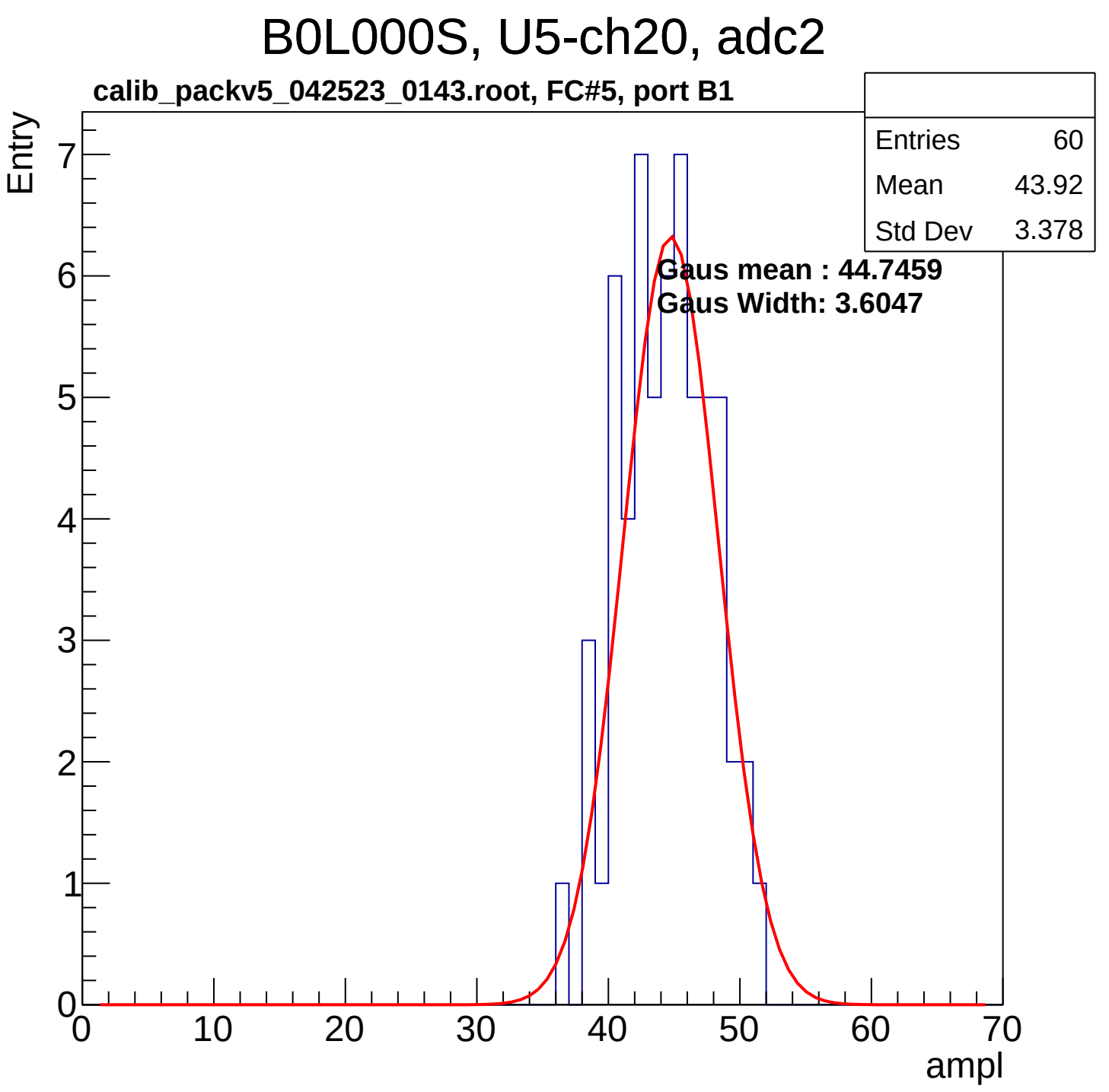
B0L000S, U5-ch20, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 43.92
Std Dev 3.378
Gaus mean : 44.7459
Gaus Width: 3.6047
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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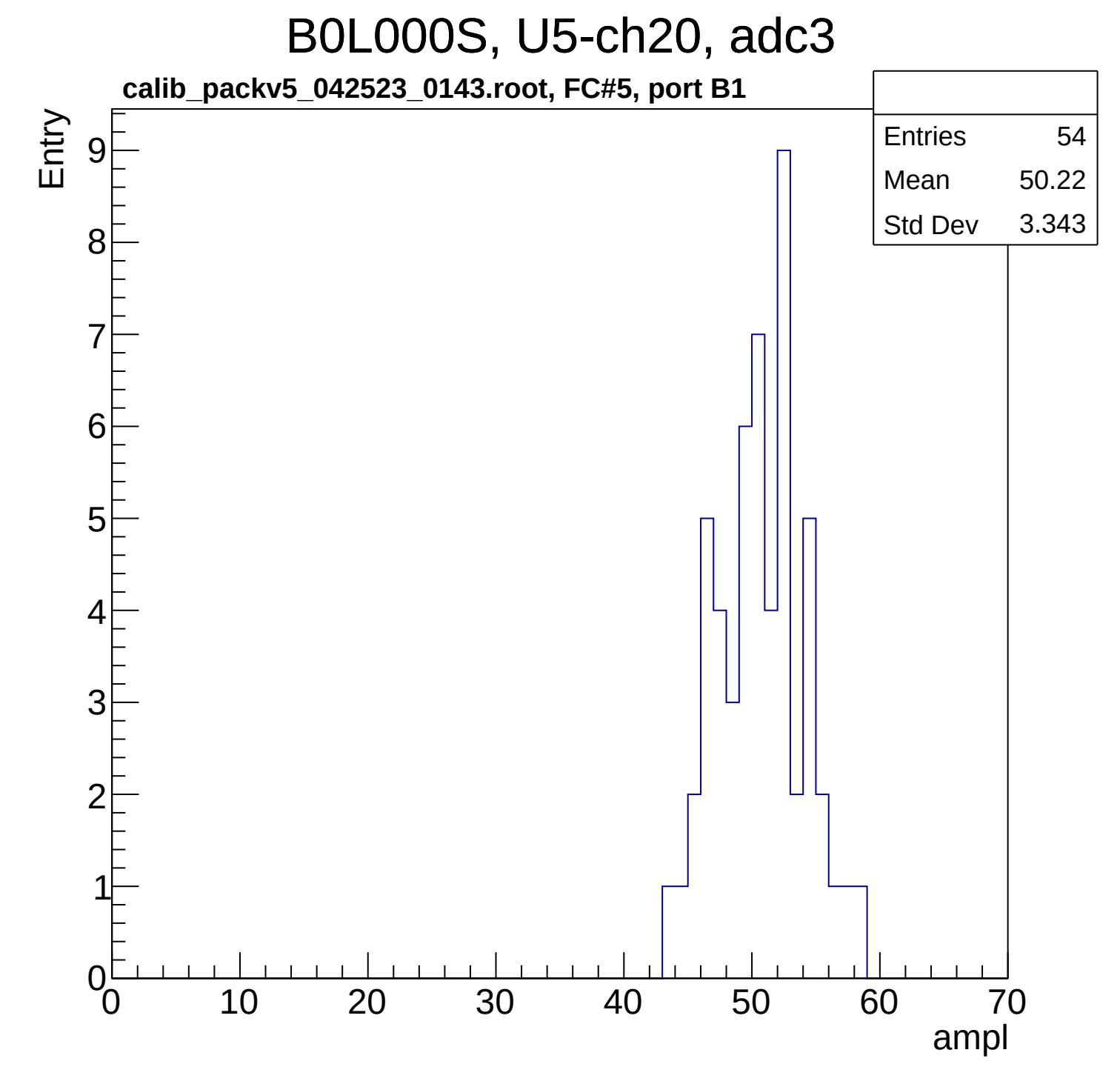
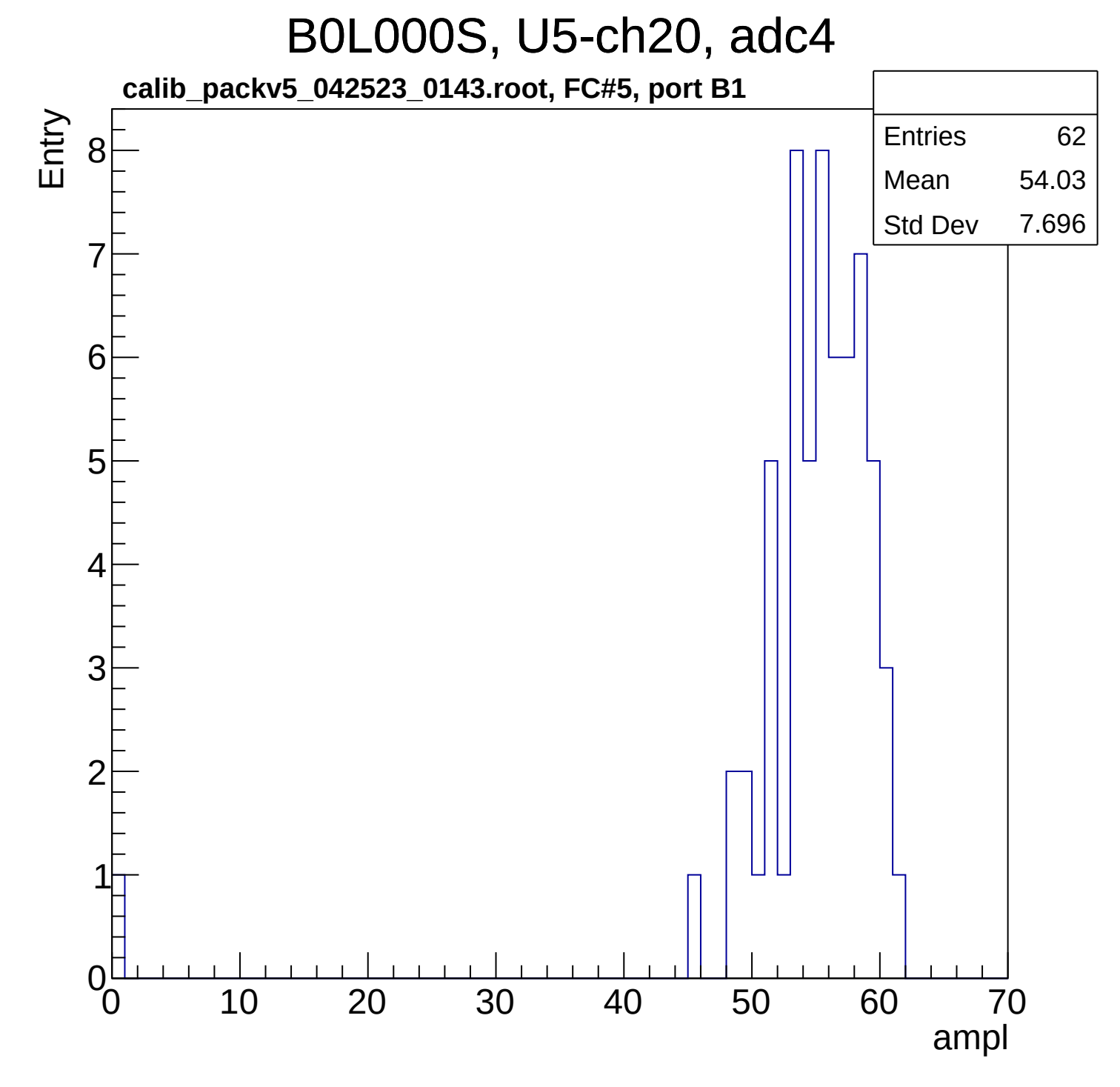

B0L000S, U5-ch20, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 50.22
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch20, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 54.03
Std Dev 7.696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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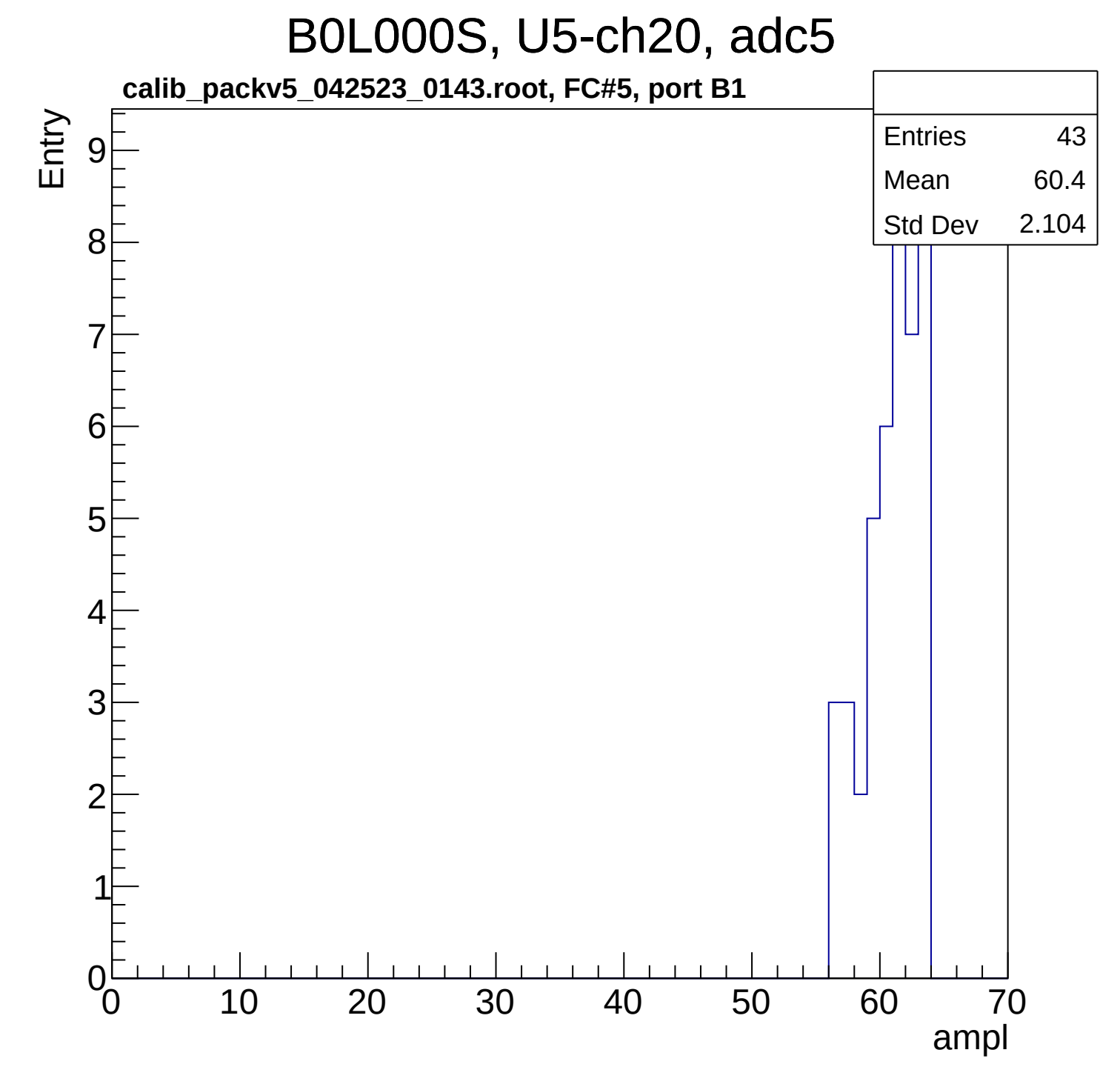
B0L000S, U5-ch20, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.4
Std Dev 2.104
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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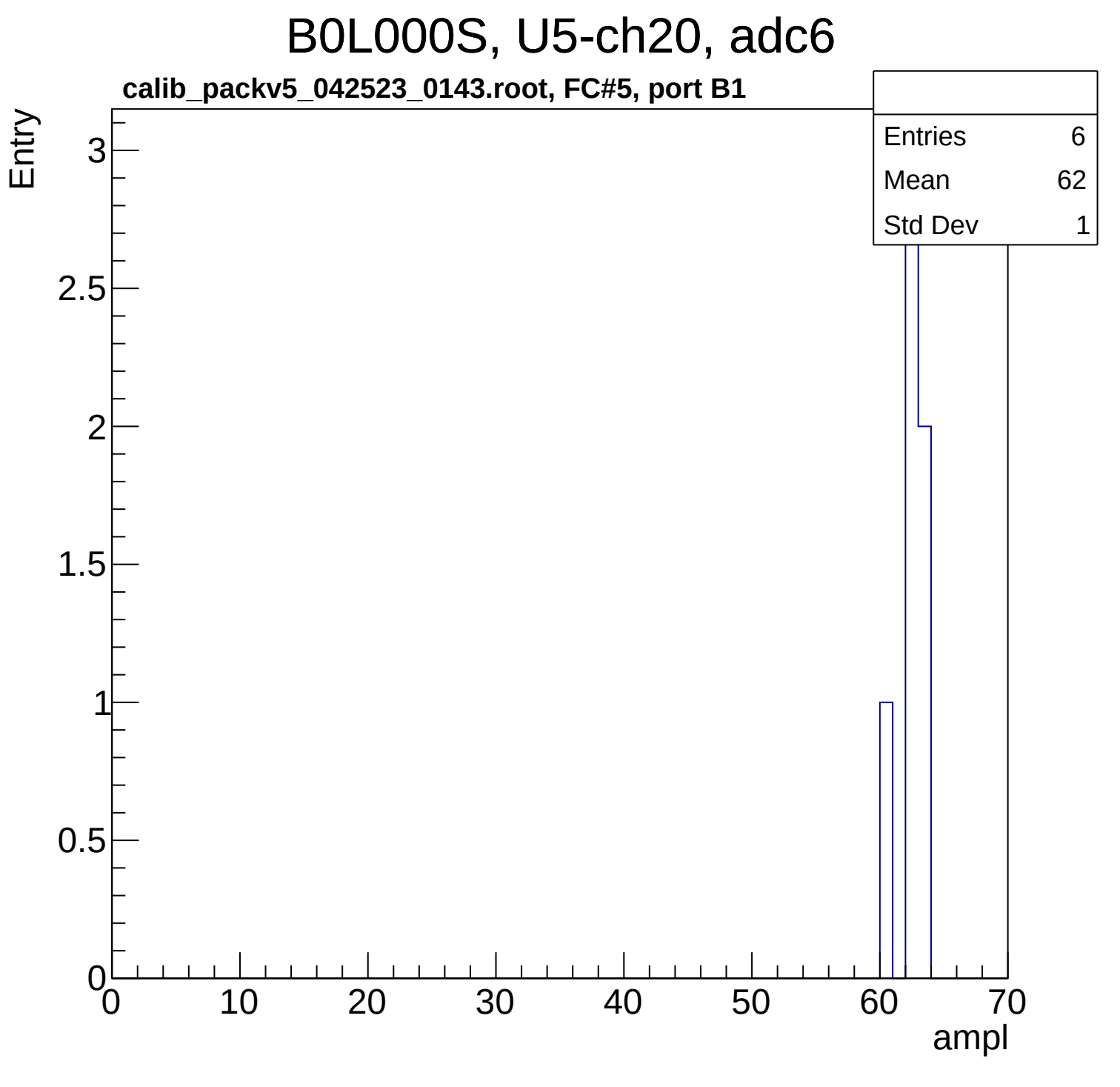
B0L000S, U5-ch20, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 1
Entry
ampl
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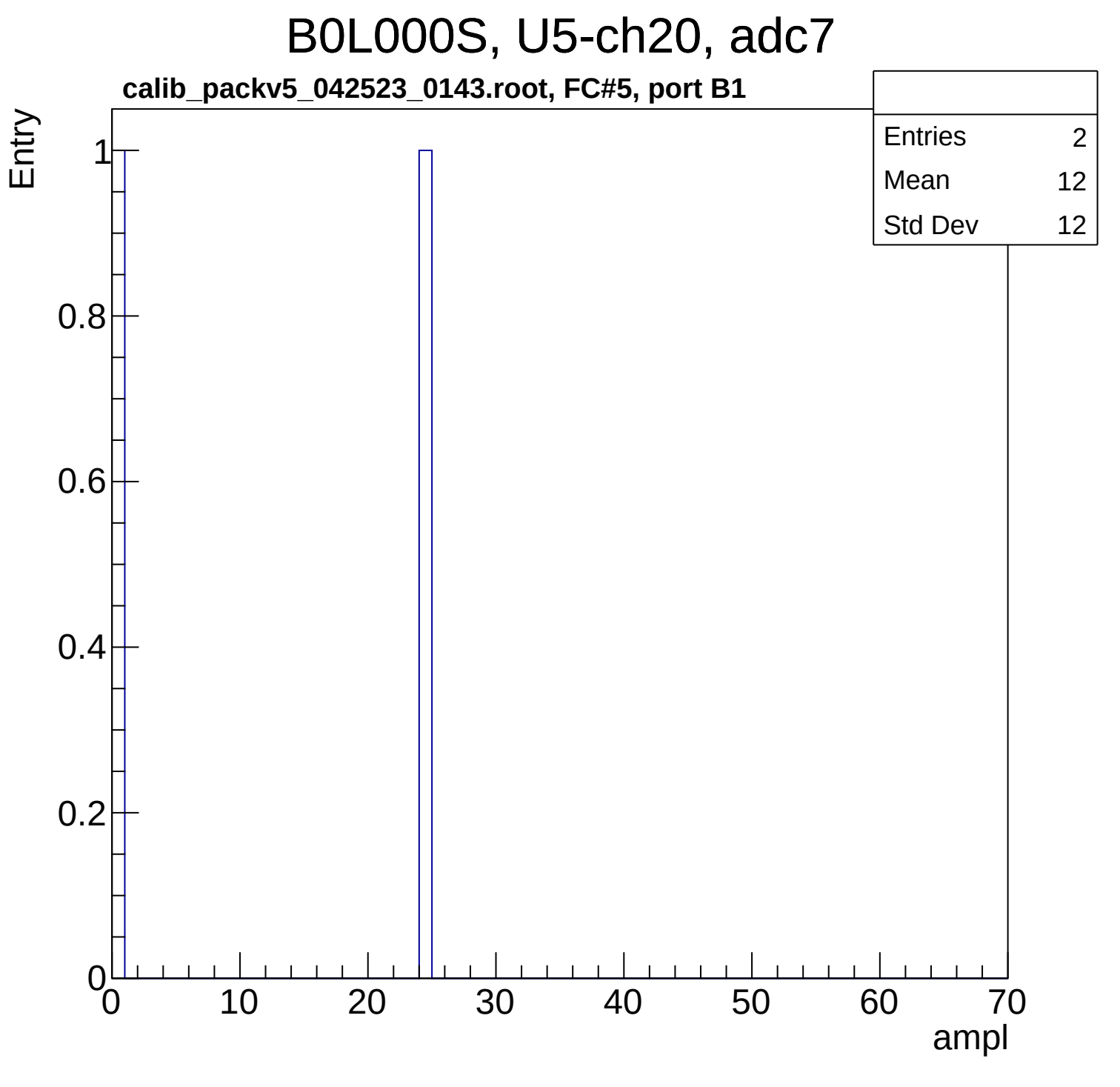
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch20, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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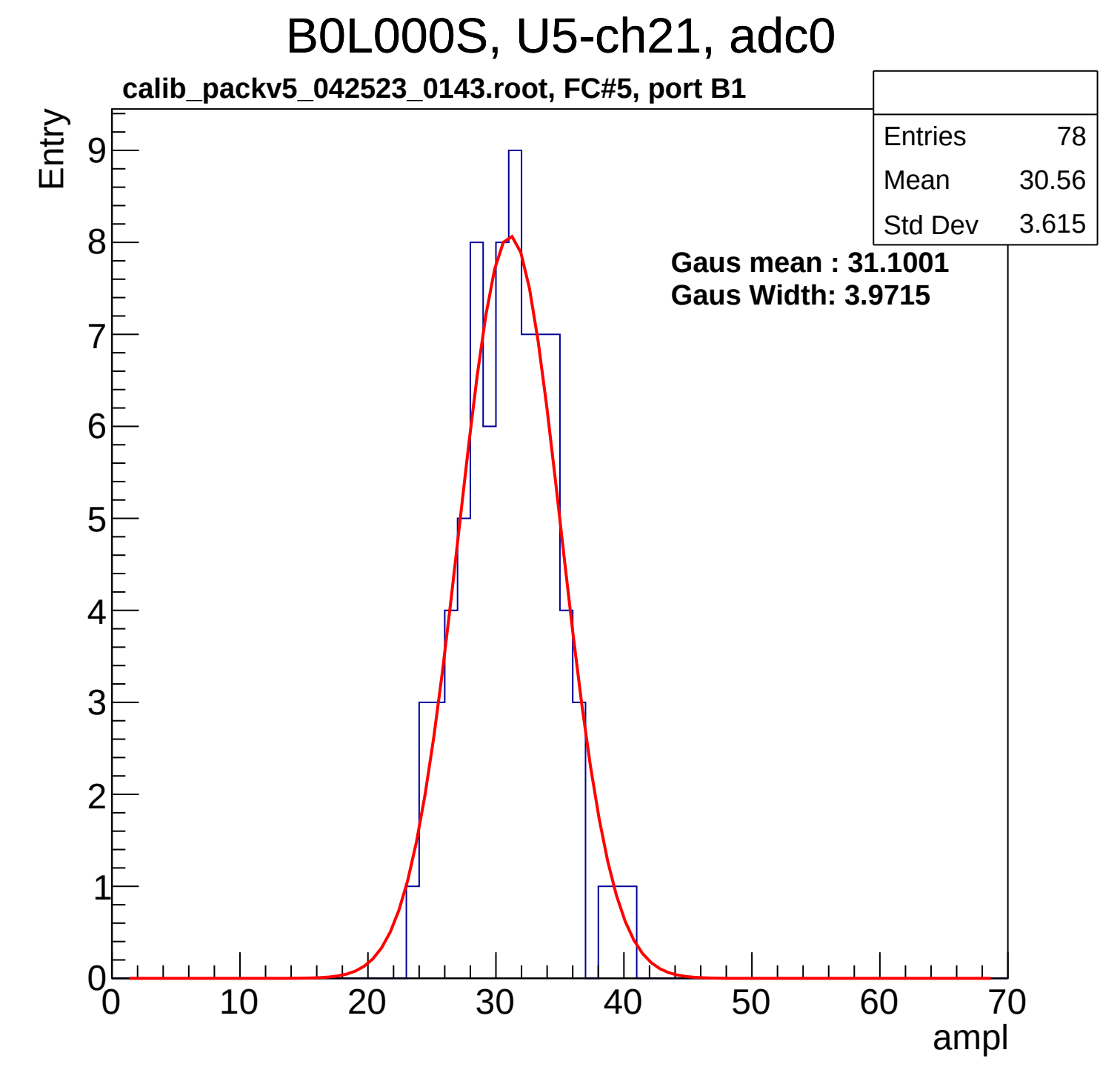
50
60
70
ampl


B0L000S, U5-ch21, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 30.56
Std Dev 3.615
Gaus mean : 31.1001
Gaus Width: 3.9715
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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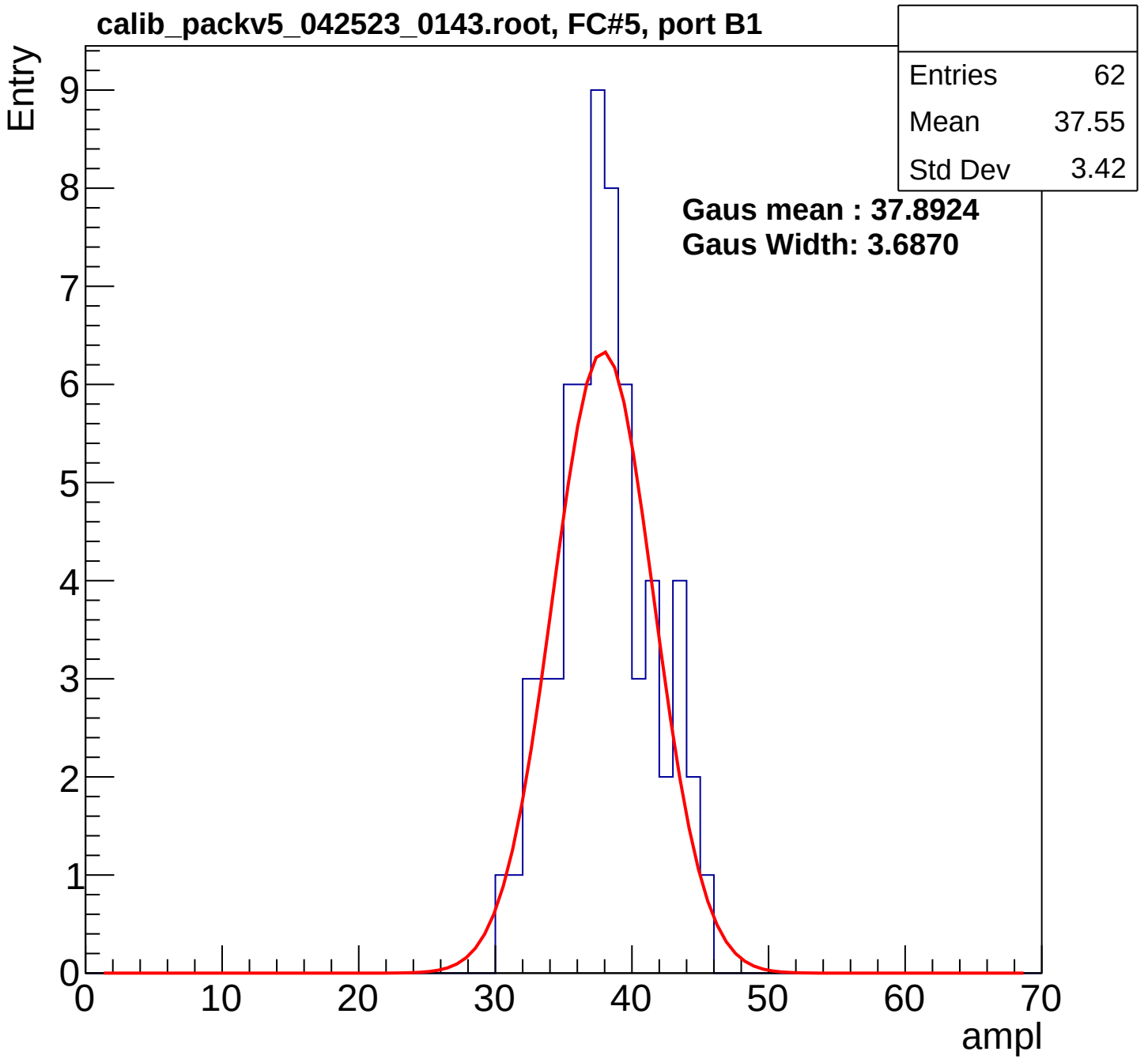
40
50
60
70


B0L000S, U5-ch21, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.55
Std Dev 3.42
Gaus mean : 37.8924
Gaus Width: 3.6870
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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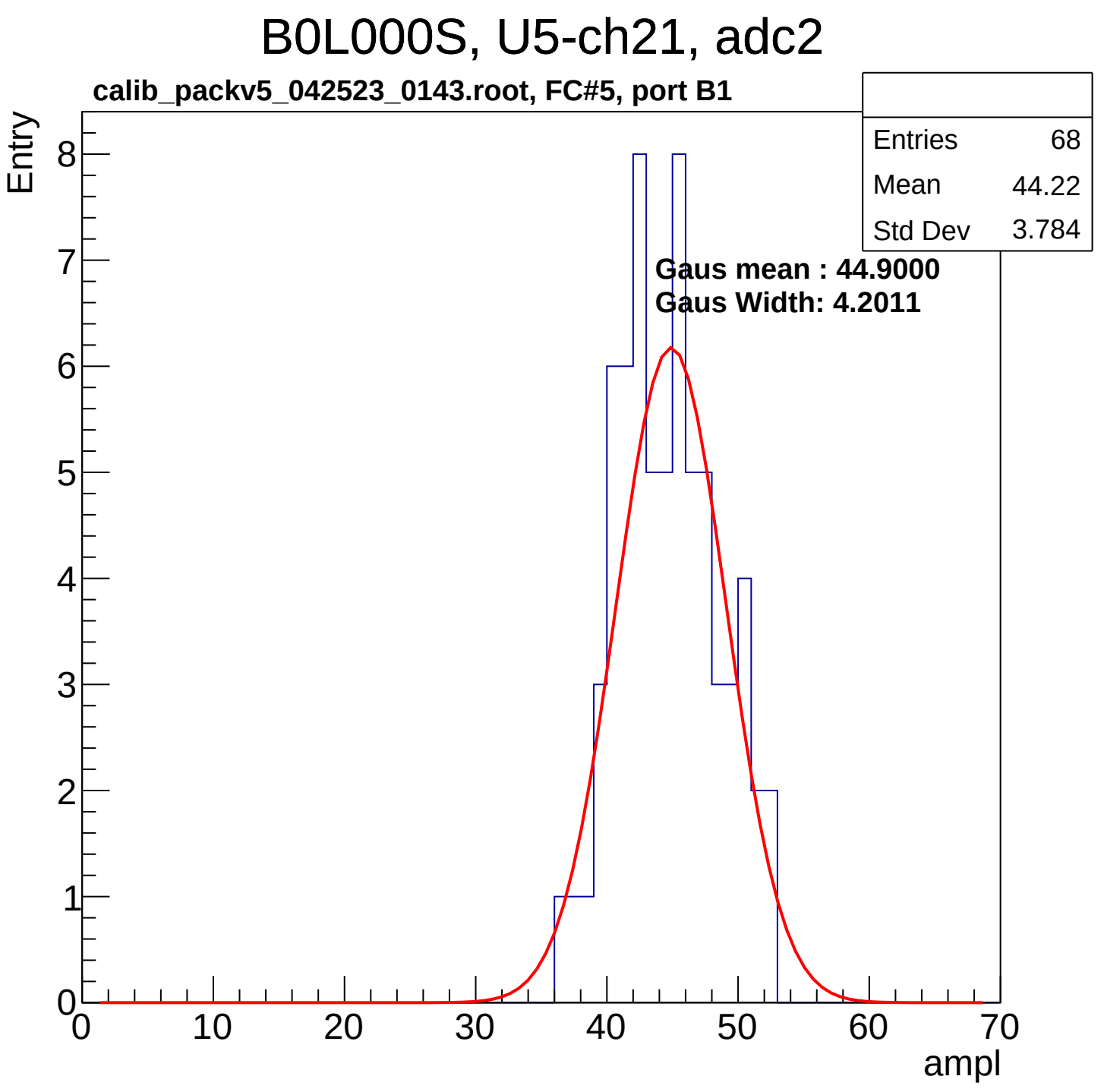
B0L000S, U5-ch21, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.22
Std Dev 3.784
Gaus mean : 44.9000
Gaus Width: 4.2011
Entry
ampl
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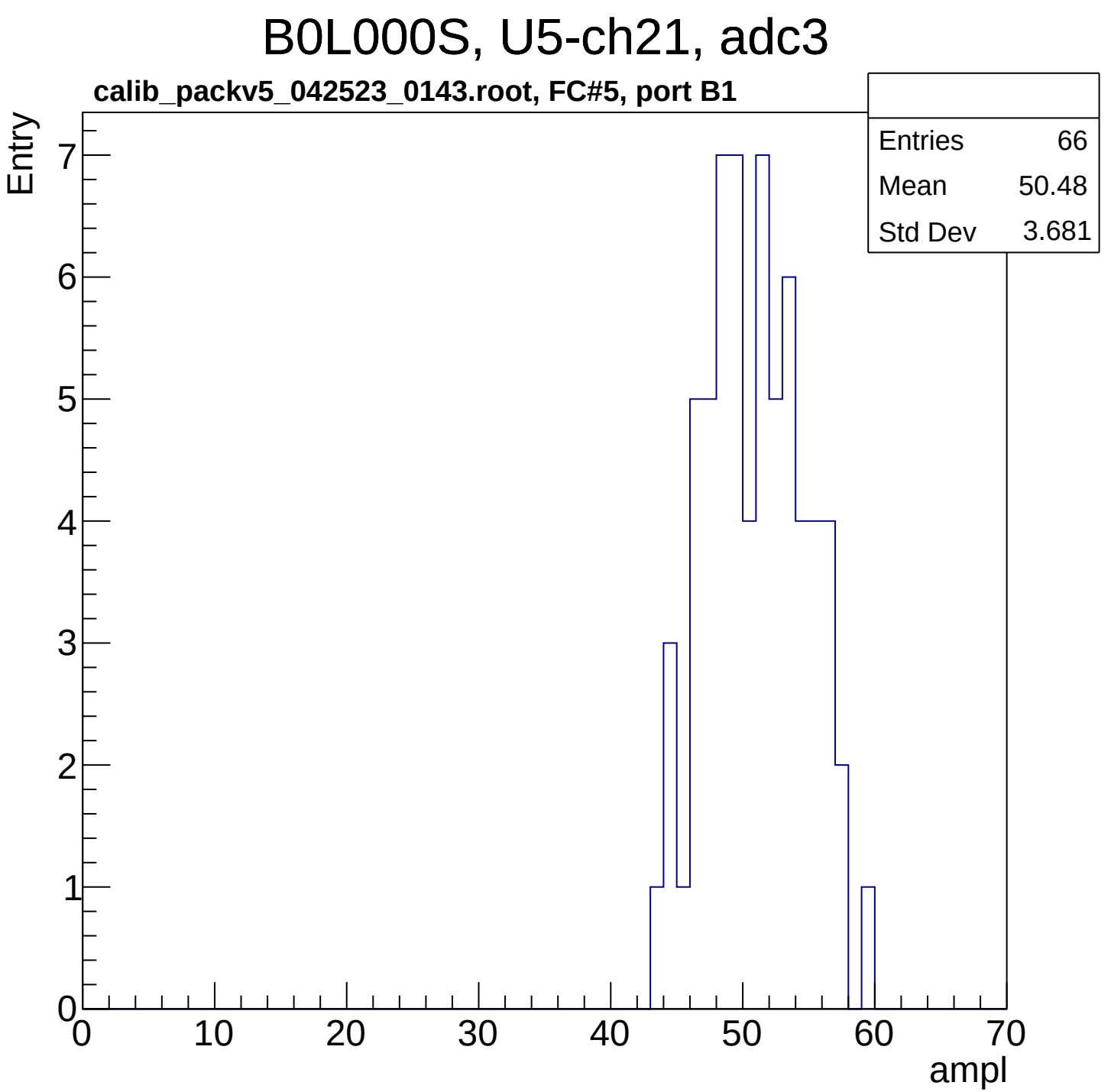
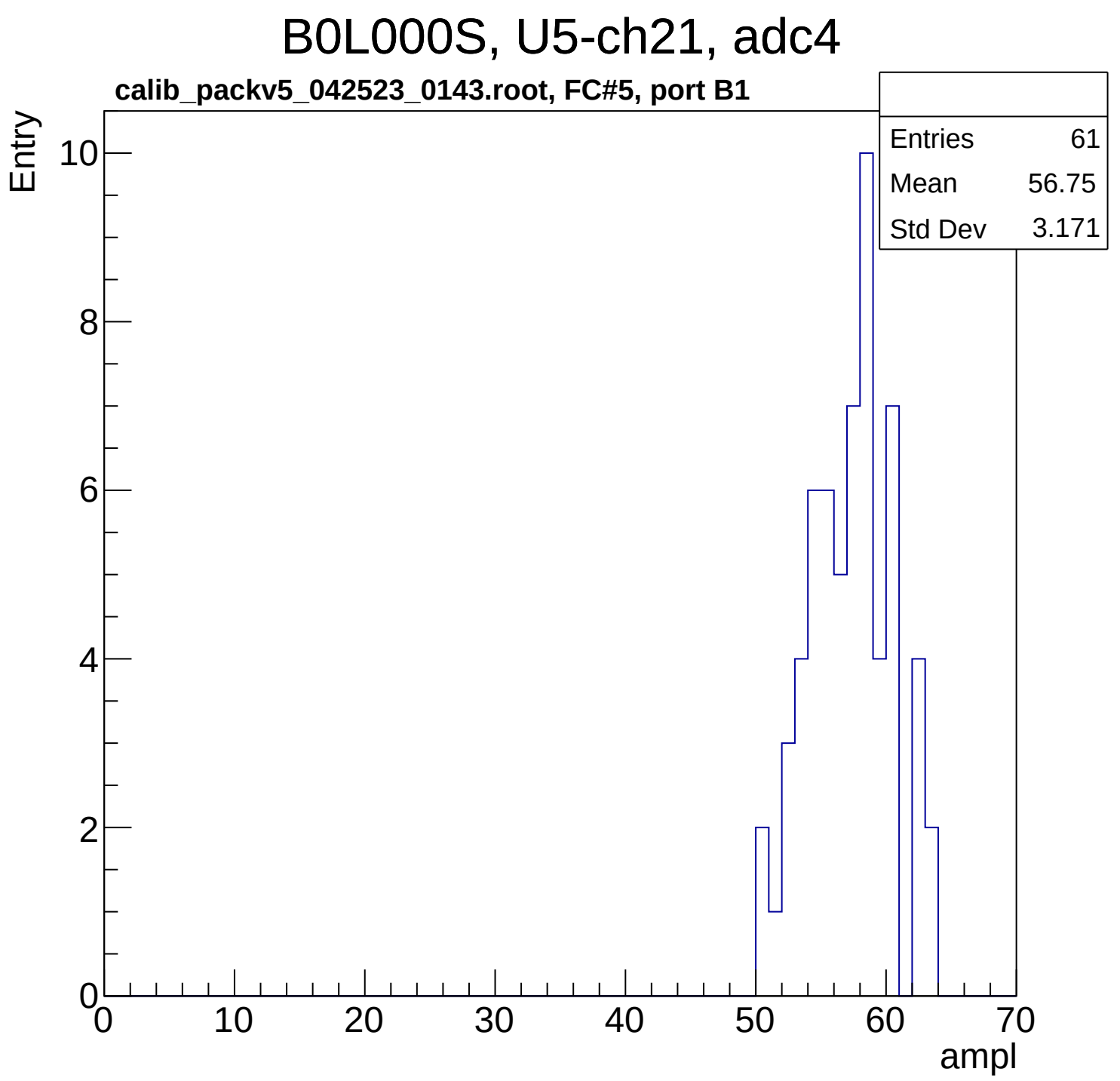
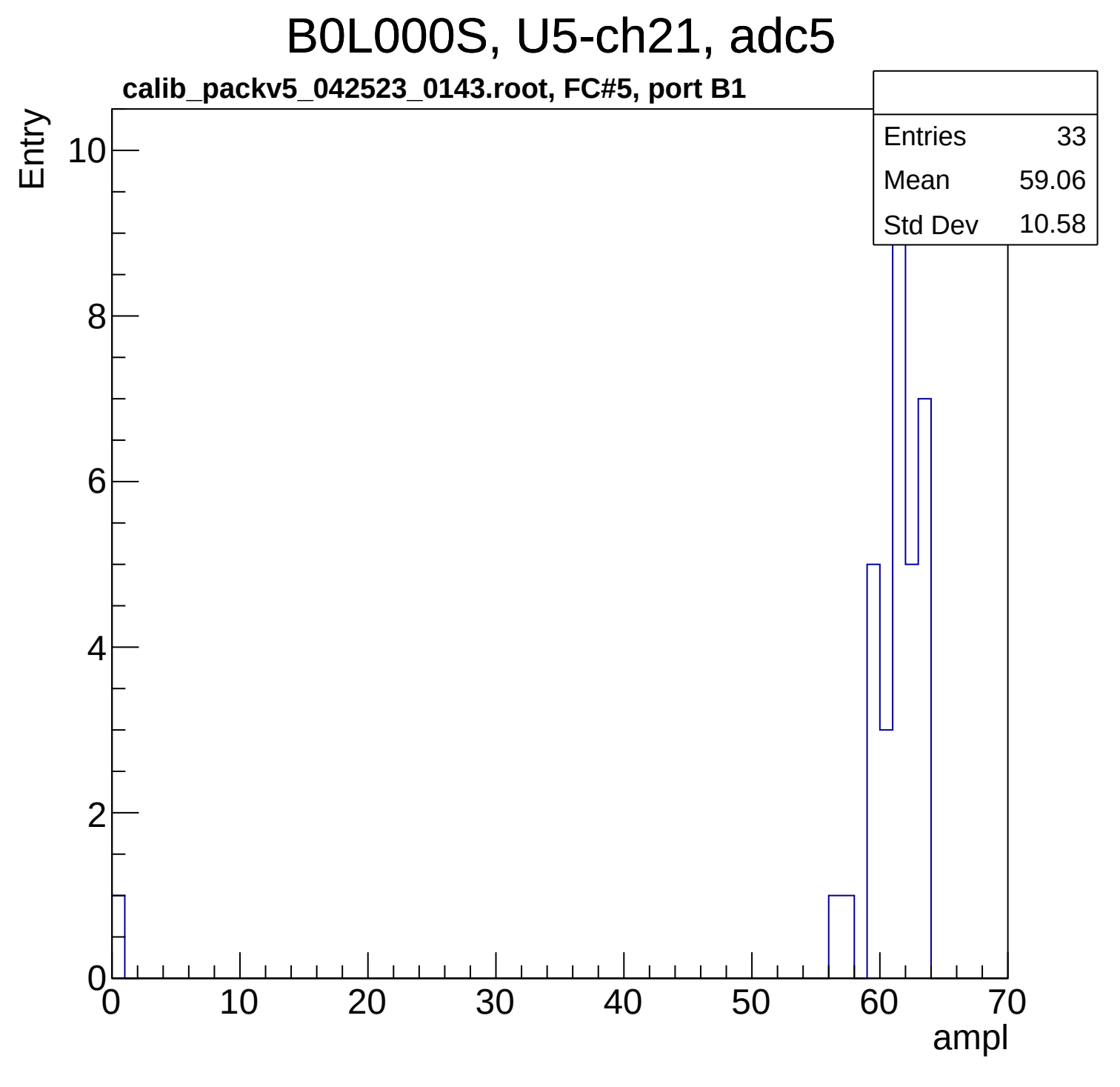
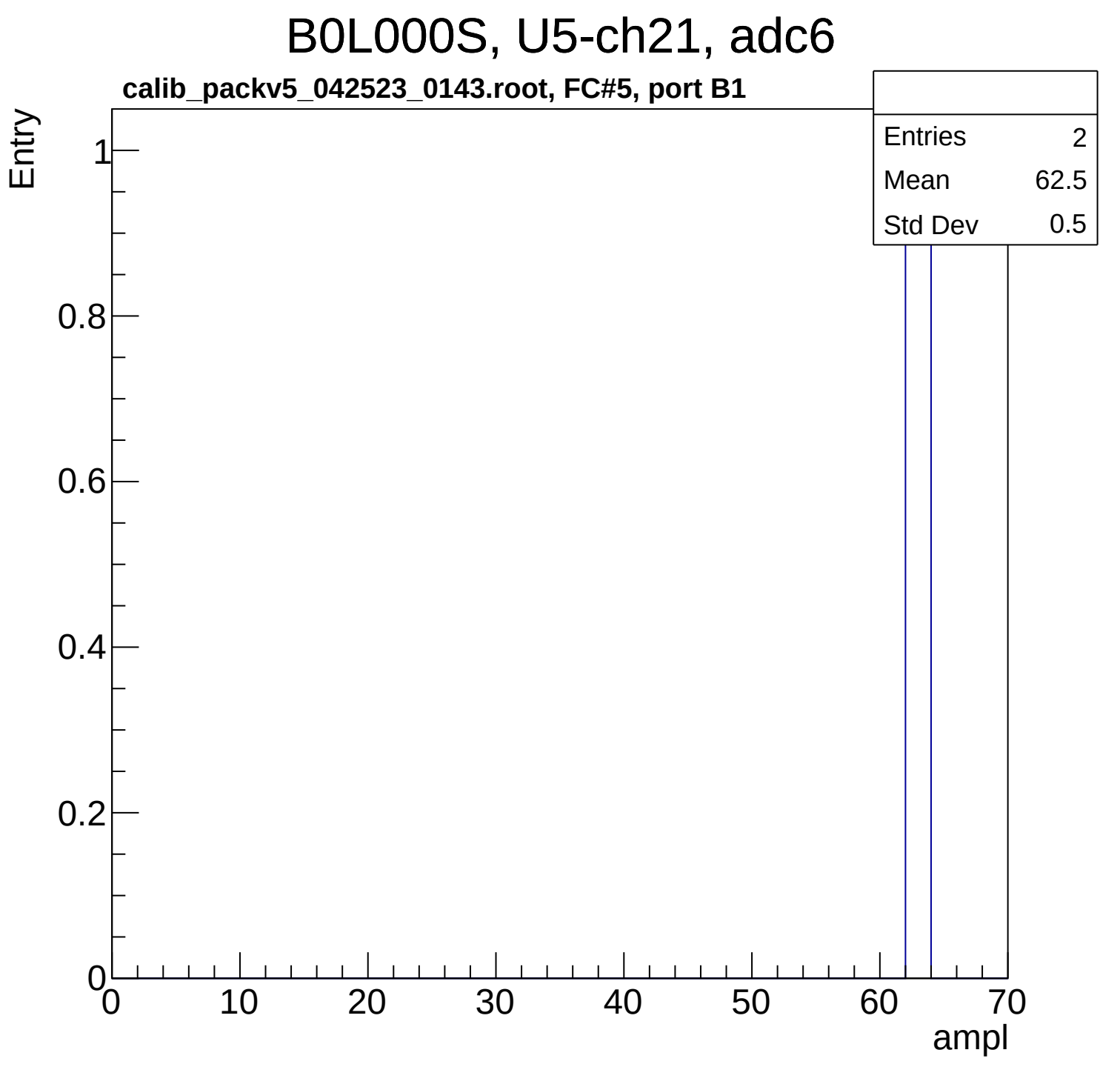
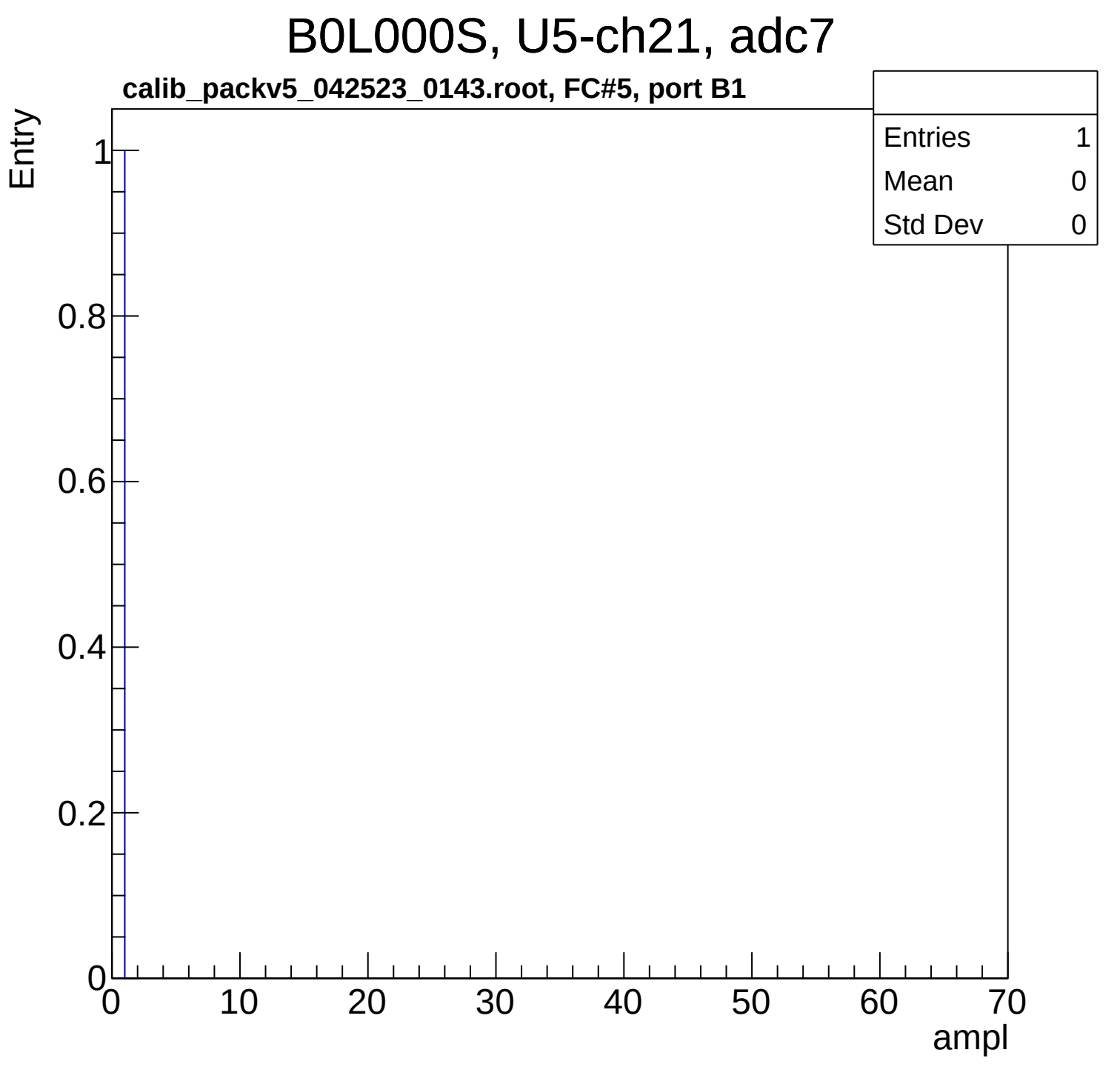
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch21, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 50.48
Std Dev 3.681
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch21, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.75
Std Dev 3.171
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch21, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 59.06
Std Dev 10.58
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch21, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch21, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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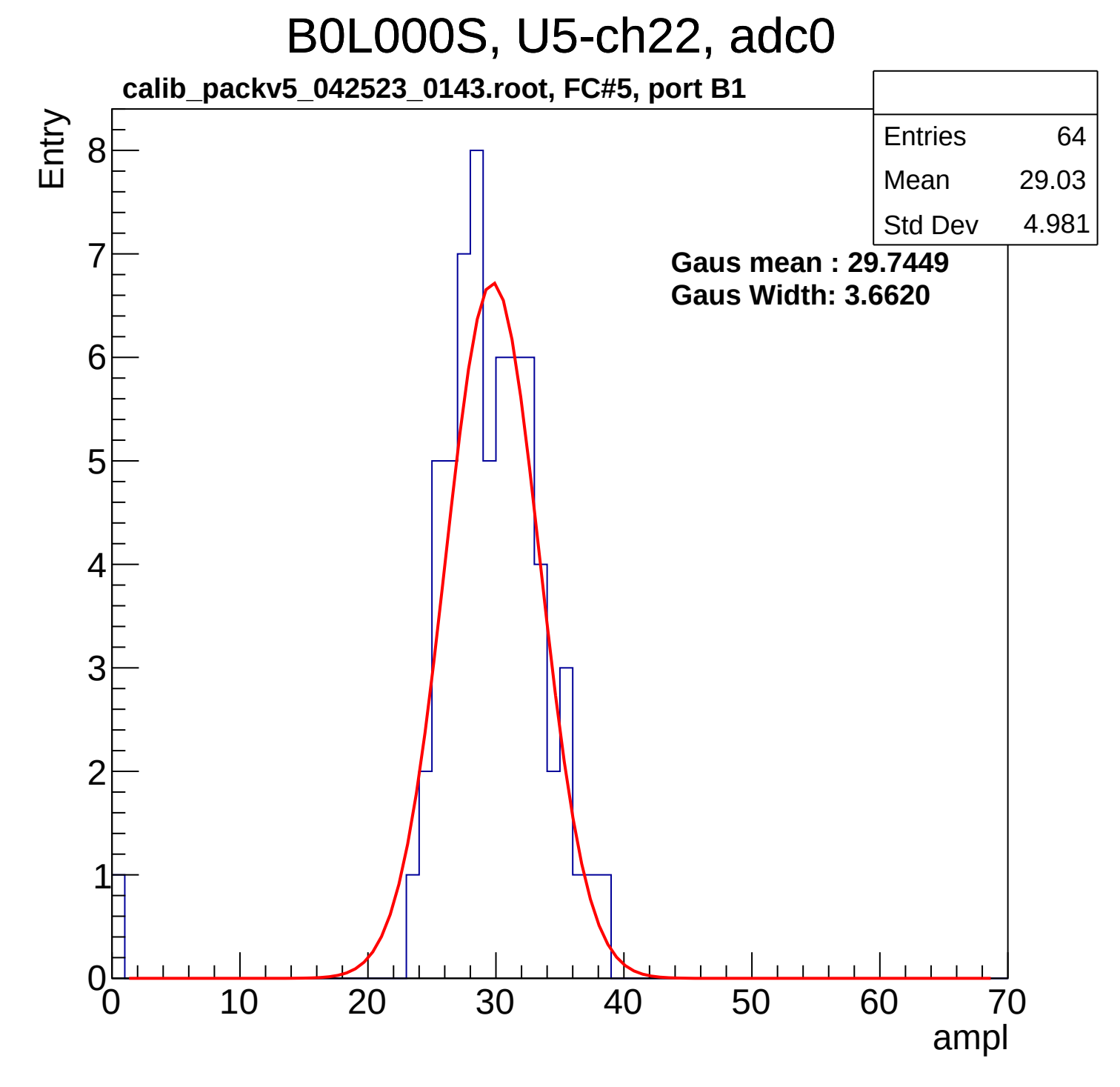
50
60
70
ampl


B0L000S, U5-ch22, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.03
Std Dev 4.981
Gaus mean : 29.7449
Gaus Width: 3.6620
Entry
ampl
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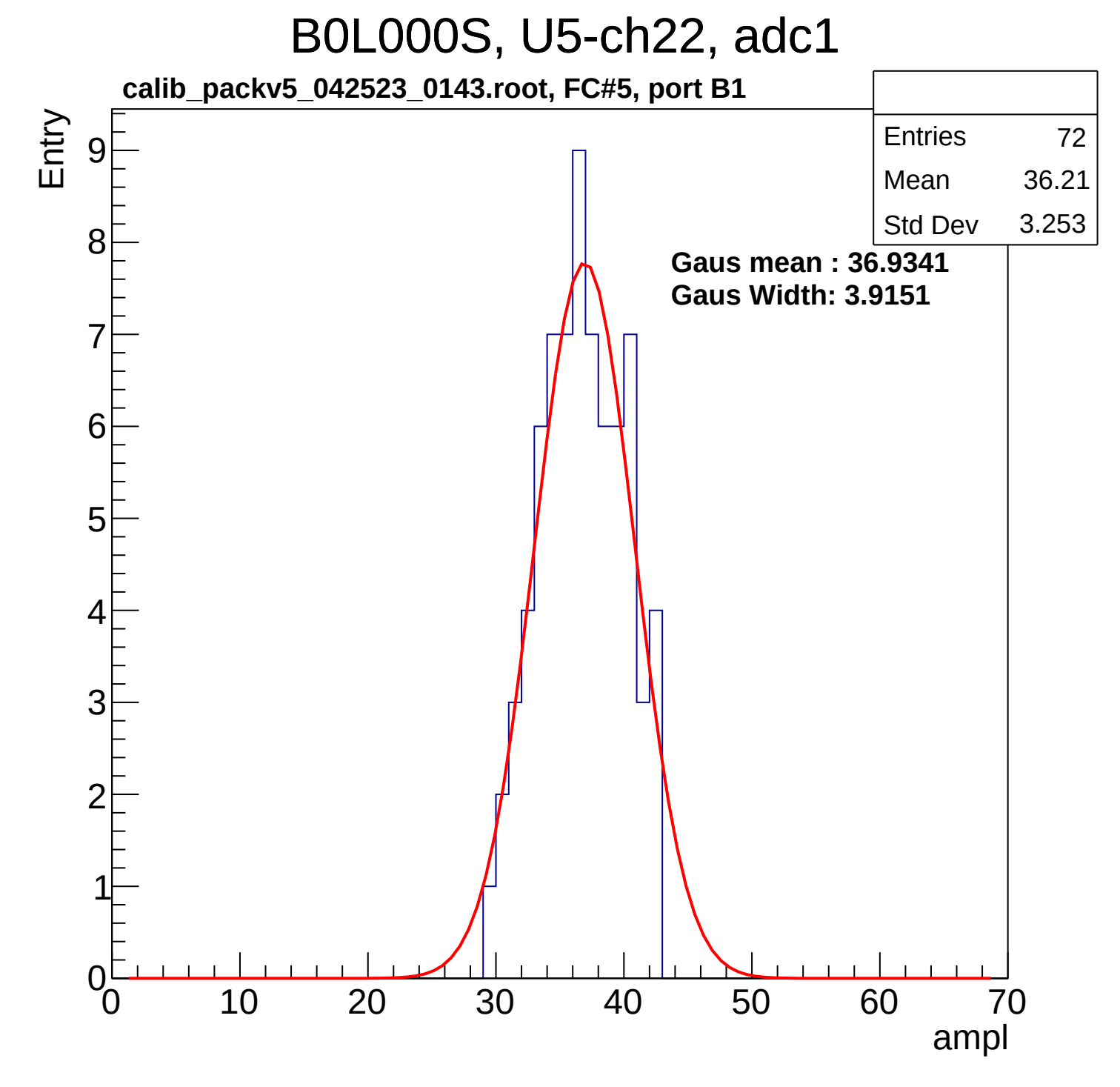
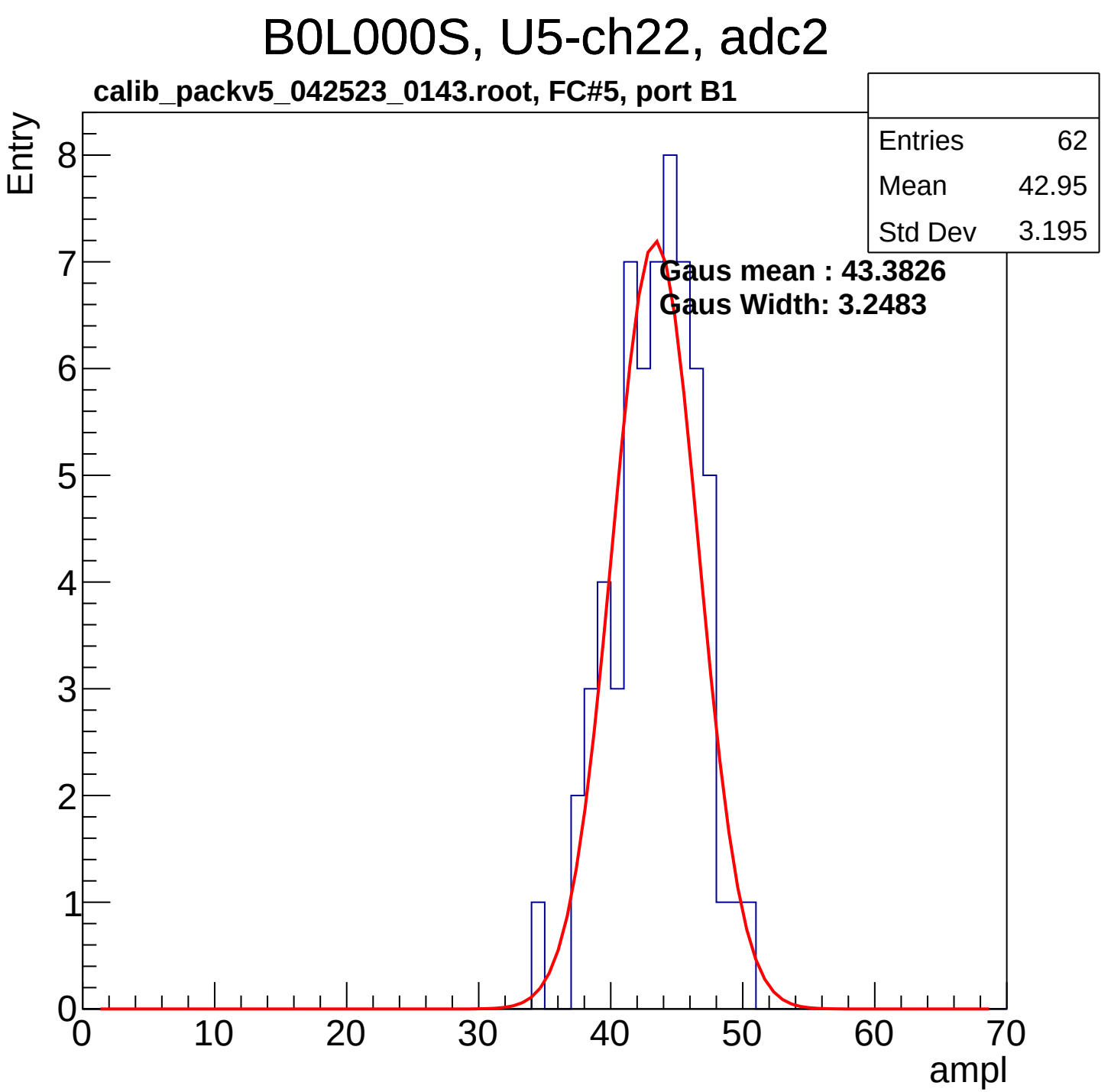
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch22, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.21
Std Dev 3.253
Gaus mean : 36.9341
Gaus Width: 3.9151
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch22, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 42.95
Std Dev 3.195
Gaus mean : 43.3826
Gaus Width: 3.2483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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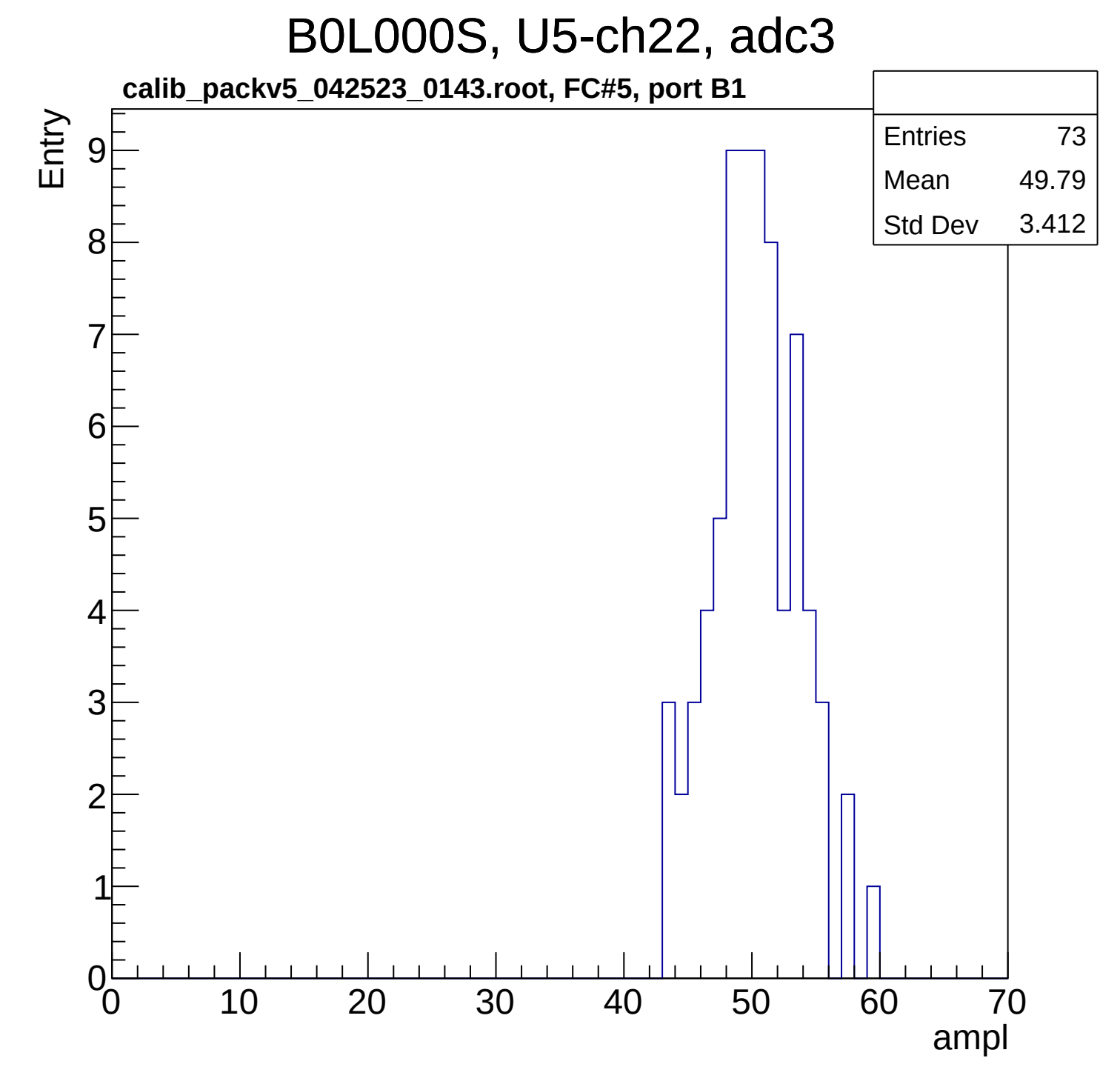
B0L000S, U5-ch22, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.79
Std Dev 3.412
Entry
ampl
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3
4
5
6
7
8
9
0
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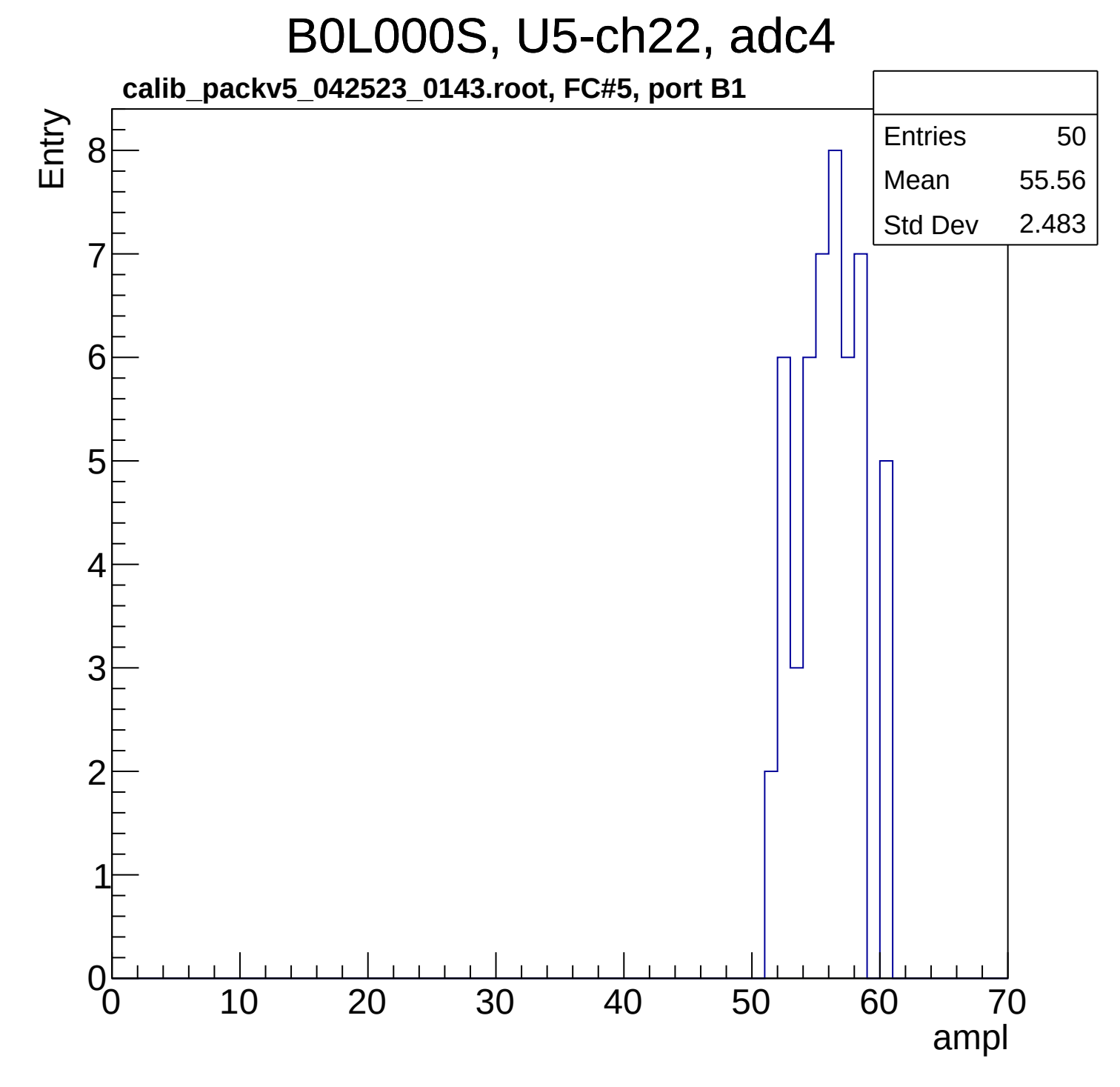
B0L000S, U5-ch22, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.56
Std Dev 2.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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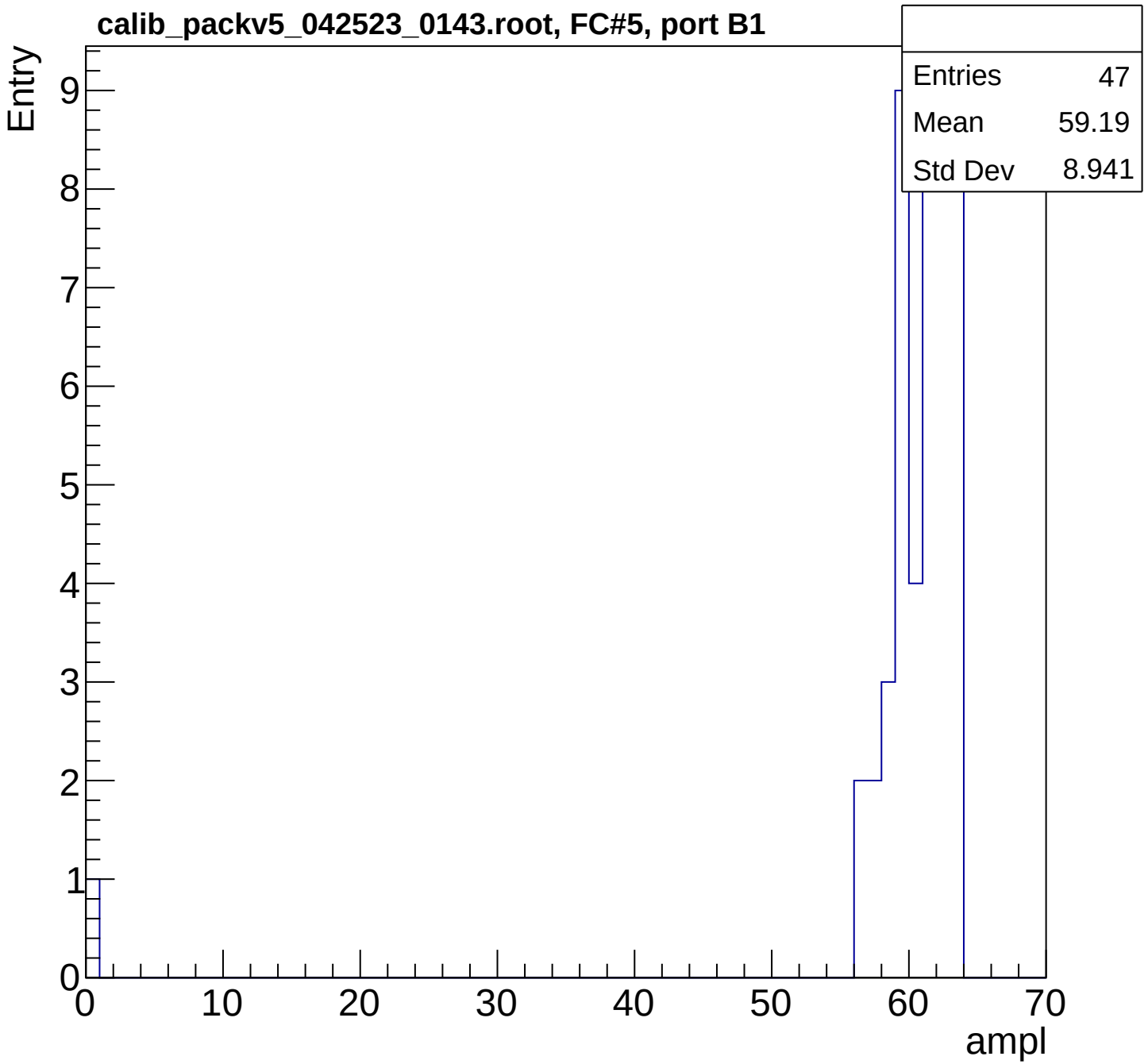
B0L000S, U5-ch22, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.19
Std Dev 8.941
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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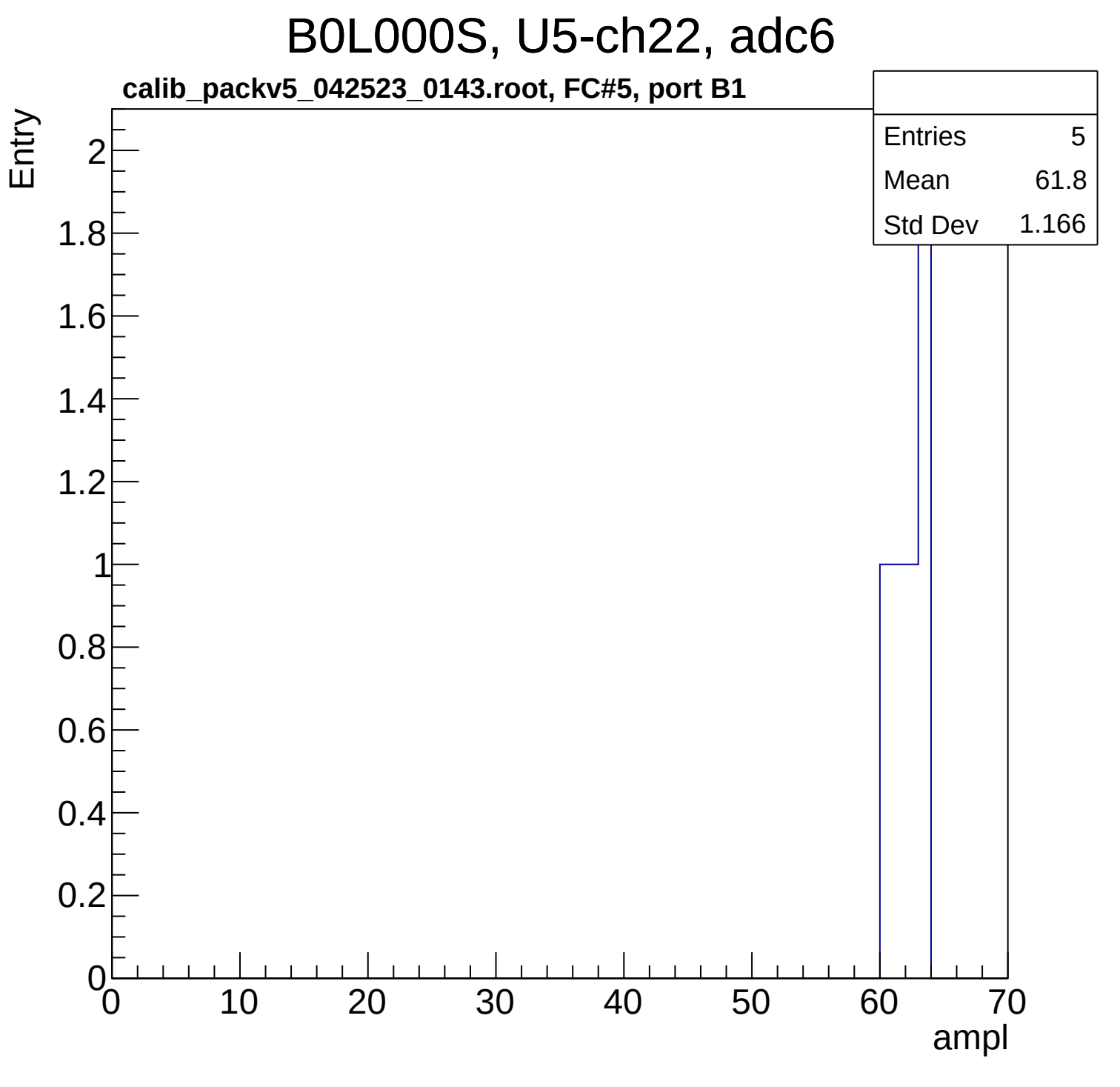
B0L000S, U5-ch22, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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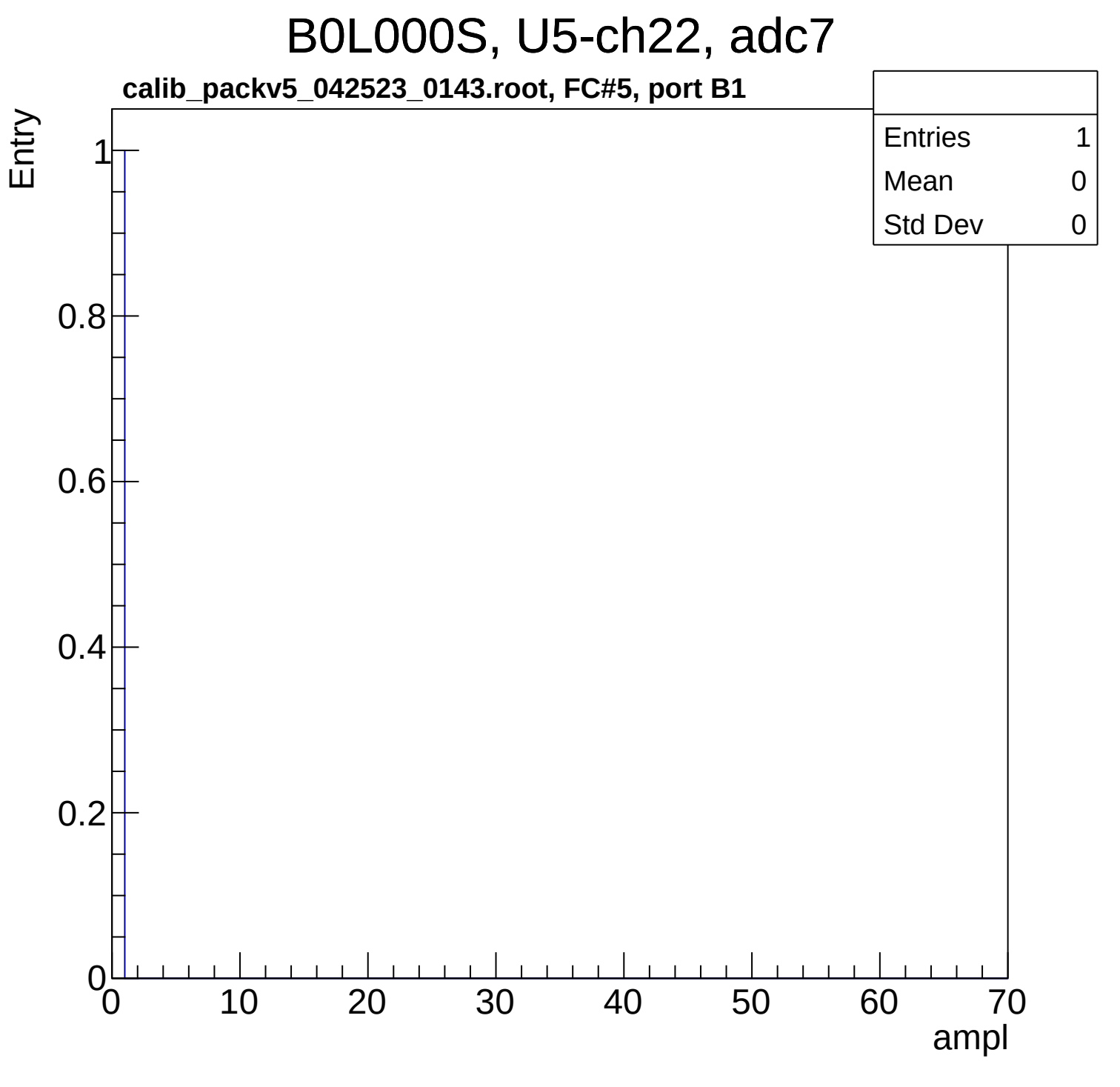
B0L000S, U5-ch22, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
30
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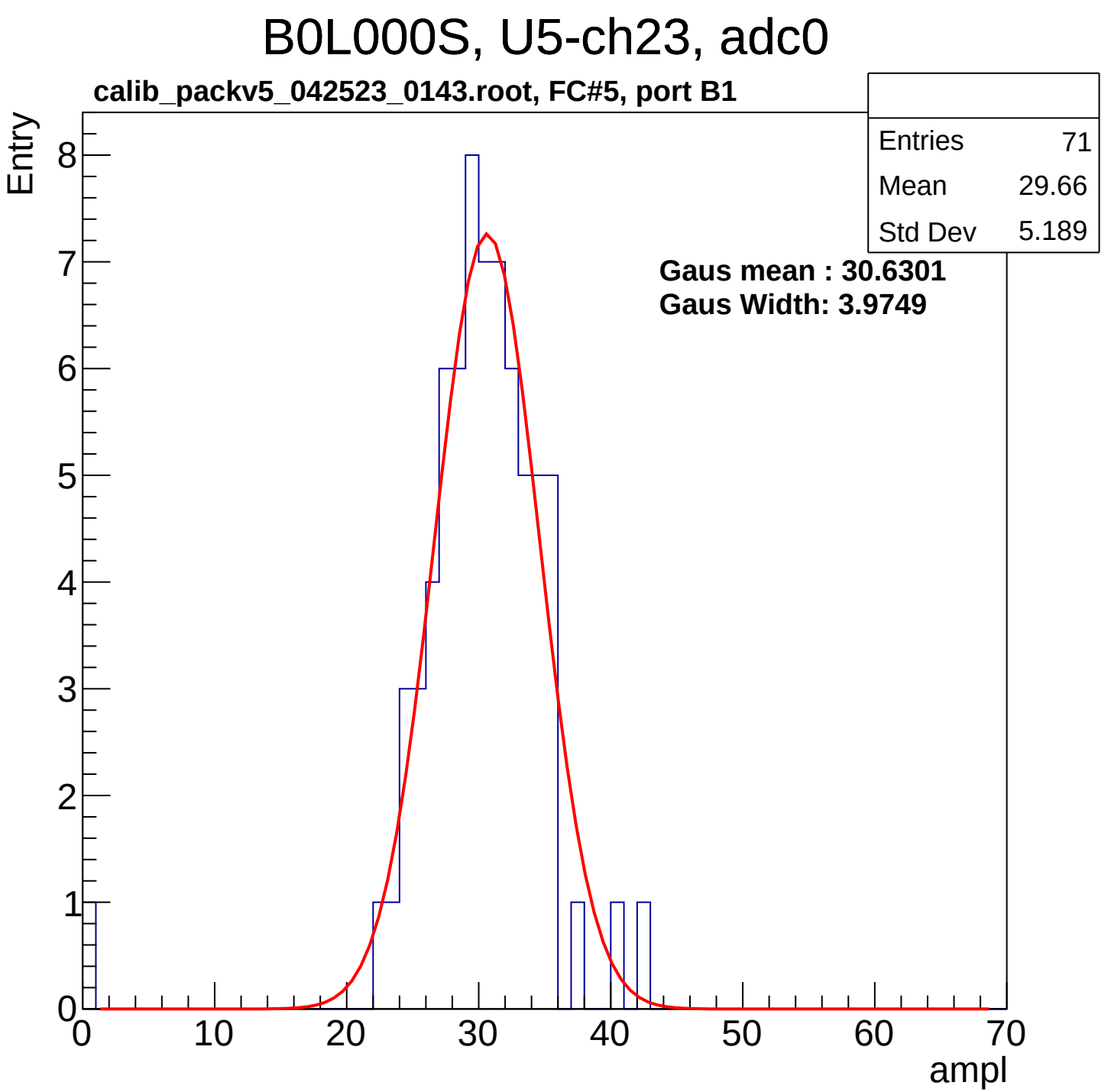
50
60
70
ampl


B0L000S, U5-ch23, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 29.66
Std Dev 5.189
Gaus mean : 30.6301
Gaus Width: 3.9749
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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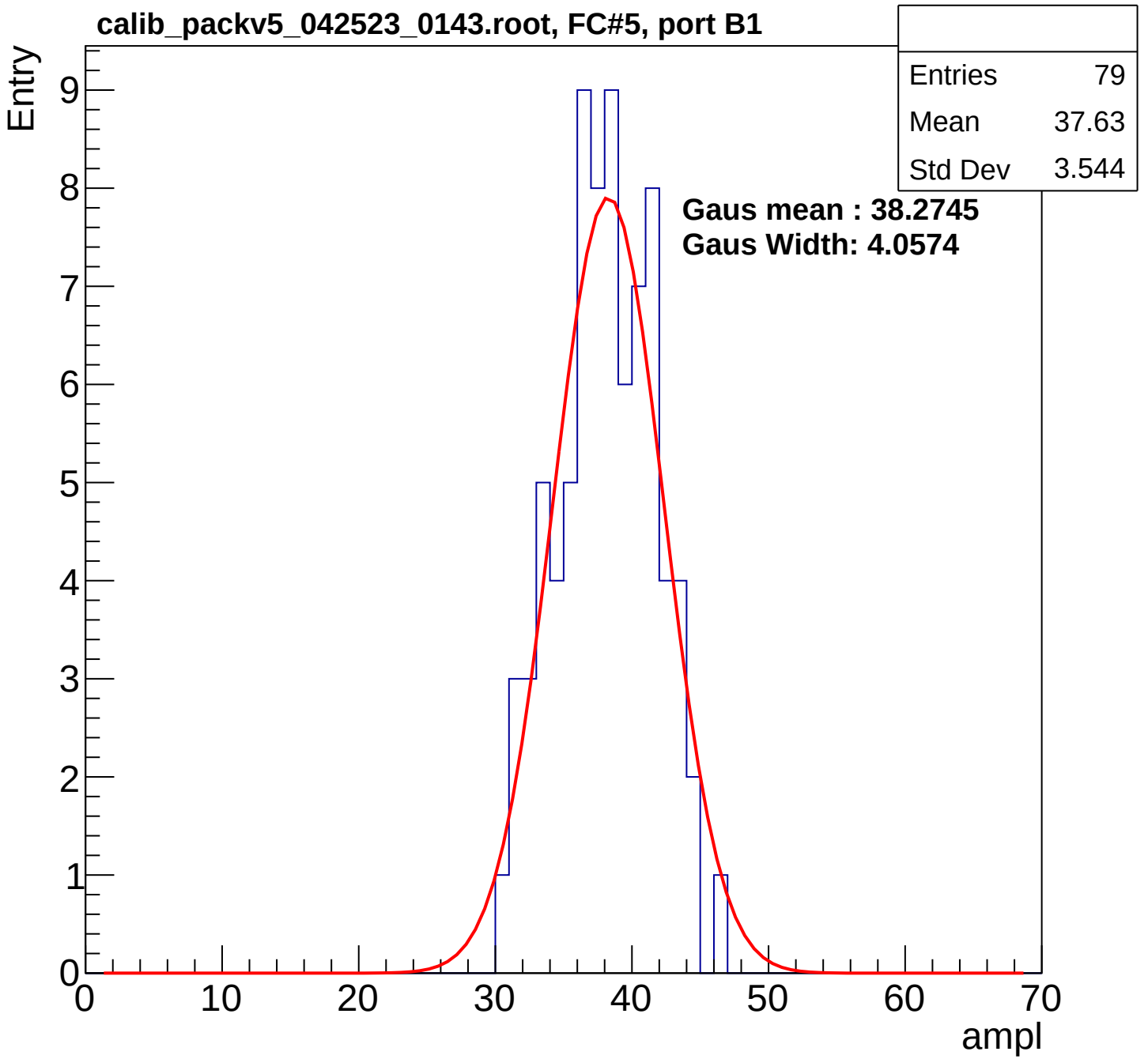
B0L000S, U5-ch23, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 37.63
Std Dev 3.544
Gaus mean : 38.2745
Gaus Width: 4.0574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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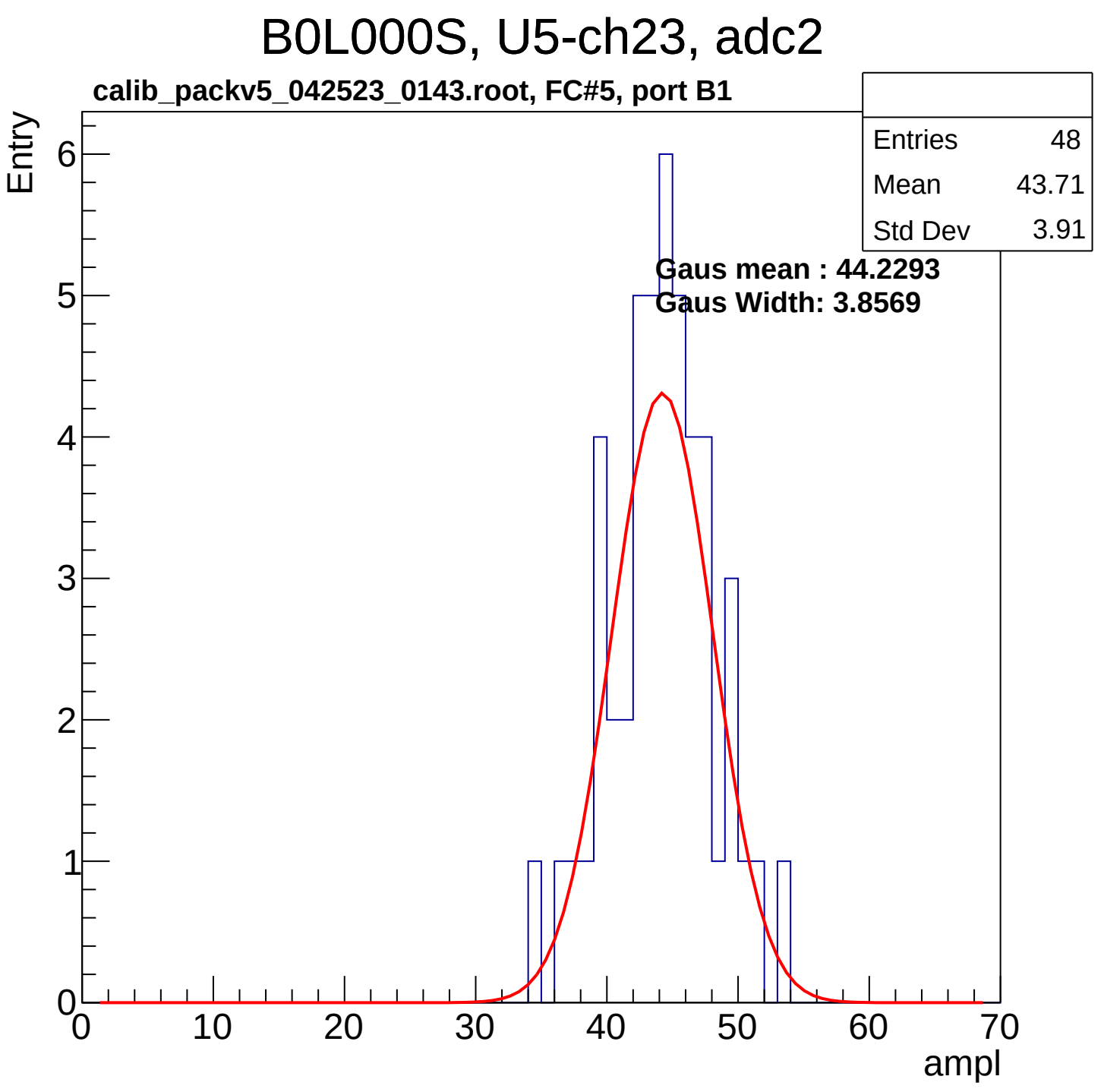
40
50
60
70


B0L000S, U5-ch23, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 43.71
Std Dev 3.91
Gaus mean : 44.2293
Gaus Width: 3.8569
Entry
ampl
0
1
2
3
4
5
6
0
10
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40
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60
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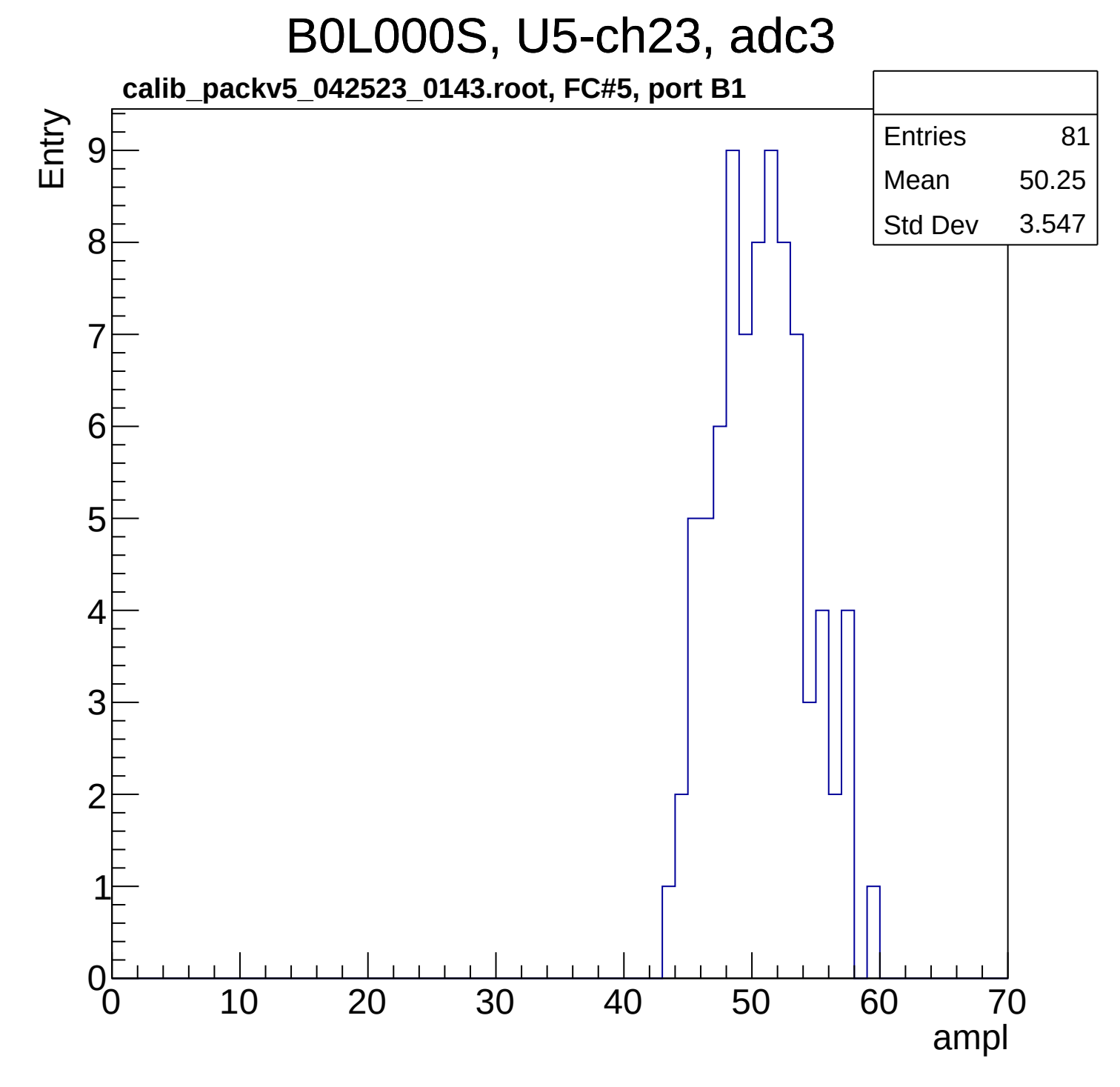
B0L000S, U5-ch23, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 50.25
Std Dev 3.547
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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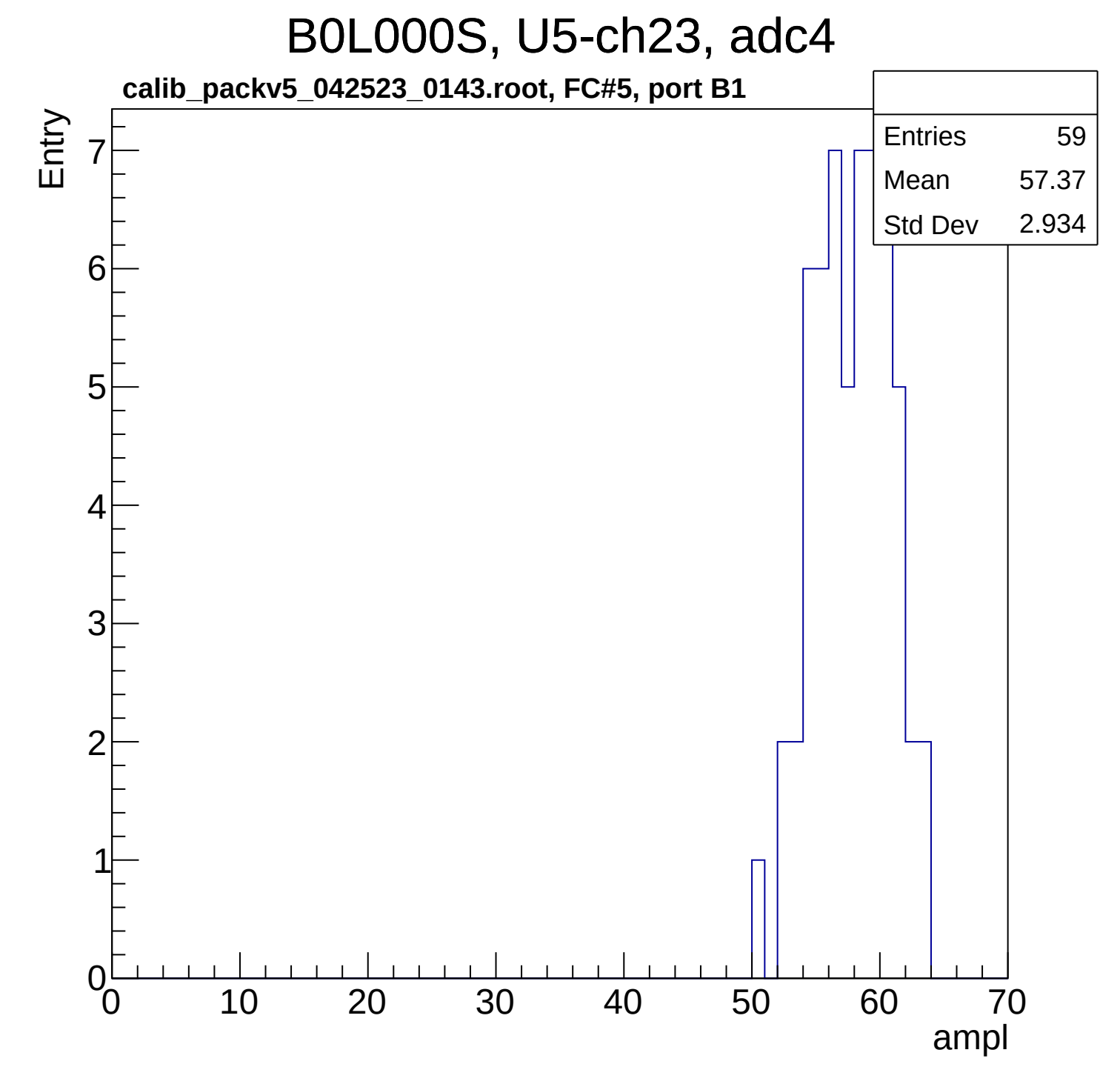
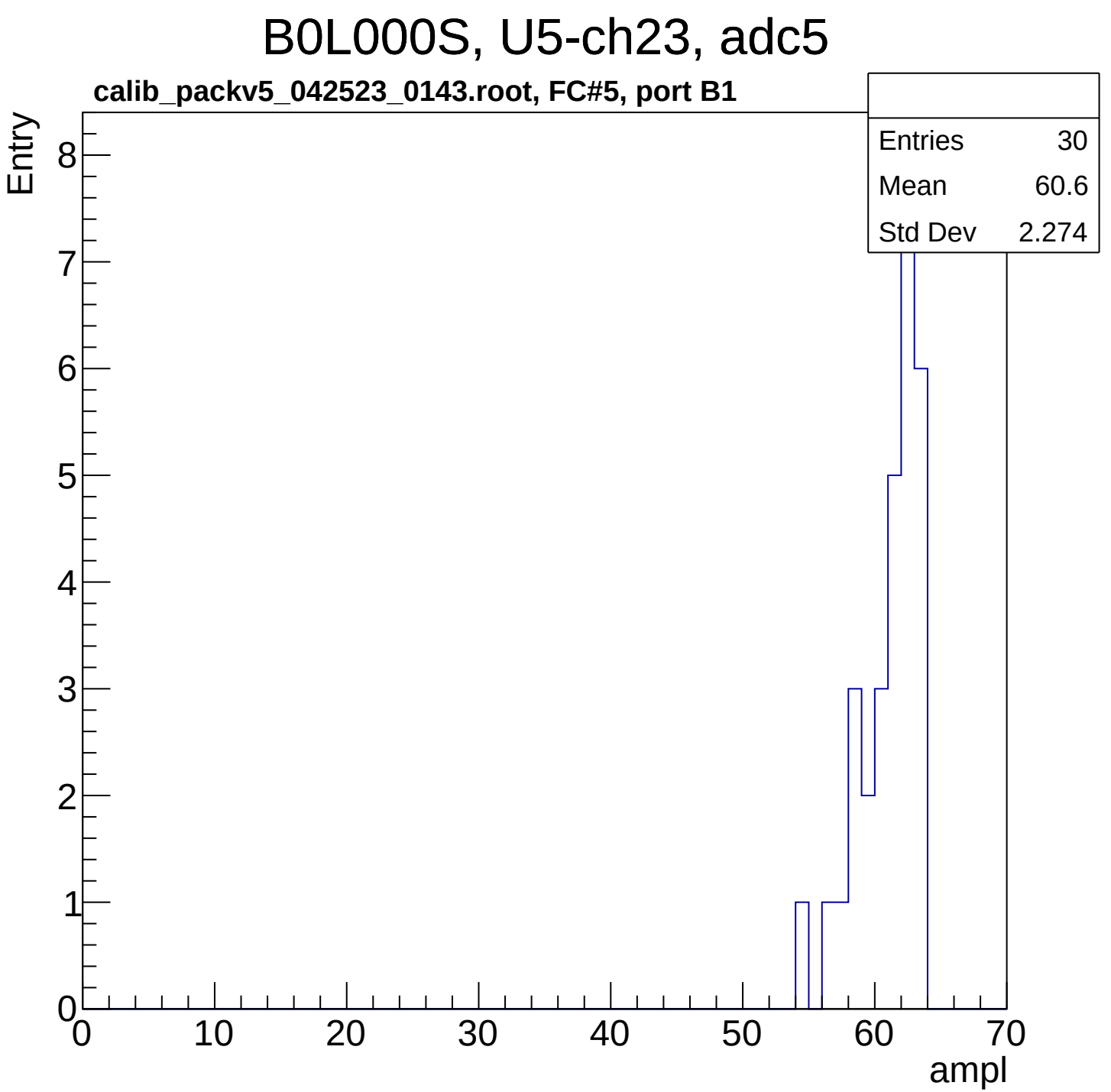
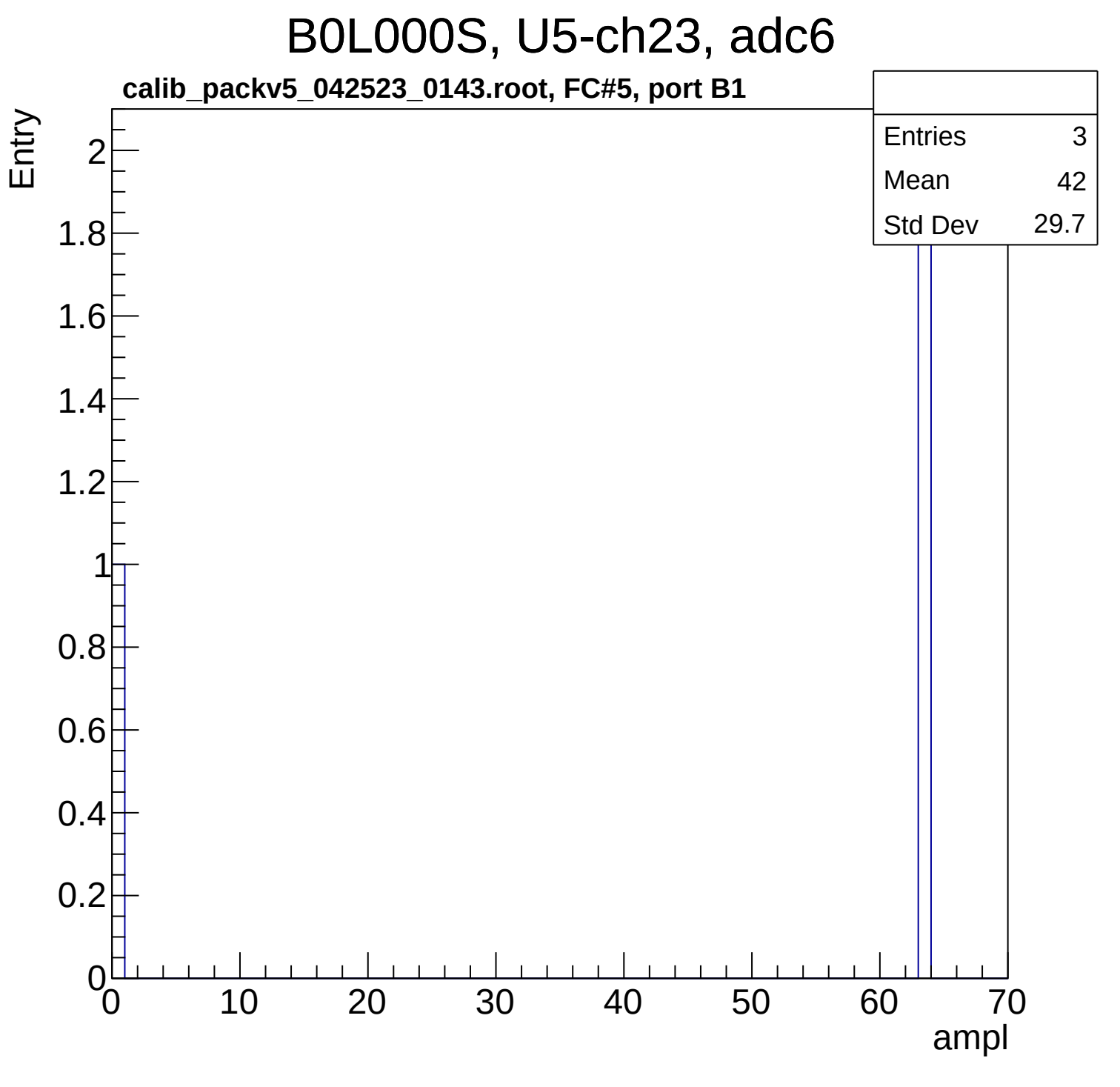
40
50
60
70


B0L000S, U5-ch23, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 57.37
Std Dev 2.934
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch23, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.6
Std Dev 2.274
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch23, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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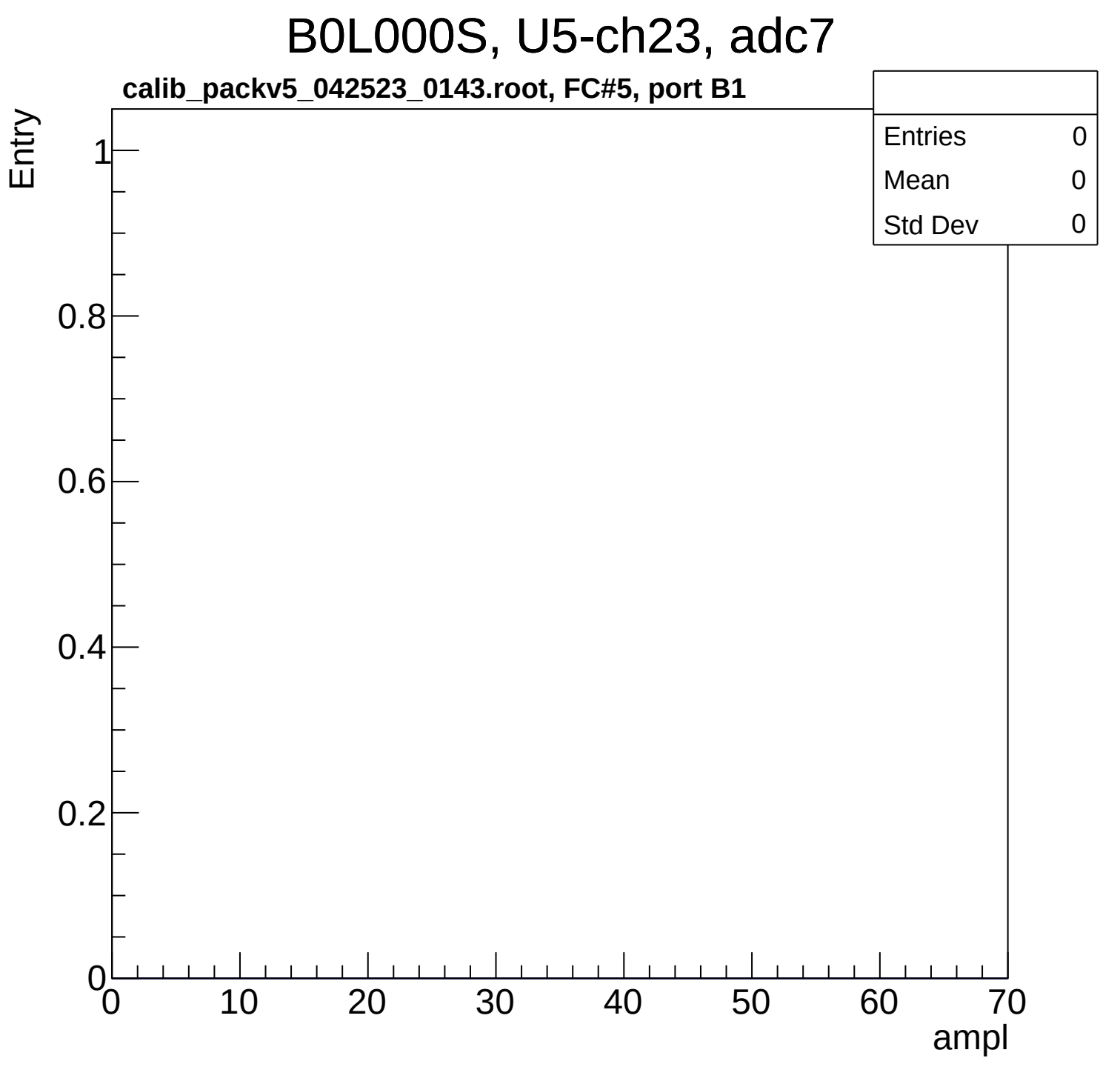
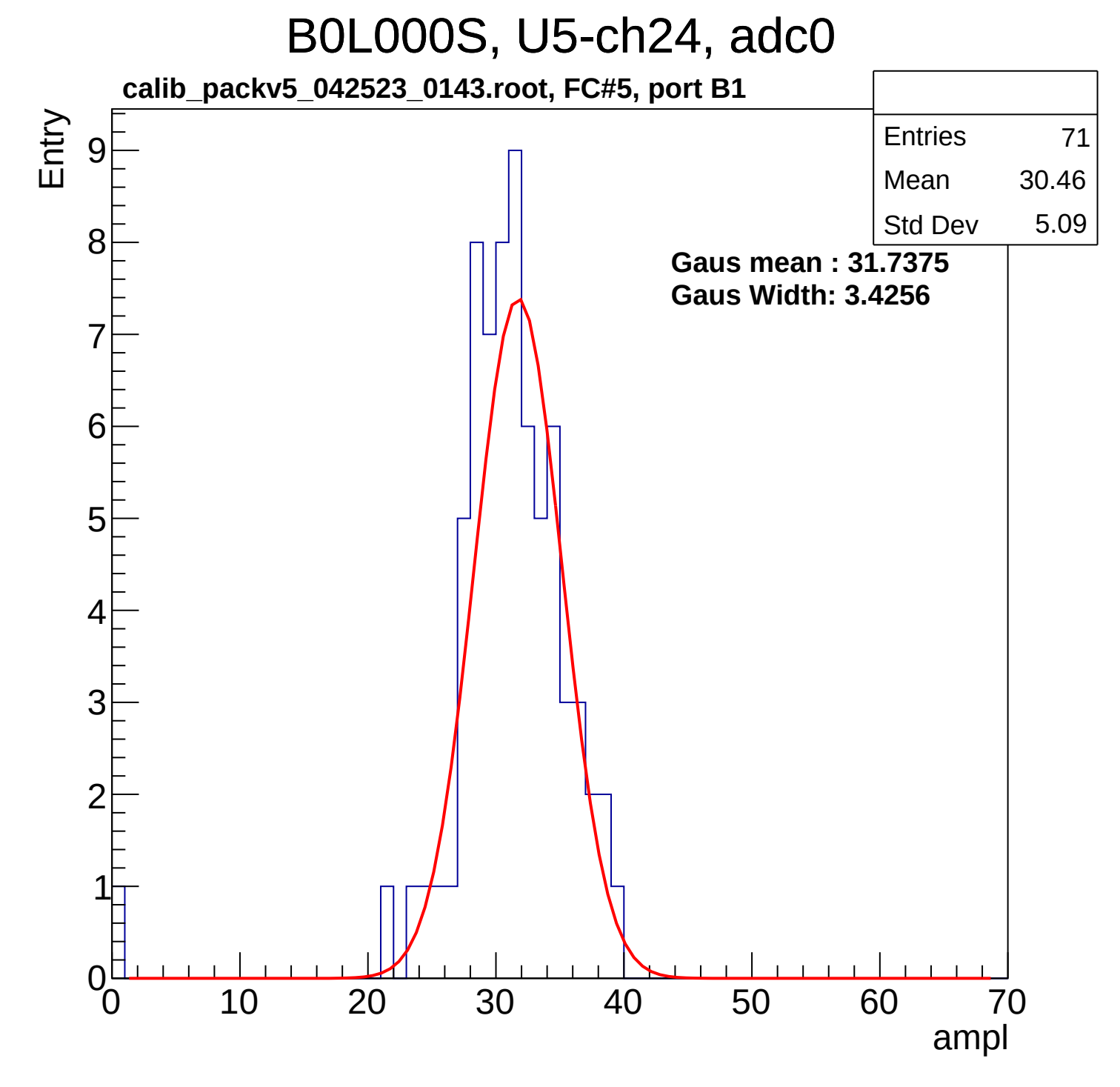

B0L000S, U5-ch23, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch24, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.46
Std Dev 5.09
Gaus mean : 31.7375
Gaus Width: 3.4256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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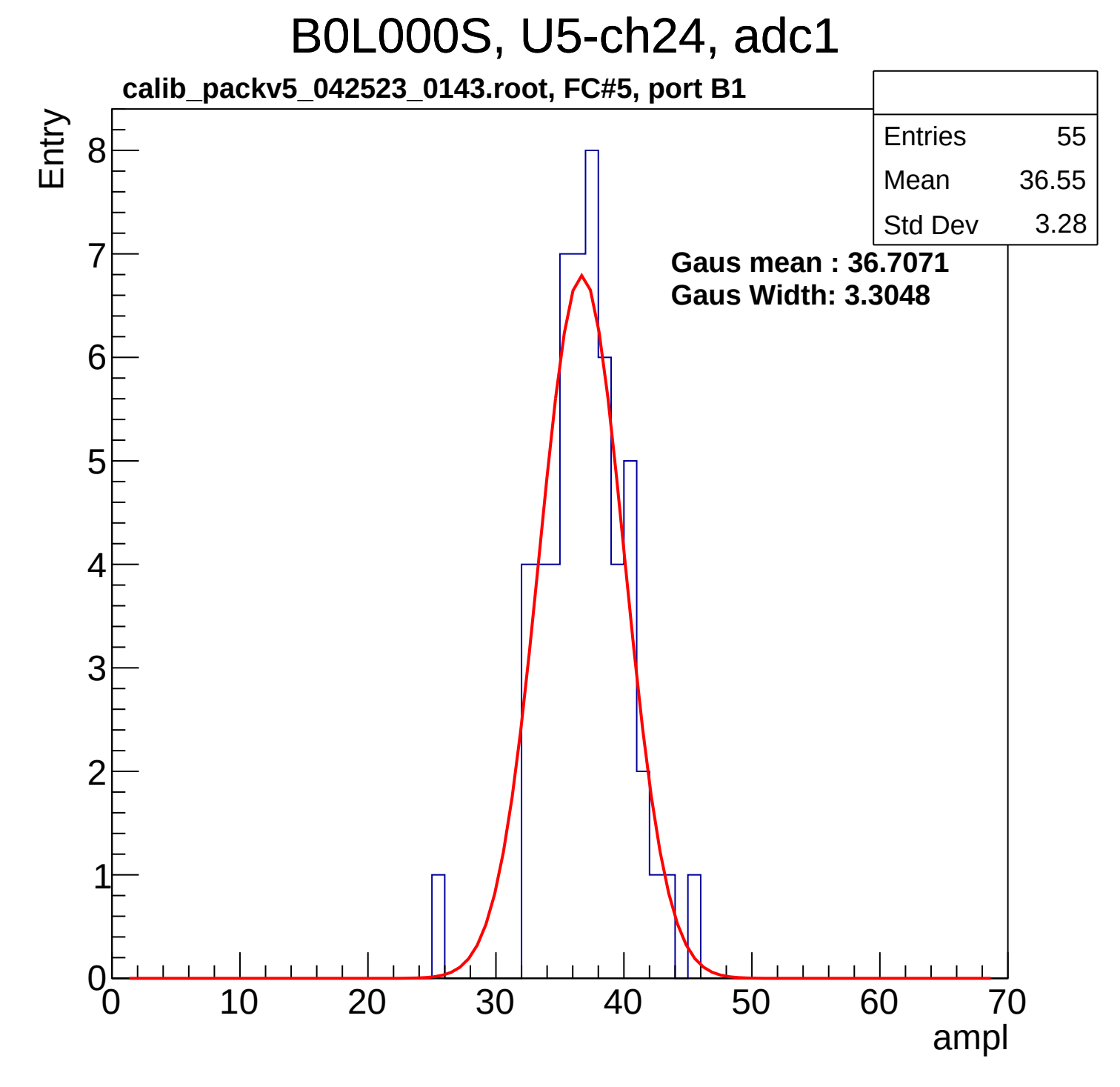
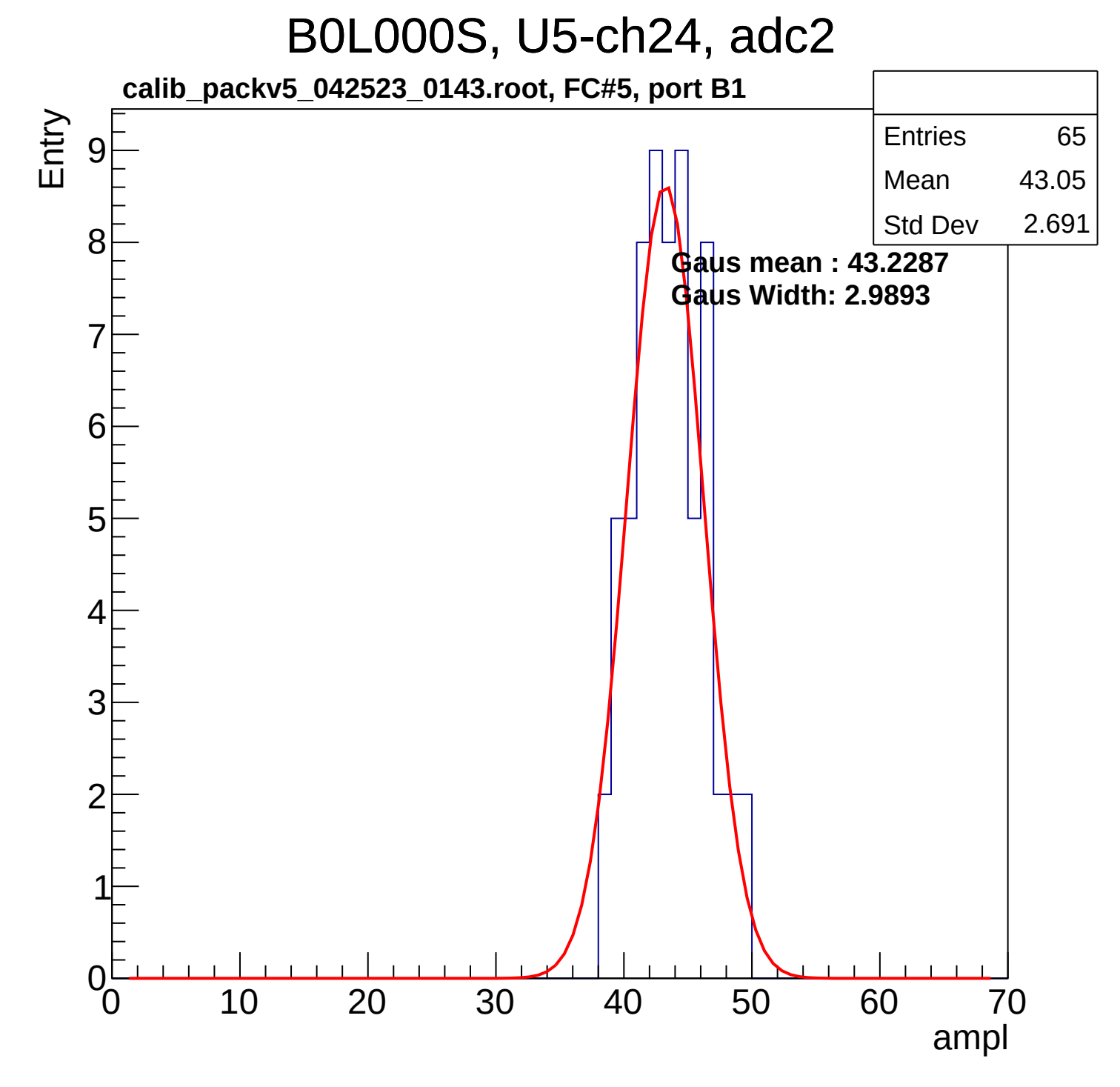
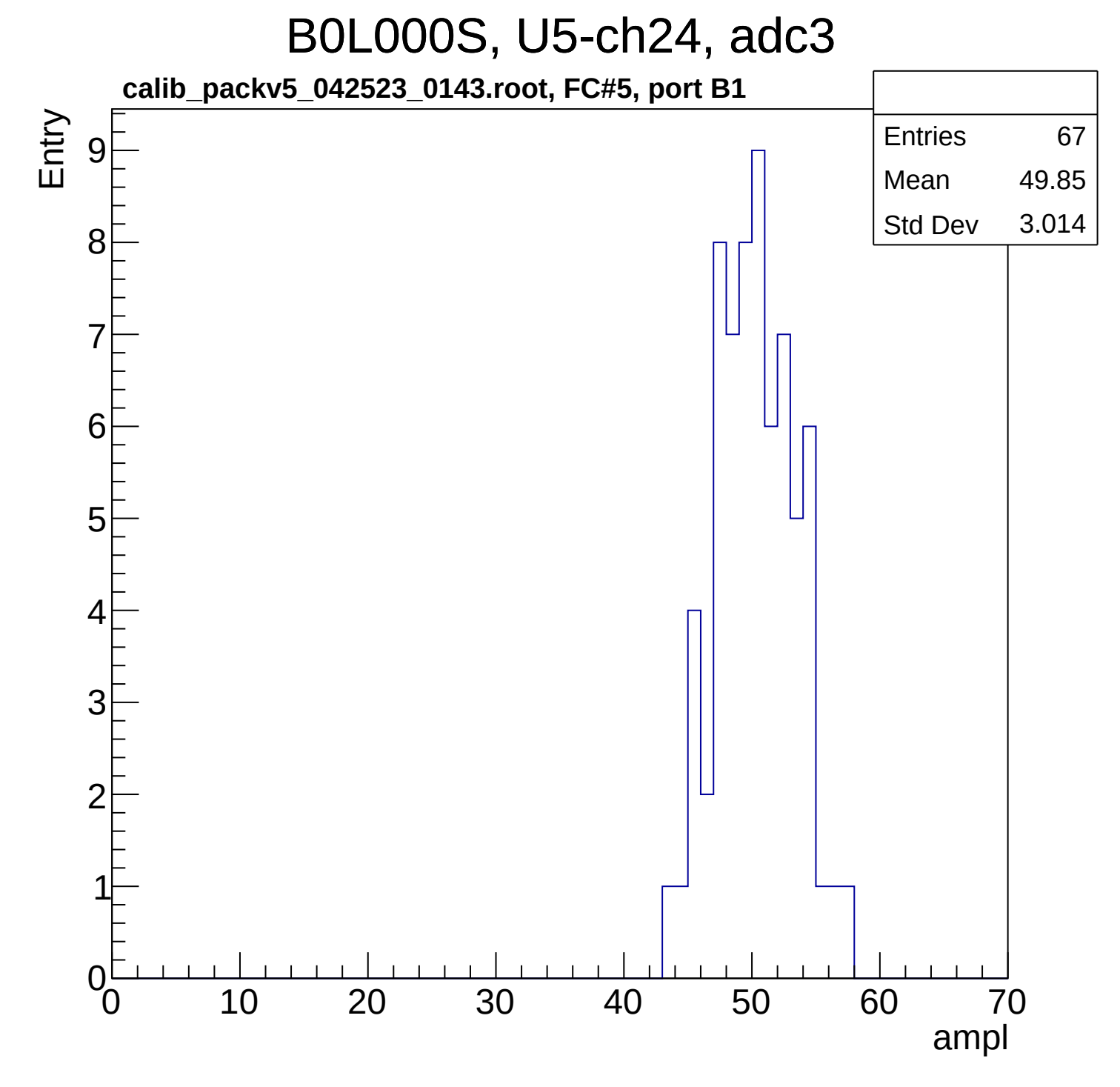

B0L000S, U5-ch24, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 36.55
Std Dev 3.28
Gaus mean : 36.7071
Gaus Width: 3.3048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch24, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.05
Std Dev 2.691
Gaus mean : 43.2287
Gaus Width: 2.9893
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch24, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.85
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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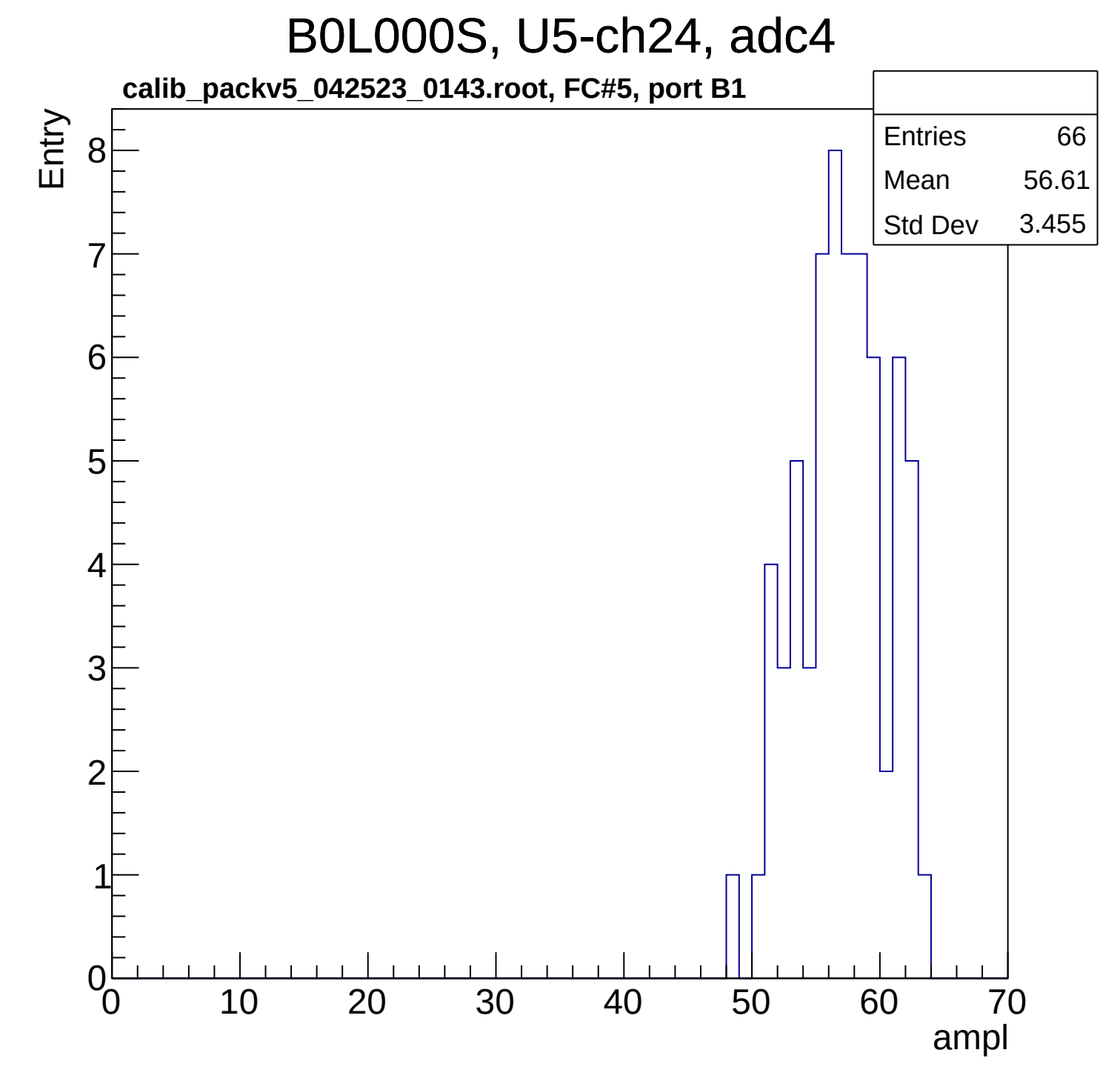
B0L000S, U5-ch24, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 56.61
Std Dev 3.455
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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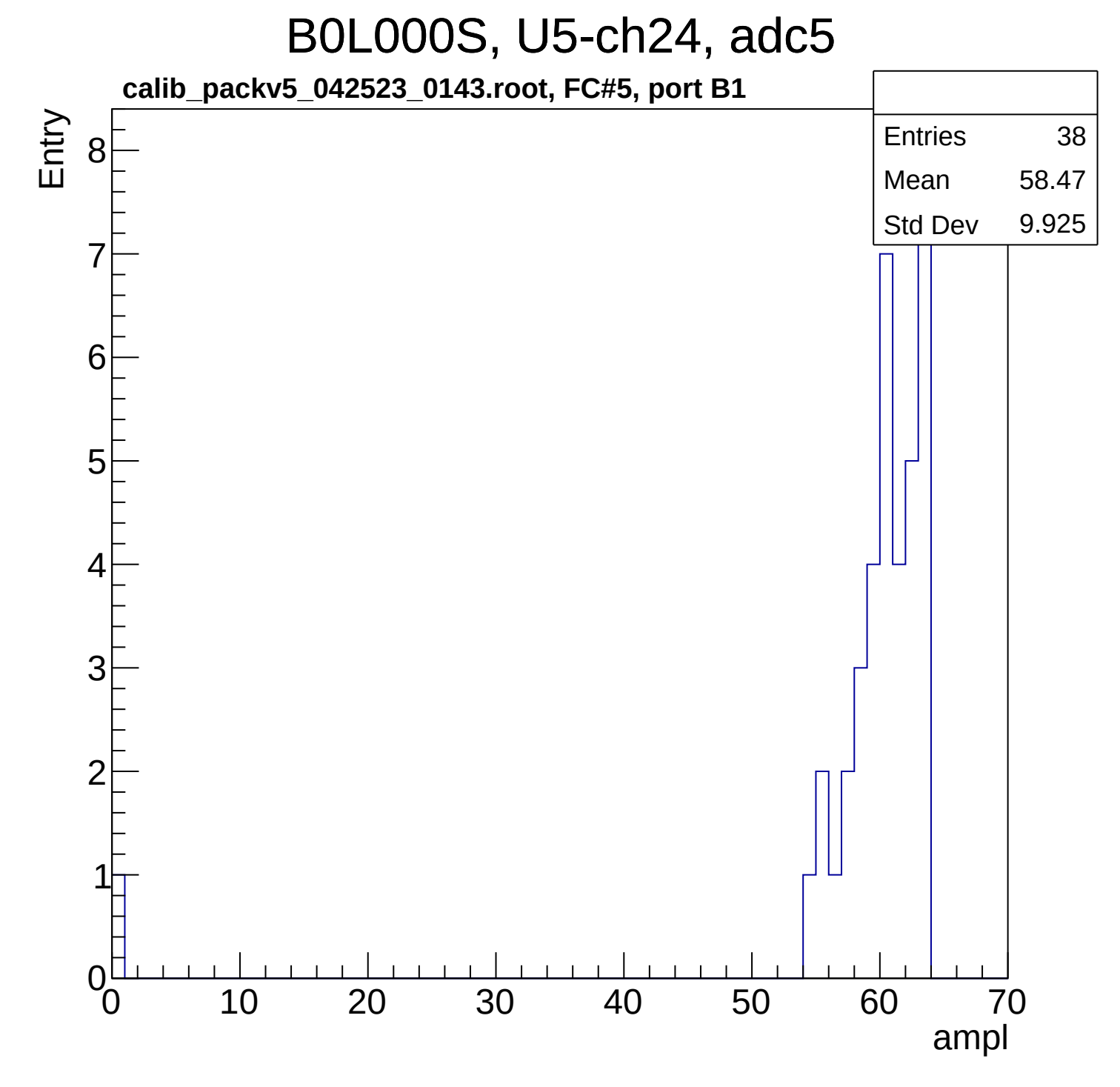
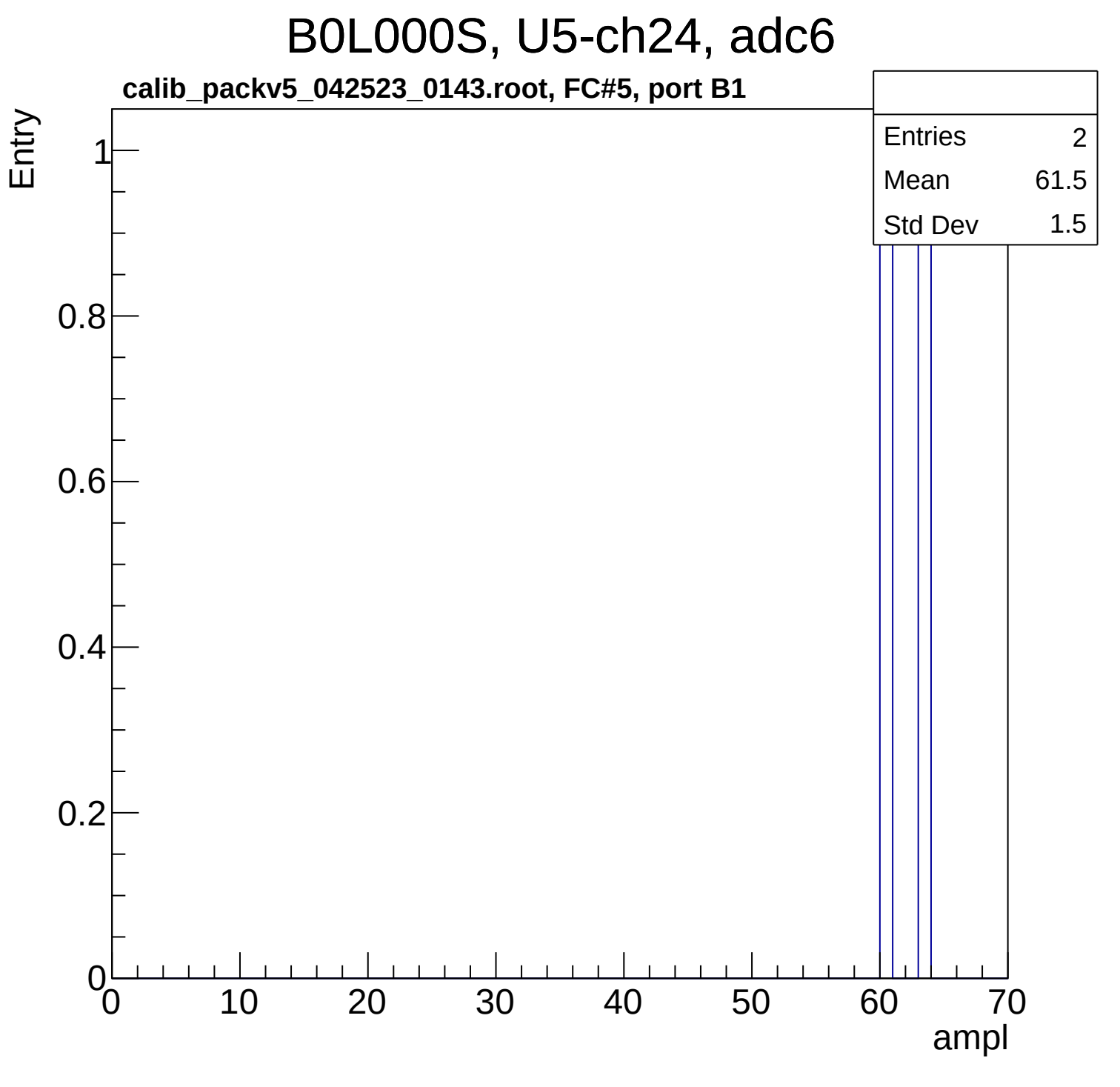
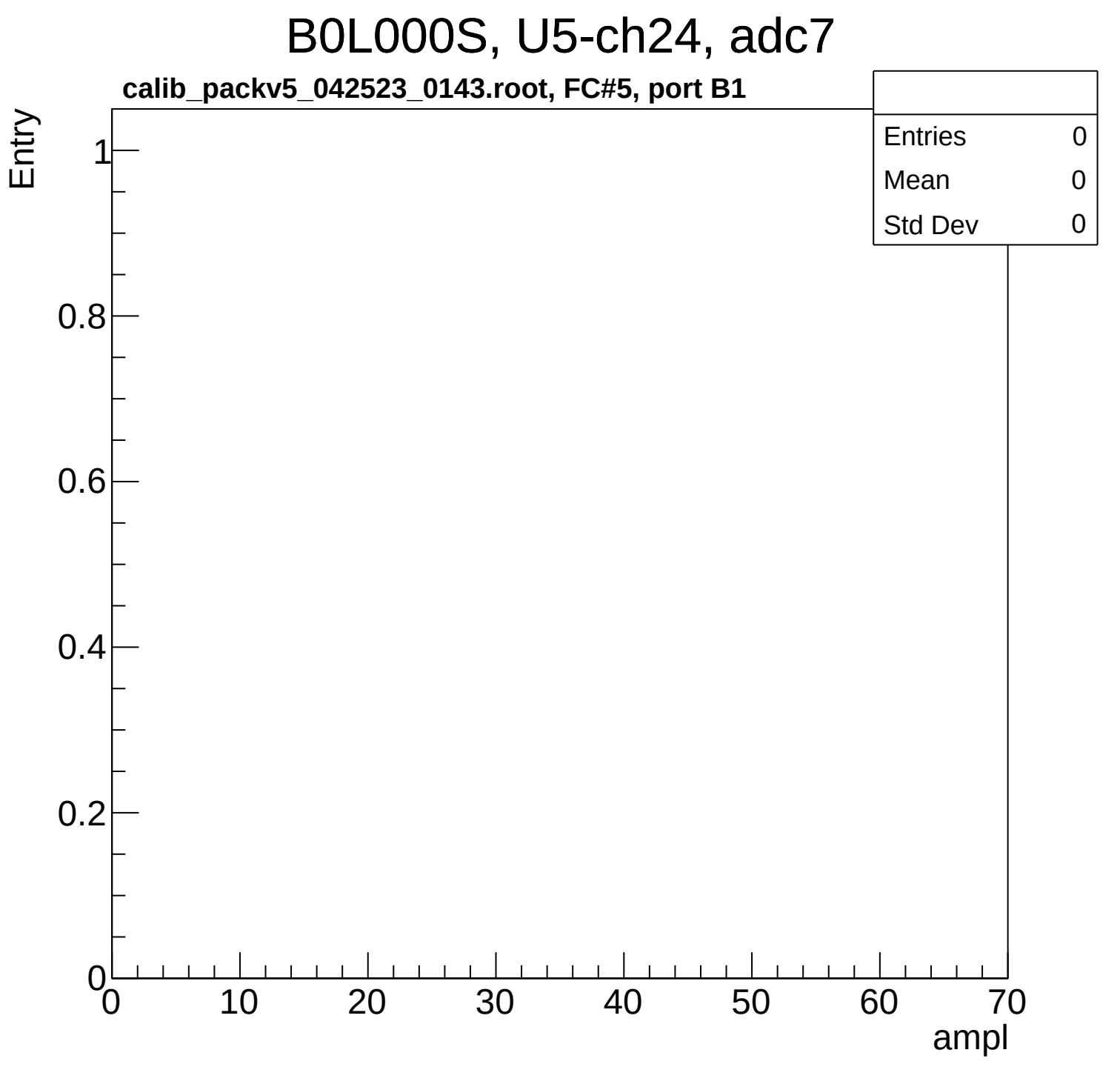
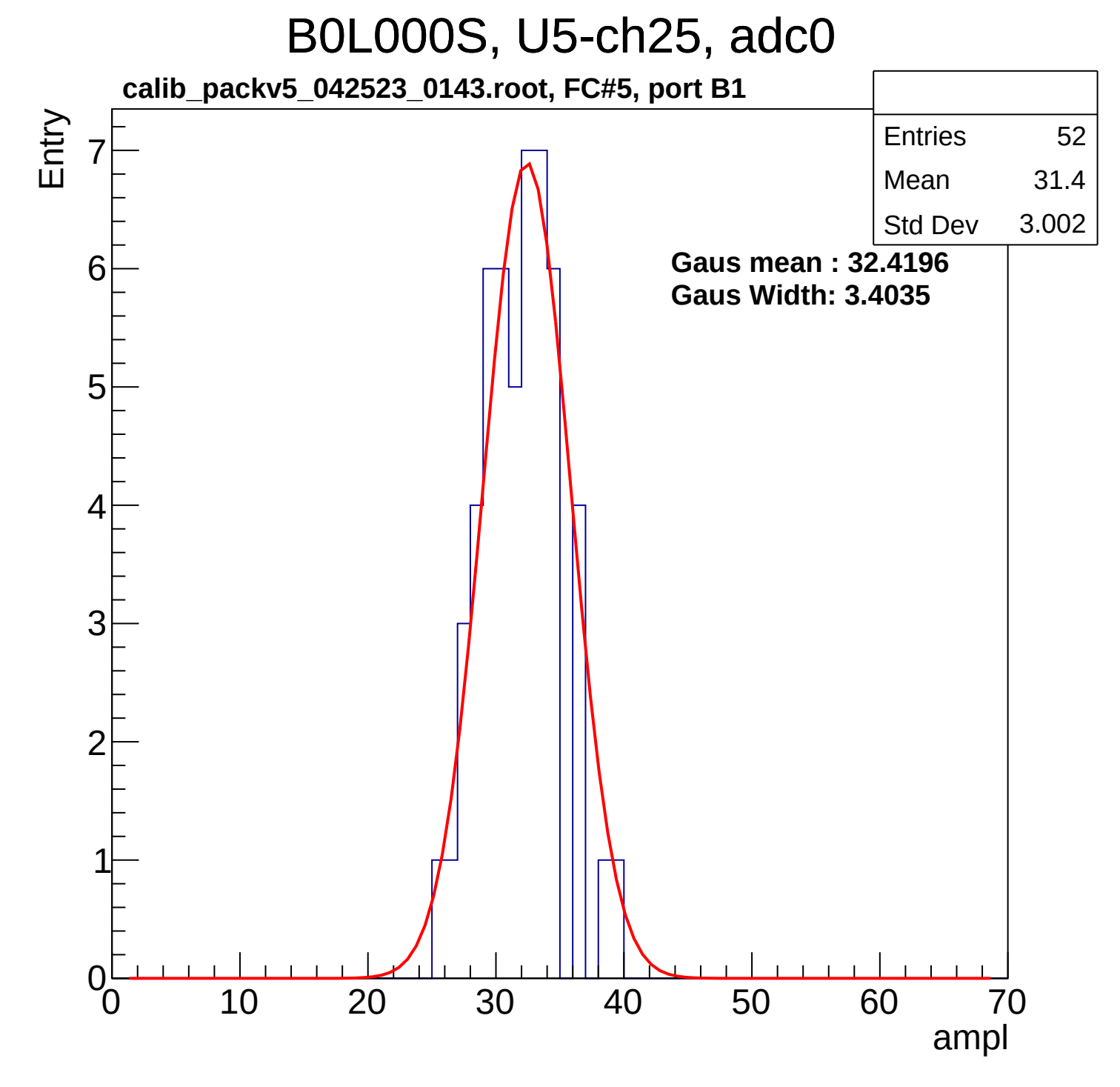
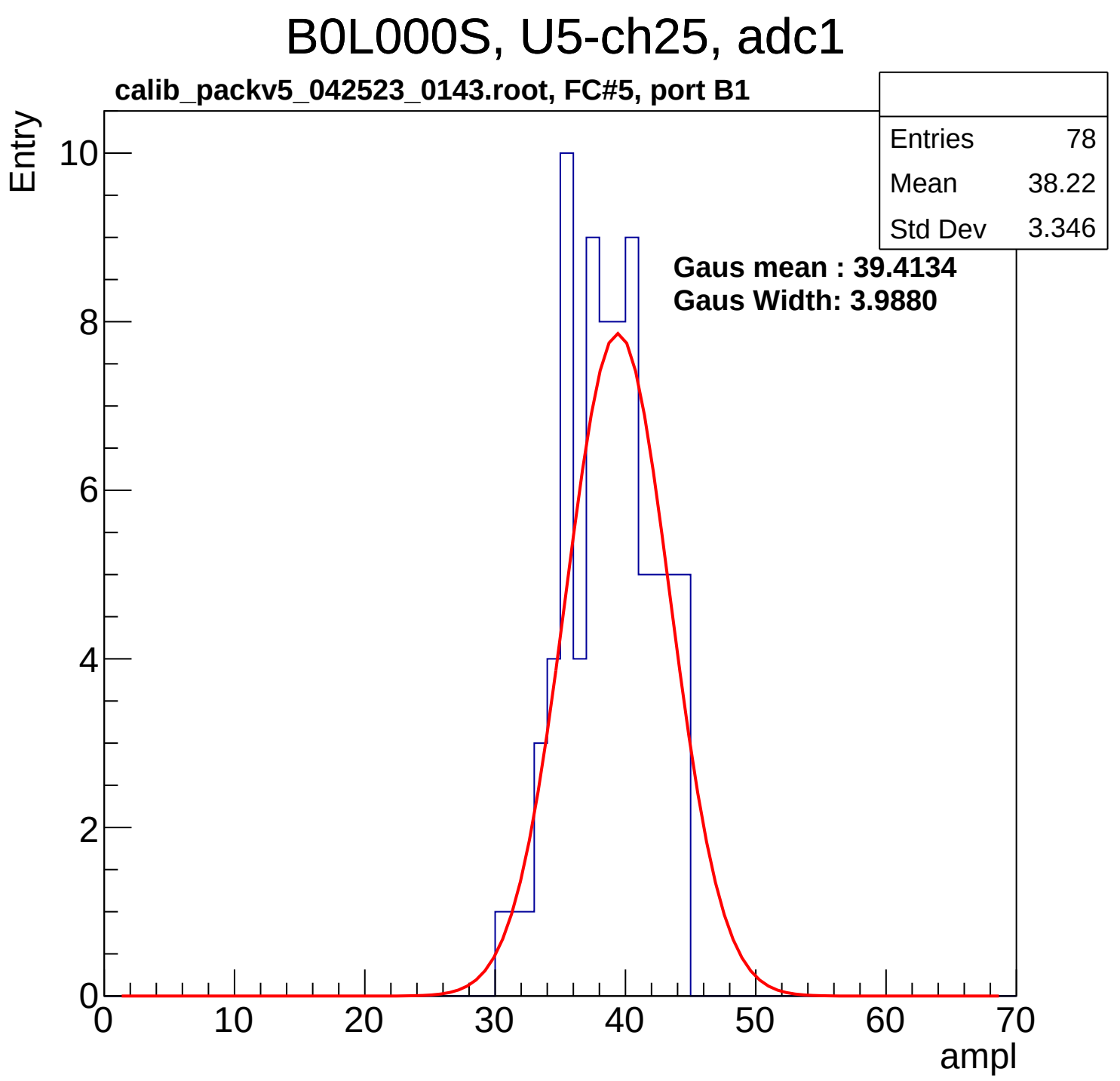
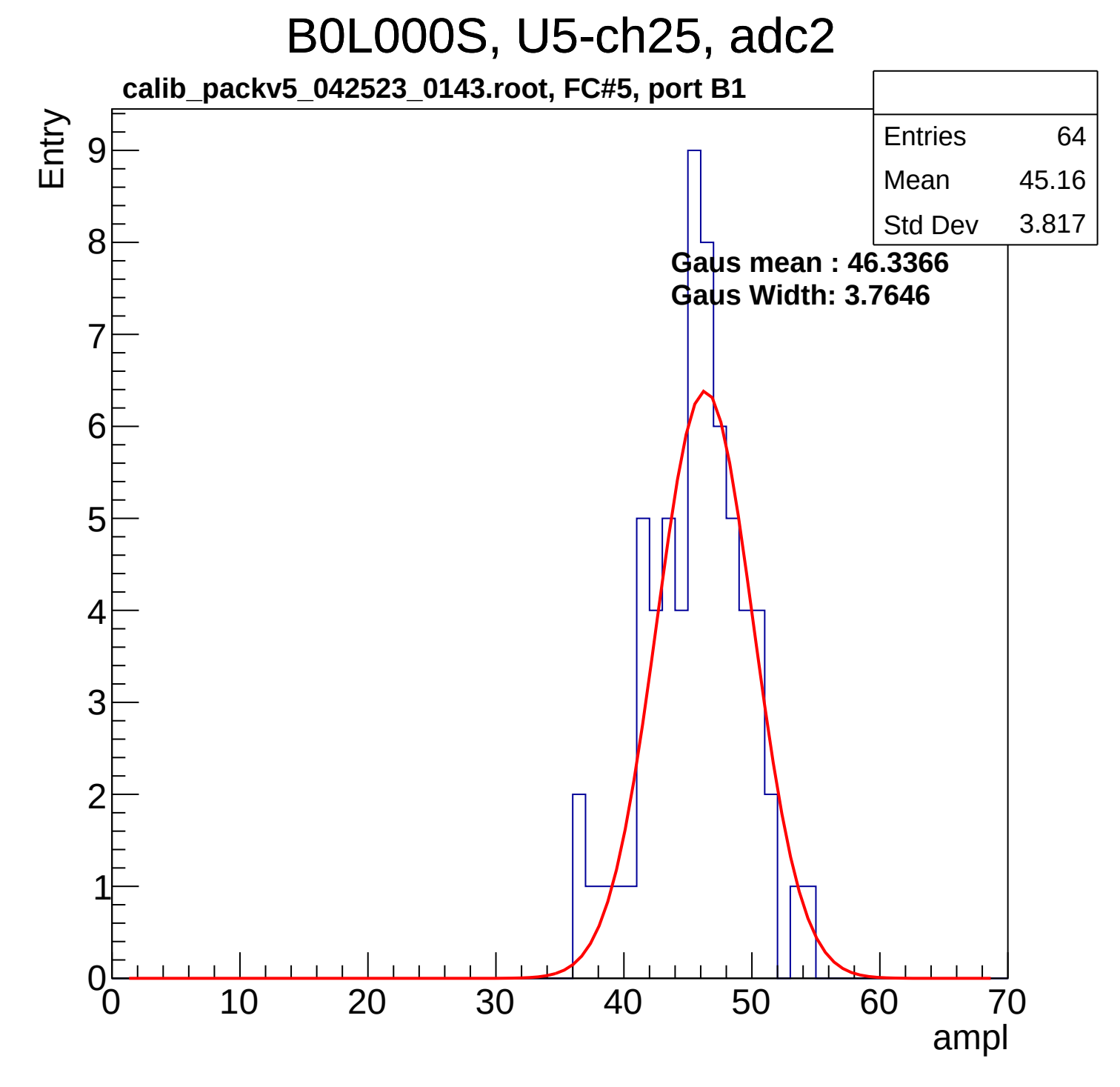

B0L000S, U5-ch24, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.47
Std Dev 9.925
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

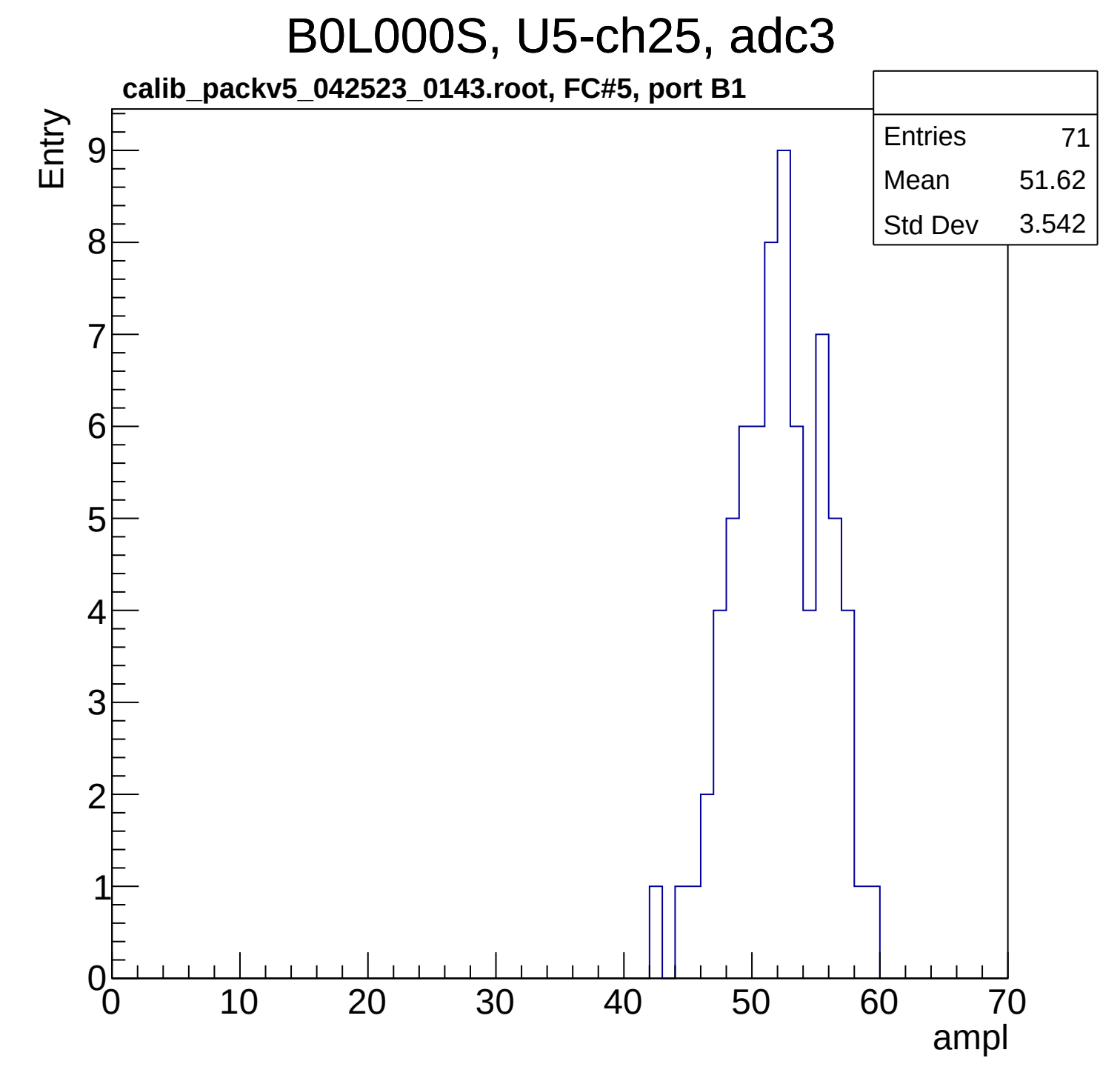

B0L000S, U5-ch24, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch24, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch25, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 31.4
Std Dev 3.002
Gaus mean : 32.4196
Gaus Width: 3.4035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch25, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 38.22
Std Dev 3.346
Gaus mean : 39.4134
Gaus Width: 3.9880
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch25, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 45.16
Std Dev 3.817
Gaus mean : 46.3366
Gaus Width: 3.7646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch25, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 51.62
Std Dev 3.542
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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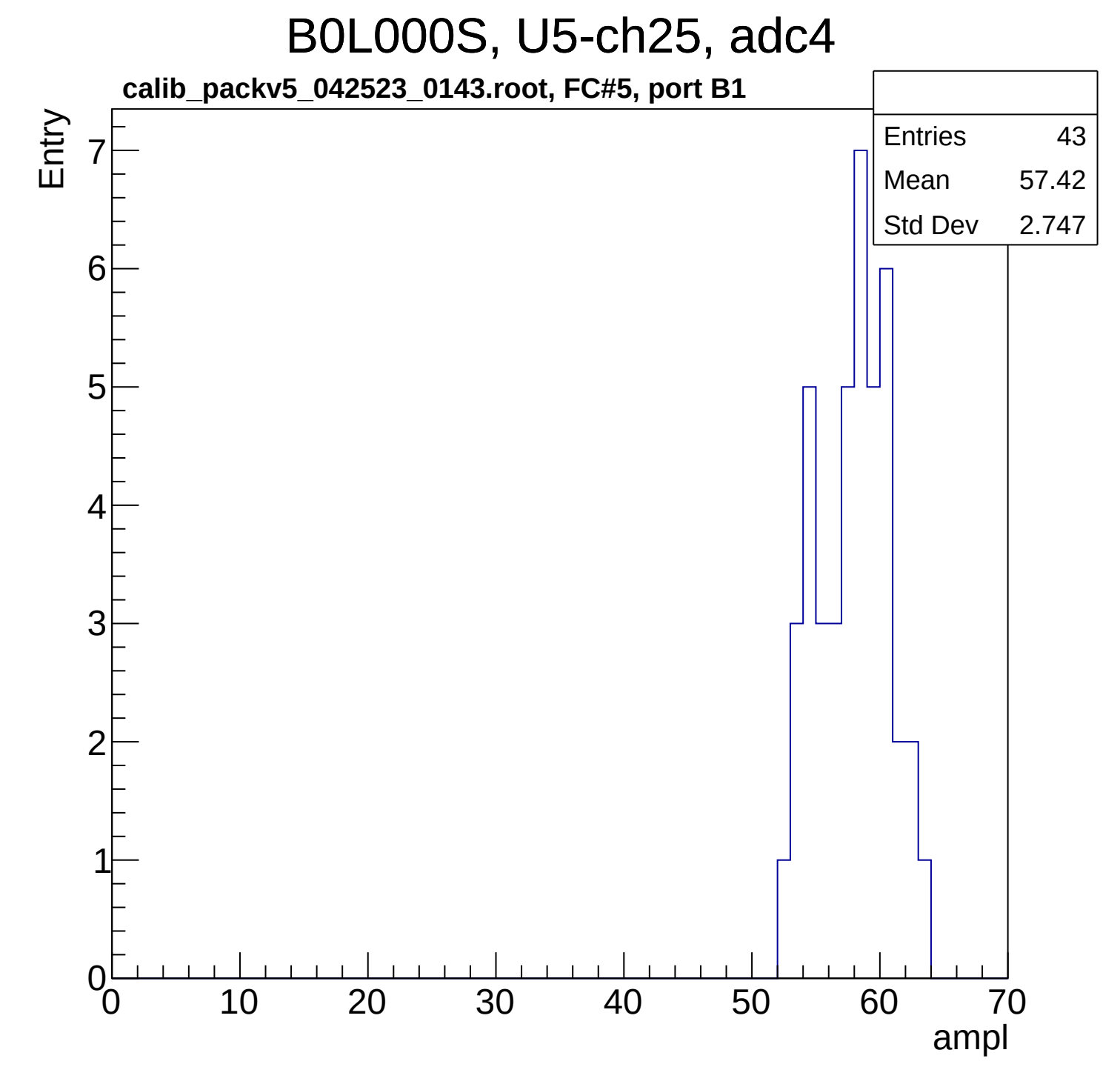
B0L000S, U5-ch25, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 57.42
Std Dev 2.747
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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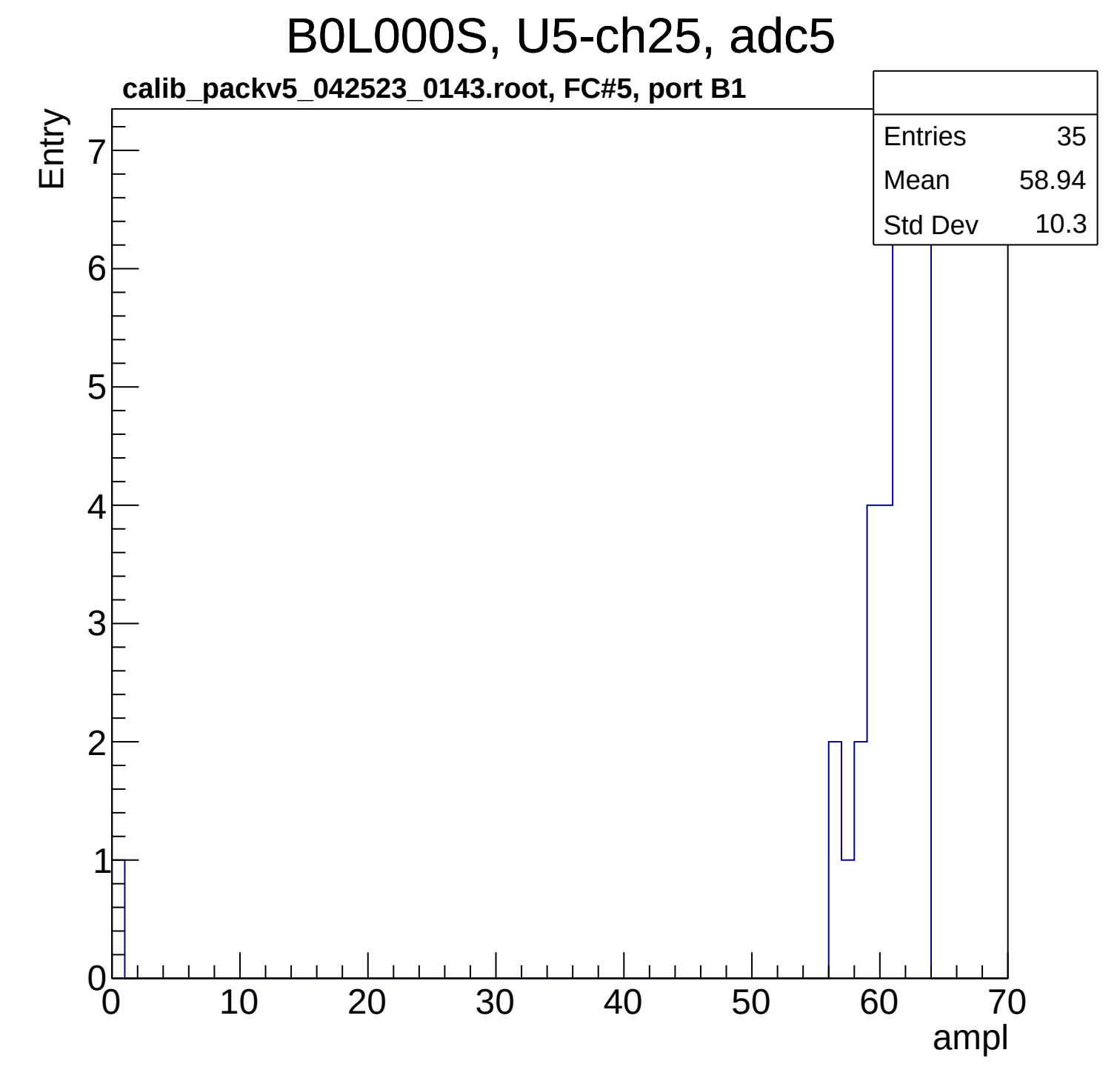
40
50
60
70


B0L000S, U5-ch25, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.94
Std Dev 10.3
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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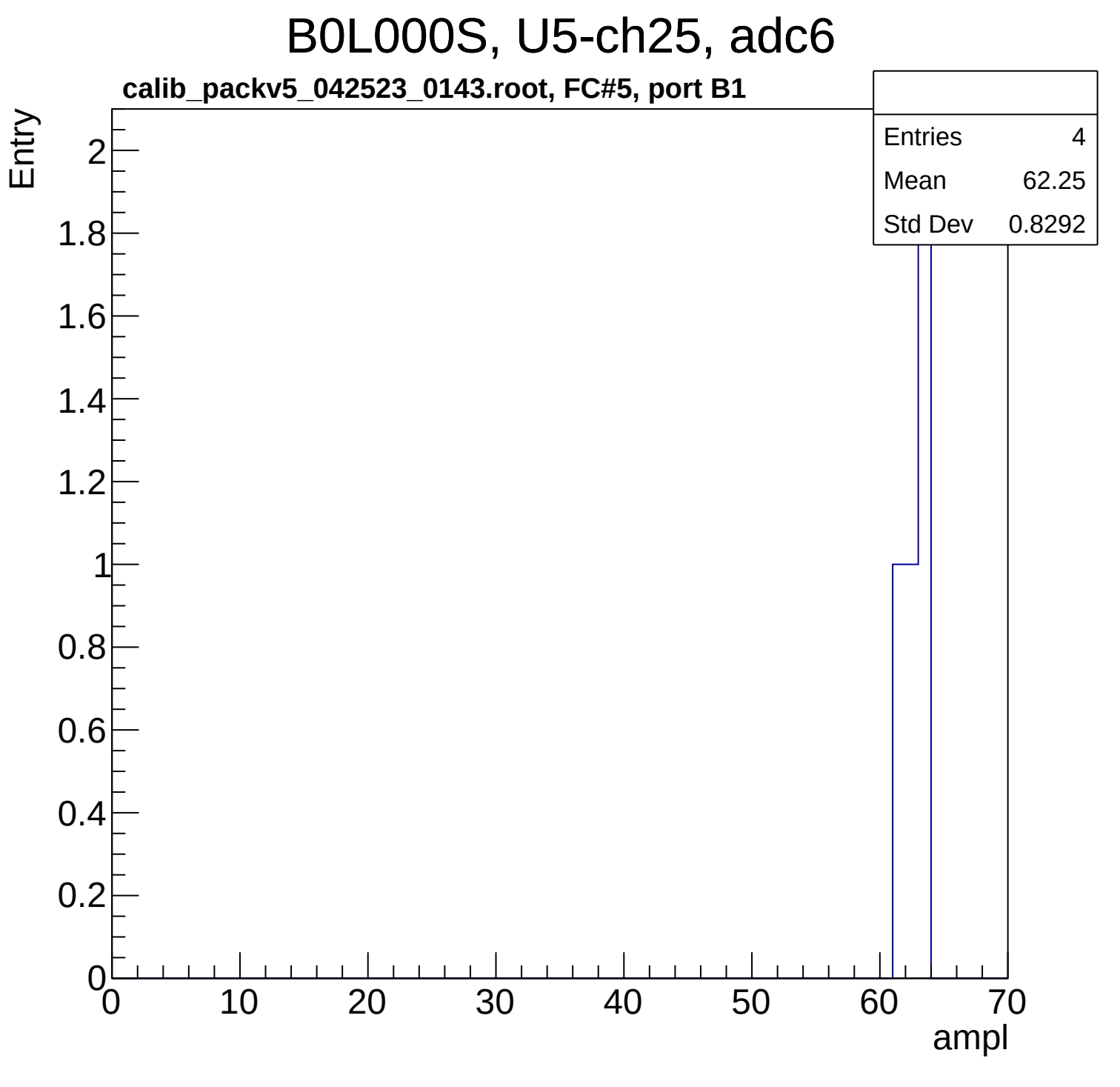
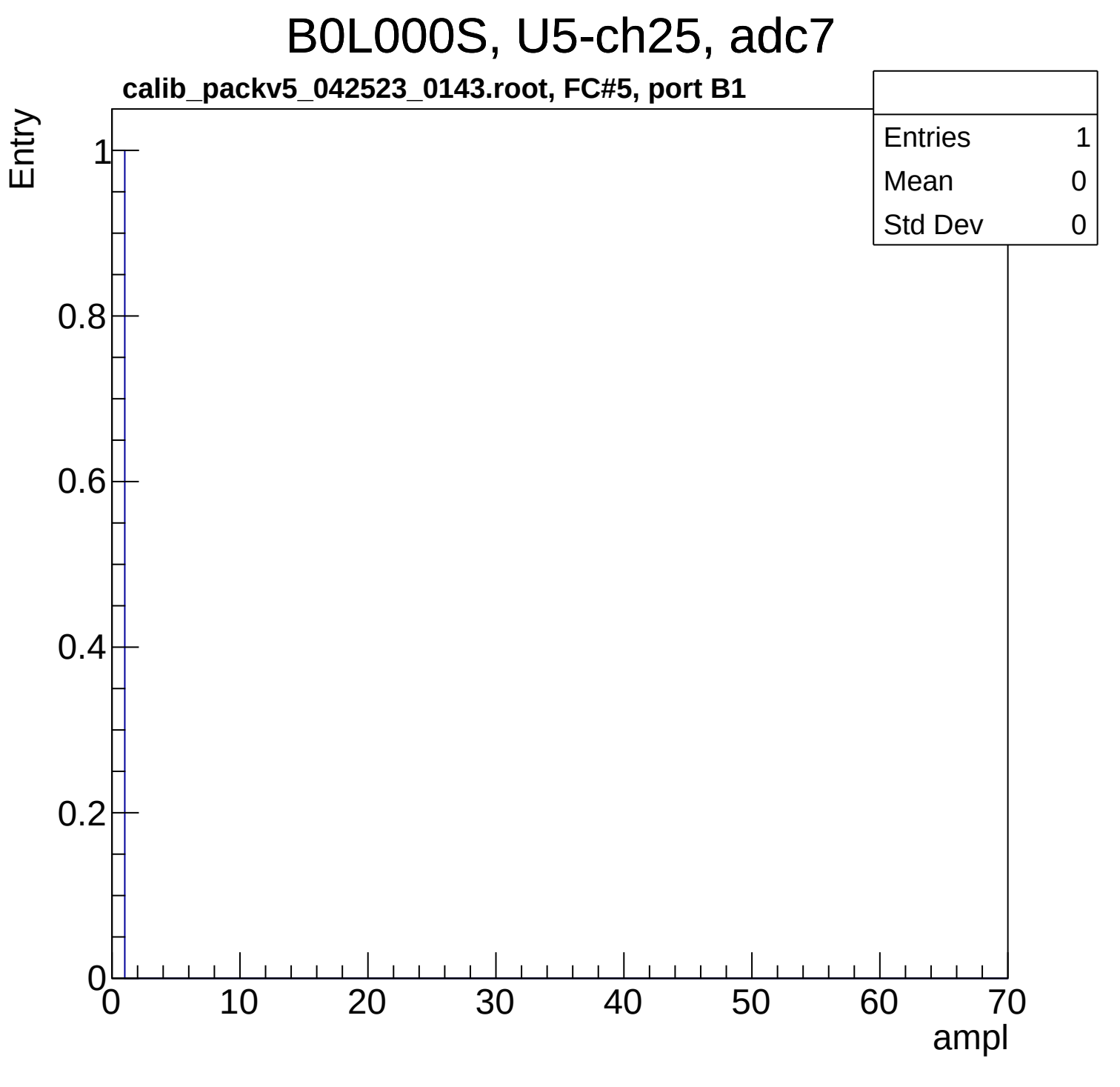
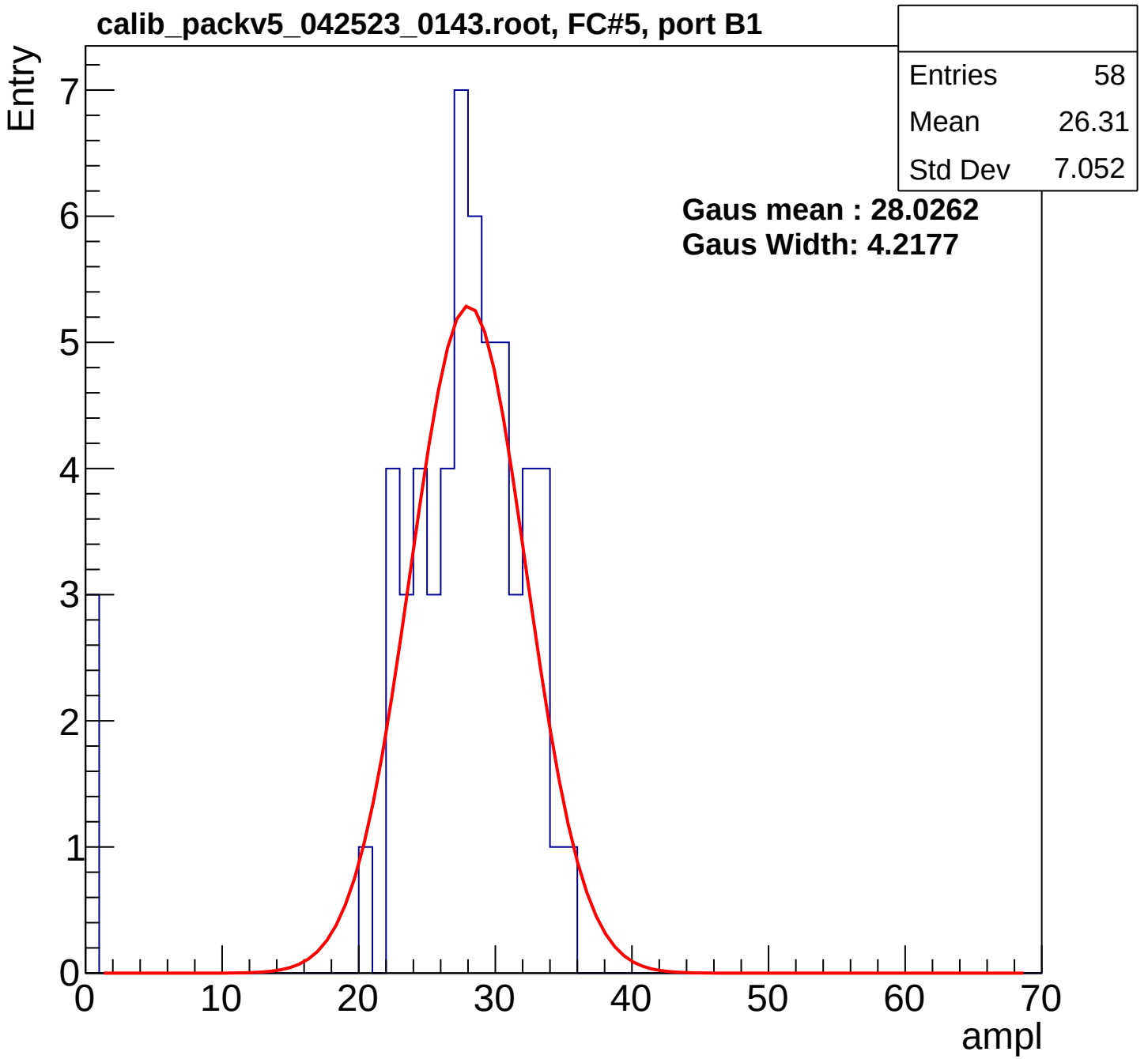
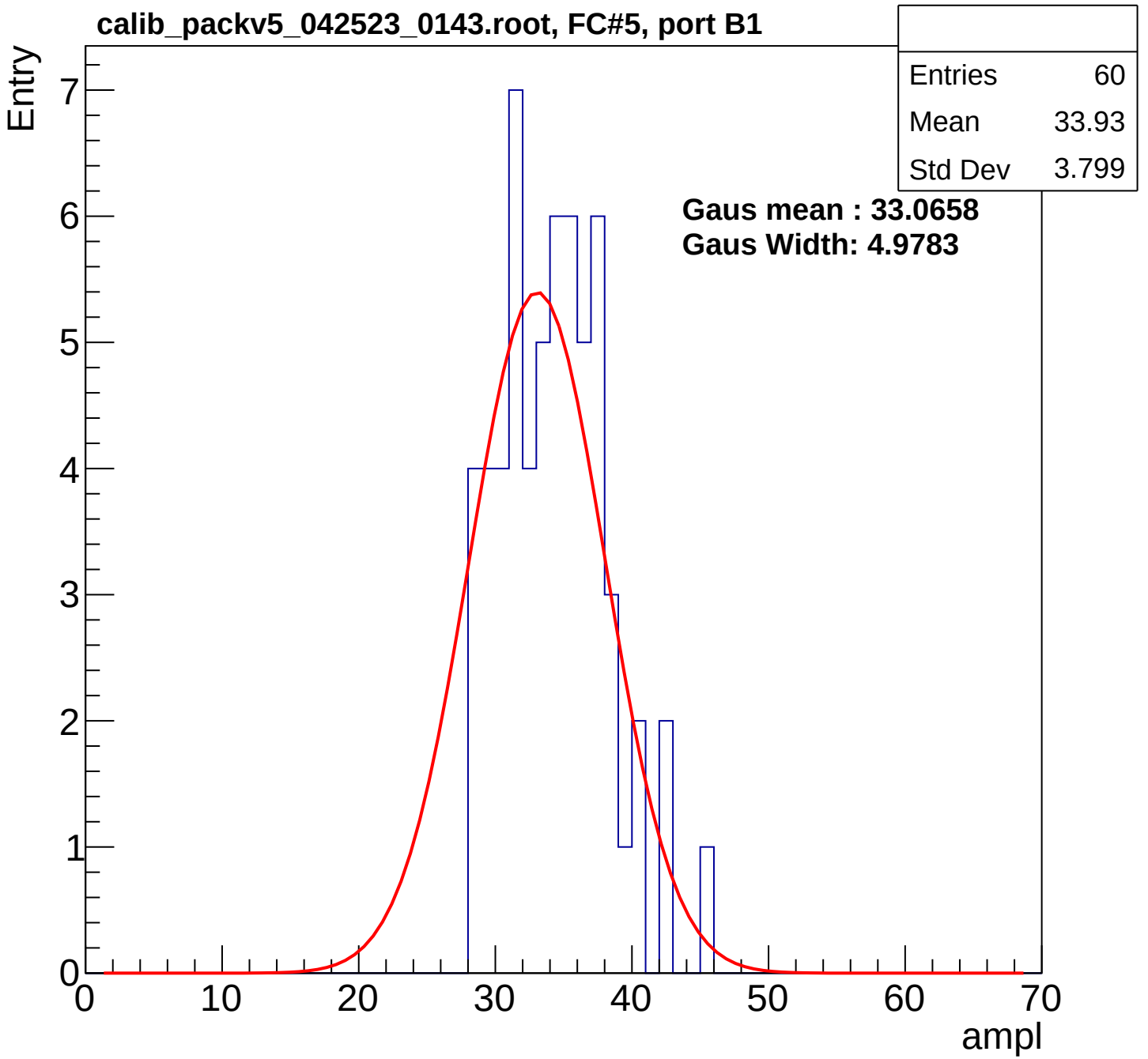
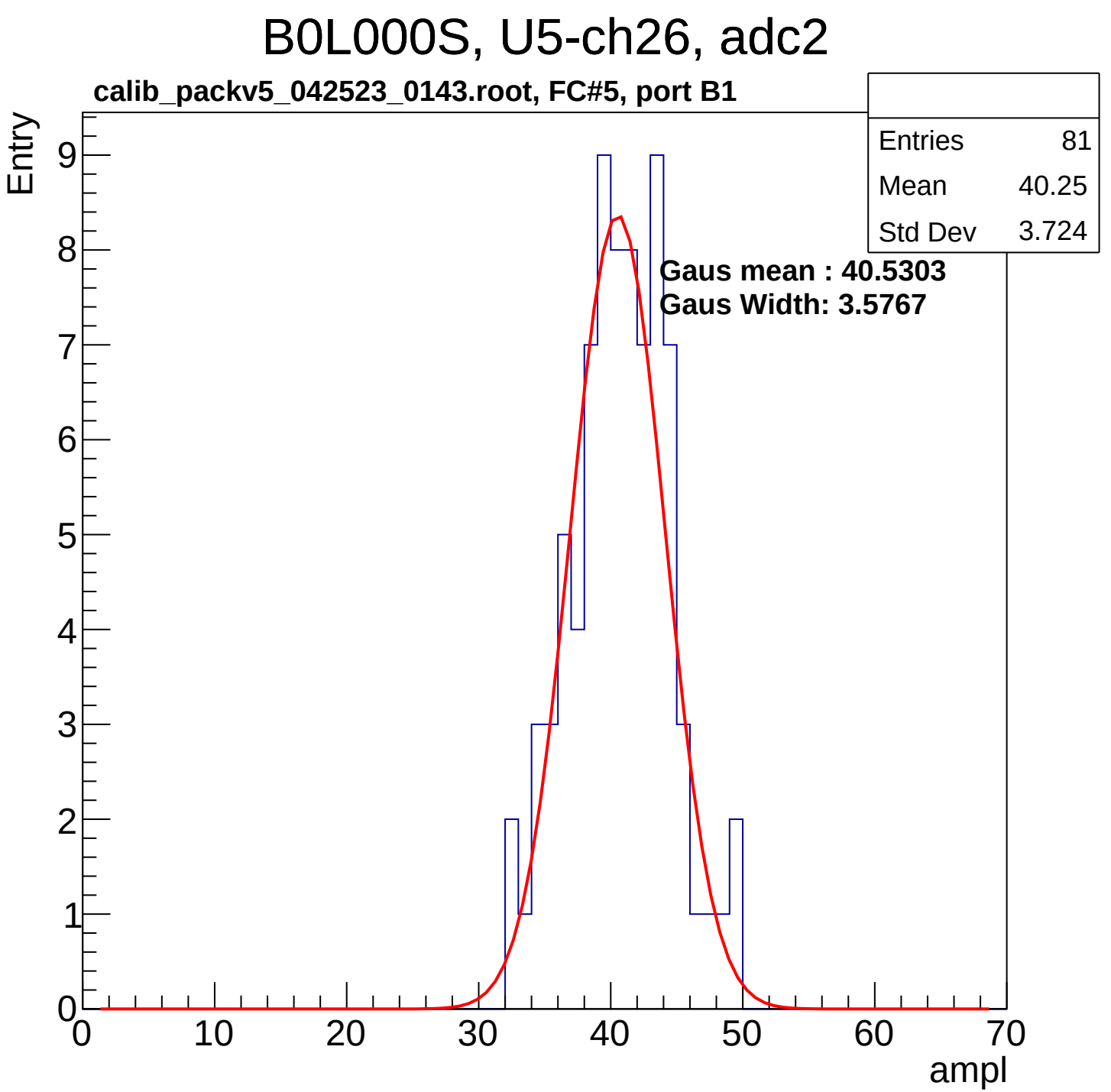
60
70


B0L000S, U5-ch25, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch25, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch26, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 26.31
Std Dev 7.052
Gaus mean : 28.0262
Gaus Width: 4.2177
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch26, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 33.93
Std Dev 3.799
Gaus mean : 33.0658
Gaus Width: 4.9783
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch26, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 40.25
Std Dev 3.724
Gaus mean : 40.5303
Gaus Width: 3.5767
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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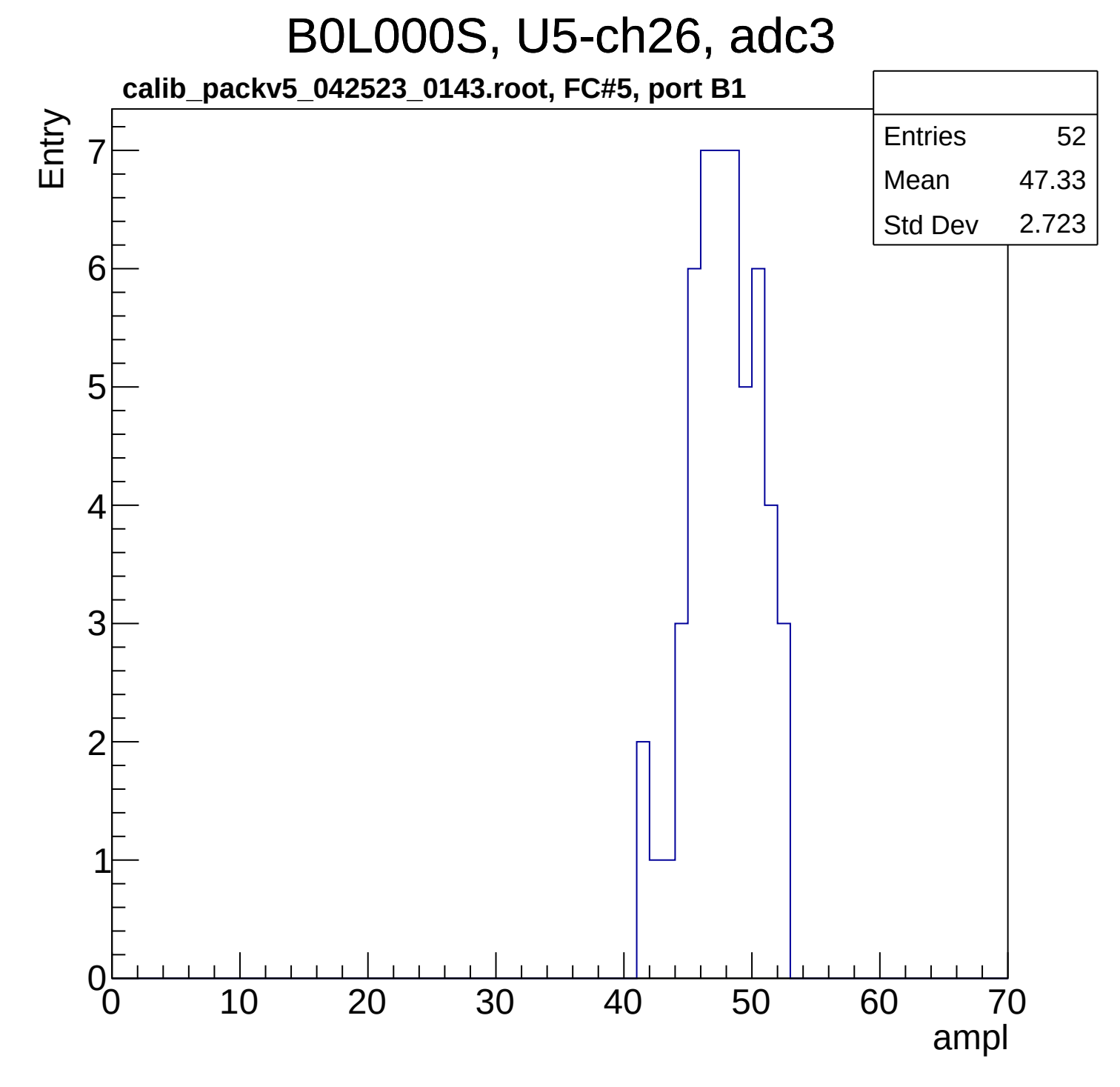
B0L000S, U5-ch26, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 47.33
Std Dev 2.723
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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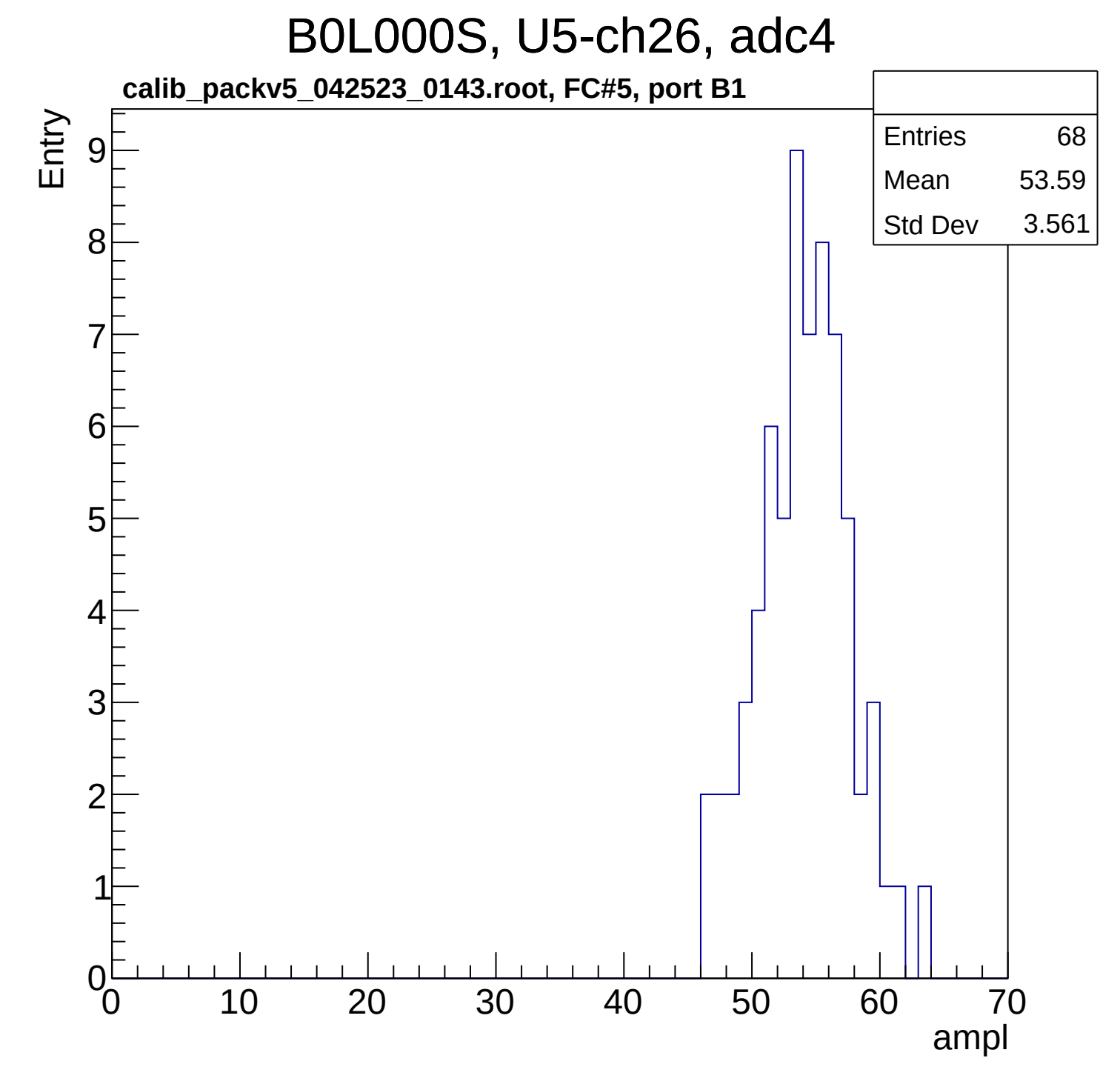
B0L000S, U5-ch26, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 53.59
Std Dev 3.561
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
50
60
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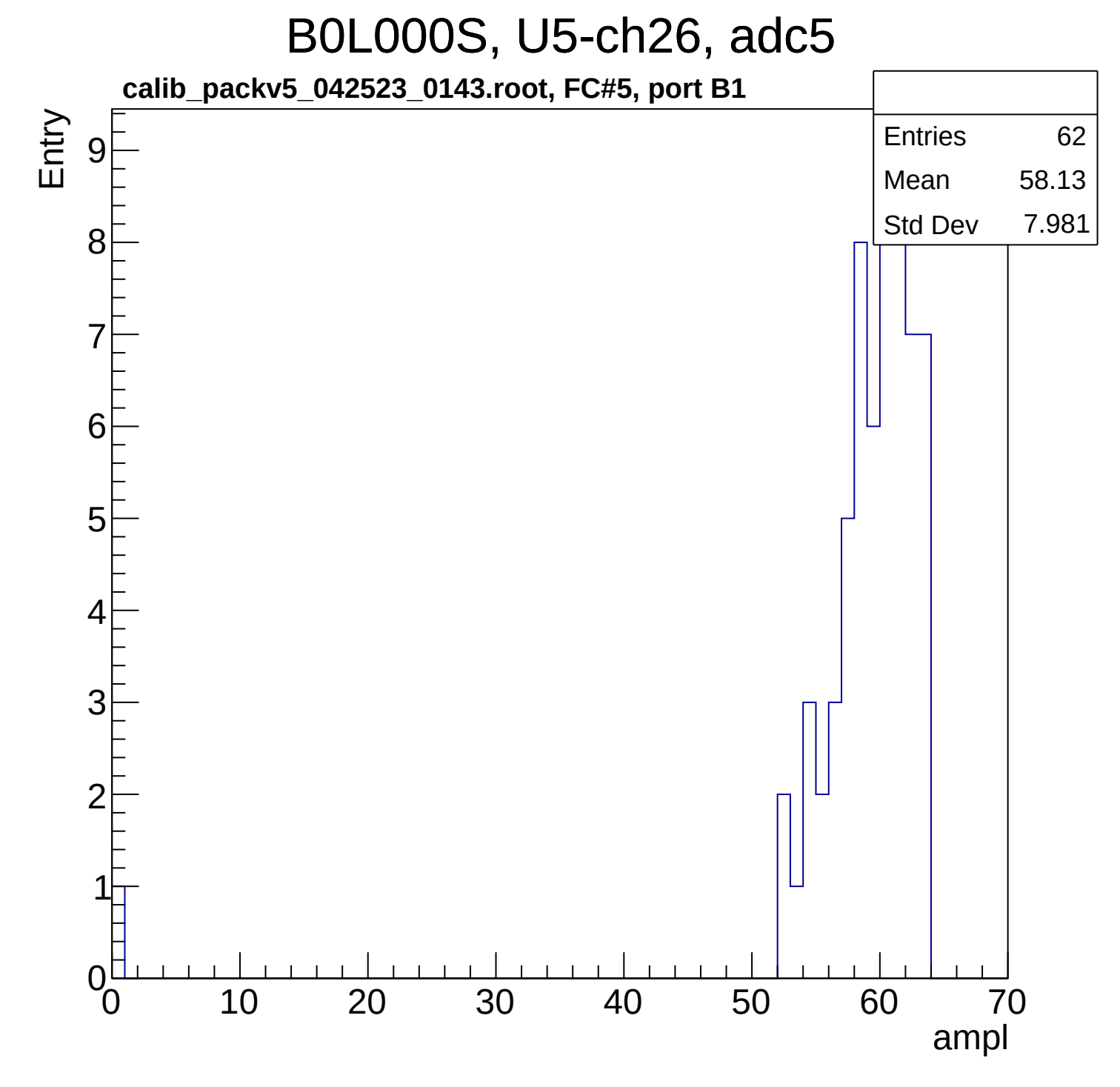
B0L000S, U5-ch26, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.13
Std Dev 7.981
Entry
ampl
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3
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7
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0
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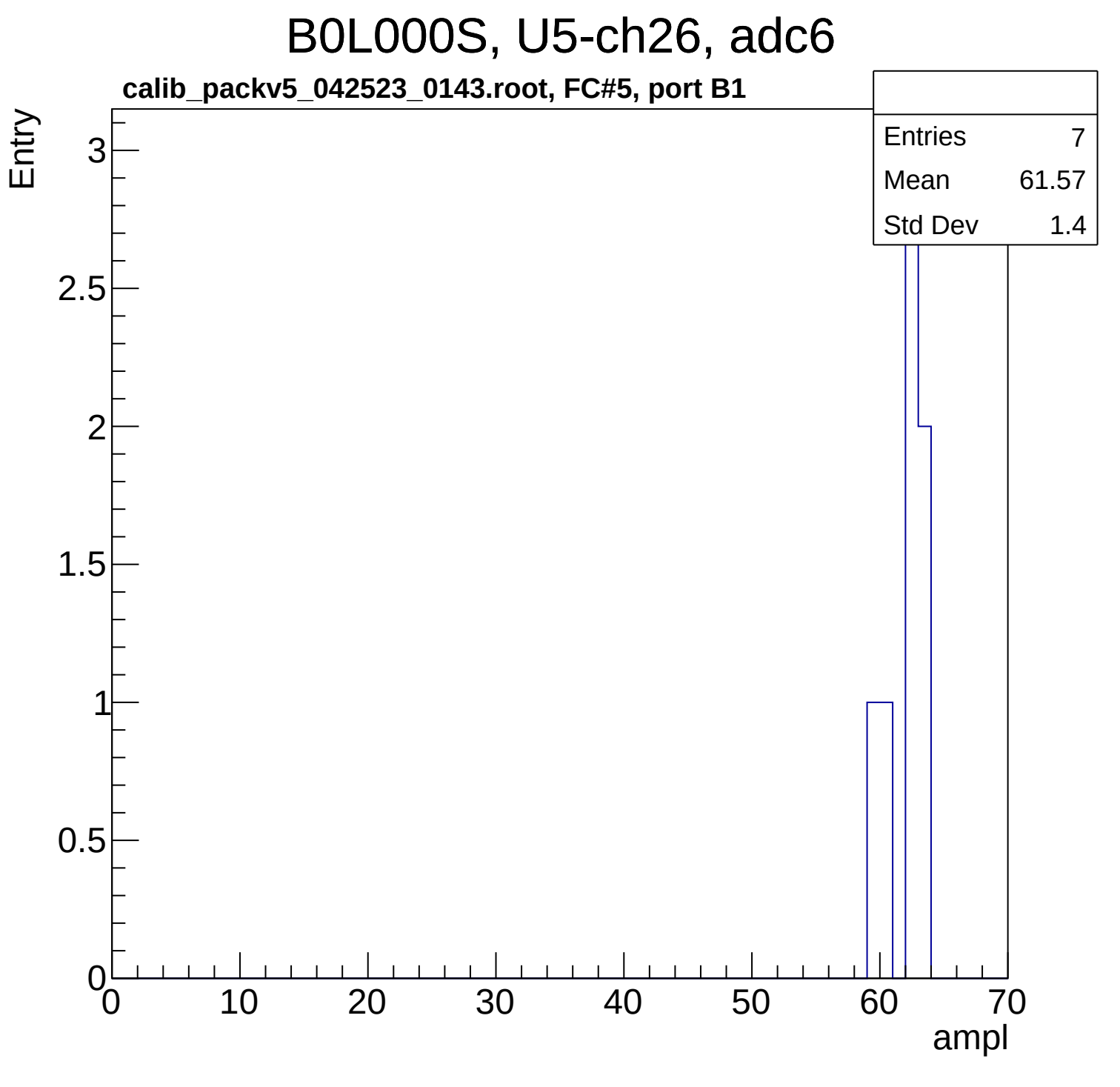
B0L000S, U5-ch26, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.57
Std Dev 1.4
Entry
ampl
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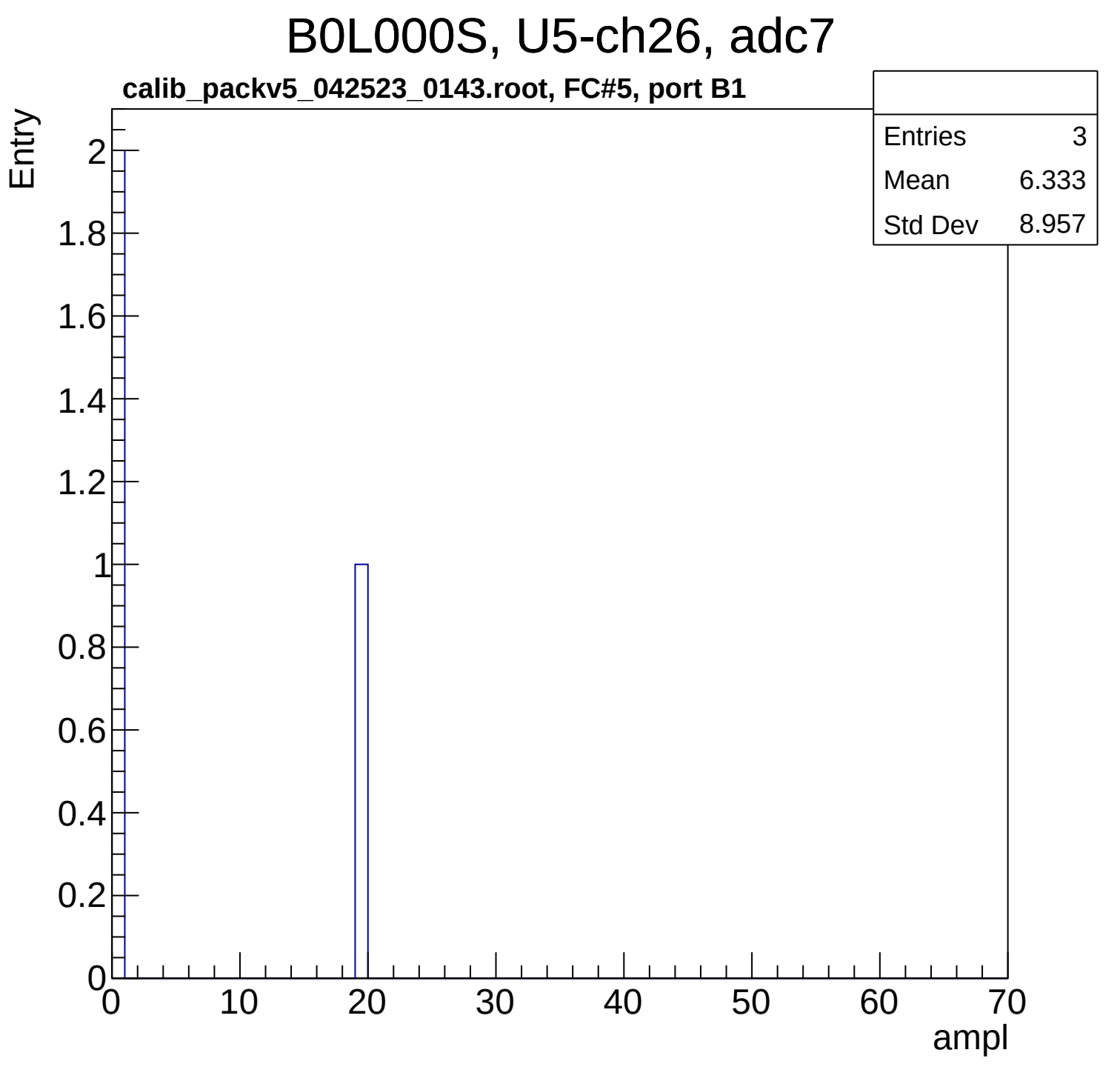
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch26, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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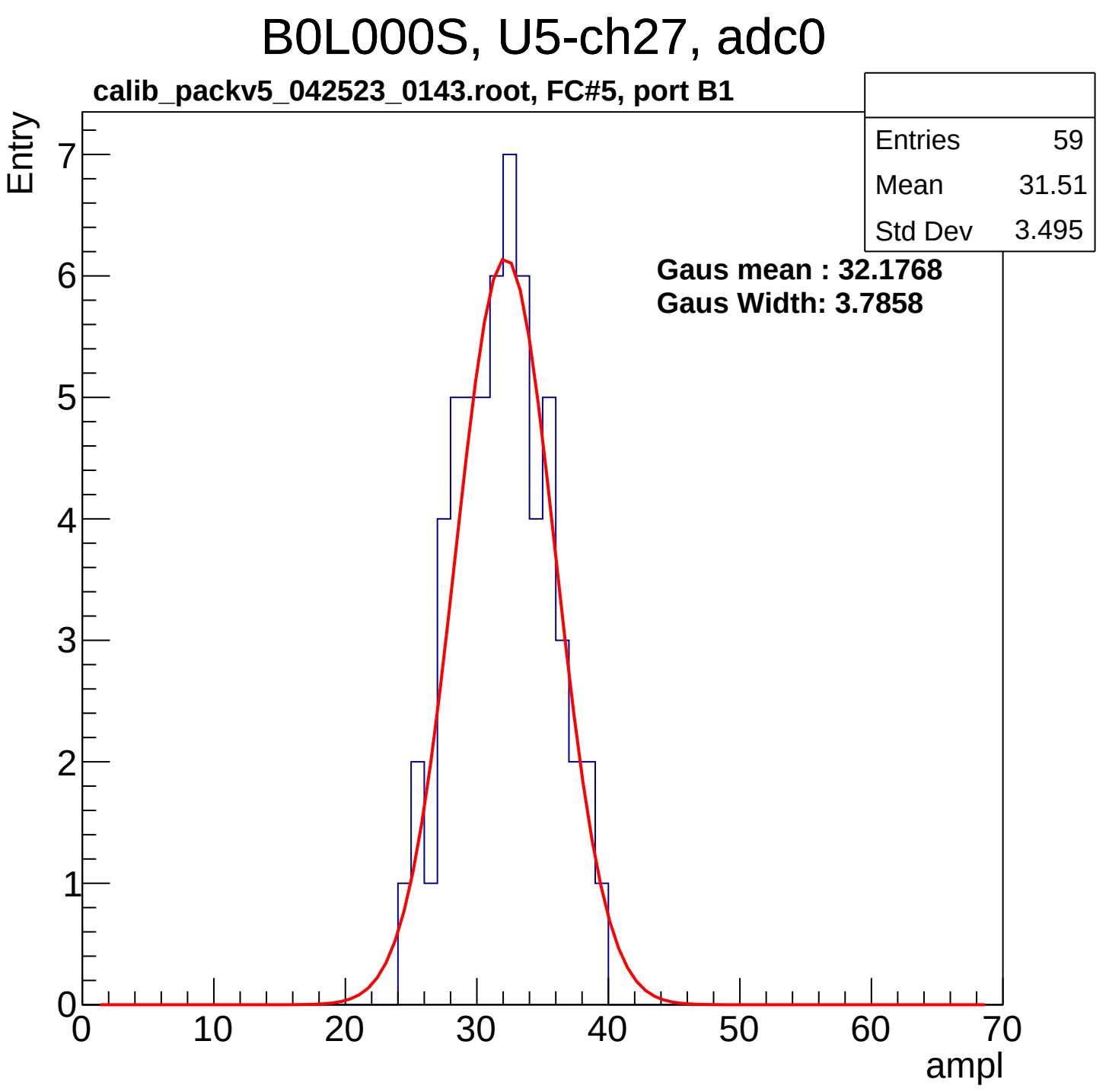
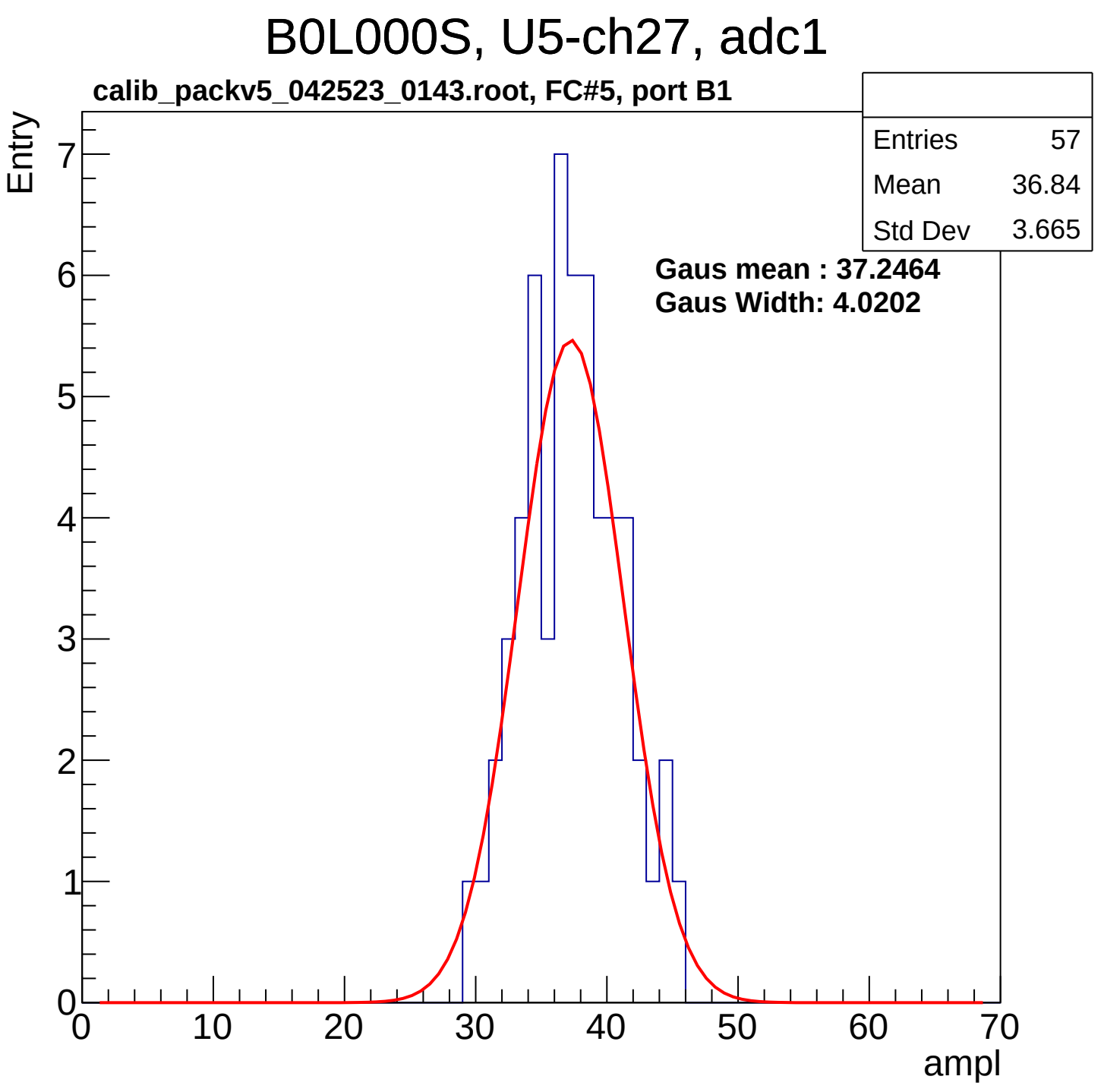

B0L000S, U5-ch27, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 31.51
Std Dev 3.495
Gaus mean : 32.1768
Gaus Width: 3.7858
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch27, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.84
Std Dev 3.665
Gaus mean : 37.2464
Gaus Width: 4.0202
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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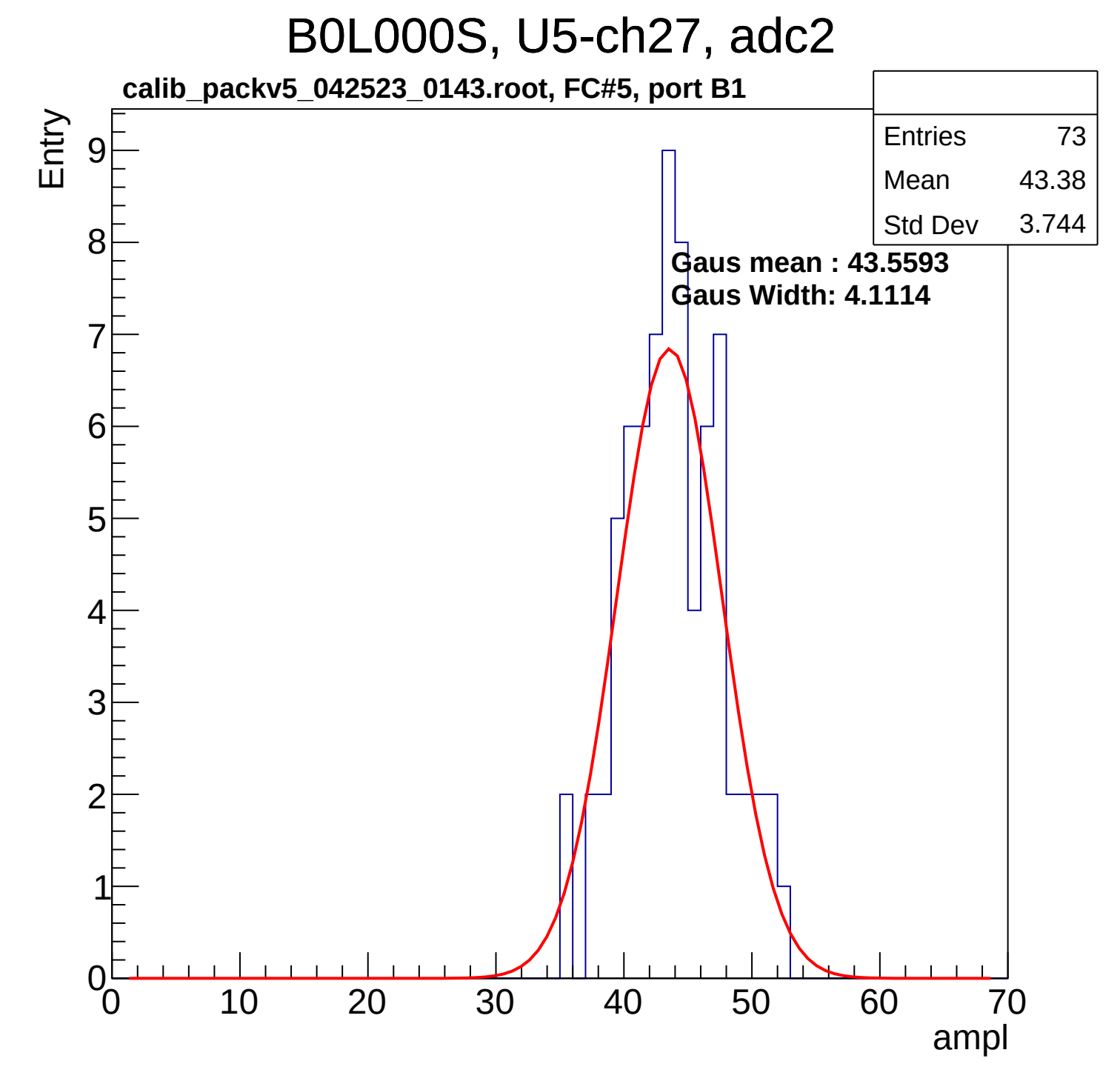
B0L000S, U5-ch27, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.38
Std Dev 3.744
Gaus mean : 43.5593
Gaus Width: 4.1114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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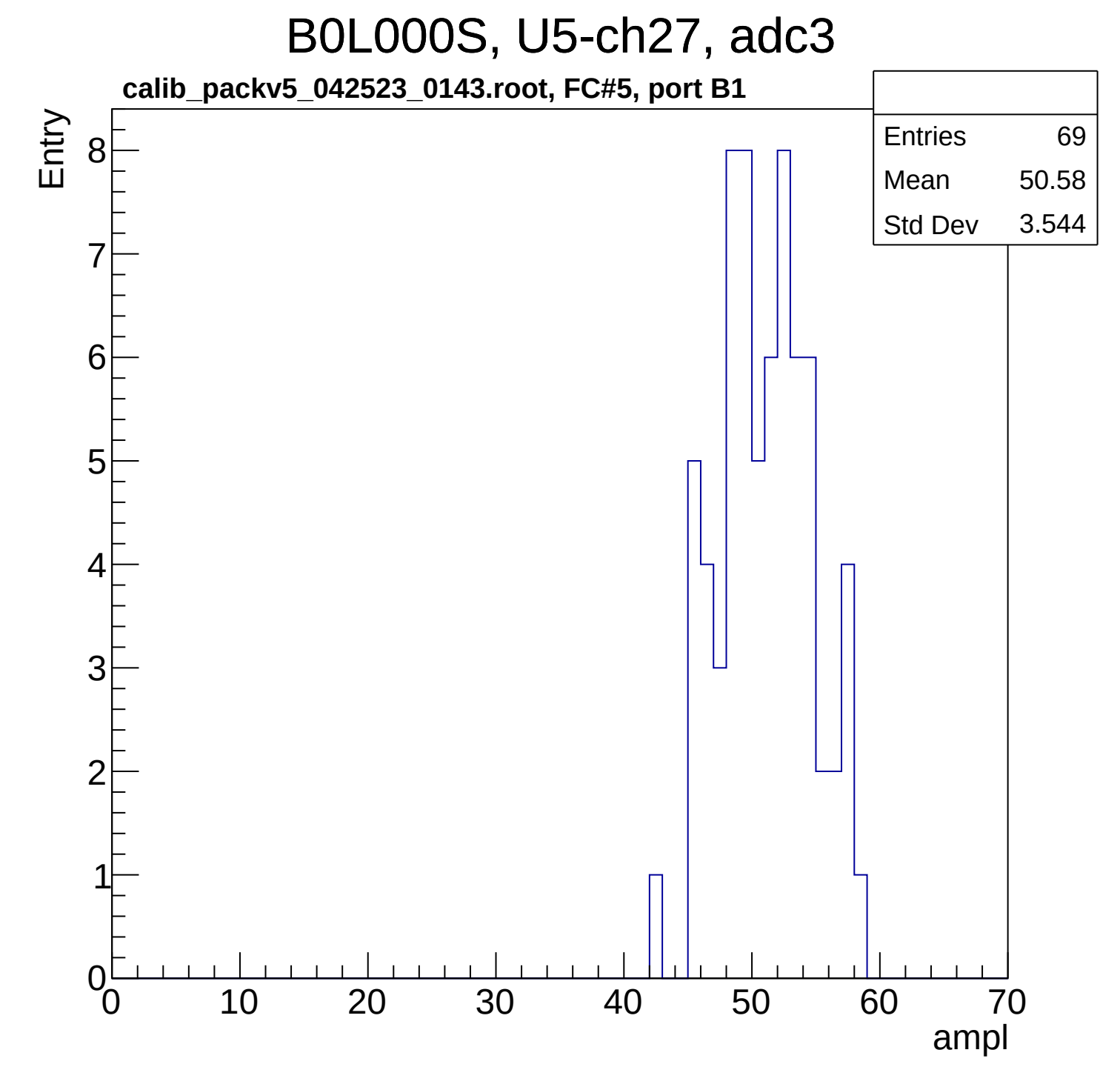
B0L000S, U5-ch27, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.58
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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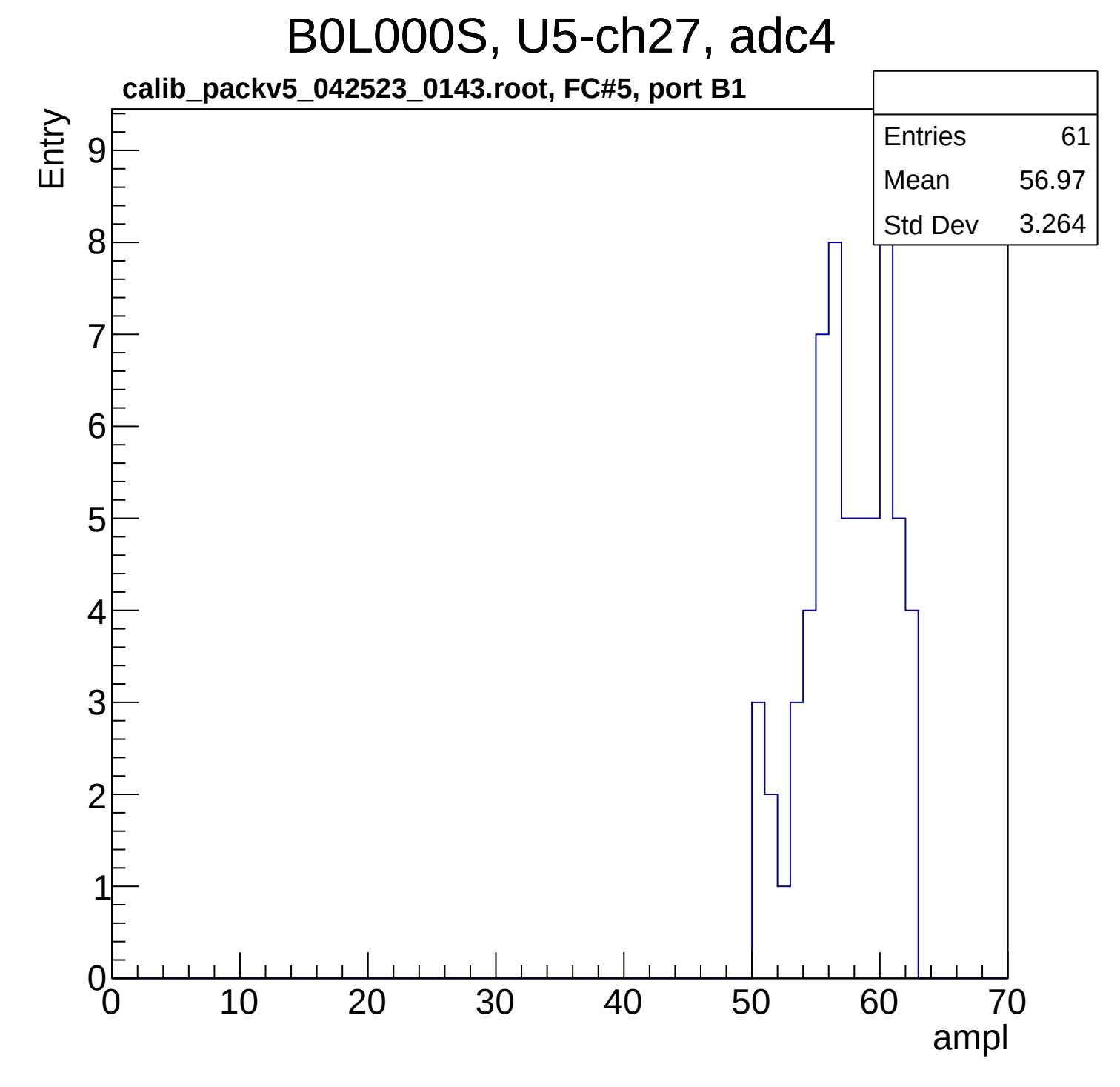
B0L000S, U5-ch27, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.97
Std Dev 3.264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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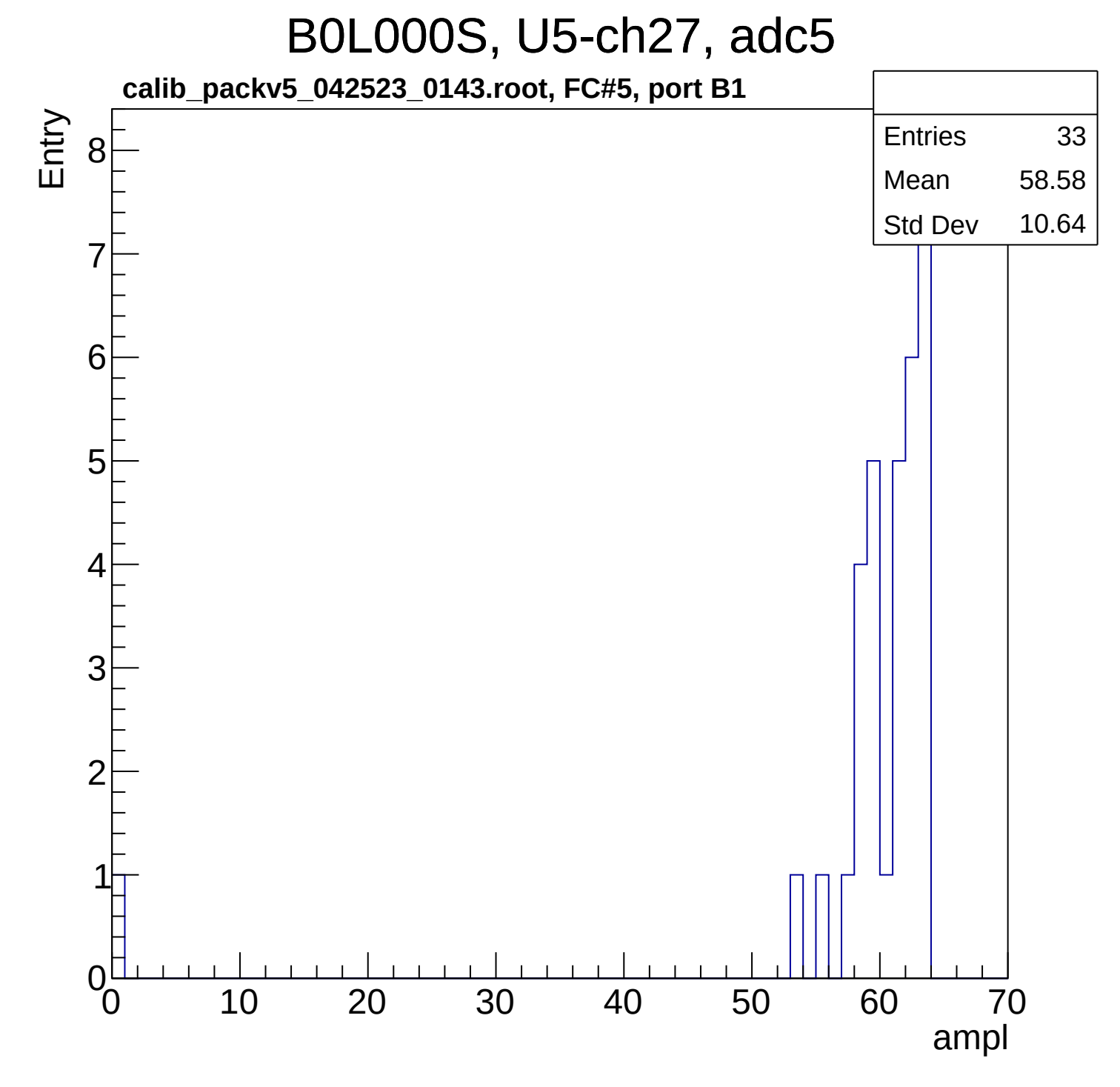
B0L000S, U5-ch27, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.58
Std Dev 10.64
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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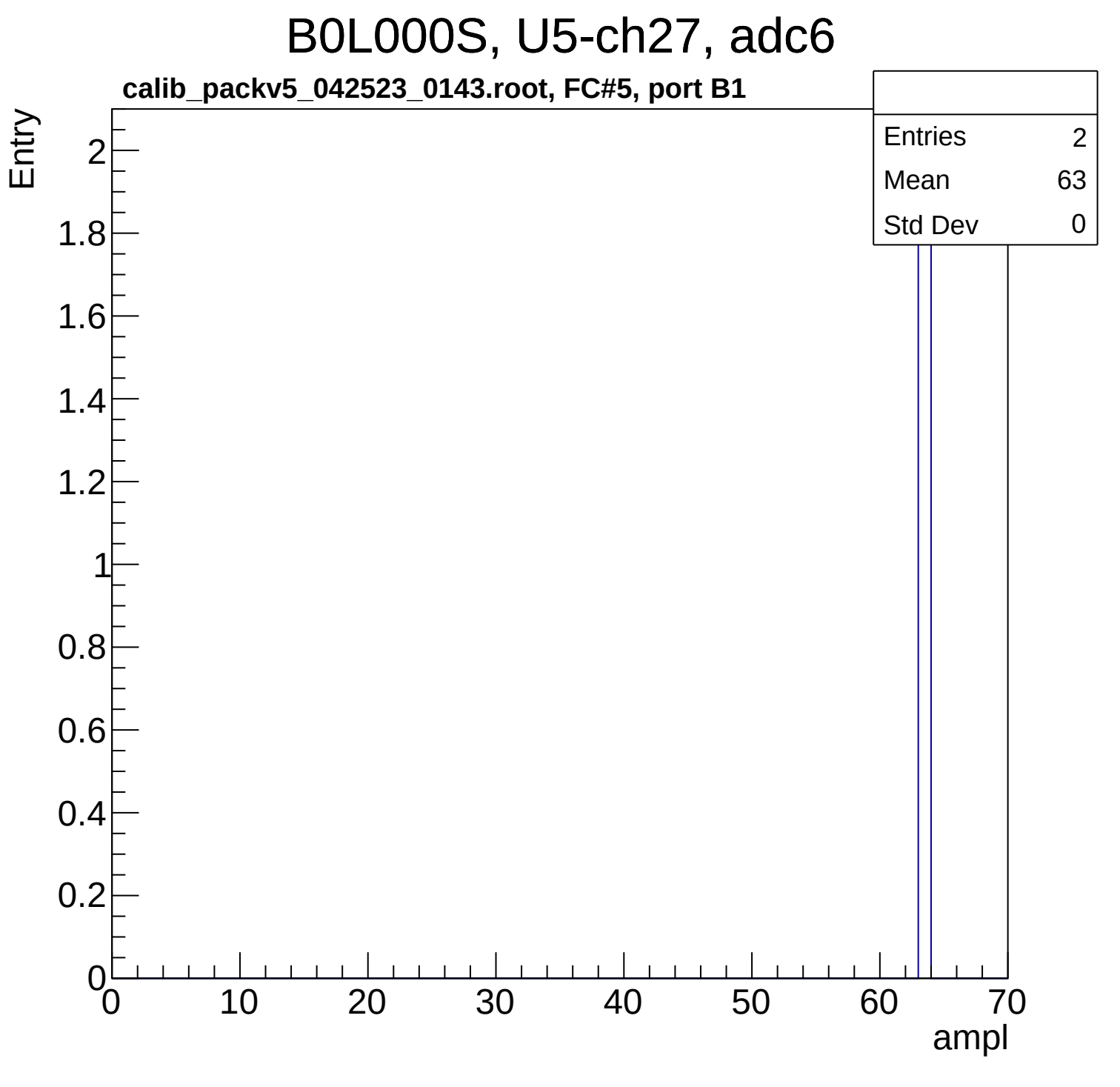
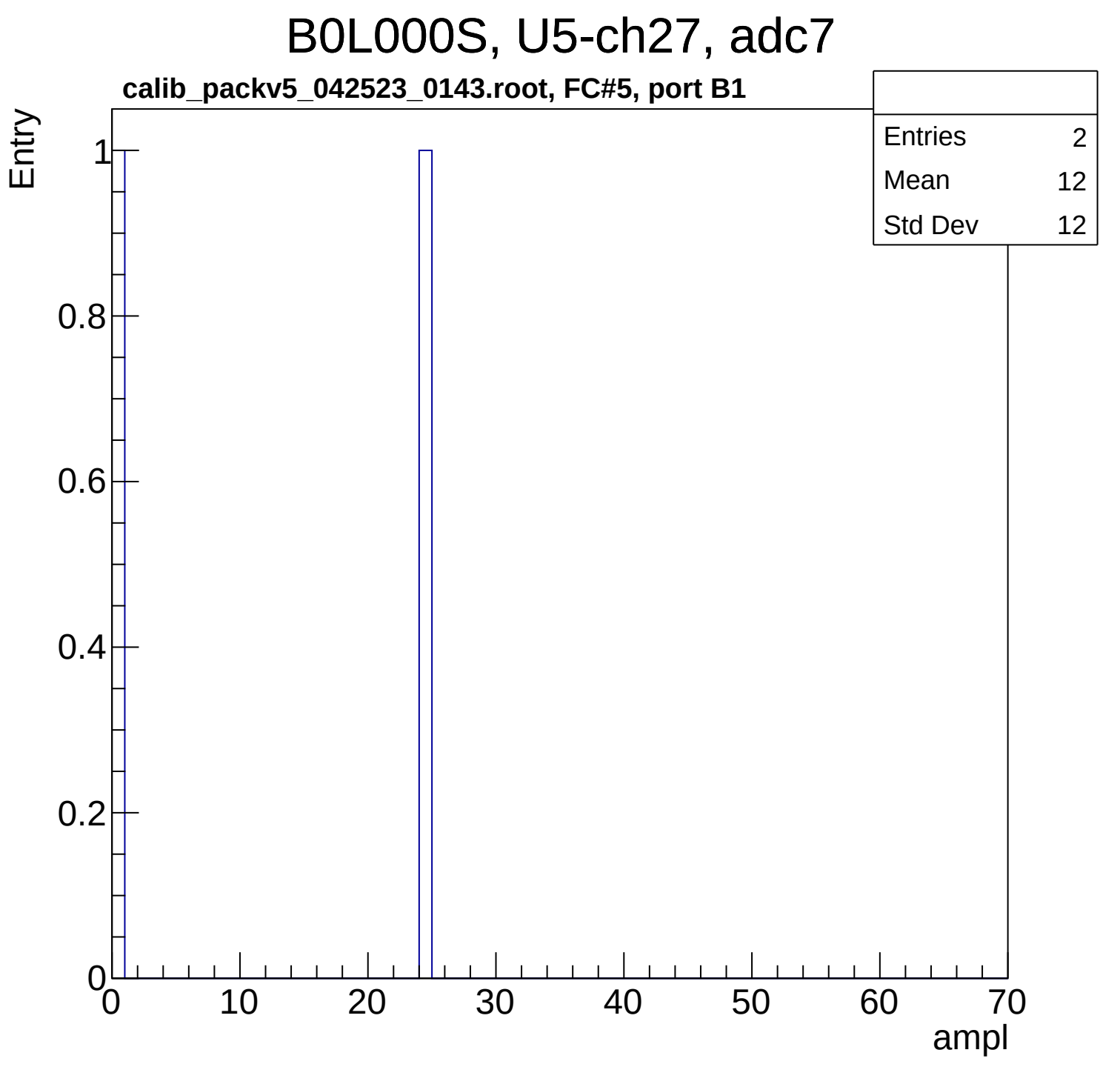
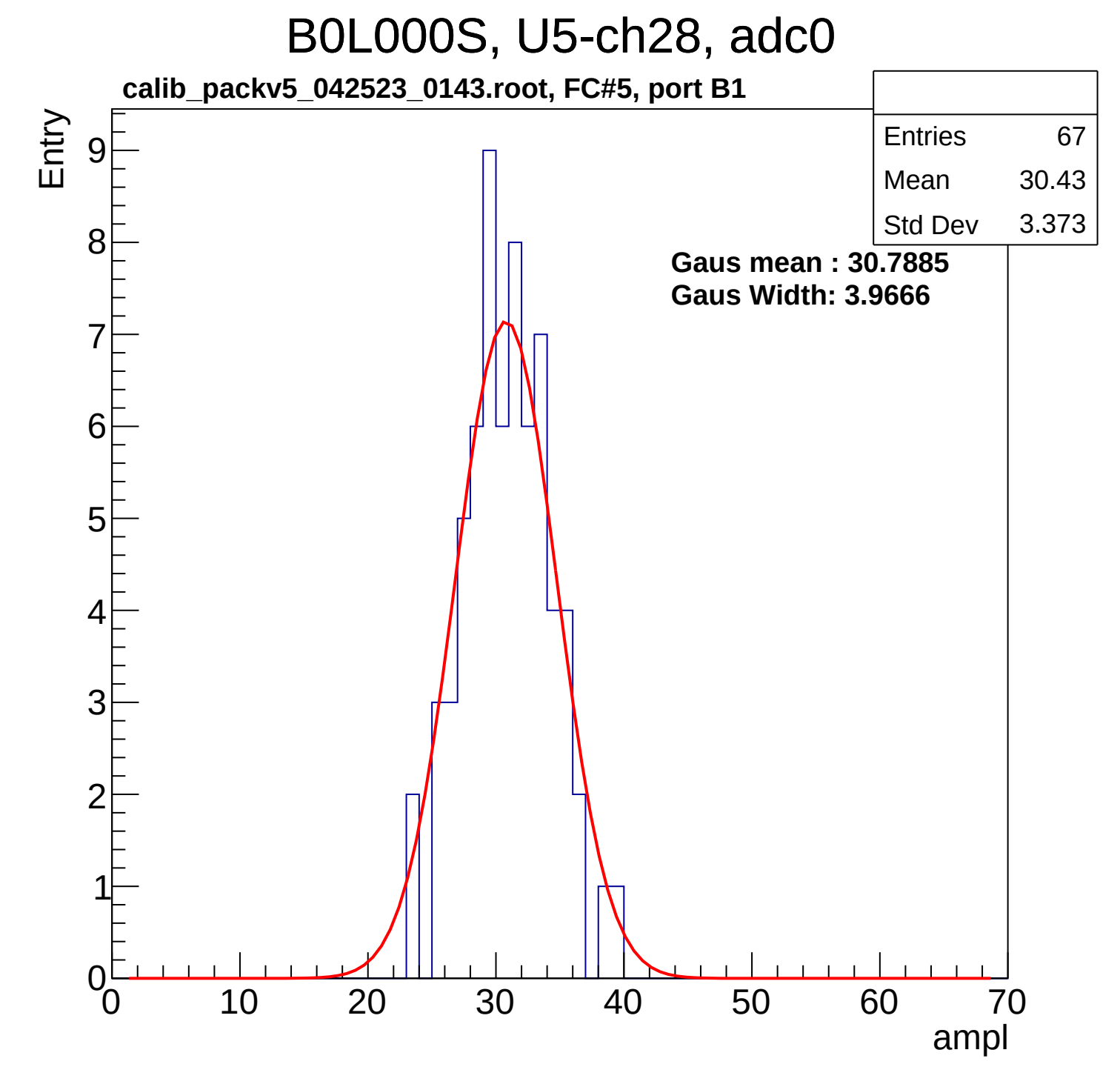

B0L000S, U5-ch27, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch27, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch28, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.43
Std Dev 3.373
Gaus mean : 30.7885
Gaus Width: 3.9666
Entry
ampl
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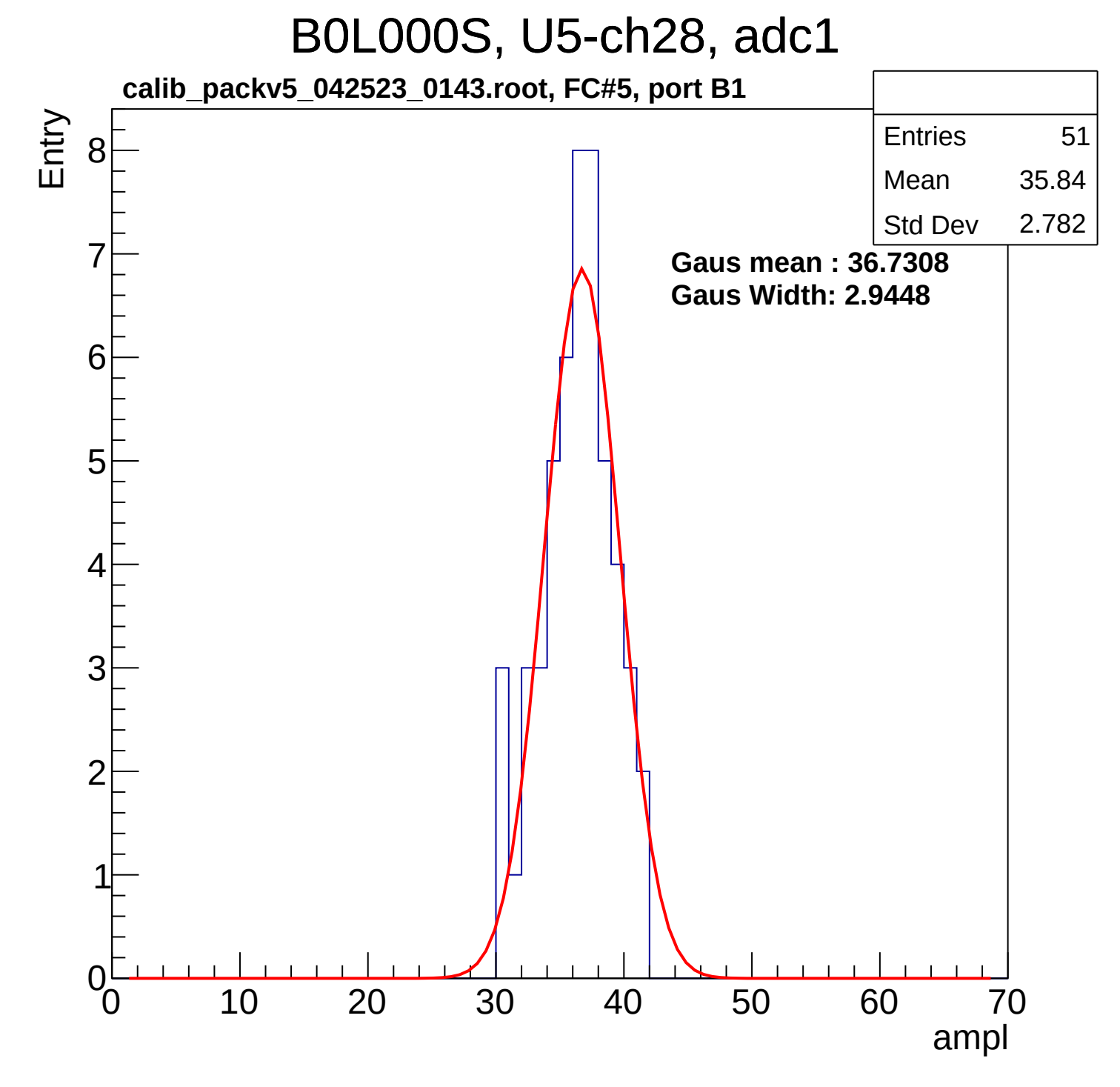
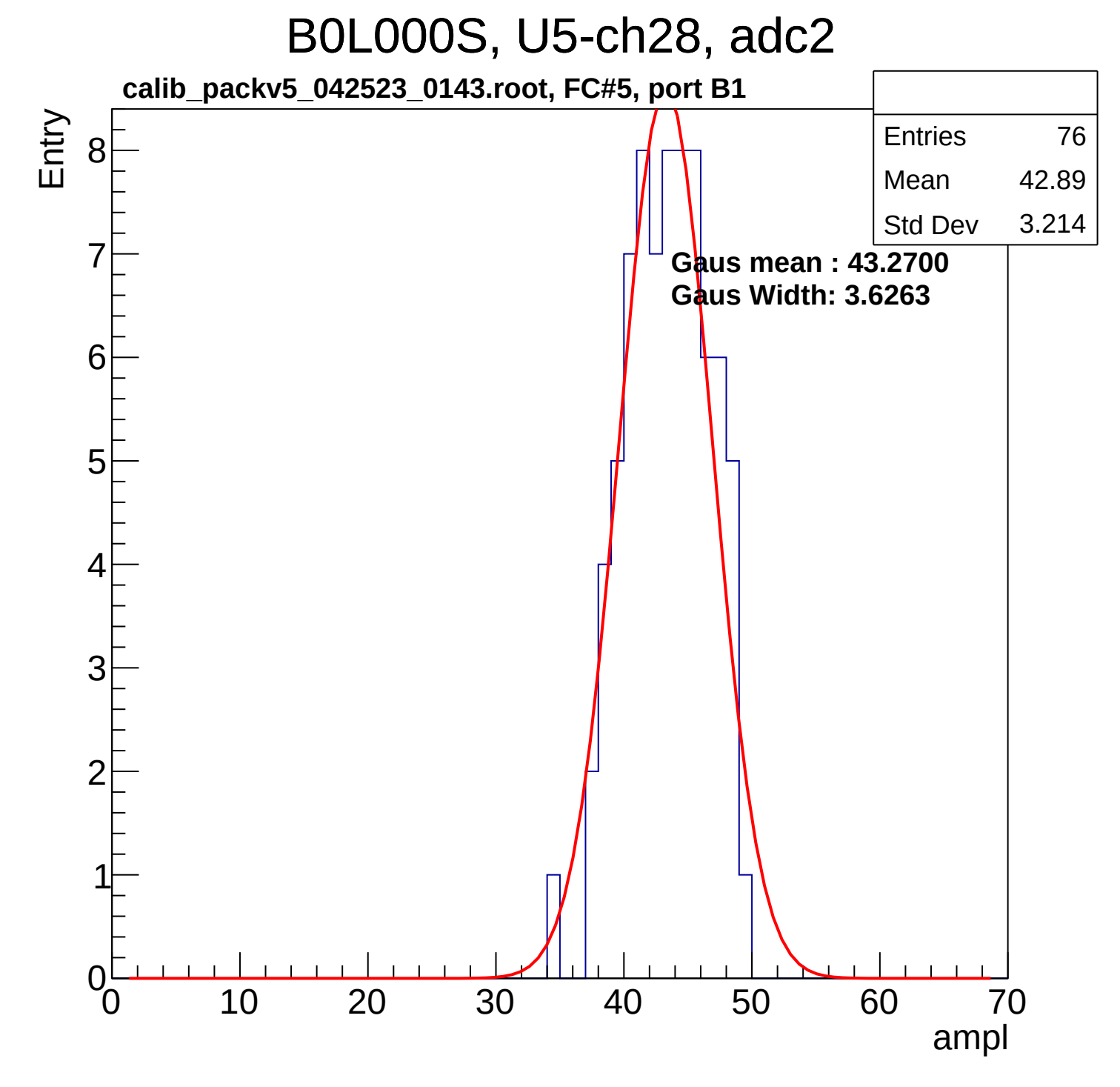
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch28, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 35.84
Std Dev 2.782
Gaus mean : 36.7308
Gaus Width: 2.9448
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch28, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 42.89
Std Dev 3.214
Gaus mean : 43.2700
Gaus Width: 3.6263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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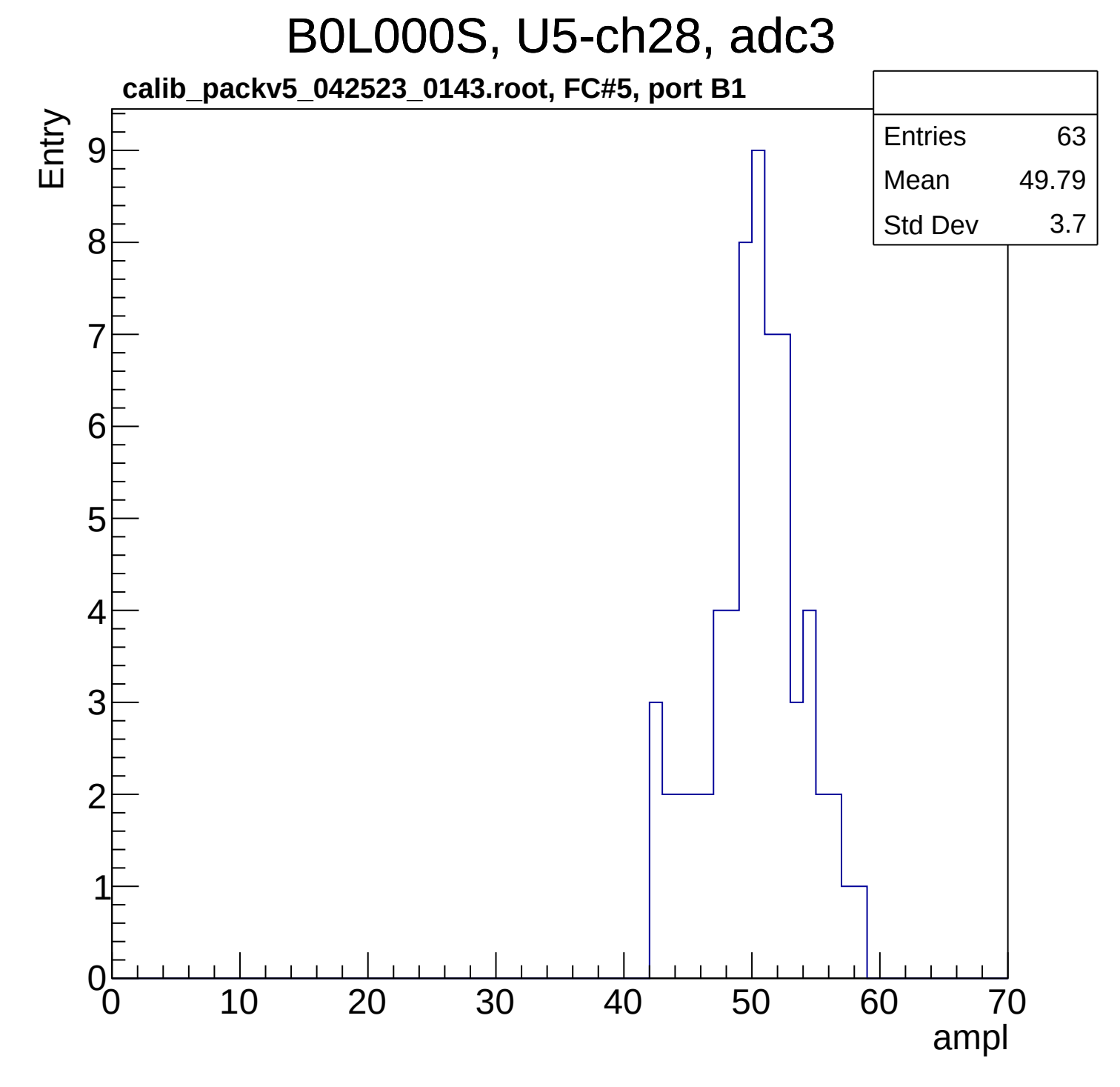
B0L000S, U5-ch28, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.79
Std Dev 3.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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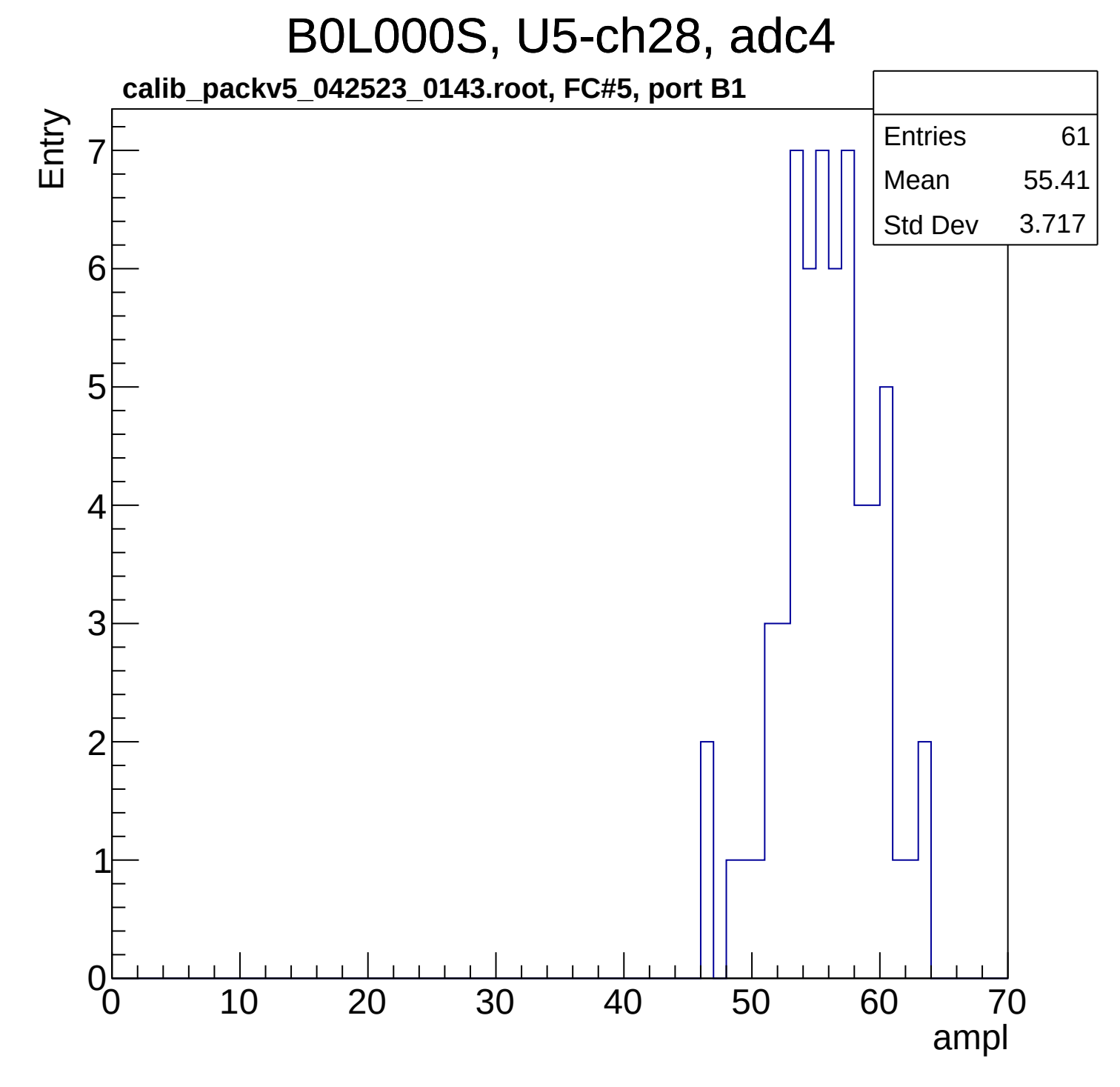
B0L000S, U5-ch28, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.41
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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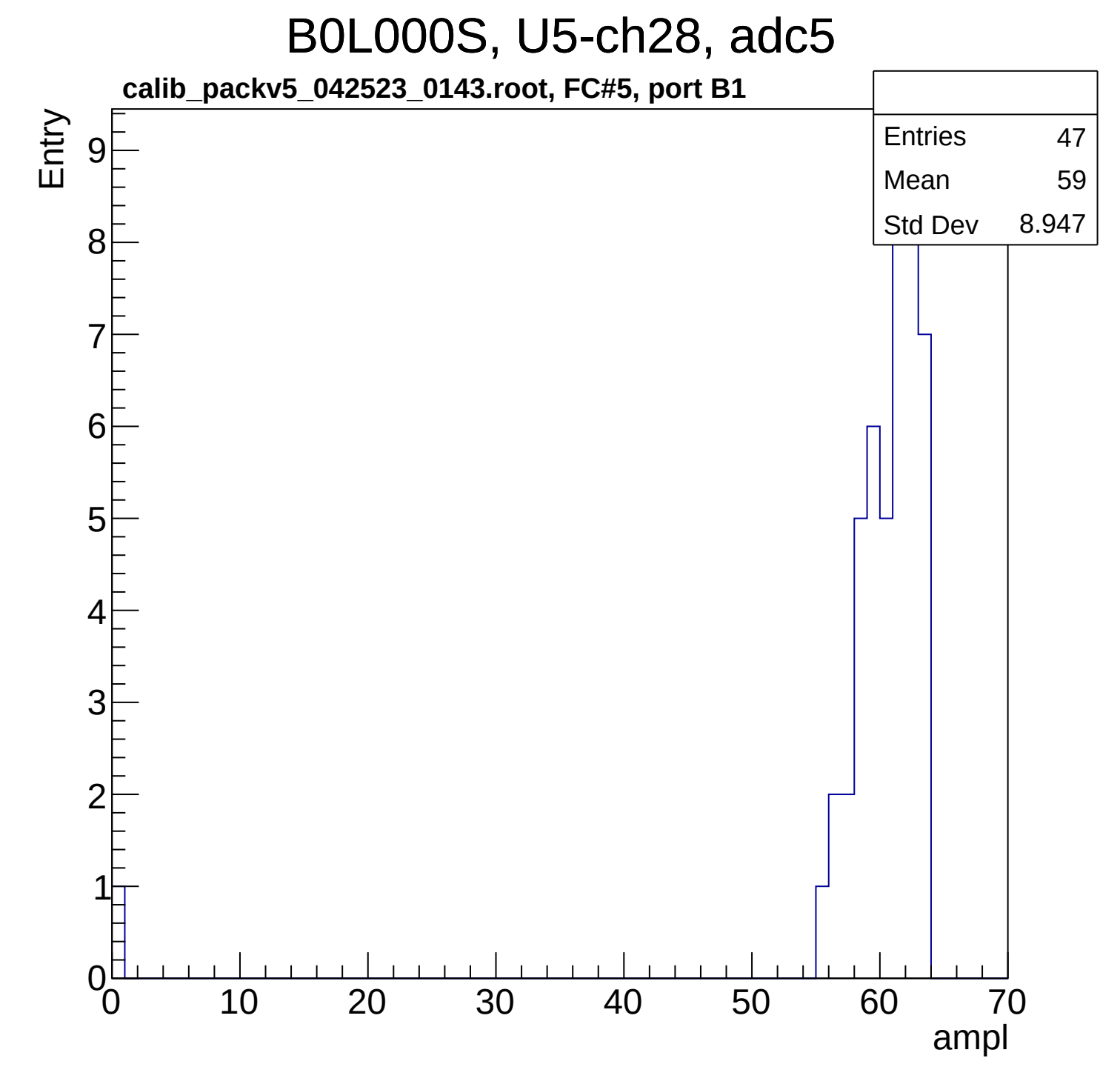
B0L000S, U5-ch28, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59
Std Dev 8.947
Entry
ampl
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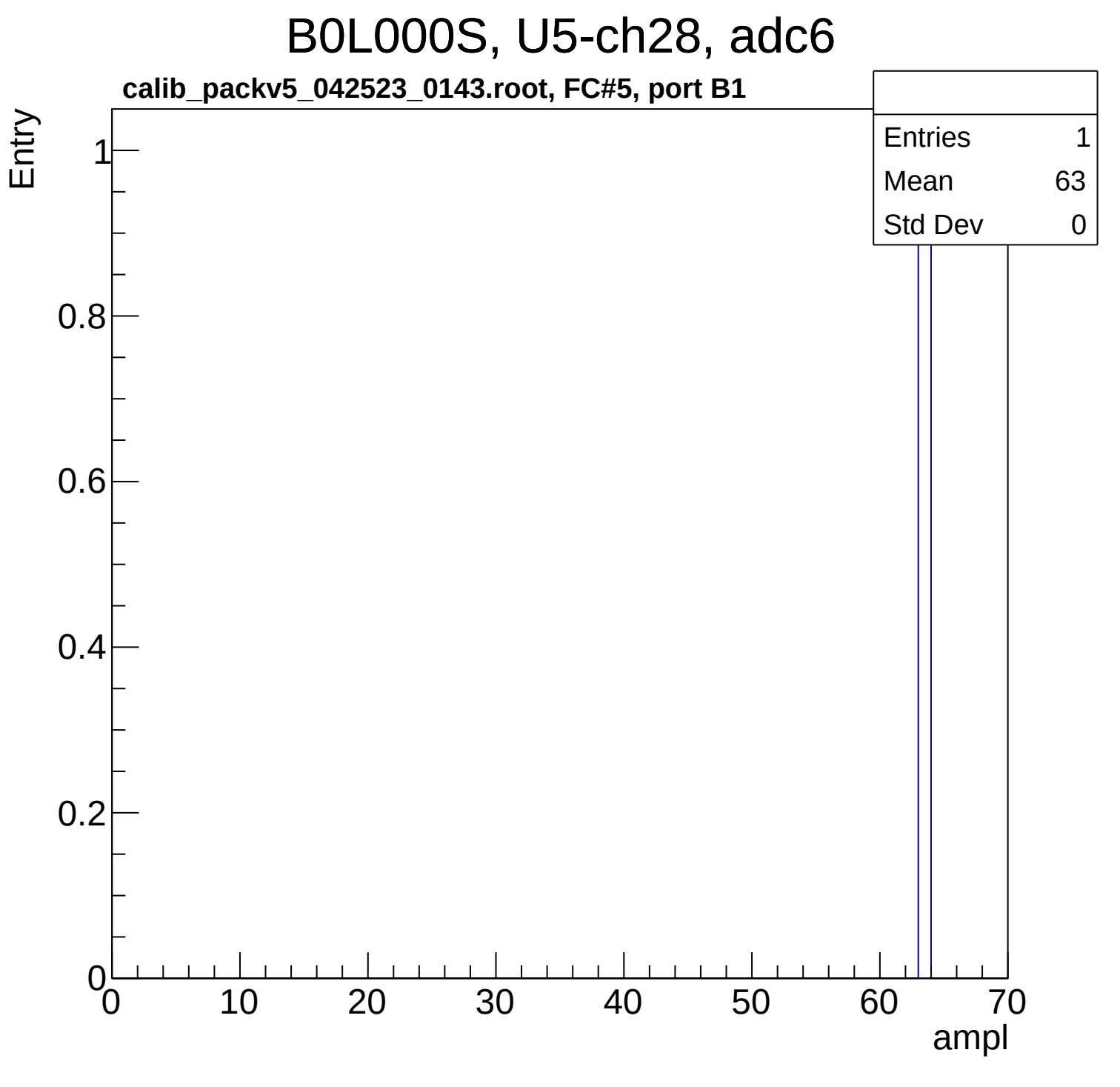
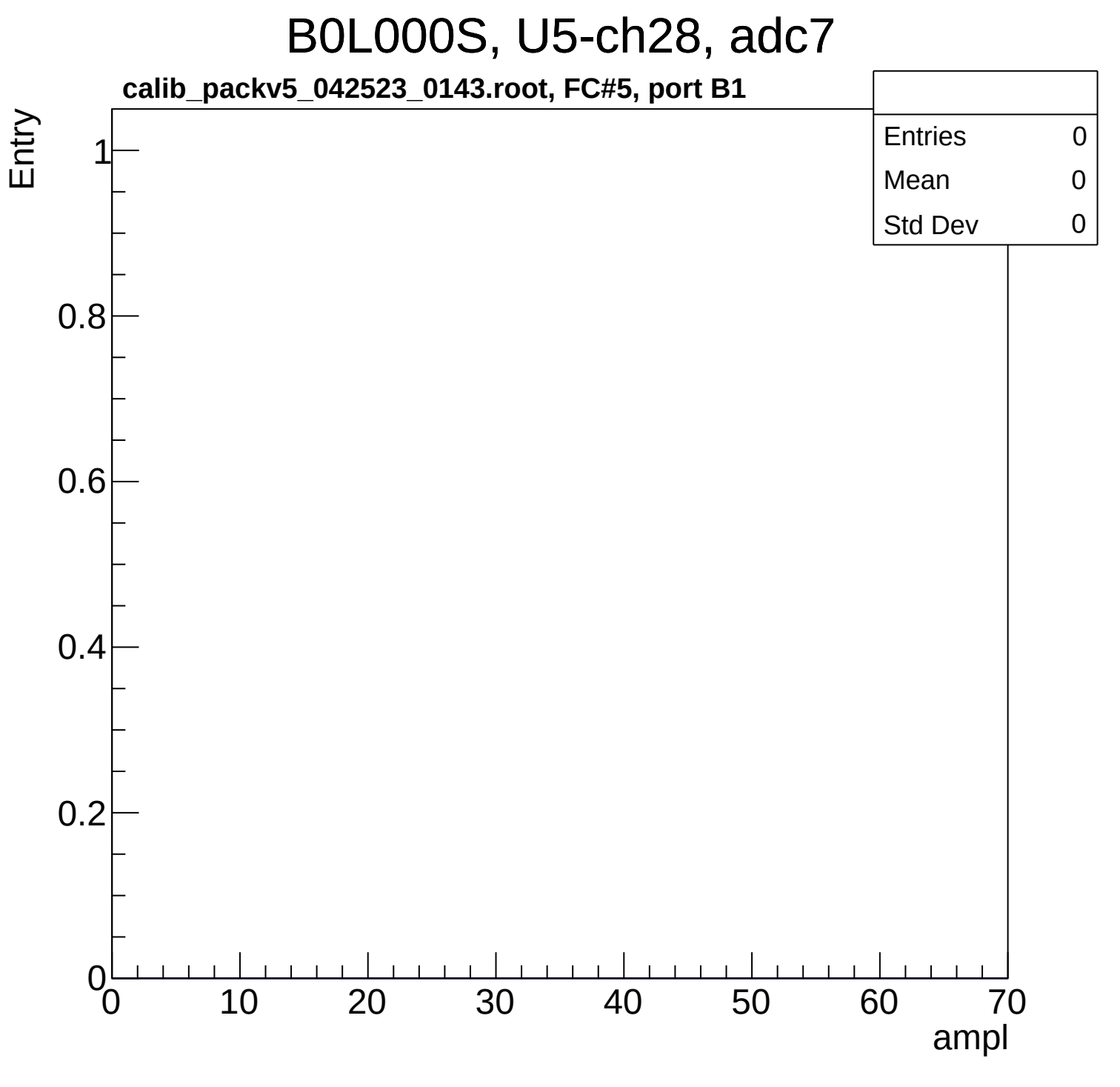
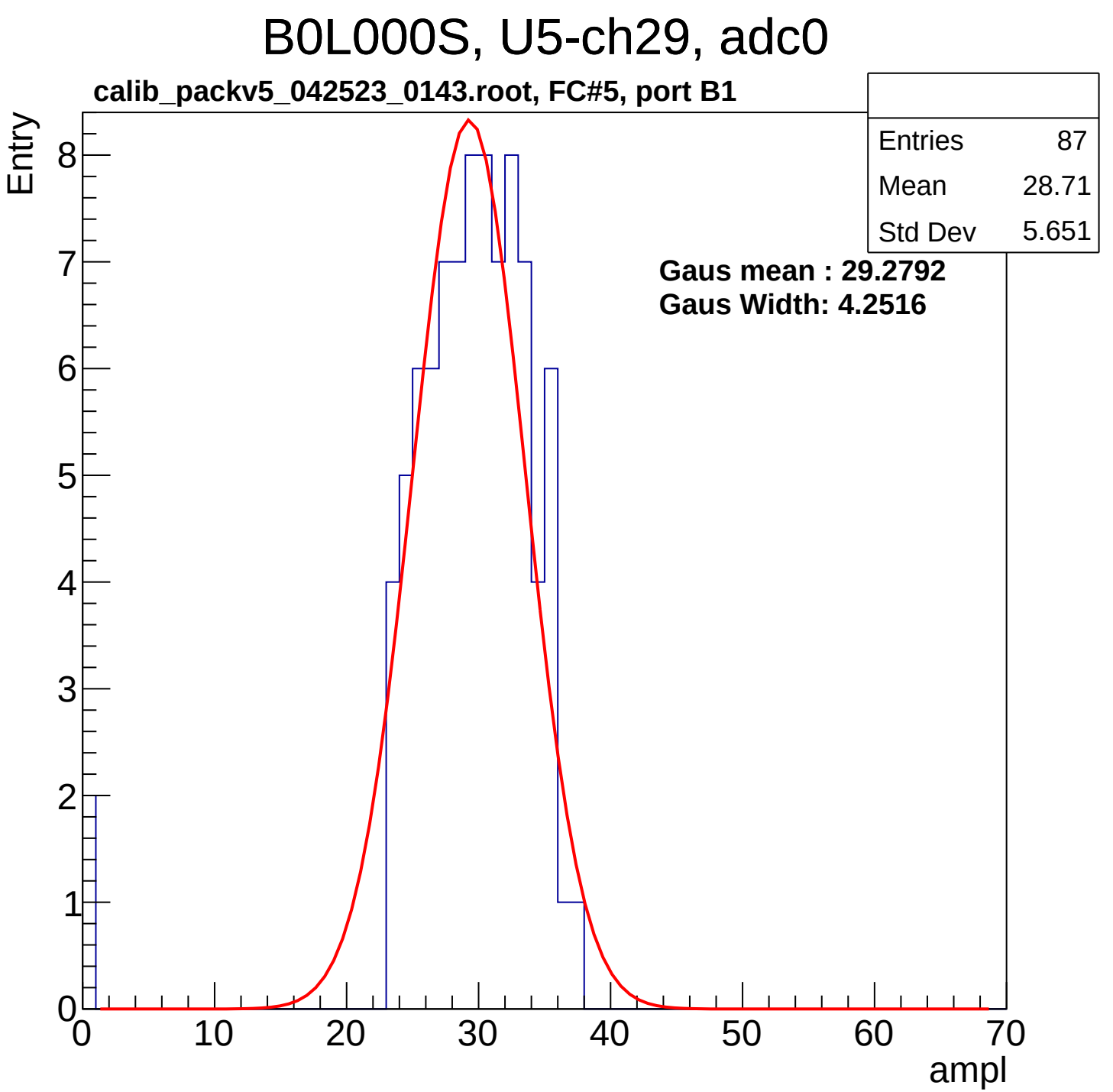
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch28, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch28, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch29, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 28.71
Std Dev 5.651
Gaus mean : 29.2792
Gaus Width: 4.2516
Entry
ampl
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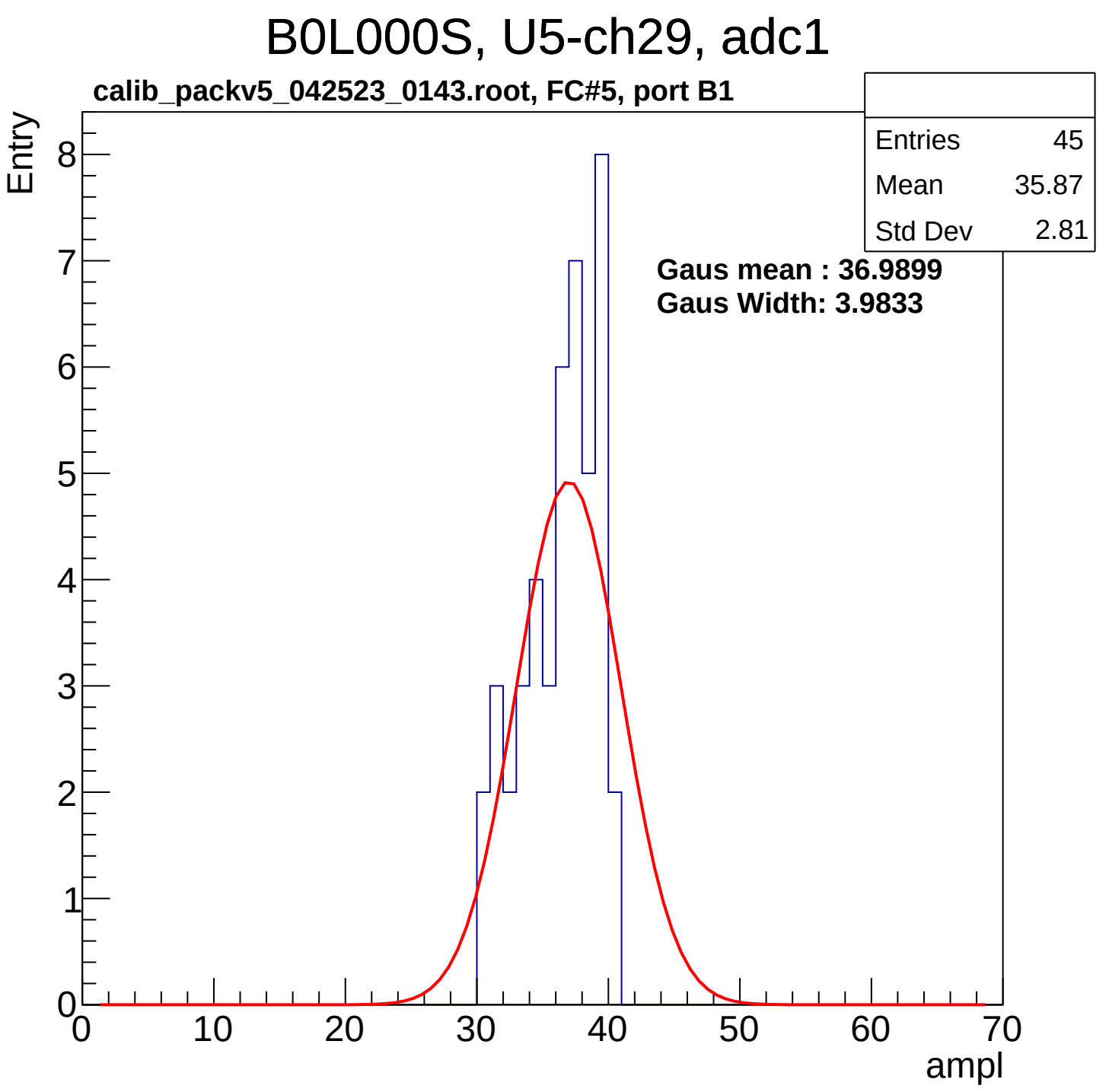
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch29, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 35.87
Std Dev 2.81
Gaus mean : 36.9899
Gaus Width: 3.9833
Entry
ampl
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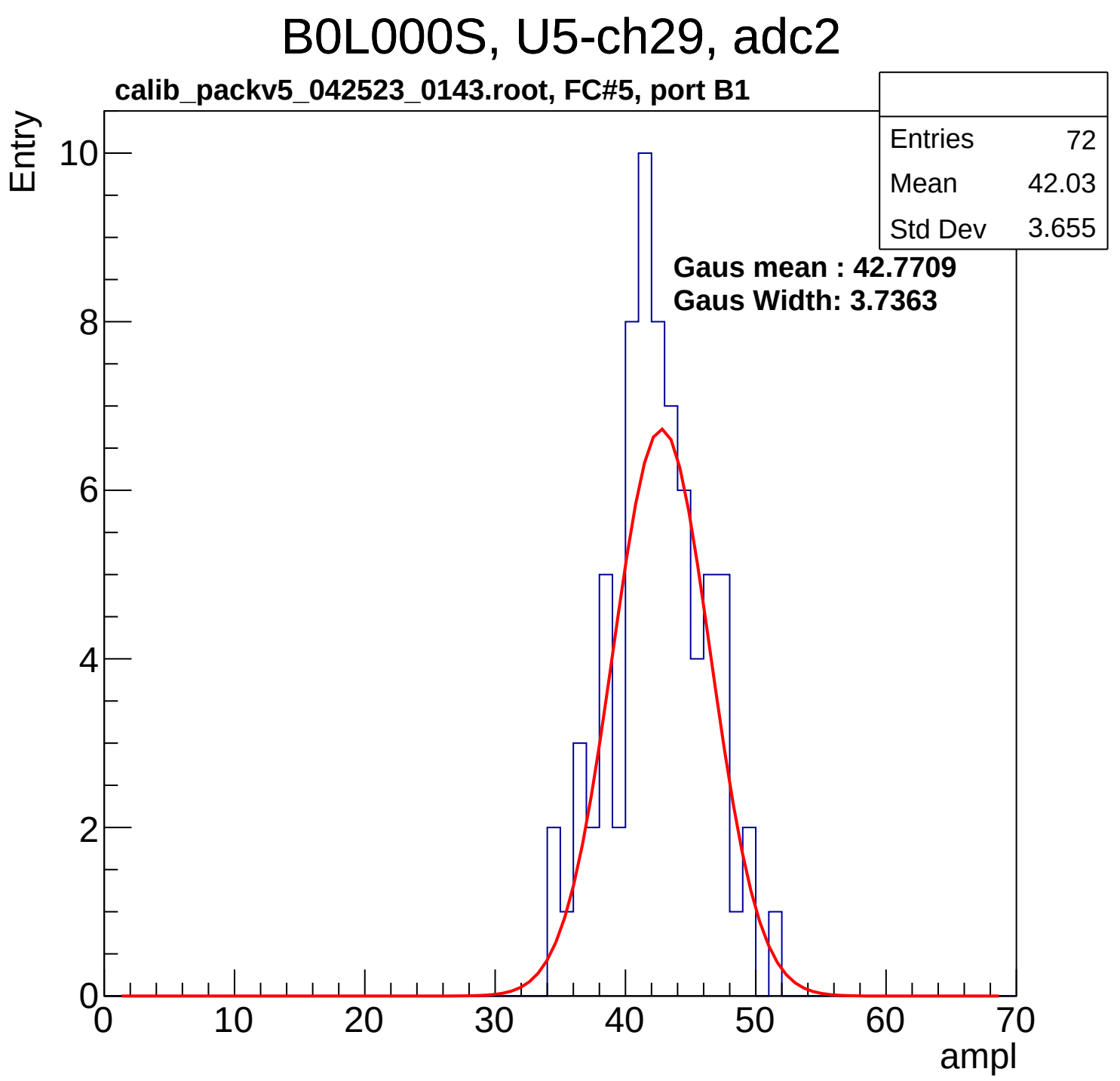
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch29, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 42.03
Std Dev 3.655
Gaus mean : 42.7709
Gaus Width: 3.7363
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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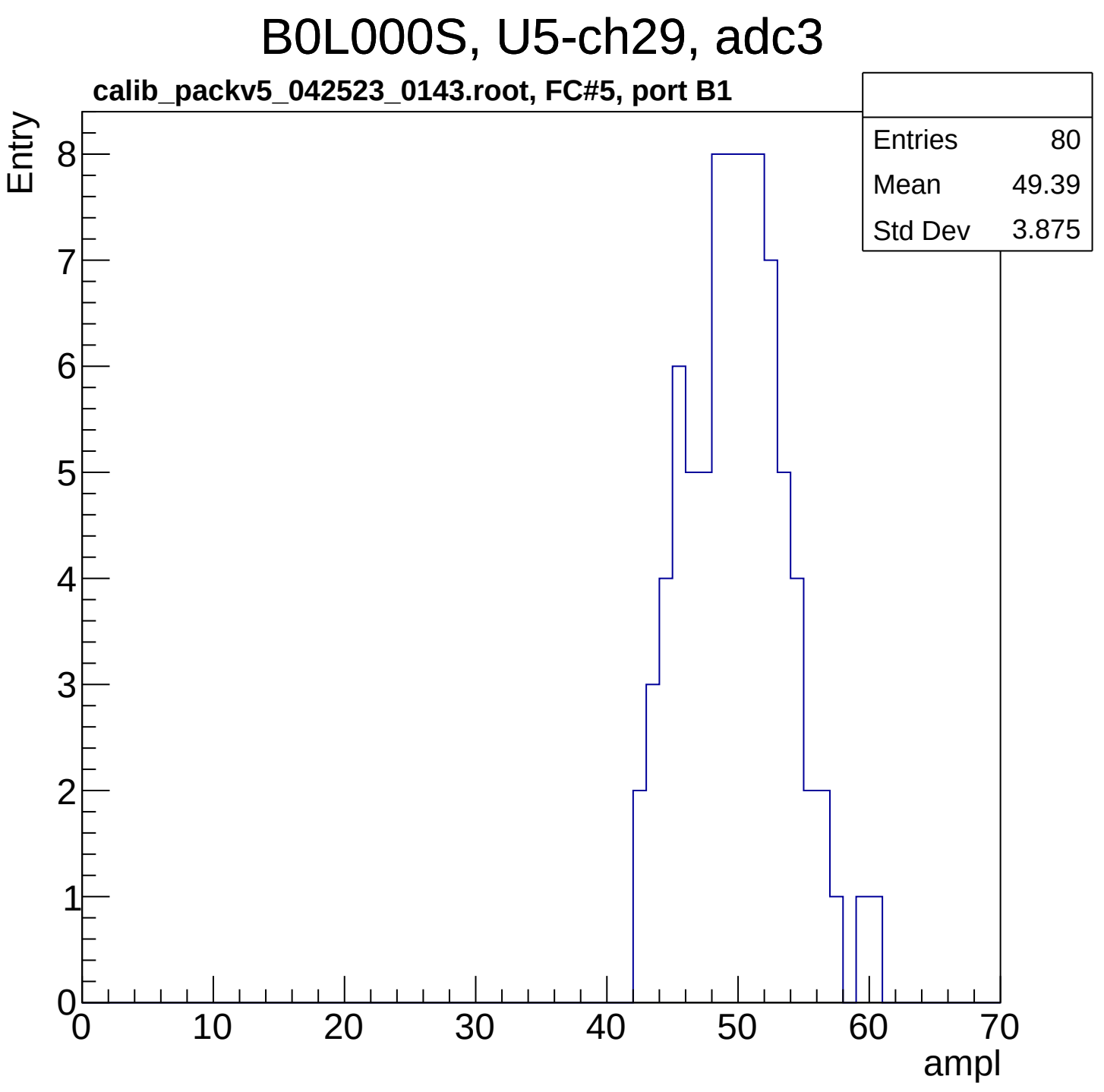
B0L000S, U5-ch29, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 49.39
Std Dev 3.875
Entry
ampl
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2
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4
5
6
7
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0
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40
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60
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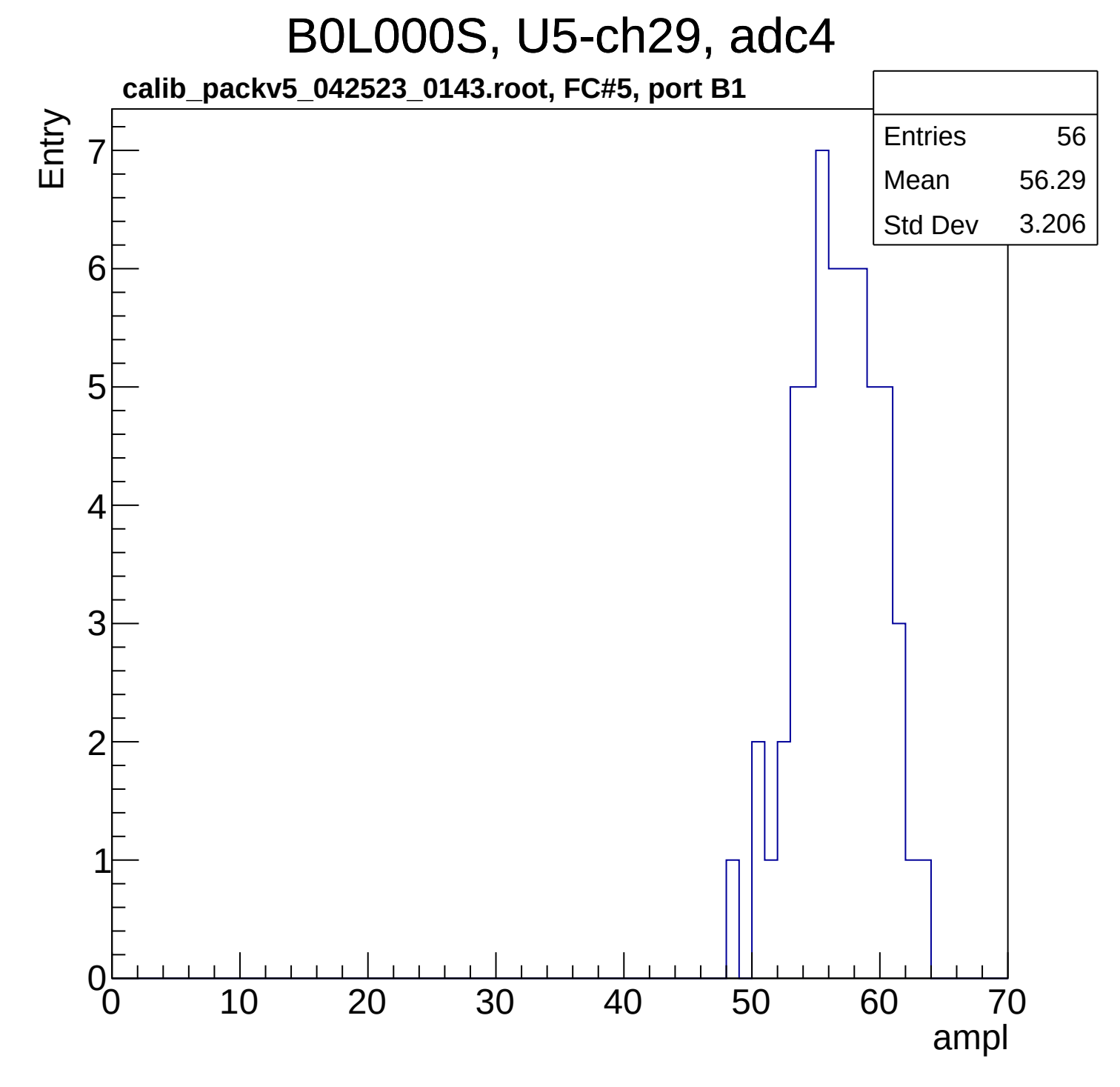
B0L000S, U5-ch29, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.29
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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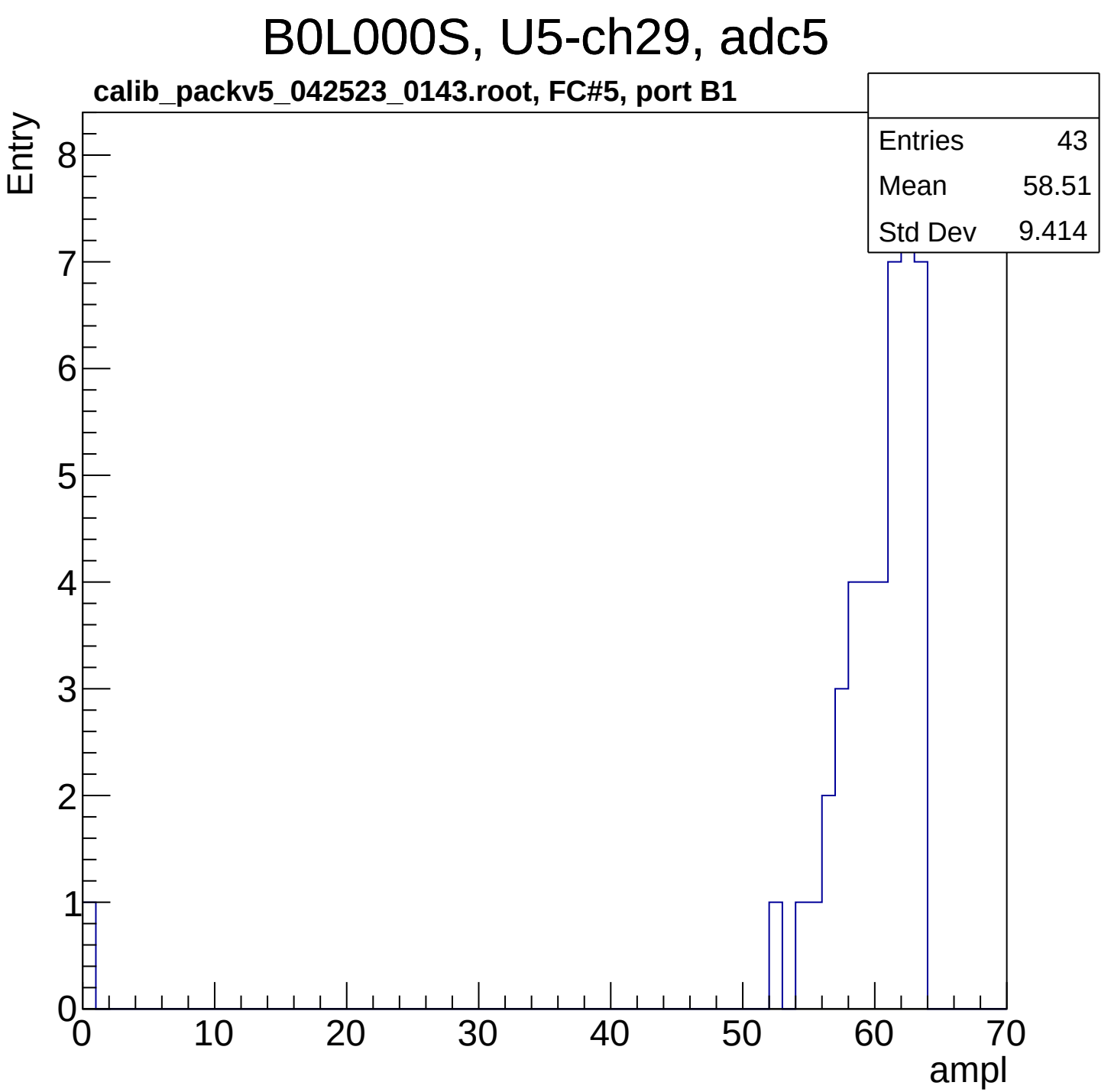
40
50
60
70


B0L000S, U5-ch29, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 58.51
Std Dev 9.414
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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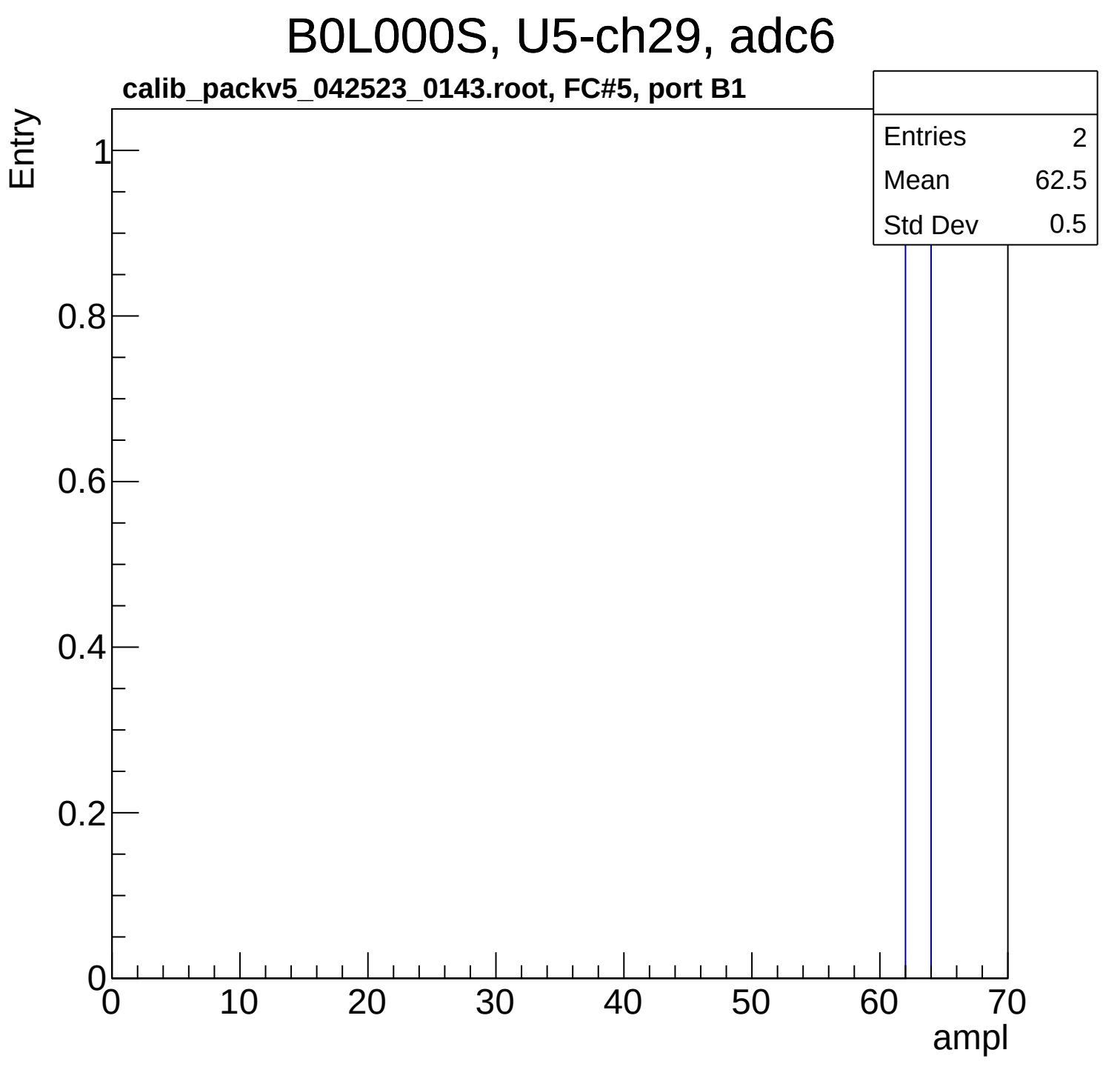
60
70


B0L000S, U5-ch29, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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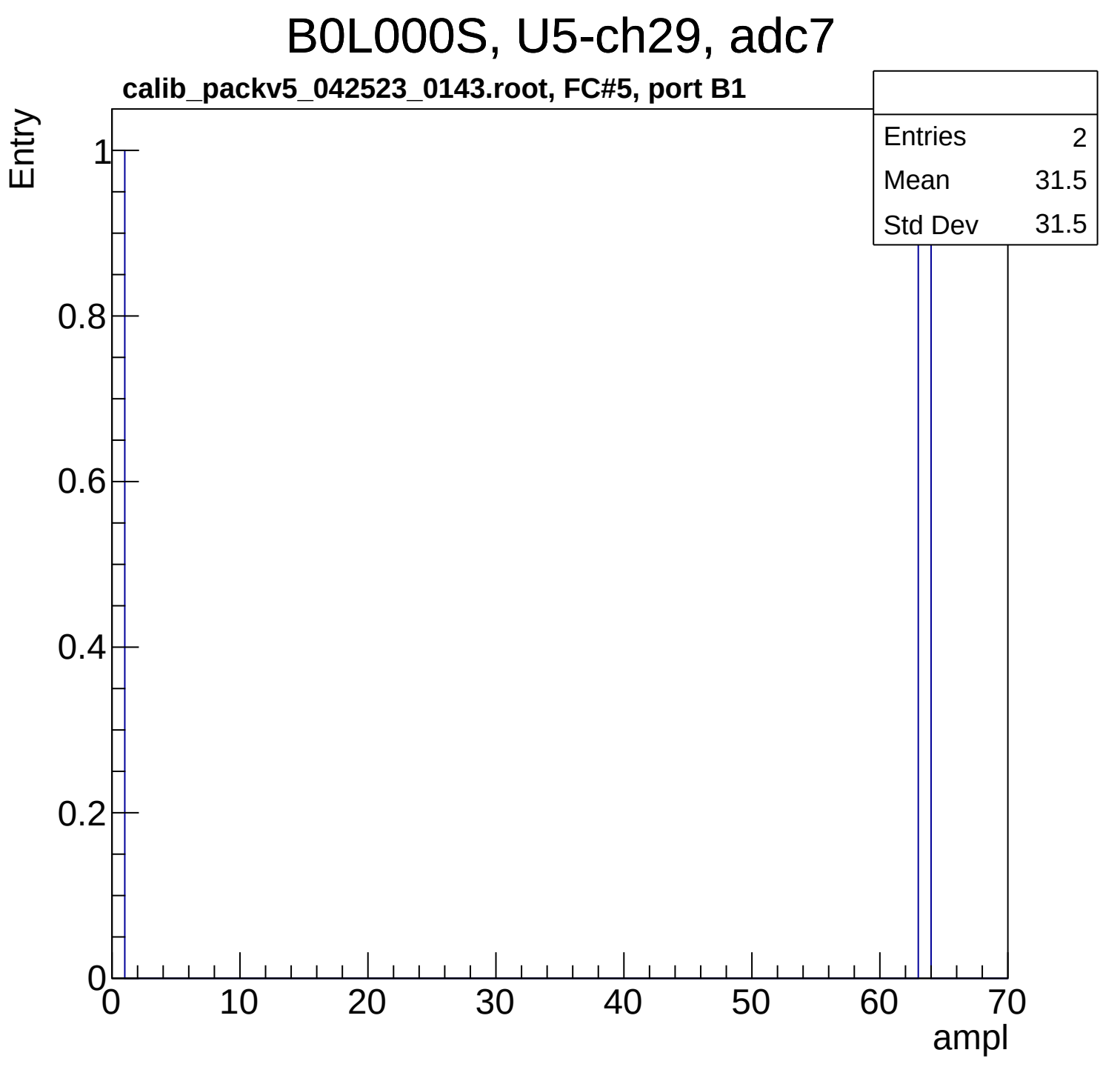
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch29, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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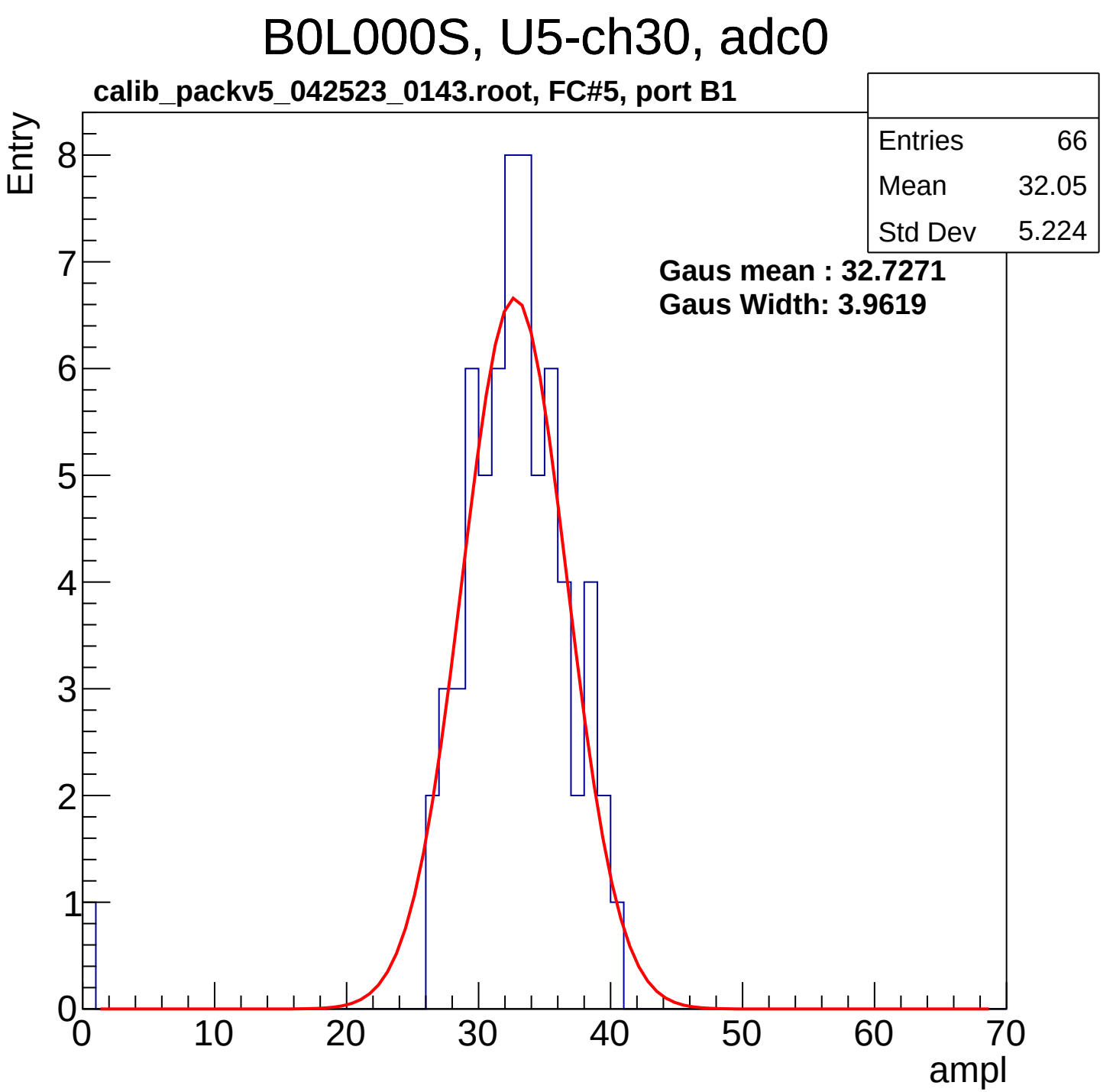
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch30, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 32.05
Std Dev 5.224
Gaus mean : 32.7271
Gaus Width: 3.9619
Entry
ampl
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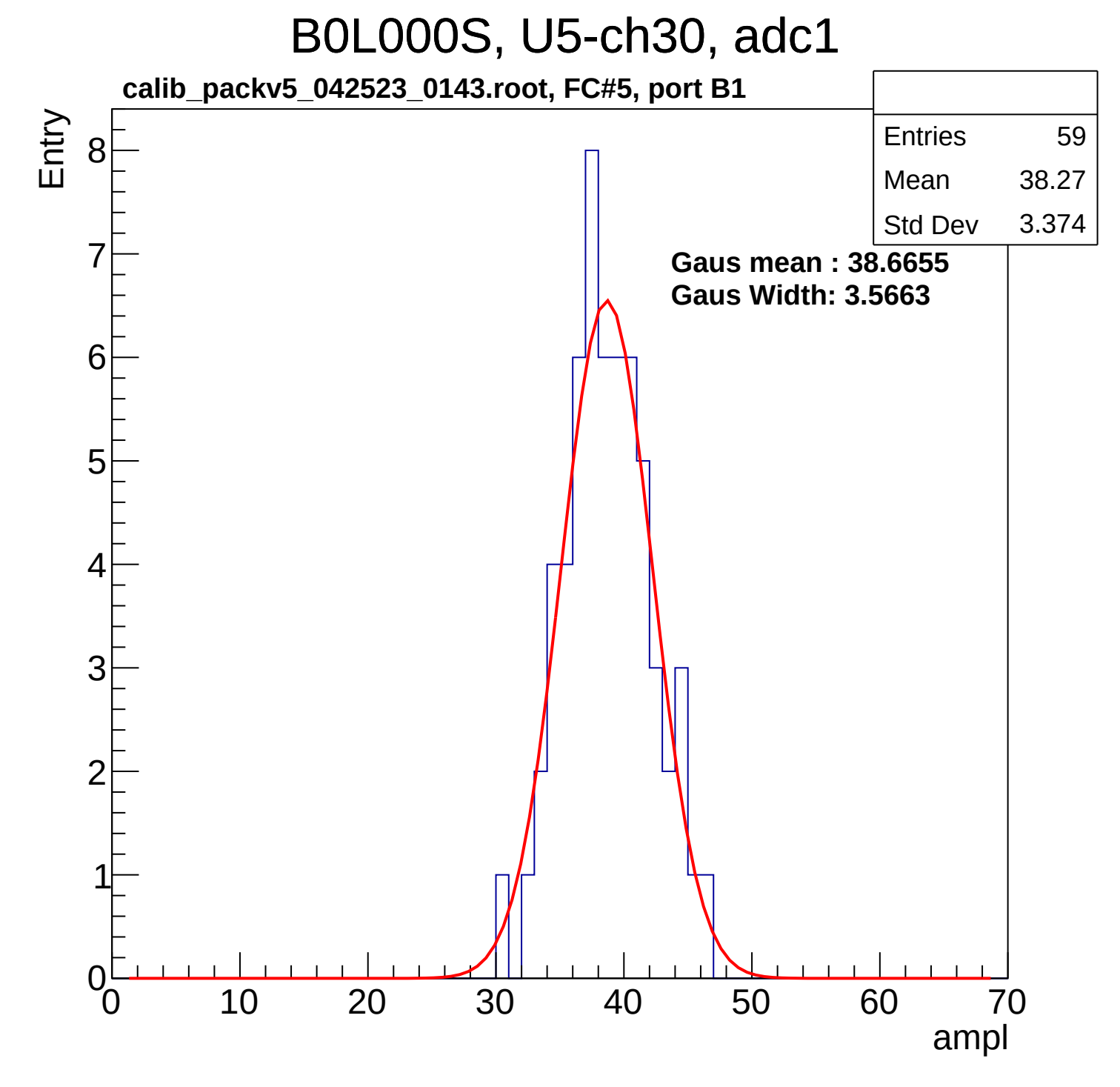
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch30, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 38.27
Std Dev 3.374
Gaus mean : 38.6655
Gaus Width: 3.5663
Entry
ampl
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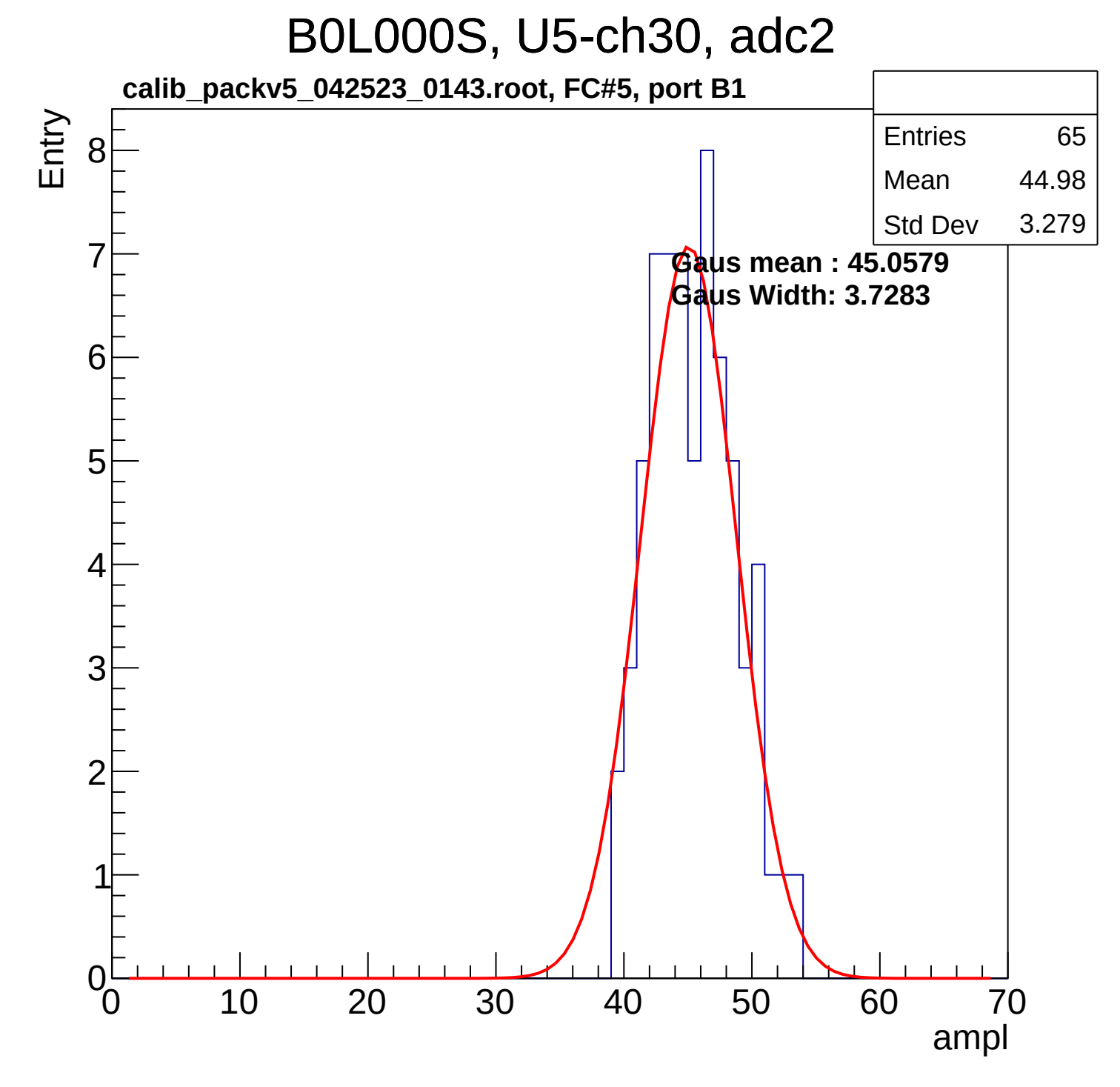
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch30, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 44.98
Std Dev 3.279
Gaus mean : 45.0579
Gaus Width: 3.7283
Entry
ampl
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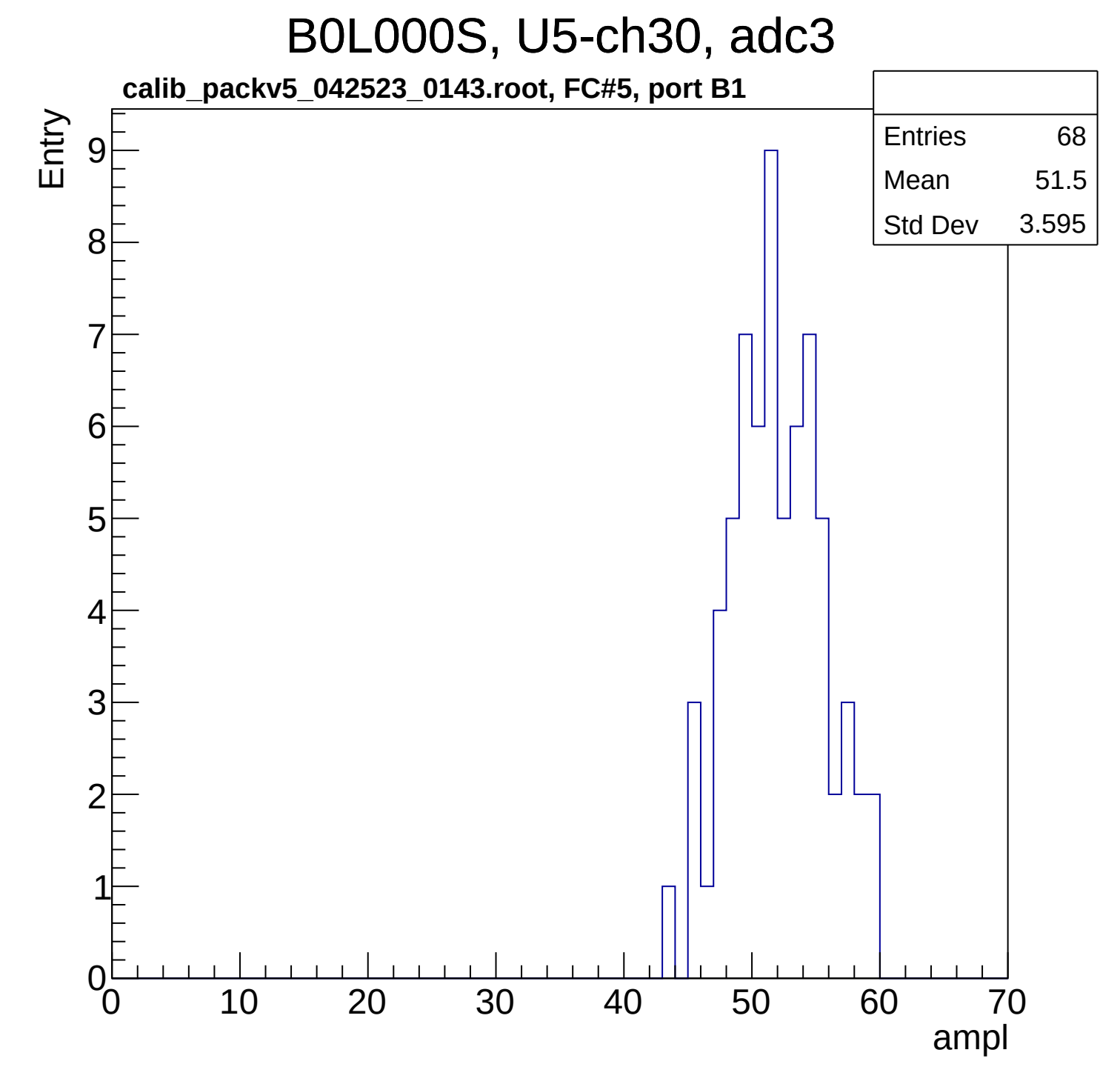
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch30, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.5
Std Dev 3.595
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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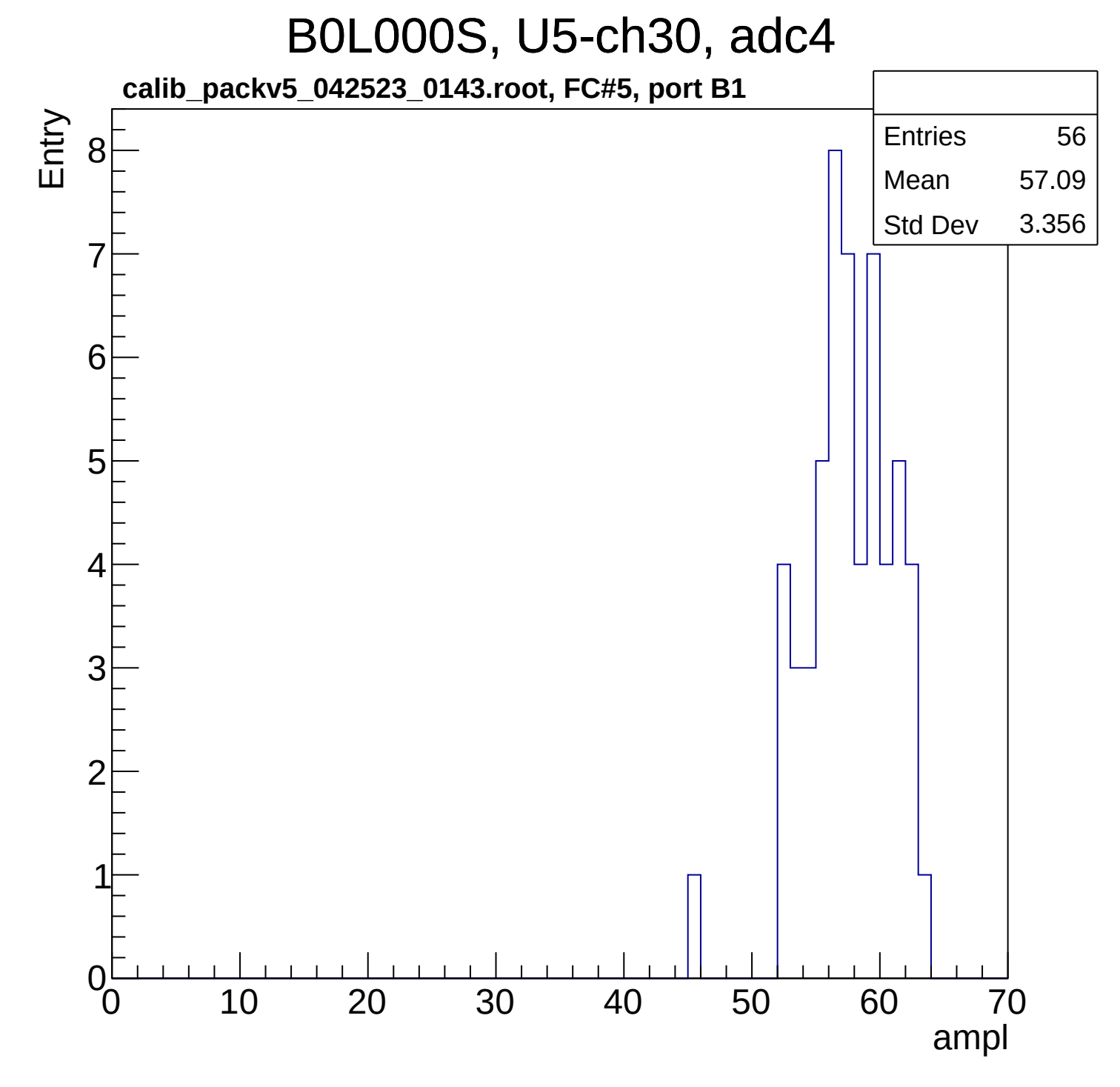
B0L000S, U5-ch30, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.09
Std Dev 3.356
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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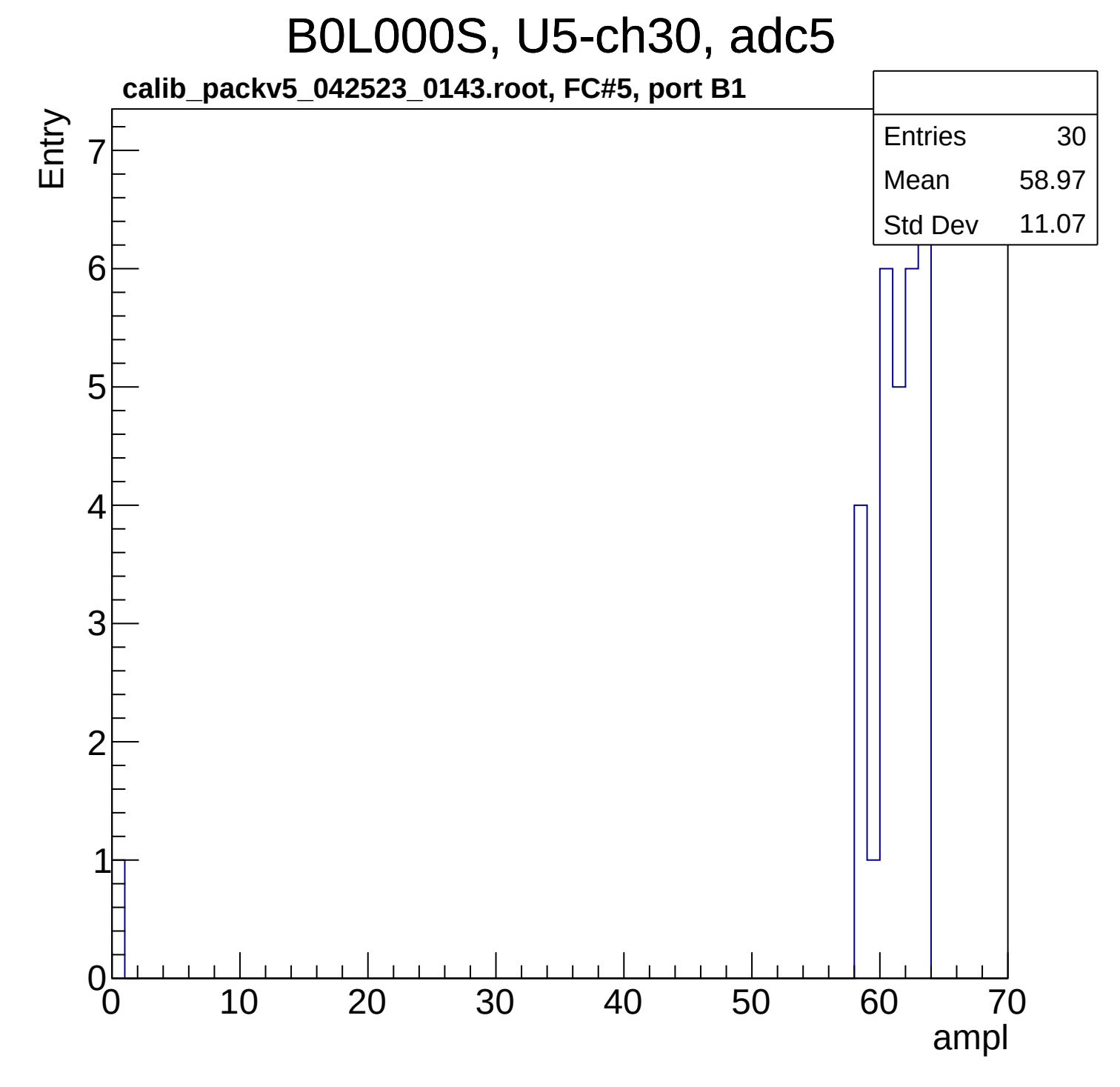
B0L000S, U5-ch30, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.97
Std Dev 11.07
Entry
ampl
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2
3
4
5
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20
30
40
50
60
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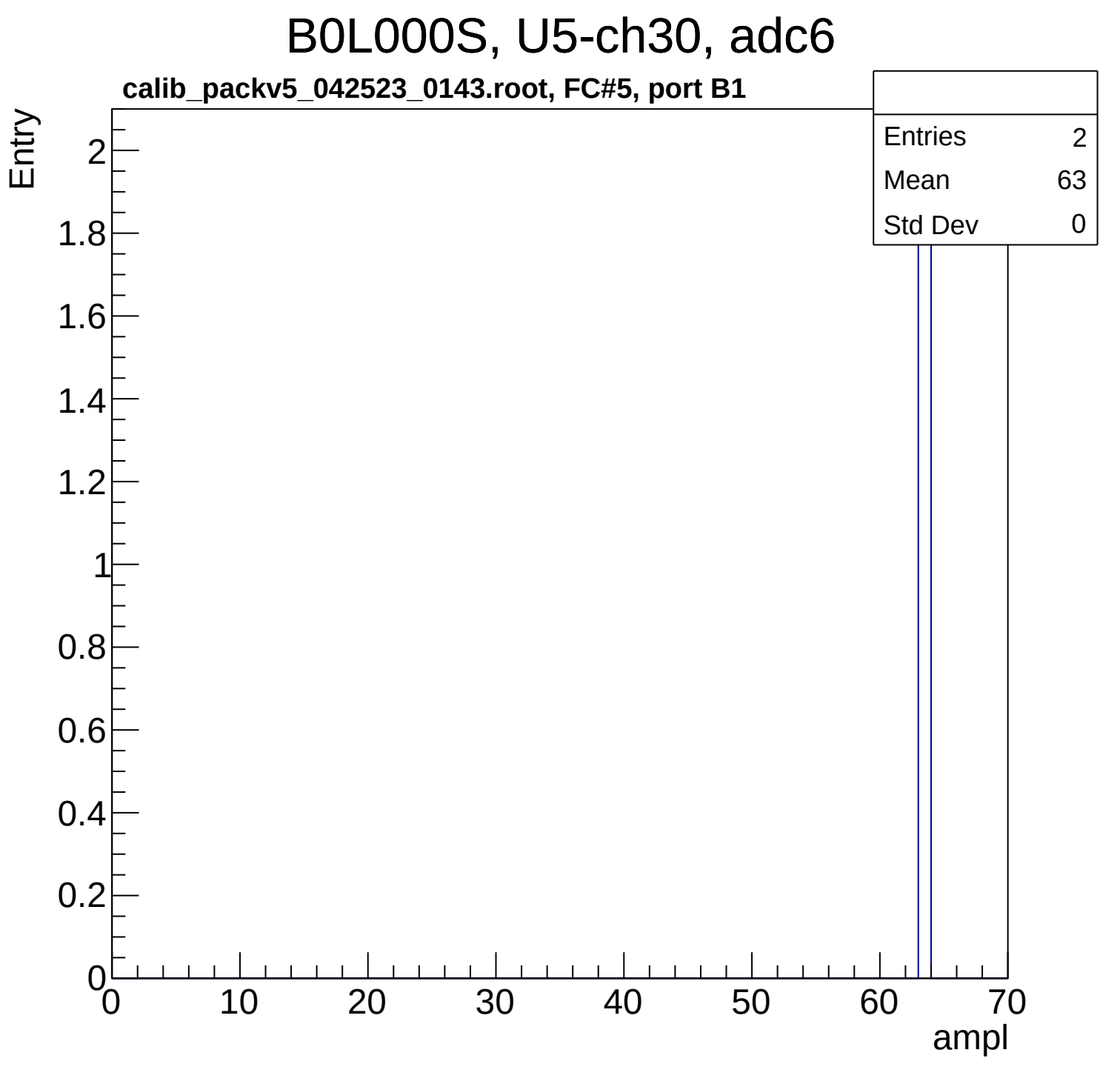
B0L000S, U5-ch30, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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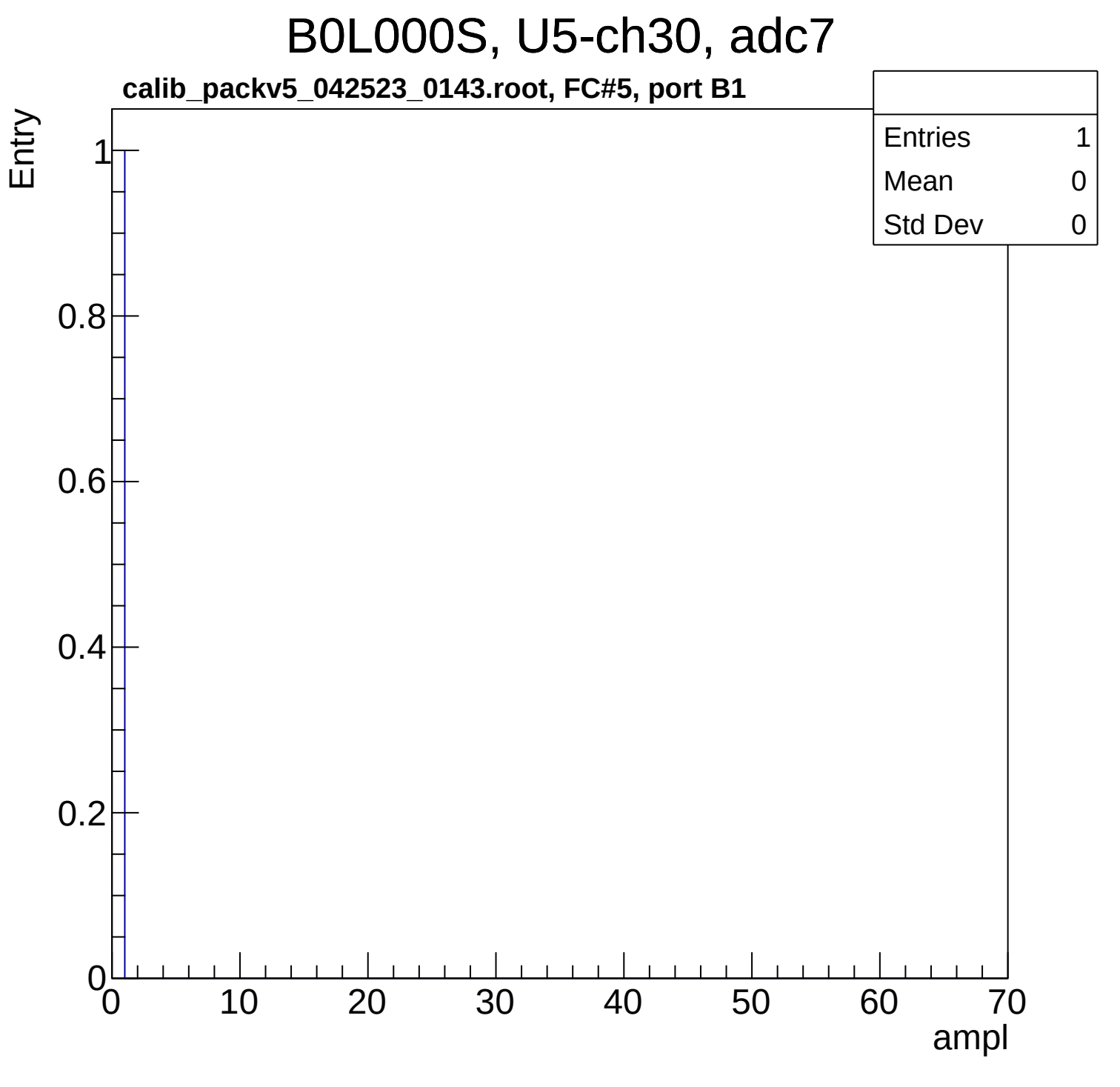
B0L000S, U5-ch30, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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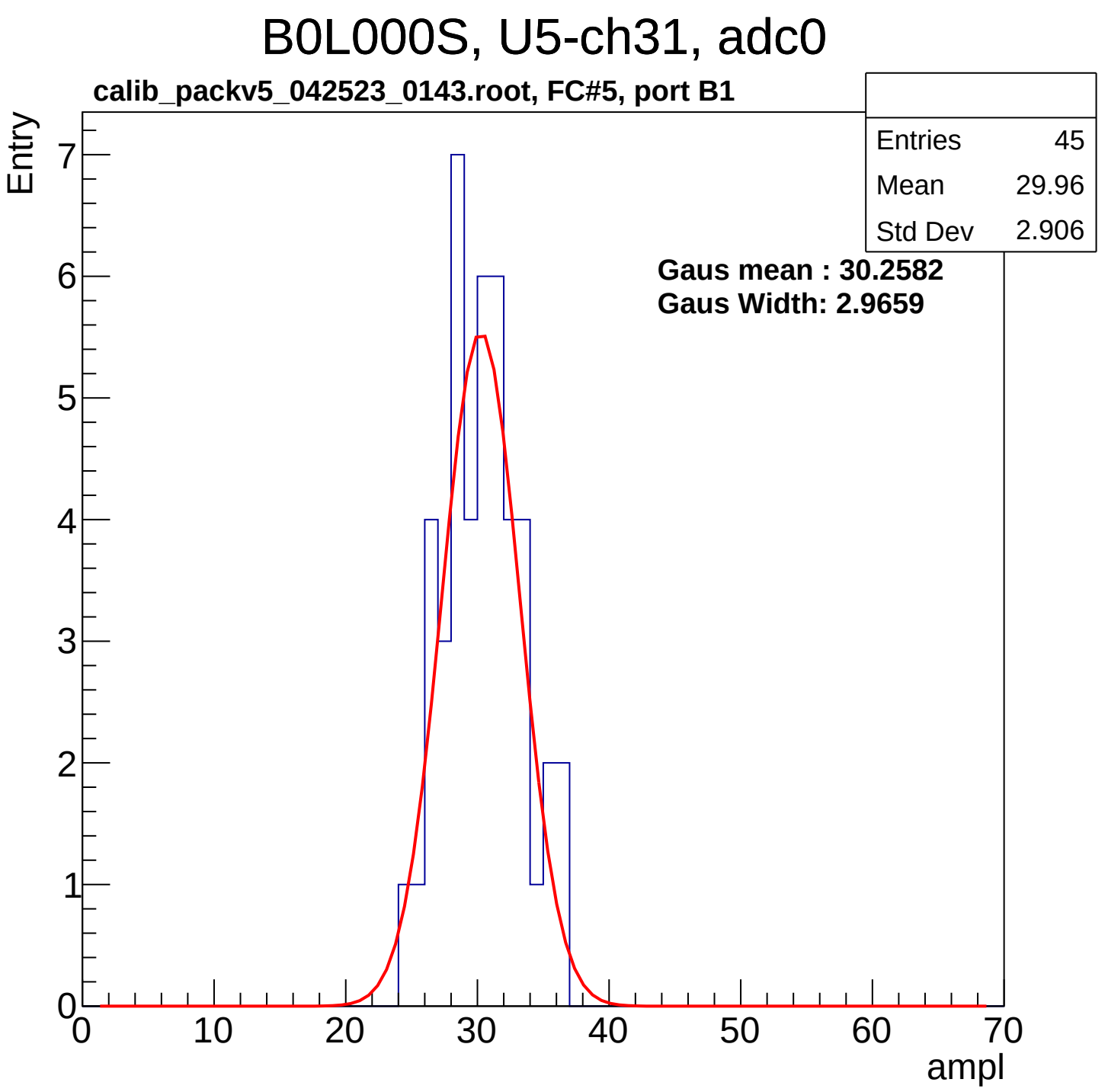
50
60
70
ampl


B0L000S, U5-ch31, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 29.96
Std Dev 2.906
Gaus mean : 30.2582
Gaus Width: 2.9659
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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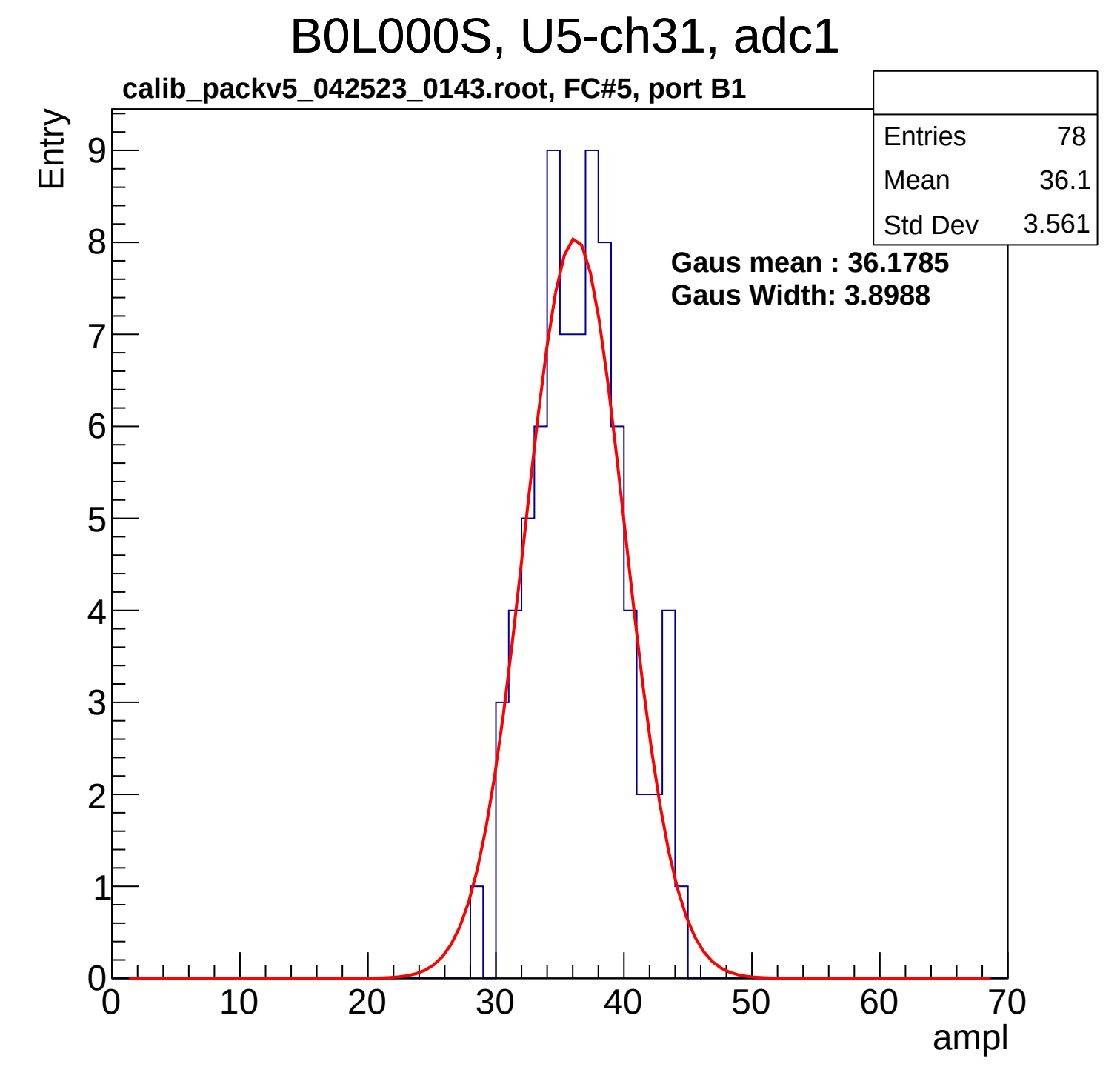
B0L000S, U5-ch31, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.1
Std Dev 3.561
Gaus mean : 36.1785
Gaus Width: 3.8988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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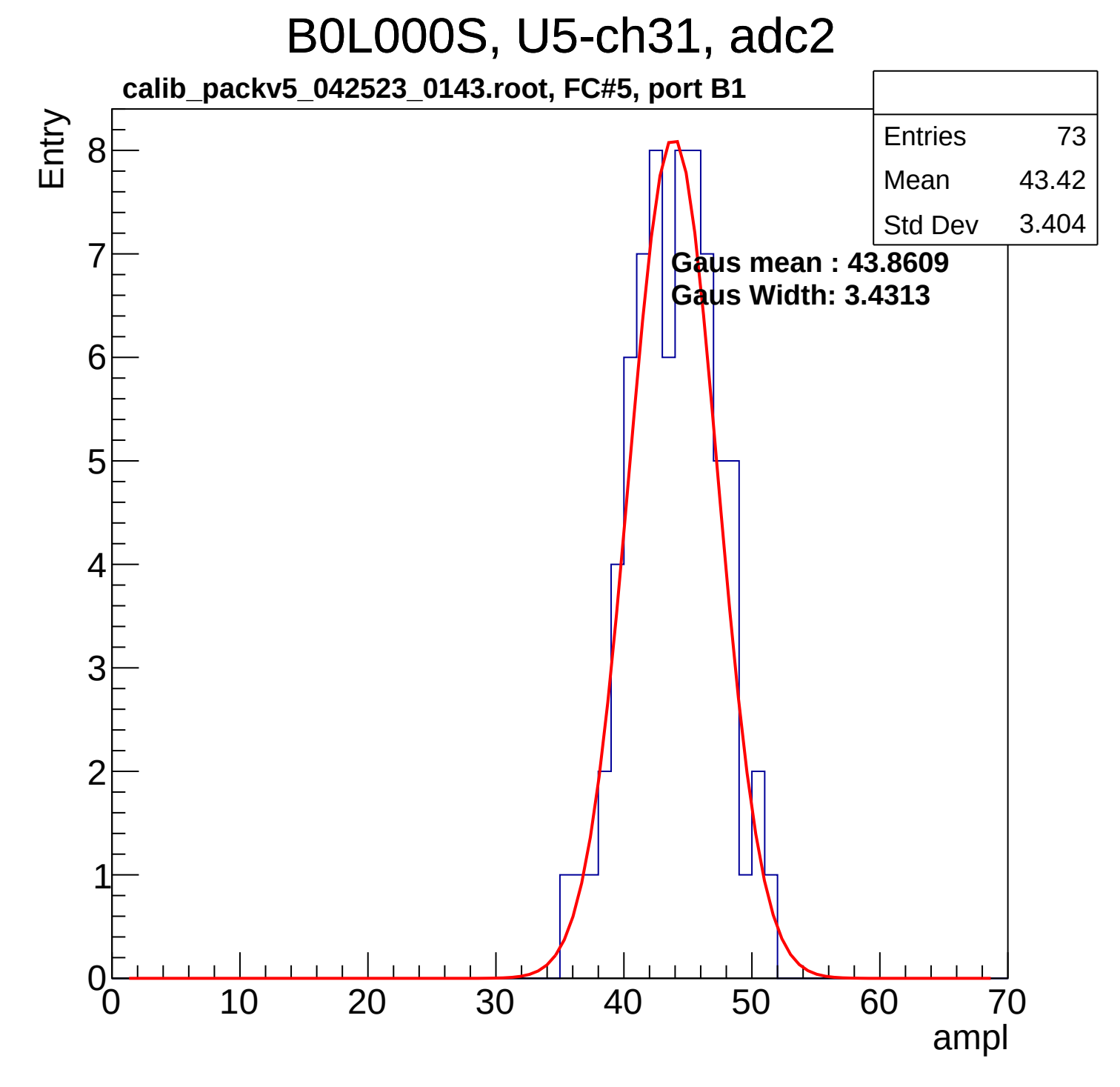
B0L000S, U5-ch31, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.42
Std Dev 3.404
Gaus mean : 43.8609
Gaus Width: 3.4313
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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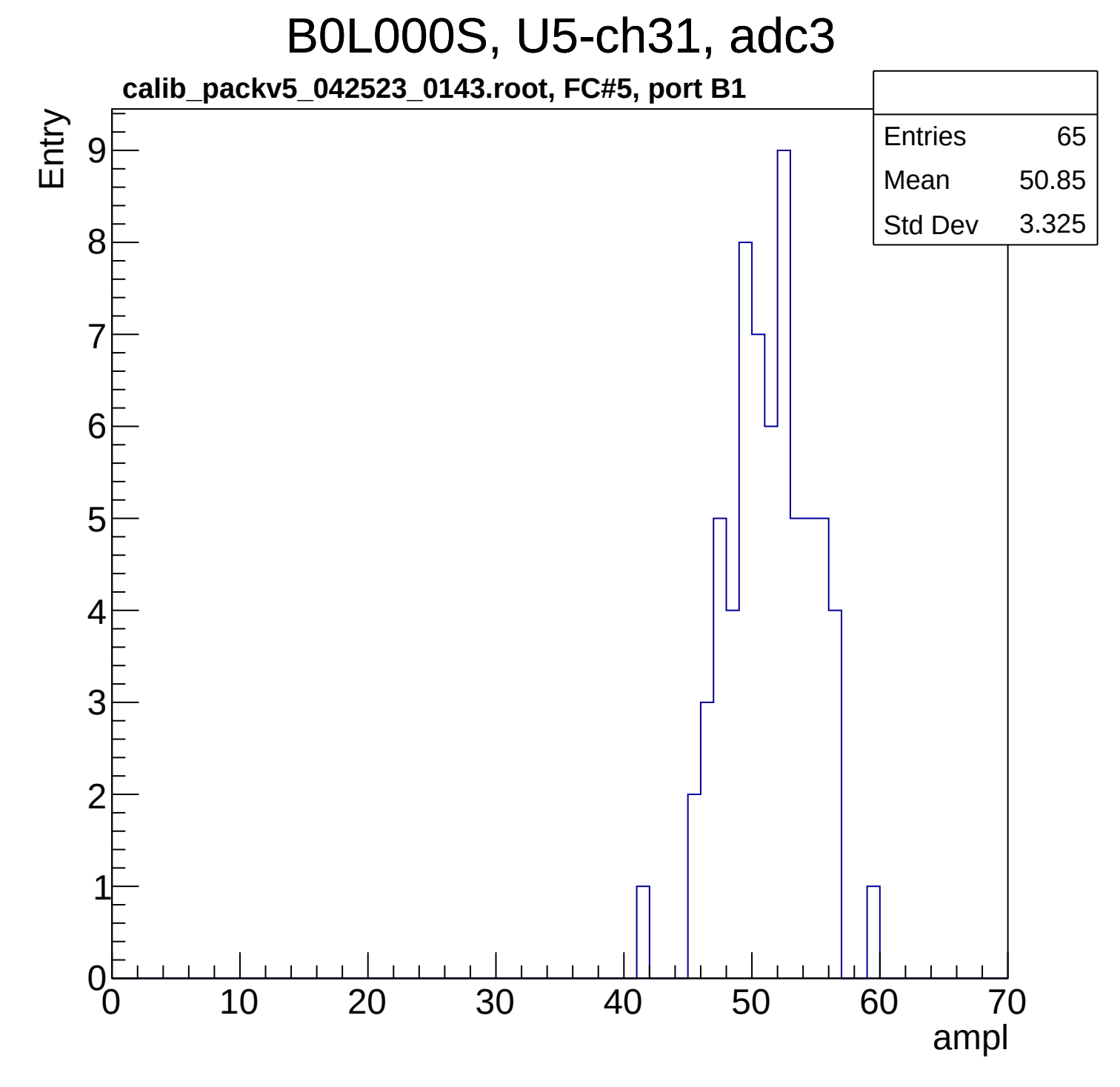
B0L000S, U5-ch31, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.85
Std Dev 3.325
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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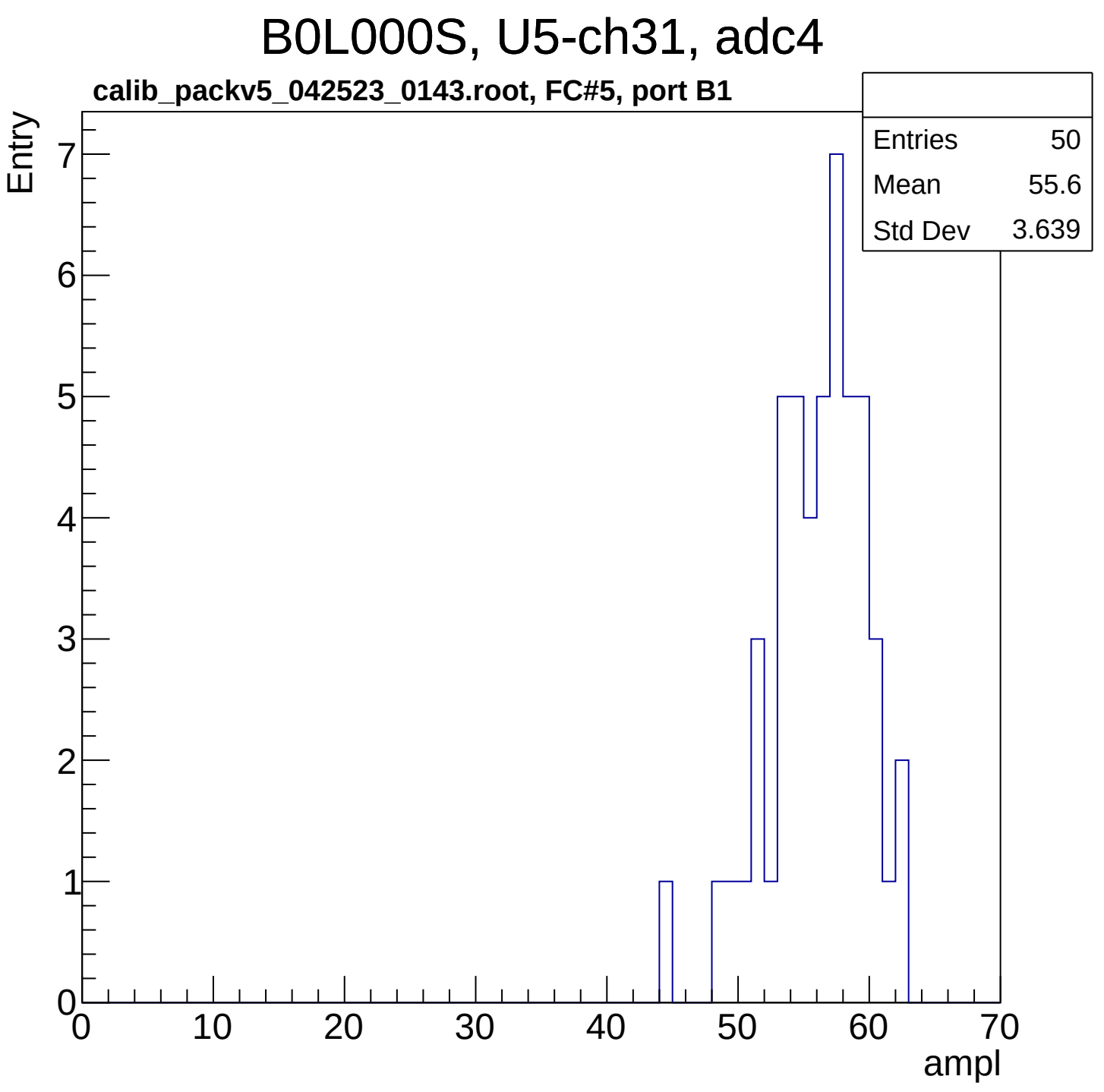
B0L000S, U5-ch31, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.6
Std Dev 3.639
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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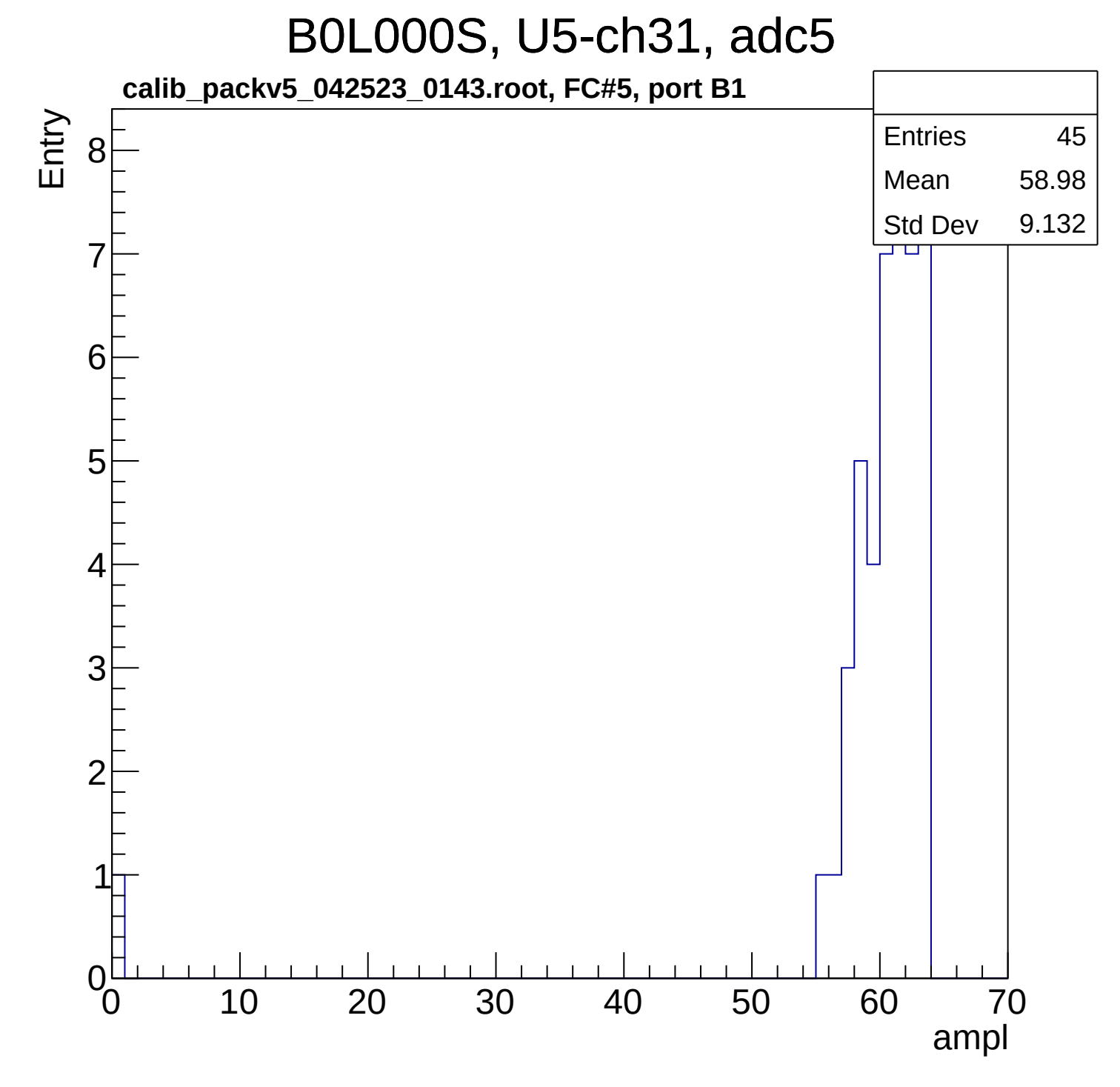
B0L000S, U5-ch31, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.98
Std Dev 9.132
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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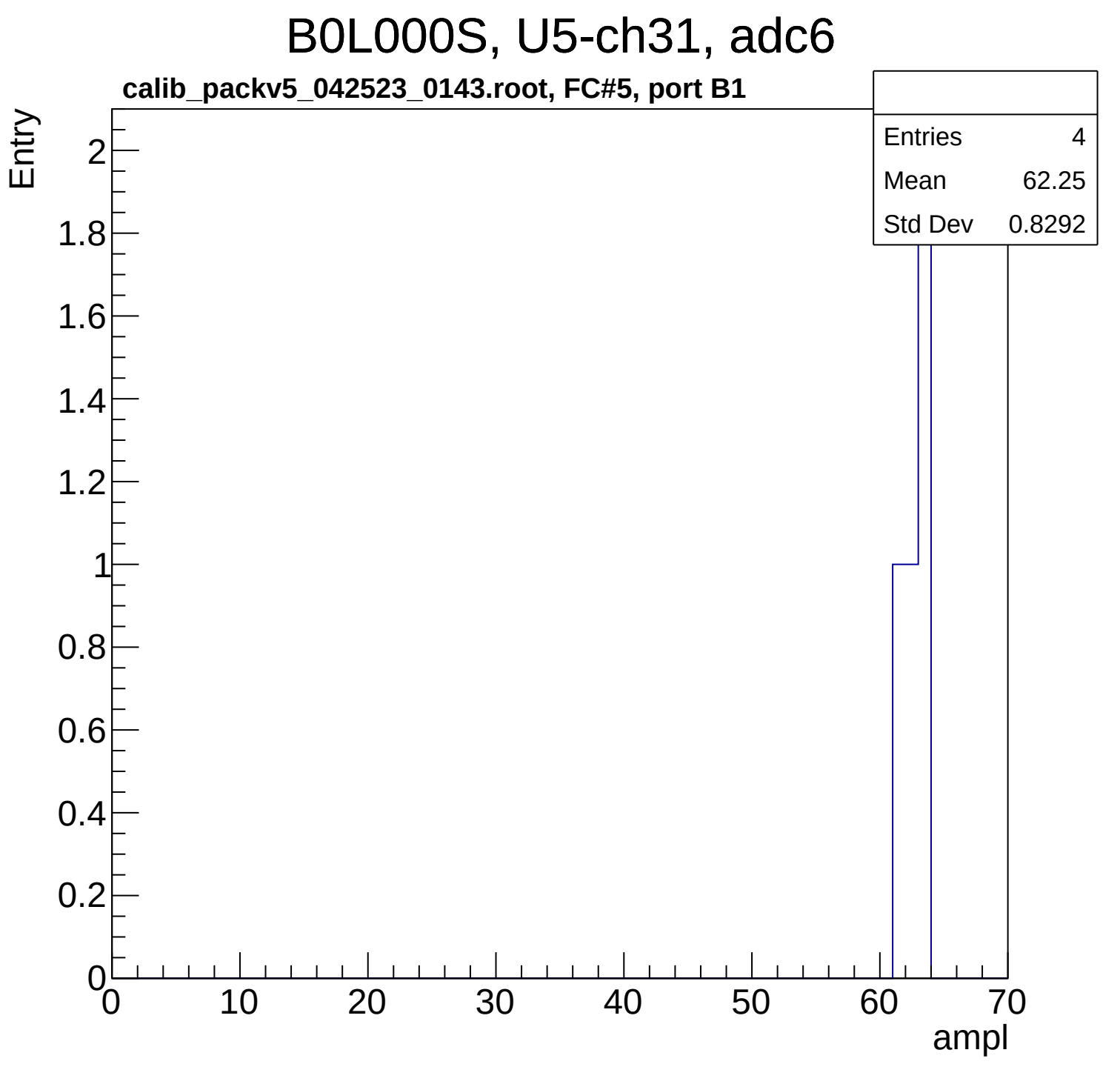
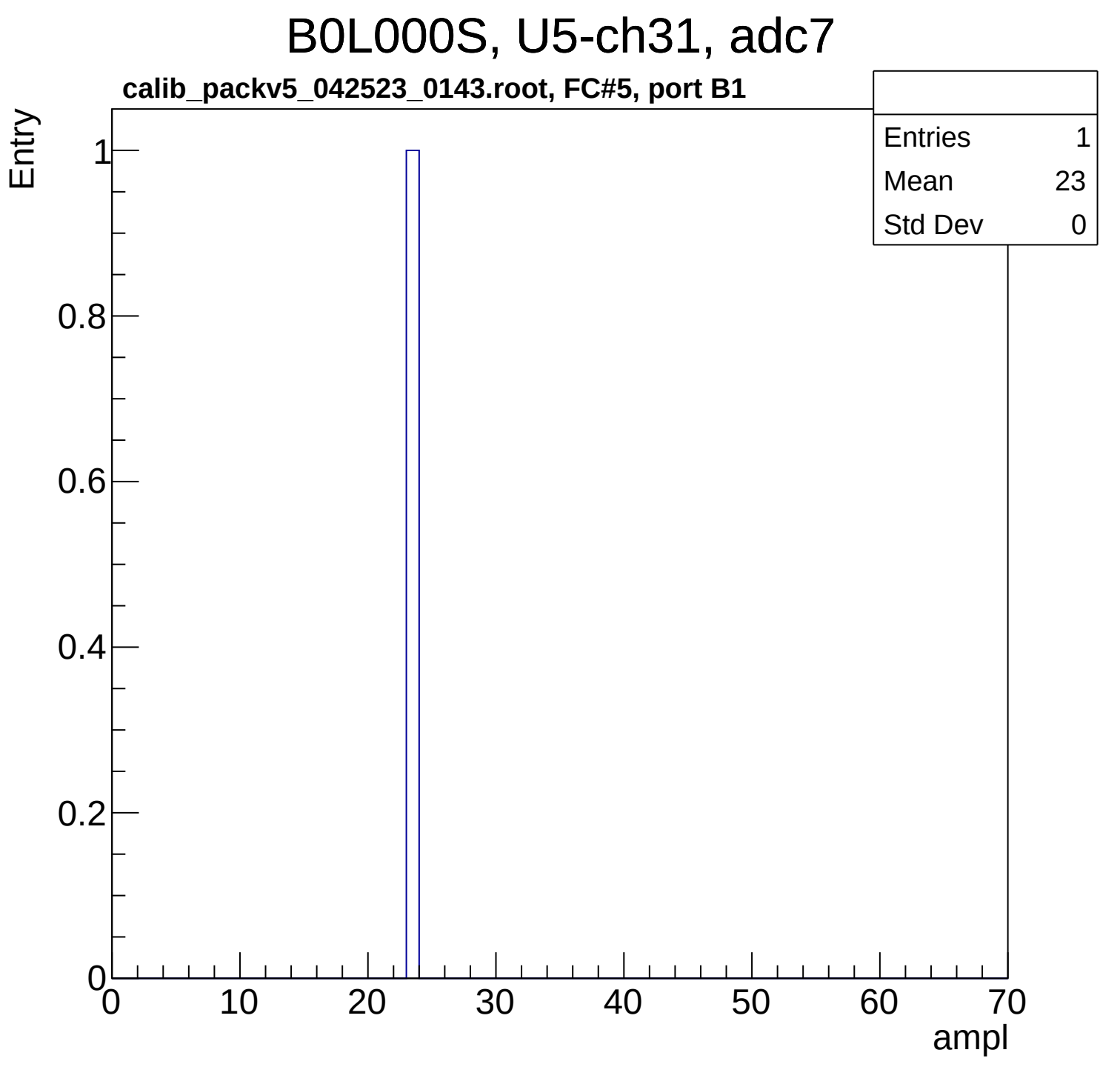

B0L000S, U5-ch31, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch31, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 23
Std Dev 0
Entry
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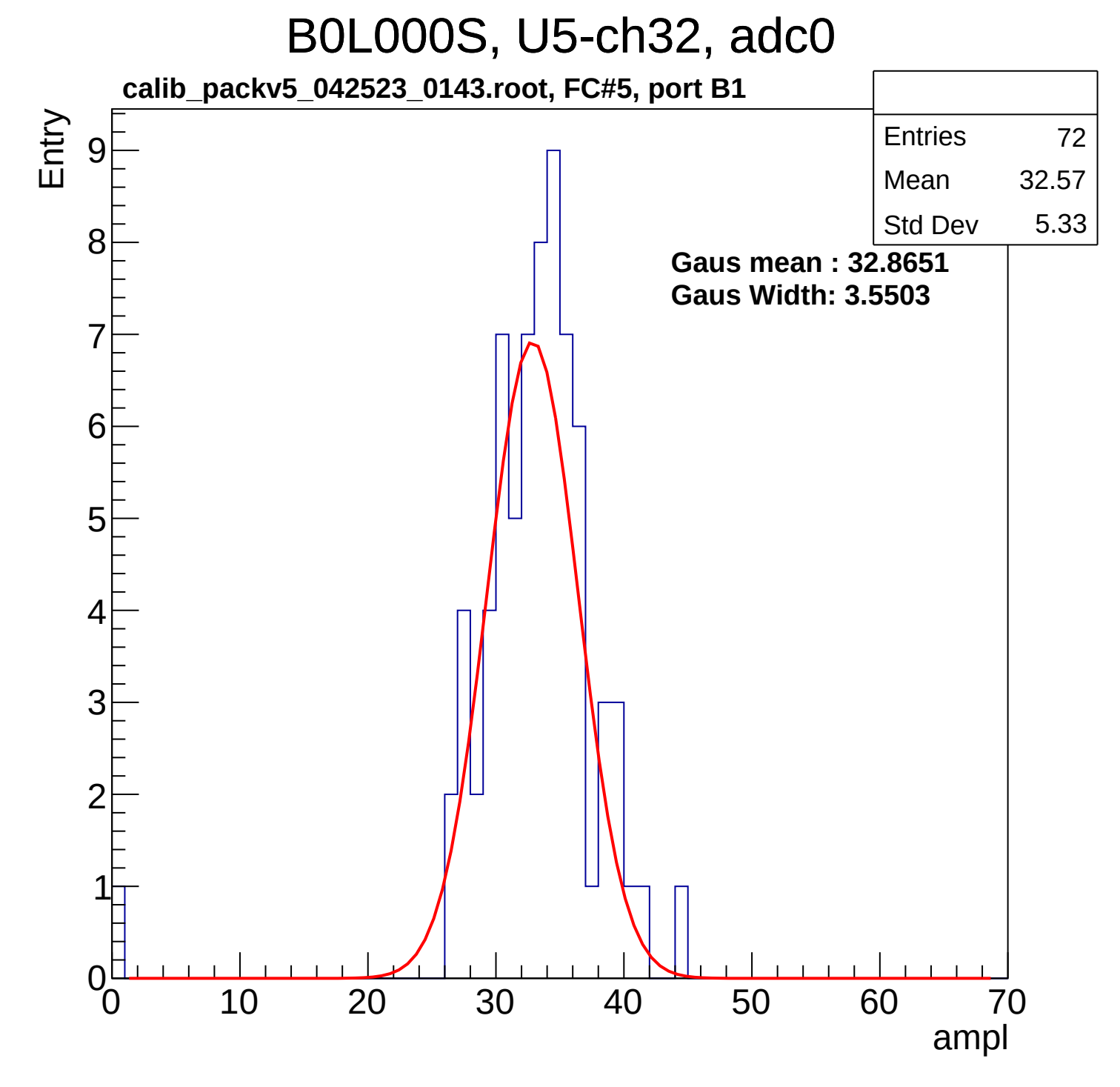
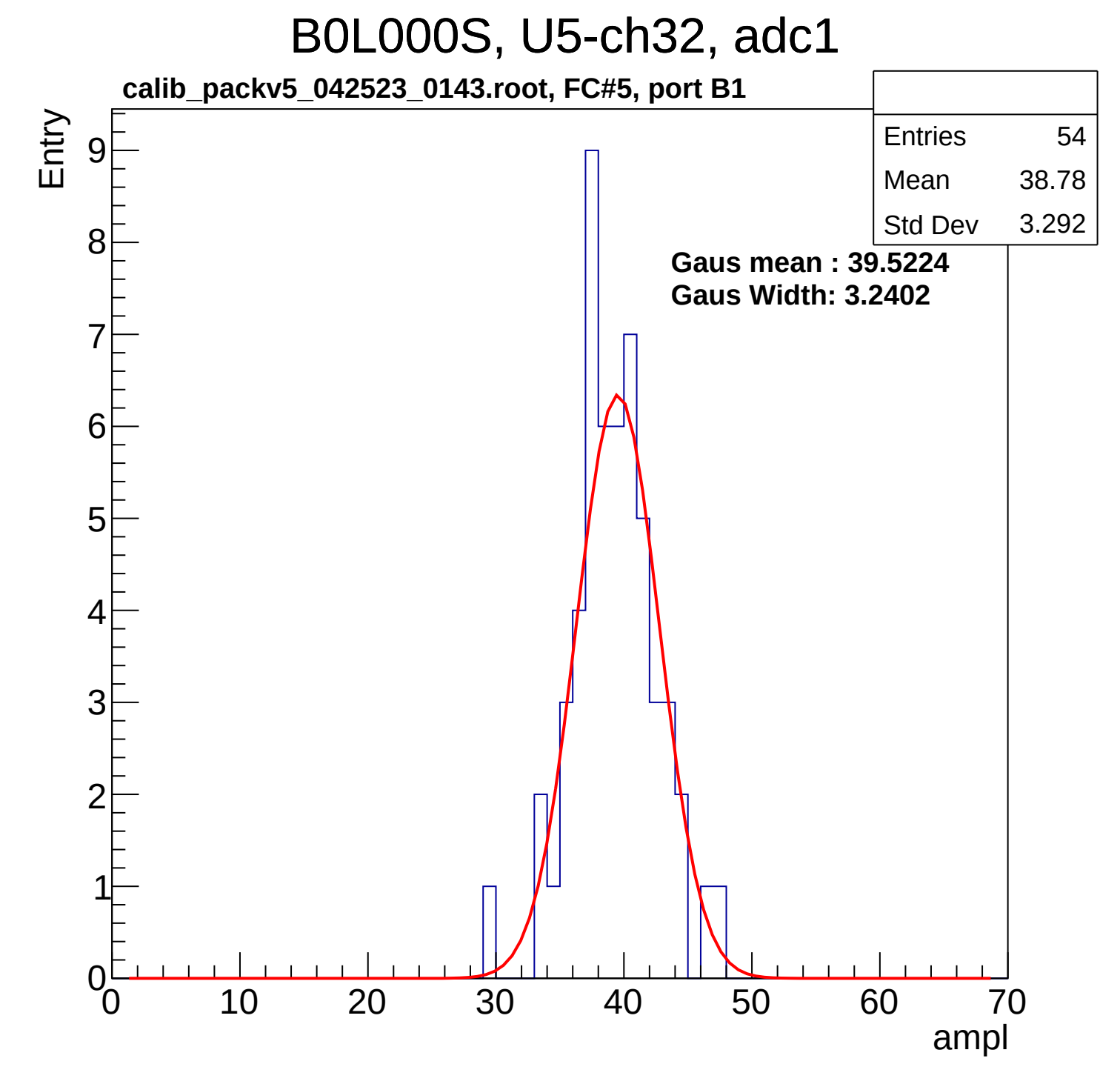
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch32, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 32.57
Std Dev 5.33
Gaus mean : 32.8651
Gaus Width: 3.5503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch32, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 38.78
Std Dev 3.292
Gaus mean : 39.5224
Gaus Width: 3.2402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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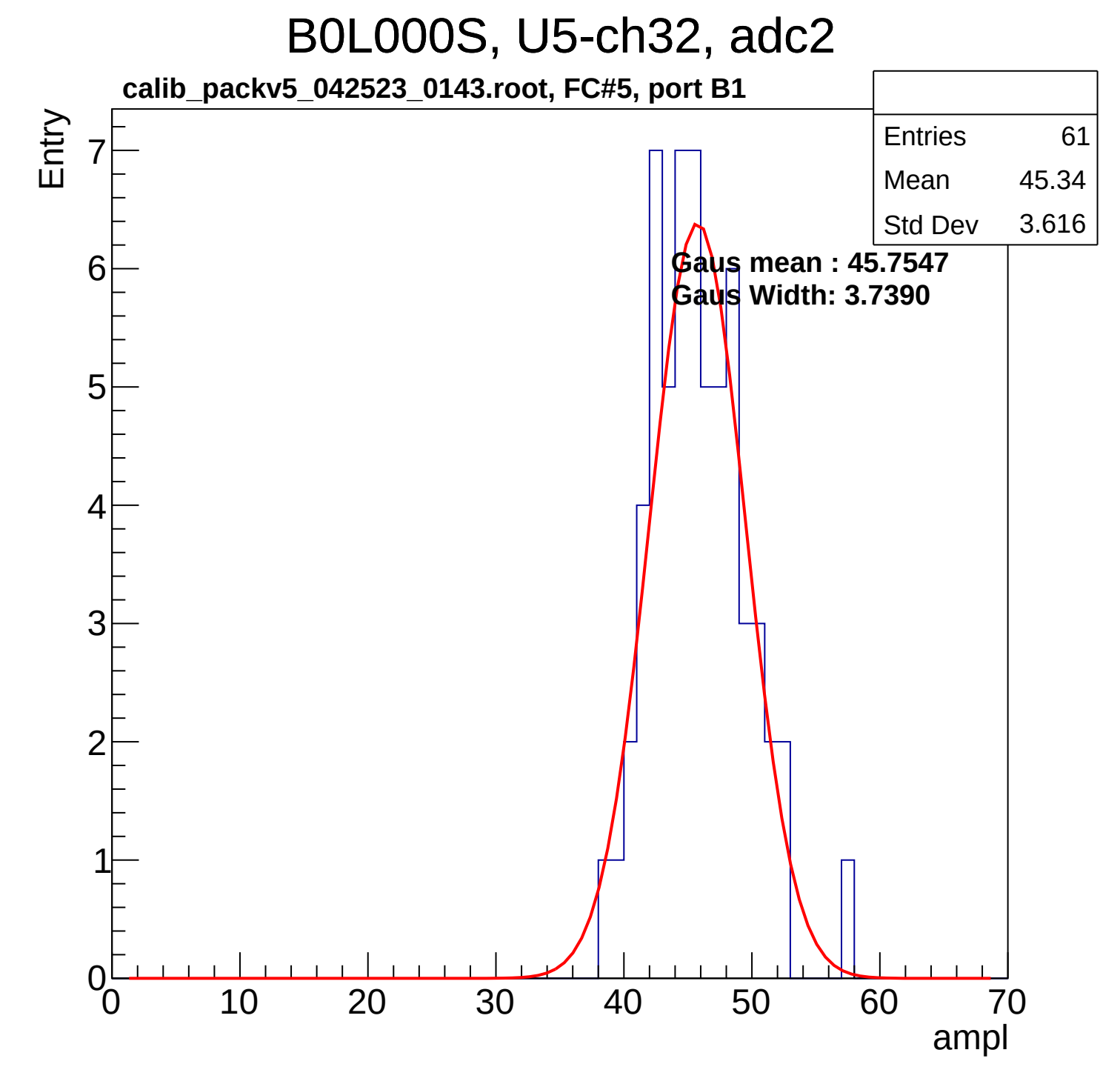
B0L000S, U5-ch32, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 45.34
Std Dev 3.616
Gaus mean : 45.7547
Gaus Width: 3.7390
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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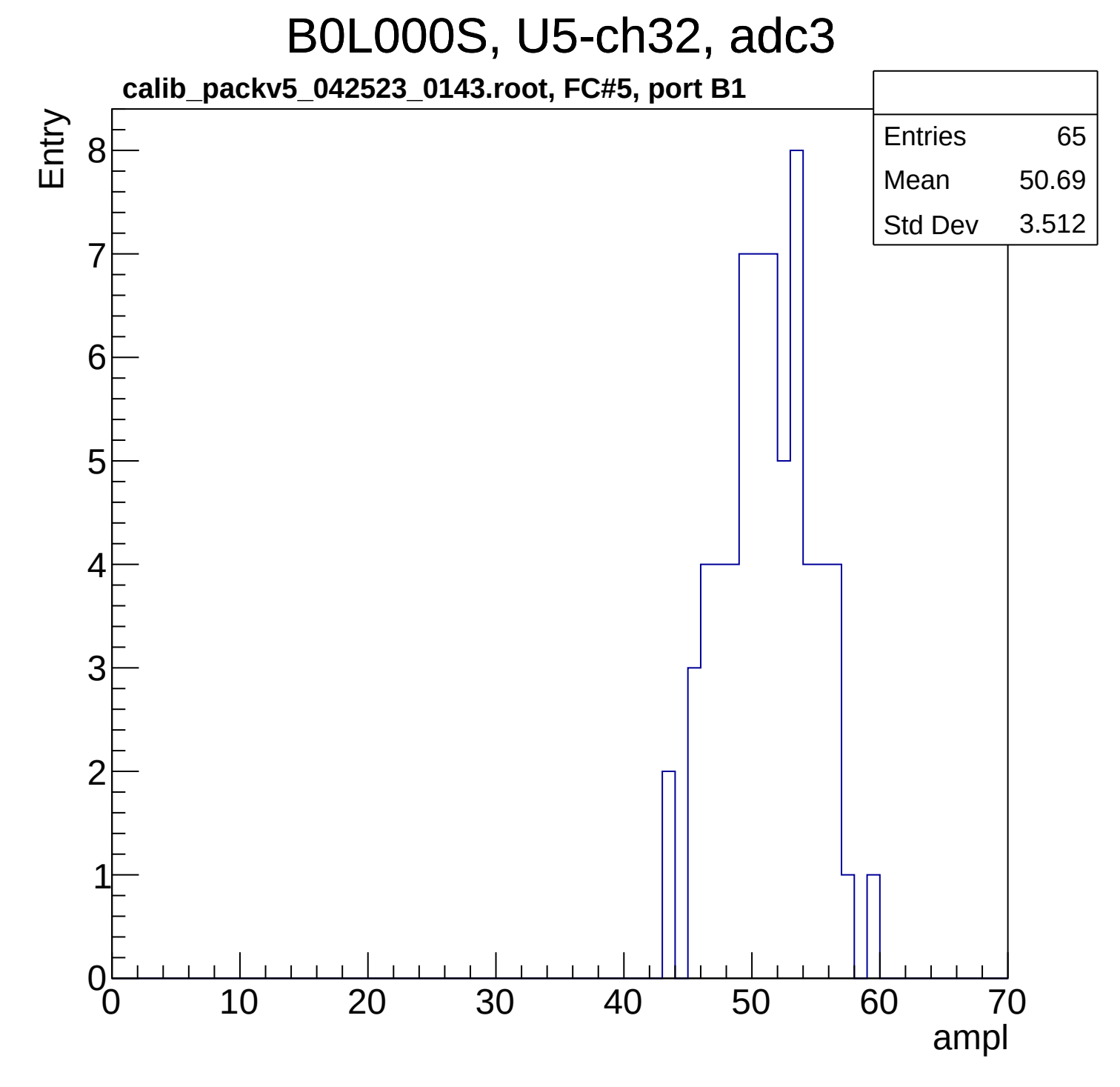
B0L000S, U5-ch32, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.69
Std Dev 3.512
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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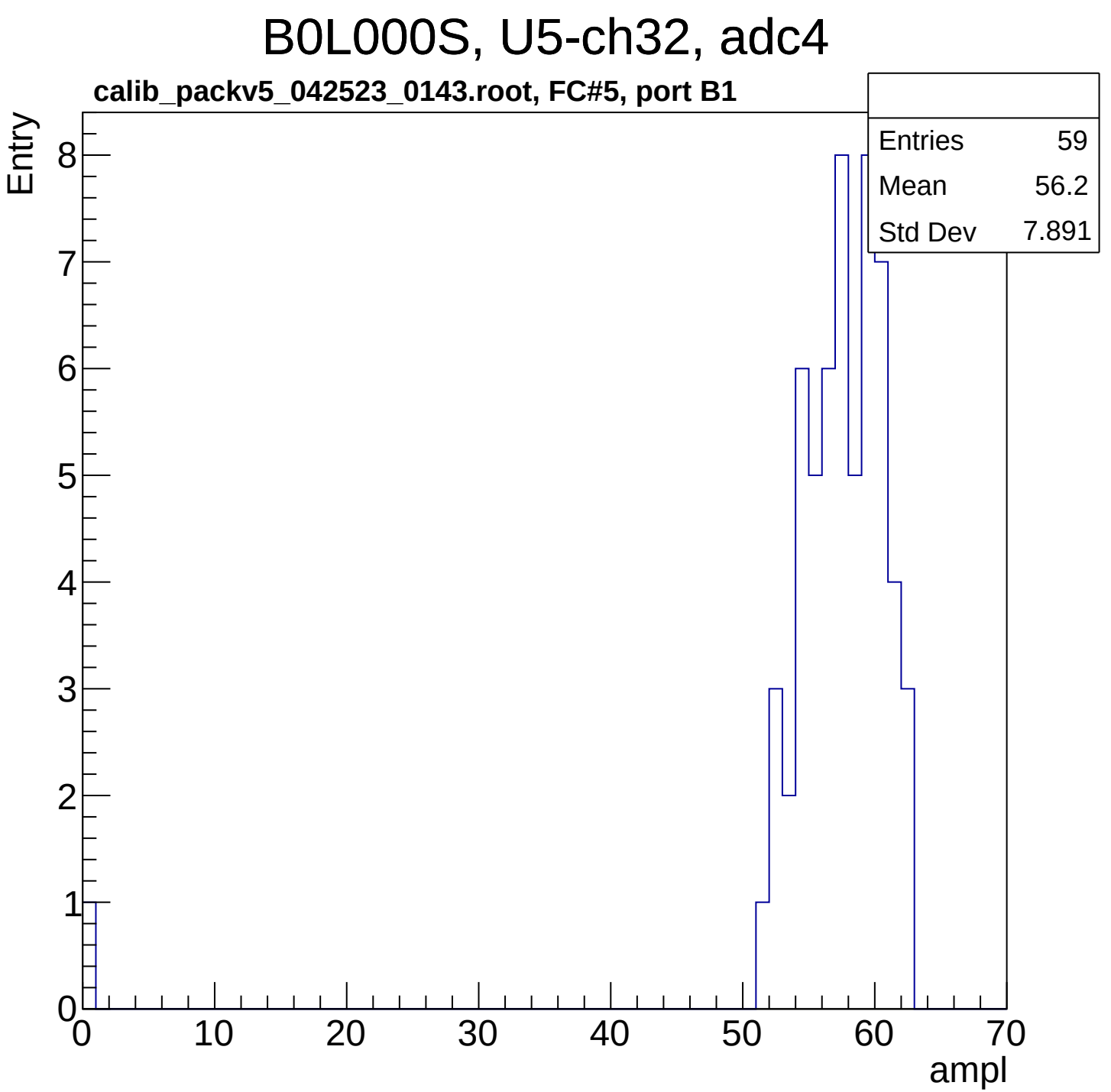
B0L000S, U5-ch32, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 56.2
Std Dev 7.891
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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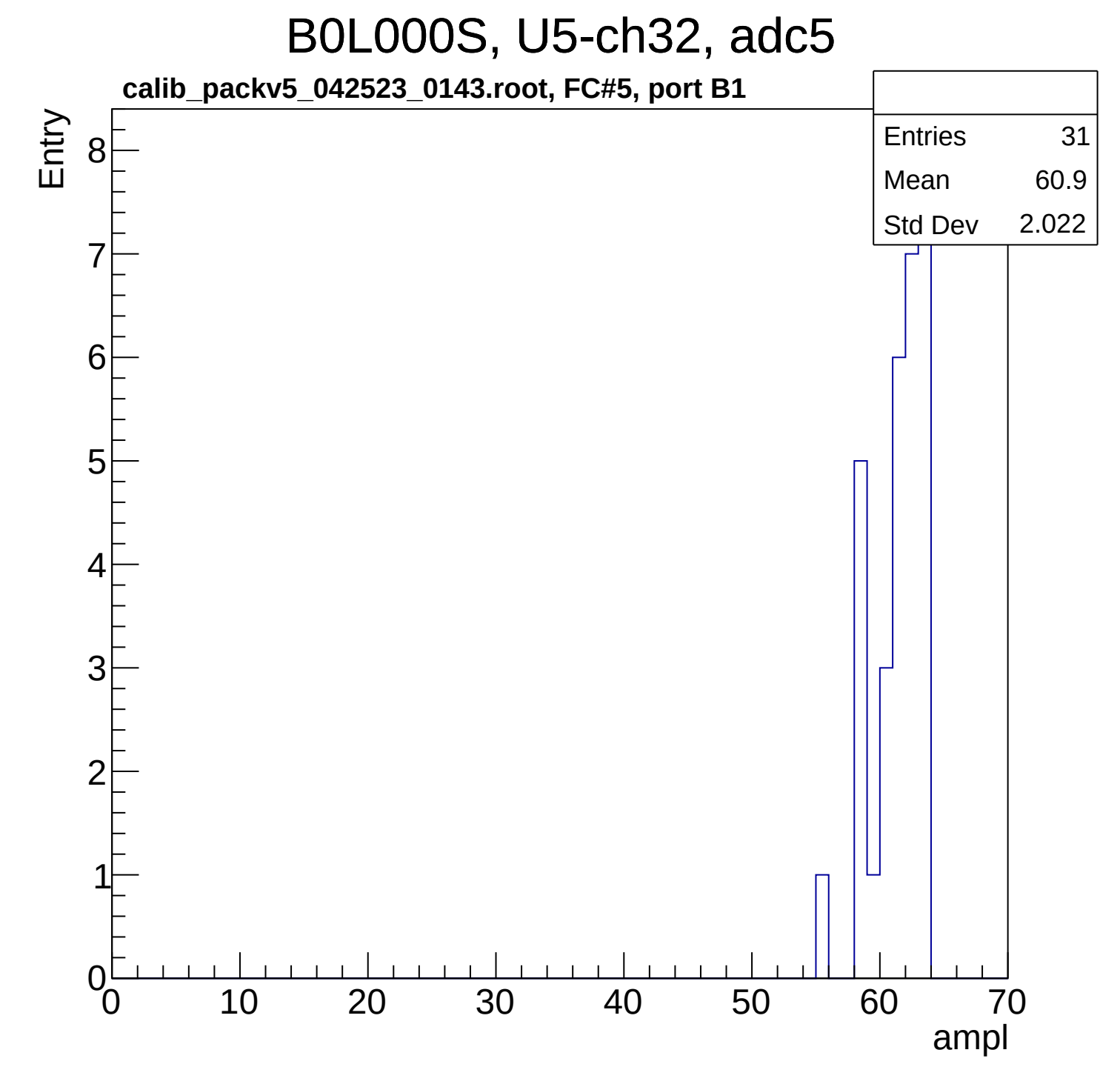
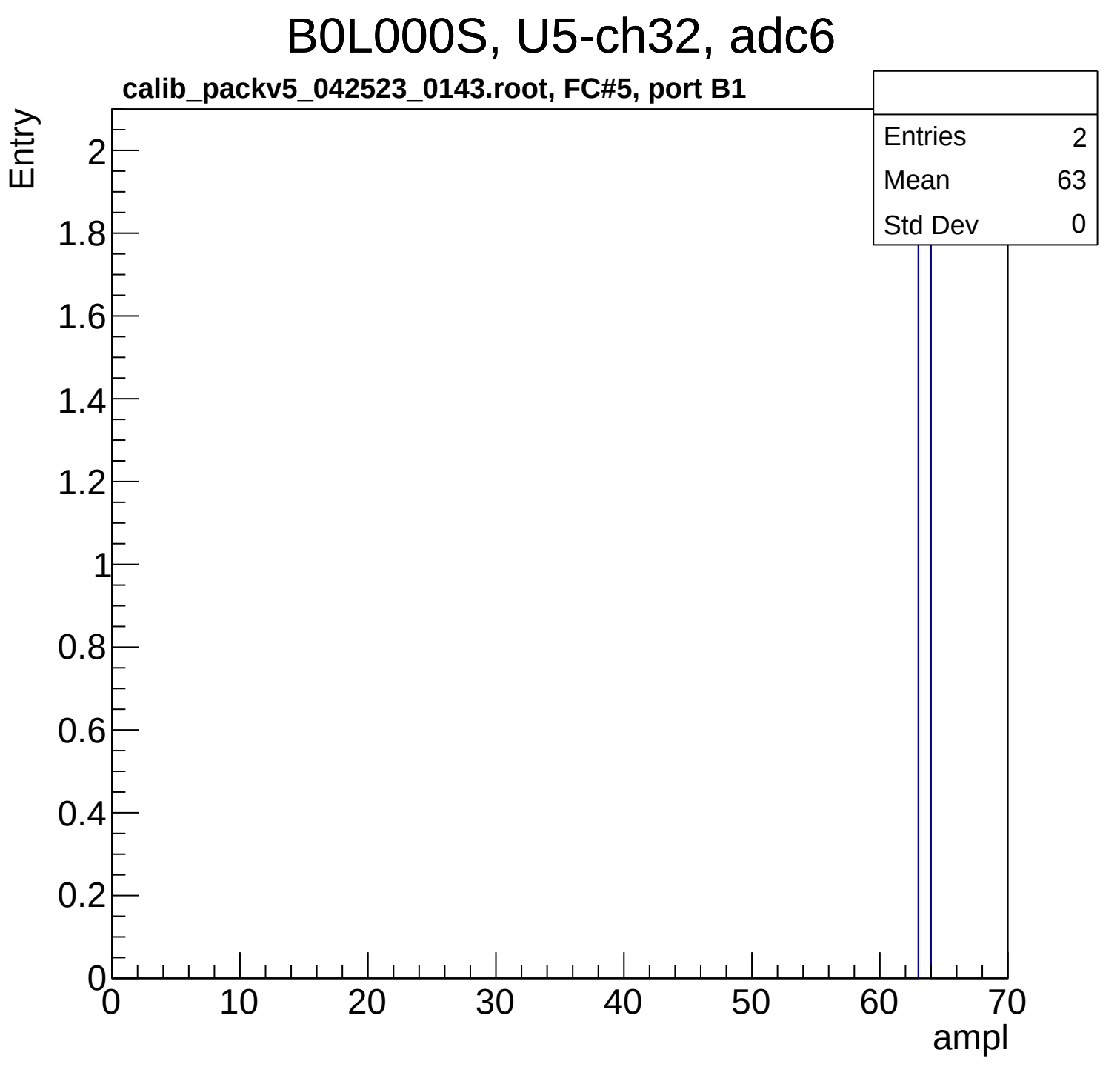

B0L000S, U5-ch32, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 60.9
Std Dev 2.022
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch32, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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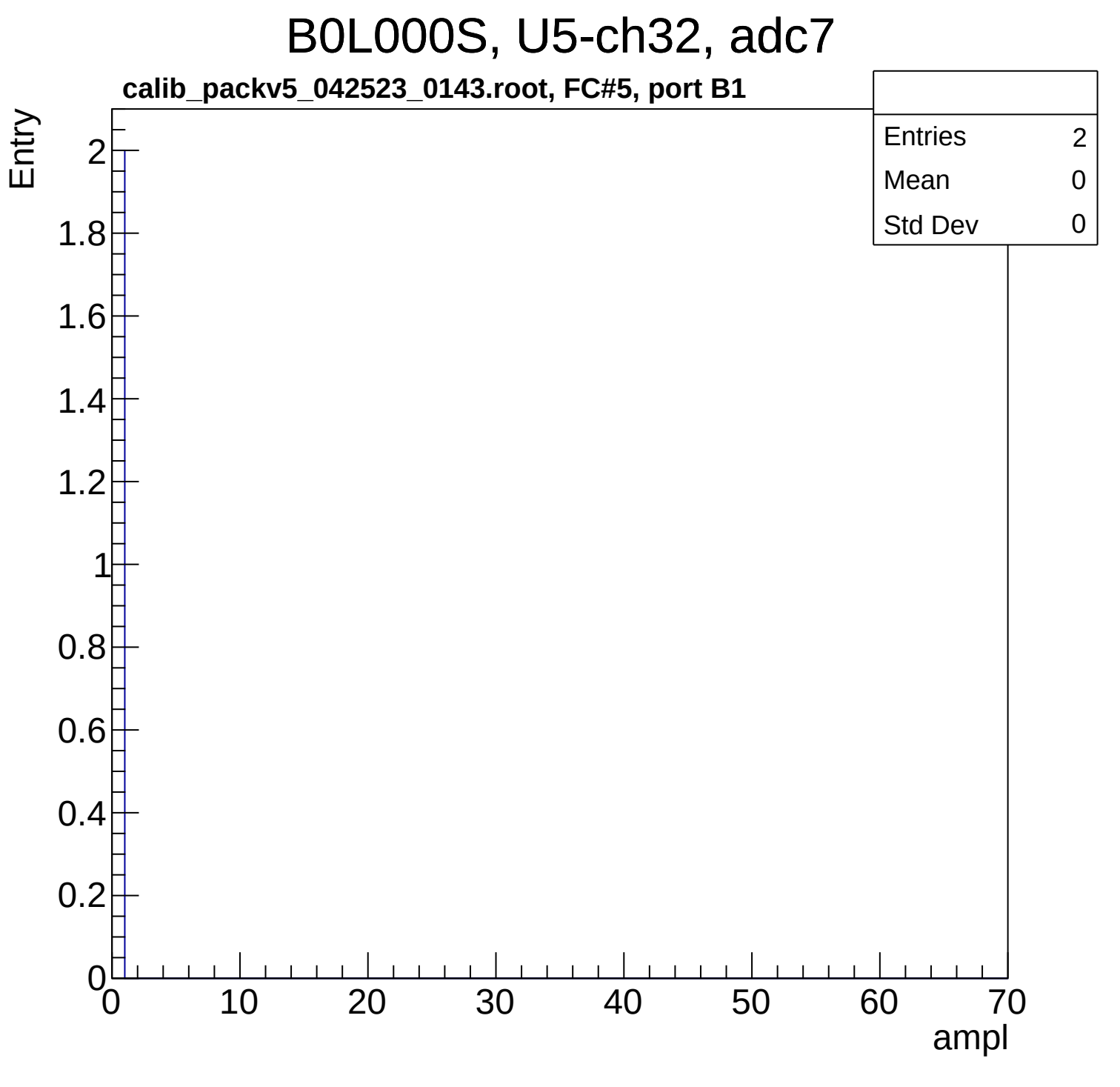
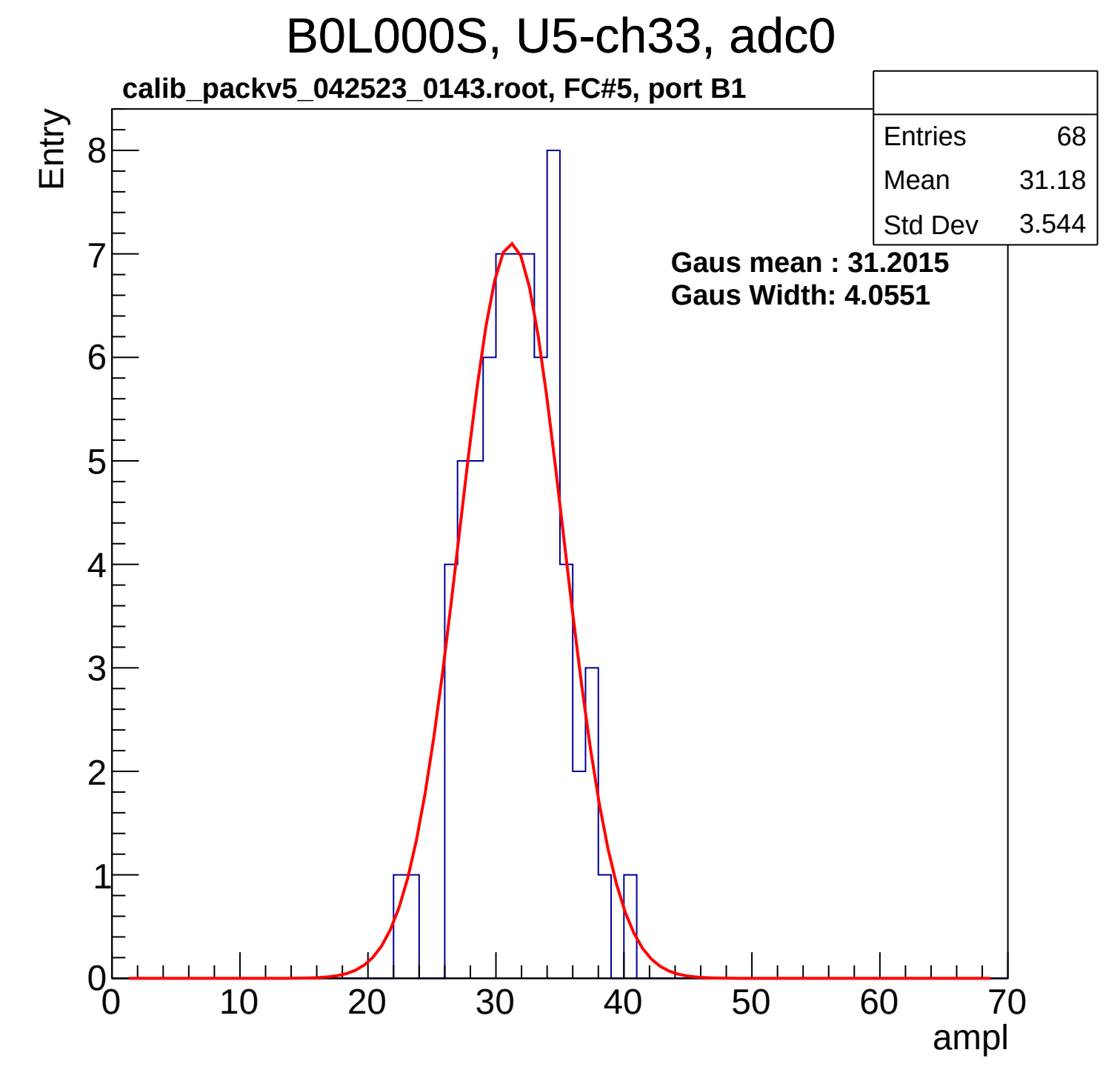

B0L000S, U5-ch32, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch33, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 31.18
Std Dev 3.544
Gaus mean : 31.2015
Gaus Width: 4.0551
Entry
ampl
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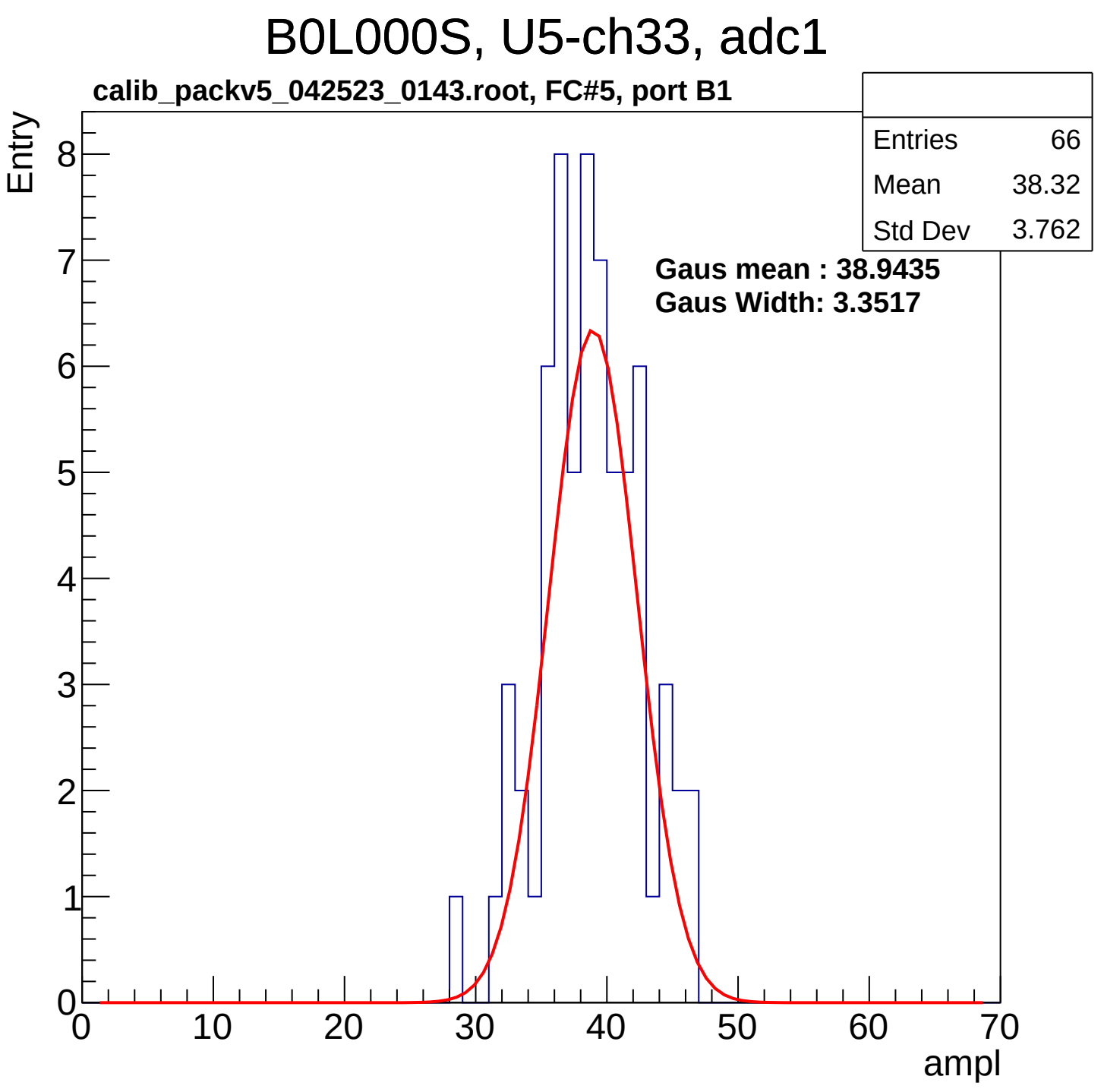
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch33, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 38.32
Std Dev 3.762
Gaus mean : 38.9435
Gaus Width: 3.3517
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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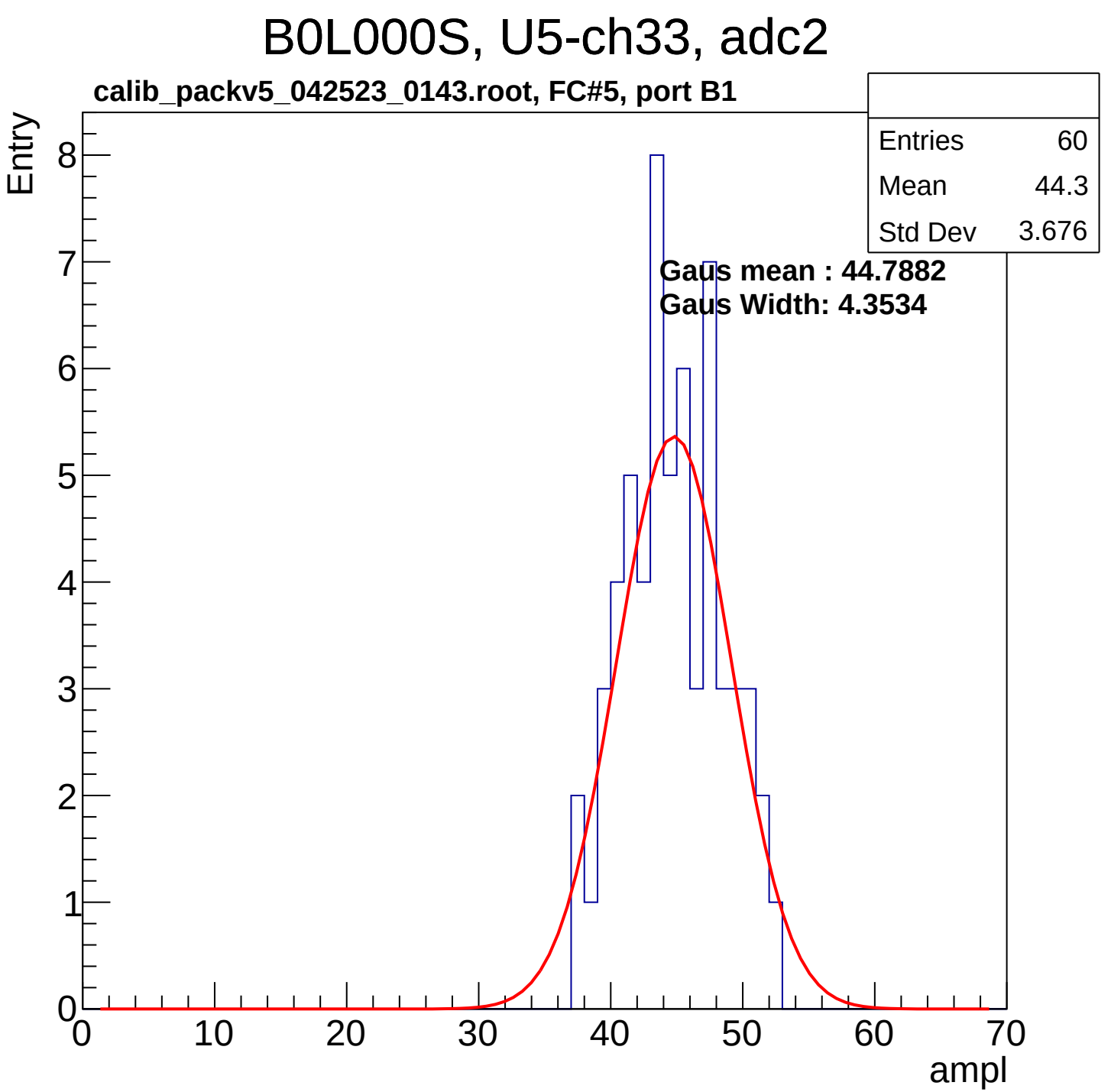
B0L000S, U5-ch33, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 44.3
Std Dev 3.676
Gaus mean : 44.7882
Gaus Width: 4.3534
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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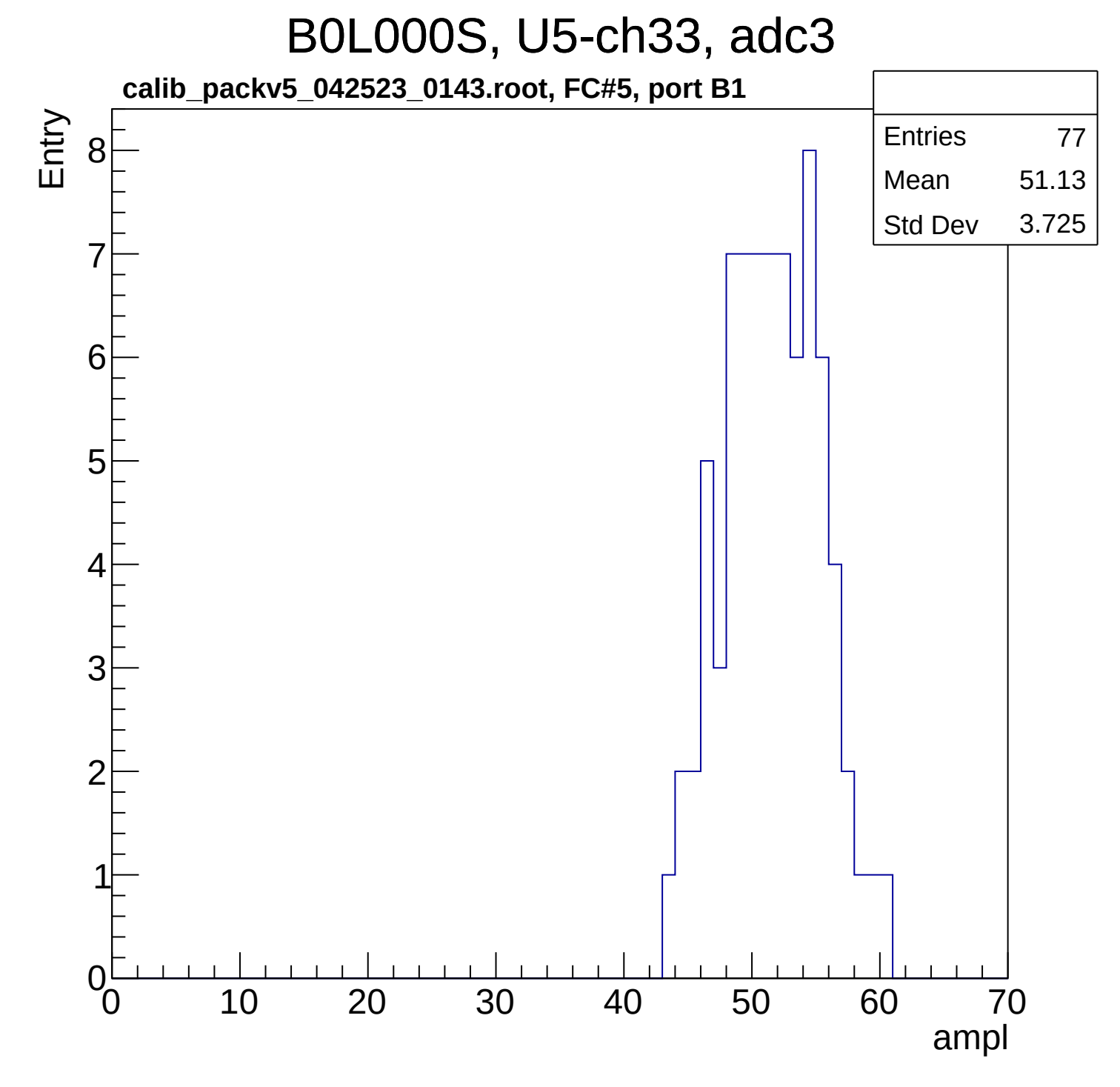
B0L000S, U5-ch33, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 51.13
Std Dev 3.725
Entry
ampl
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4
5
6
7
8
0
10
20
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60
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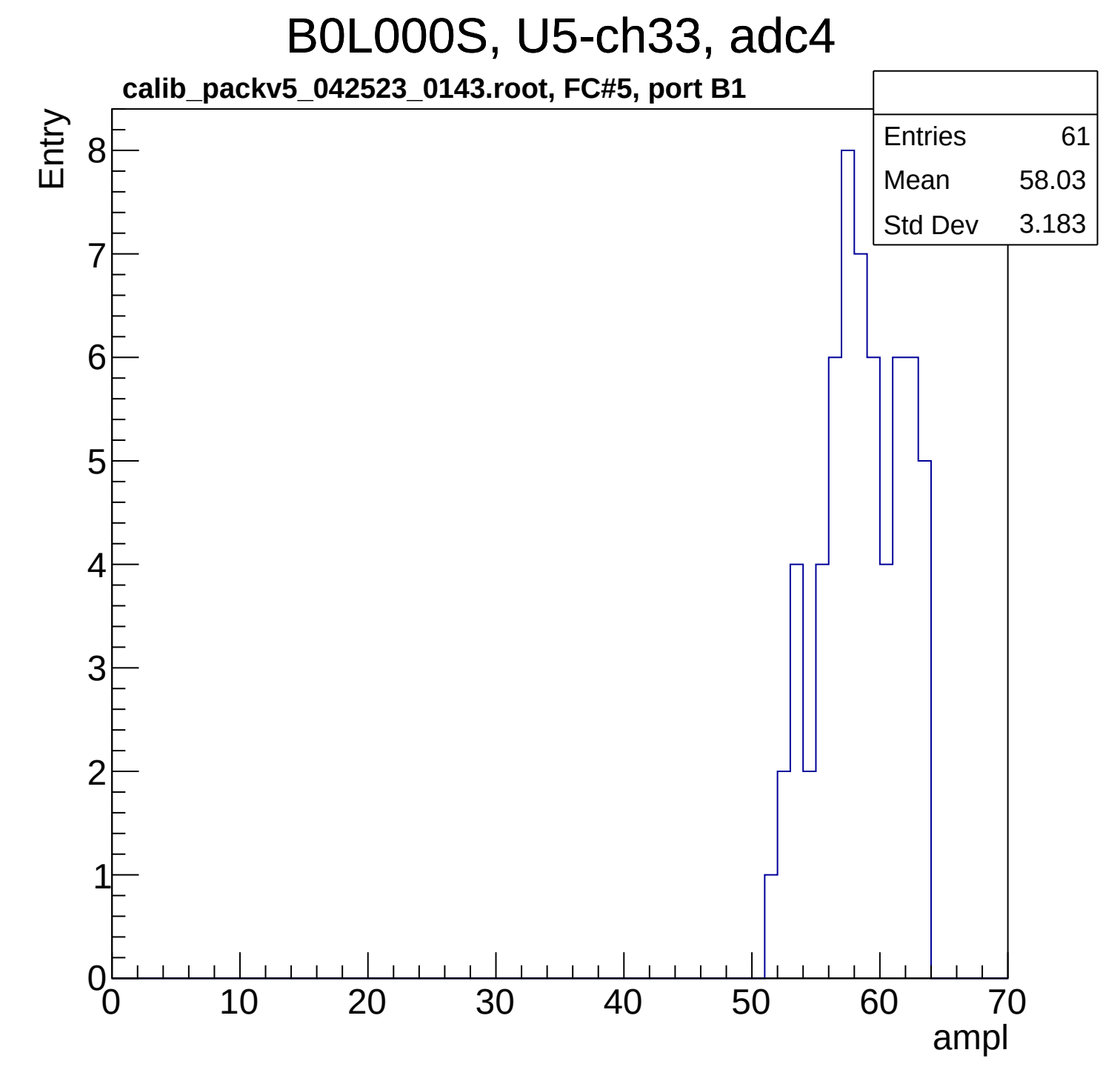
B0L000S, U5-ch33, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 58.03
Std Dev 3.183
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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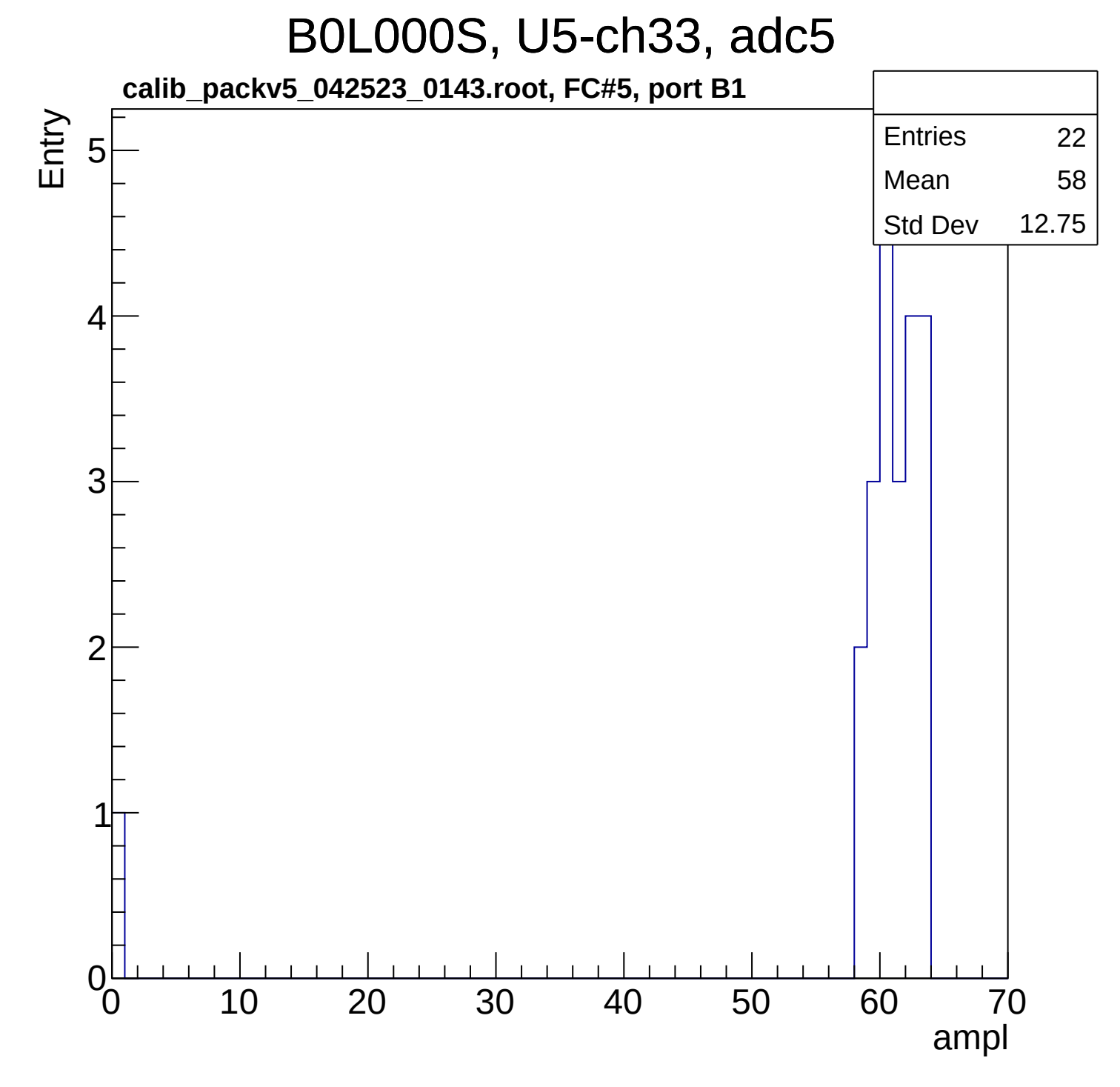
60
70


B0L000S, U5-ch33, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 58
Std Dev 12.75
Entry
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3
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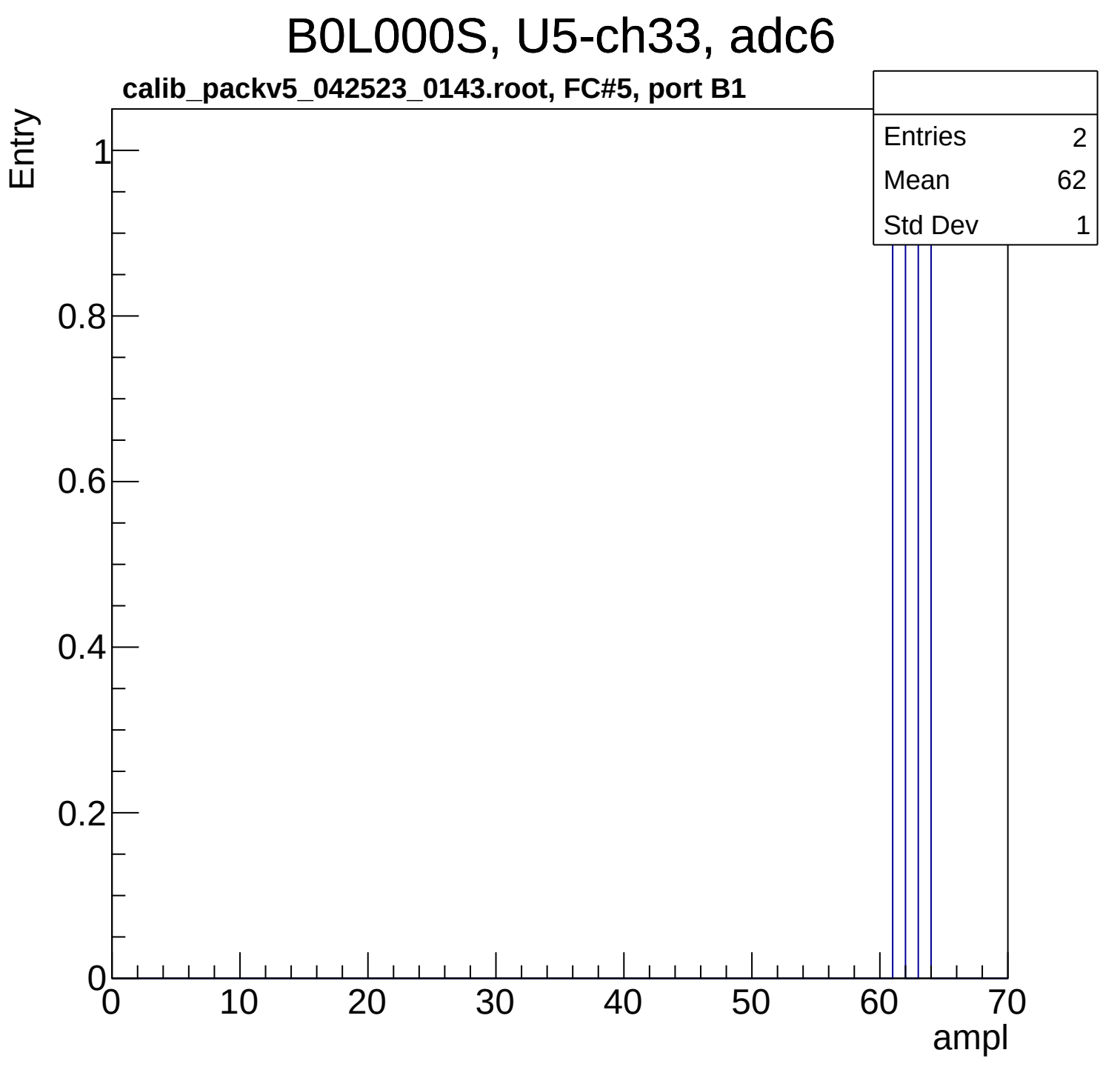
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch33, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
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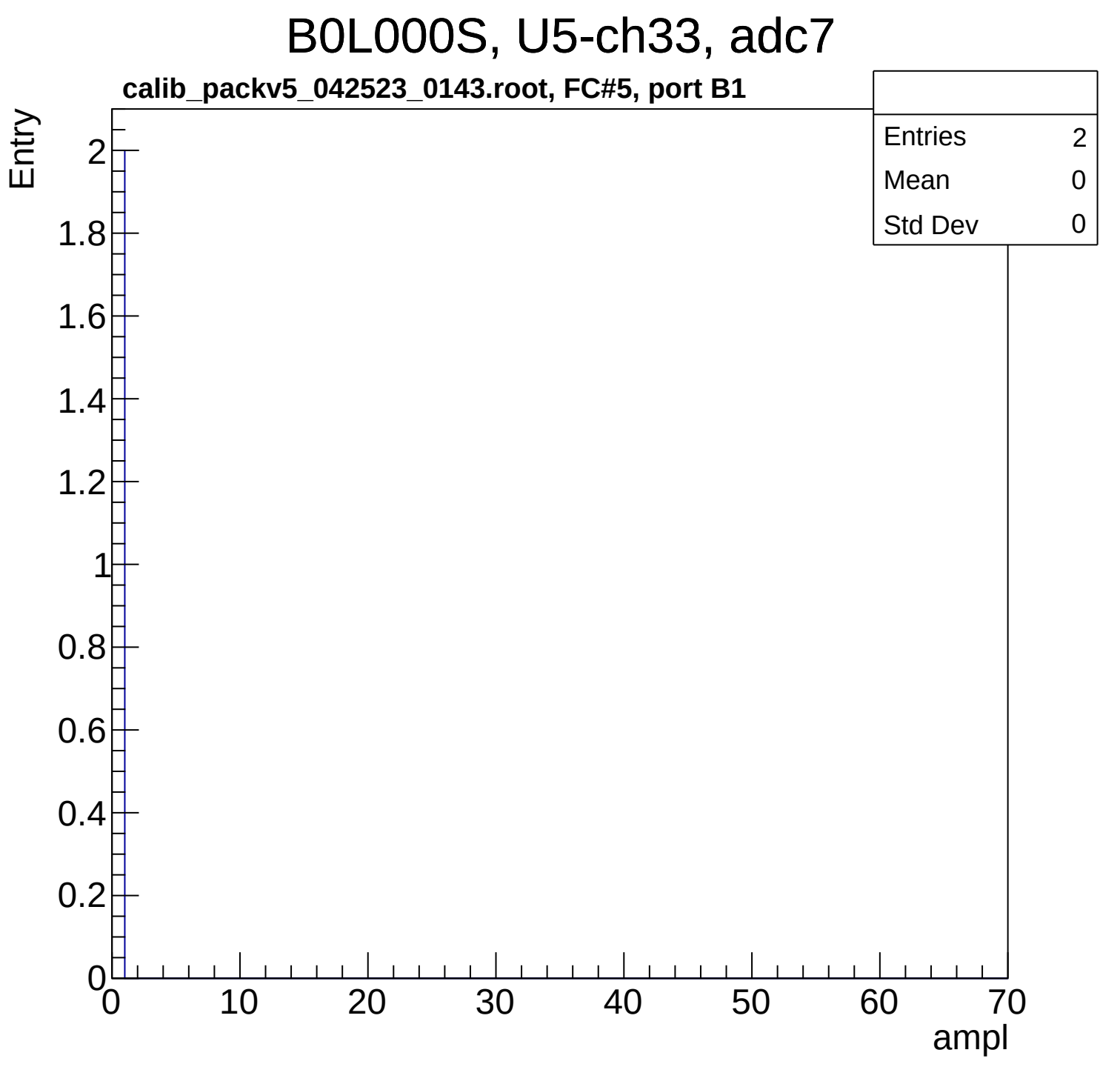
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch33, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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10
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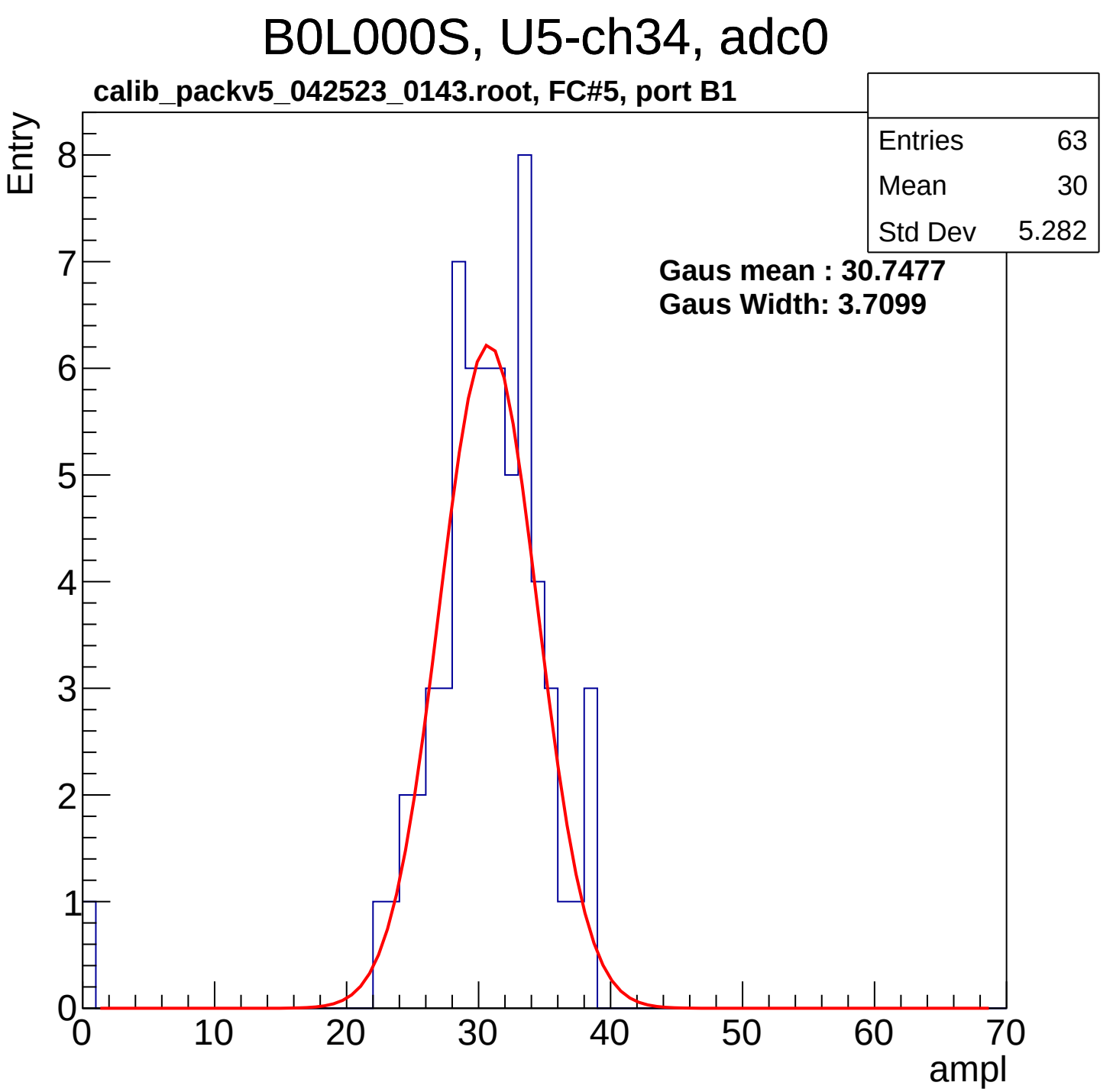
50
60
70
ampl


B0L000S, U5-ch34, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30
Std Dev 5.282
Gaus mean : 30.7477
Gaus Width: 3.7099
Entry
ampl
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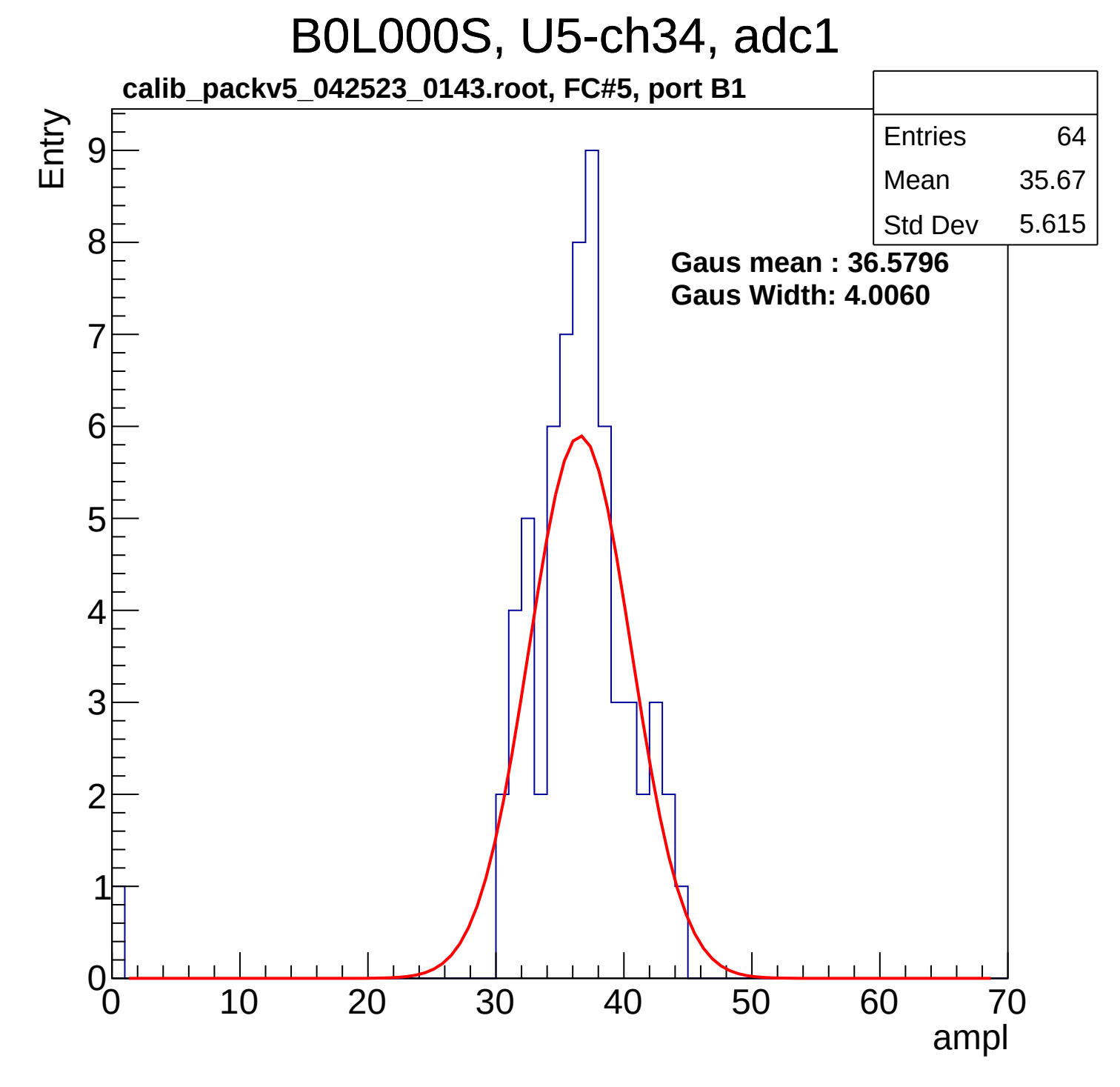
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch34, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 35.67
Std Dev 5.615
Gaus mean : 36.5796
Gaus Width: 4.0060
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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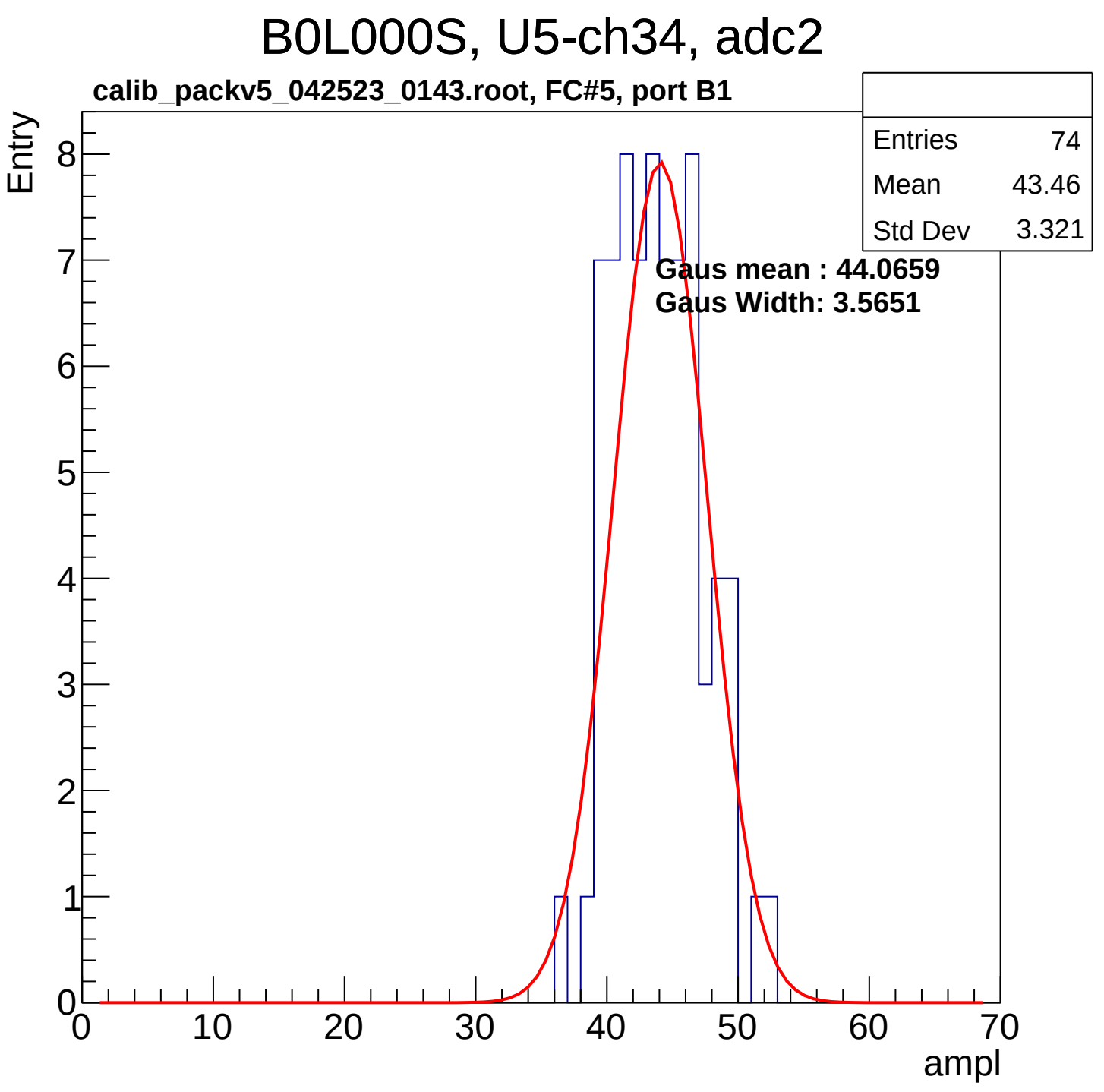
B0L000S, U5-ch34, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.46
Std Dev 3.321
Gaus mean : 44.0659
Gaus Width: 3.5651
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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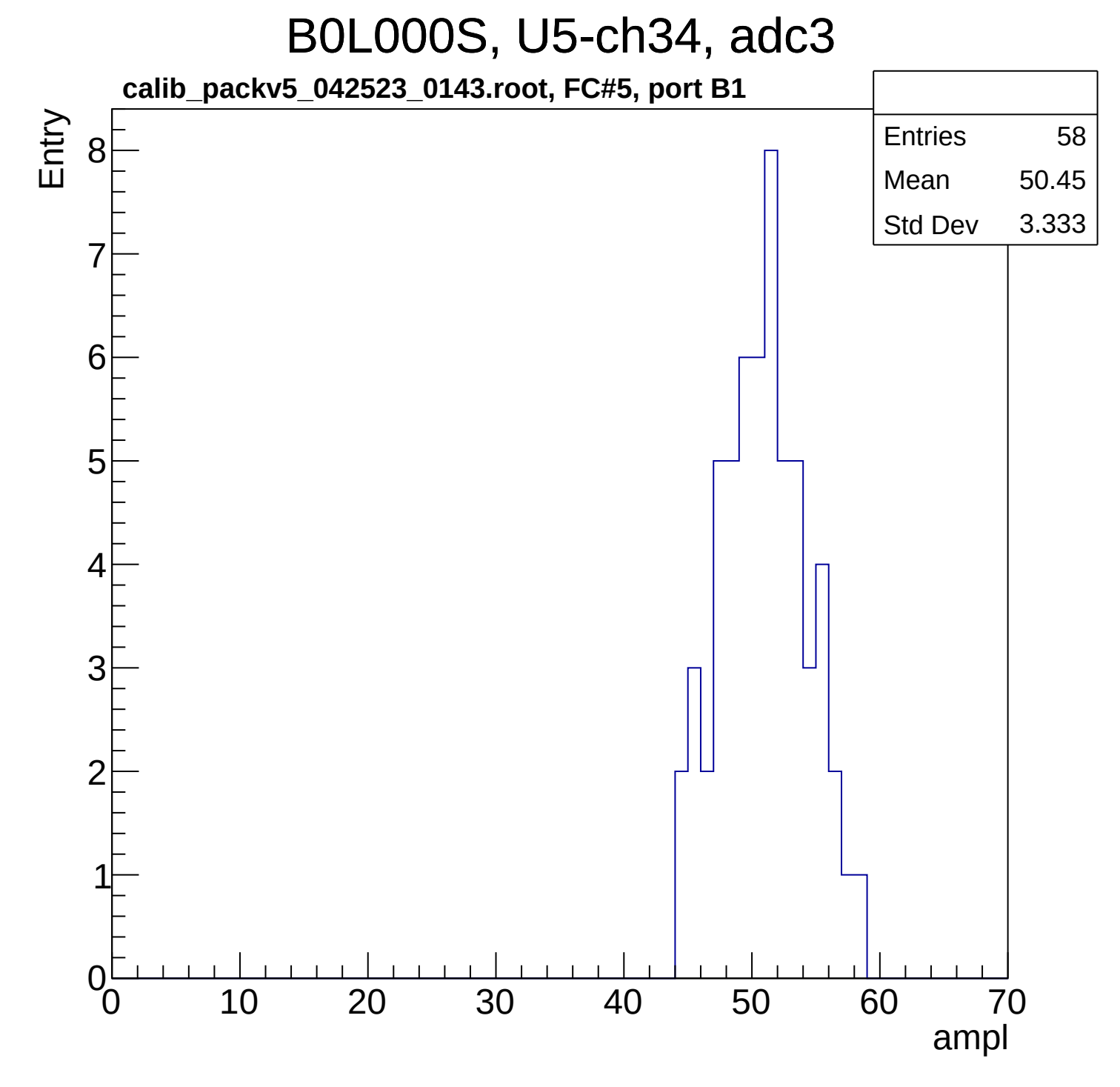
B0L000S, U5-ch34, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 50.45
Std Dev 3.333
Entry
ampl
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2
3
4
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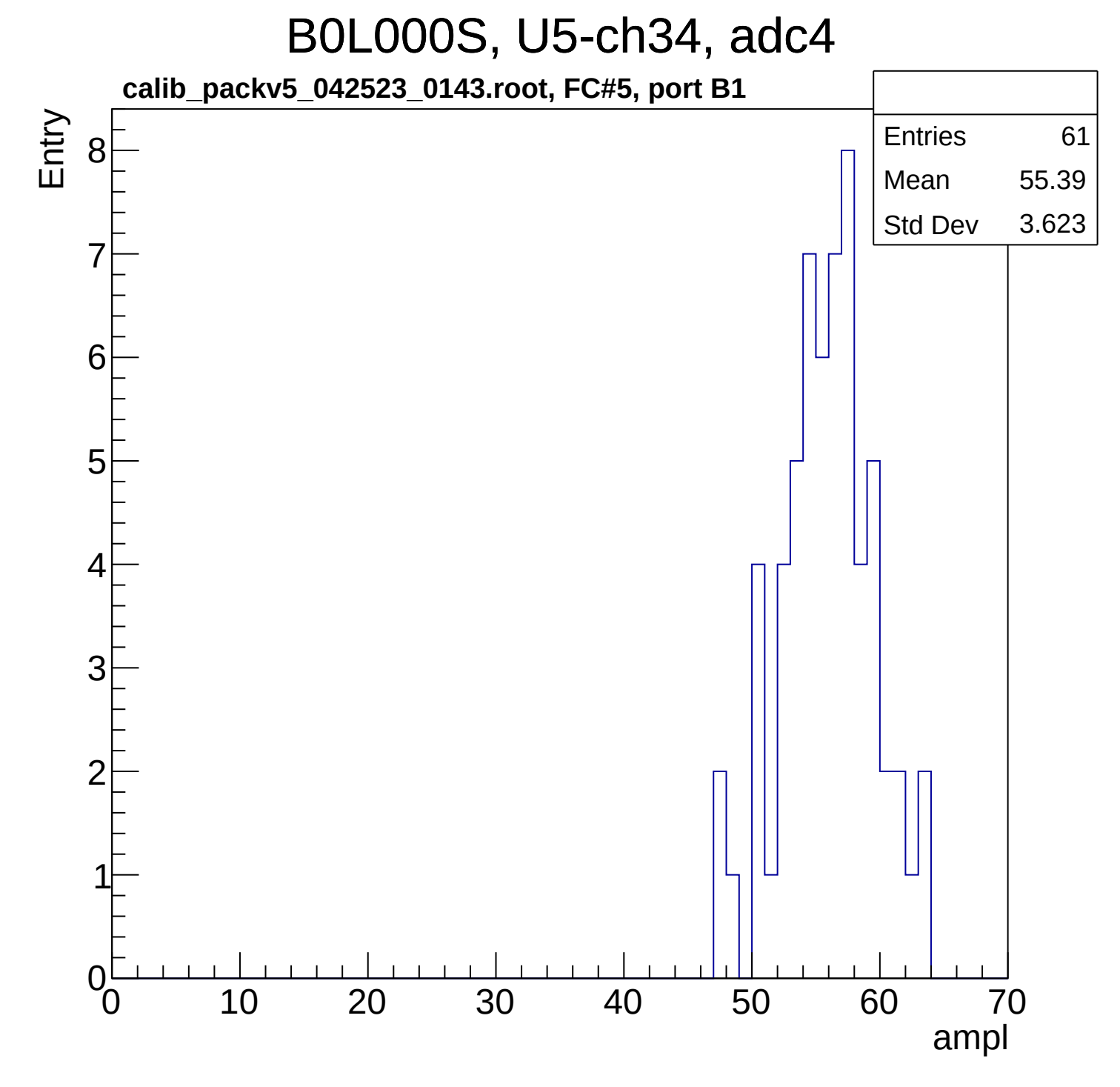
B0L000S, U5-ch34, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 55.39
Std Dev 3.623
Entry
ampl
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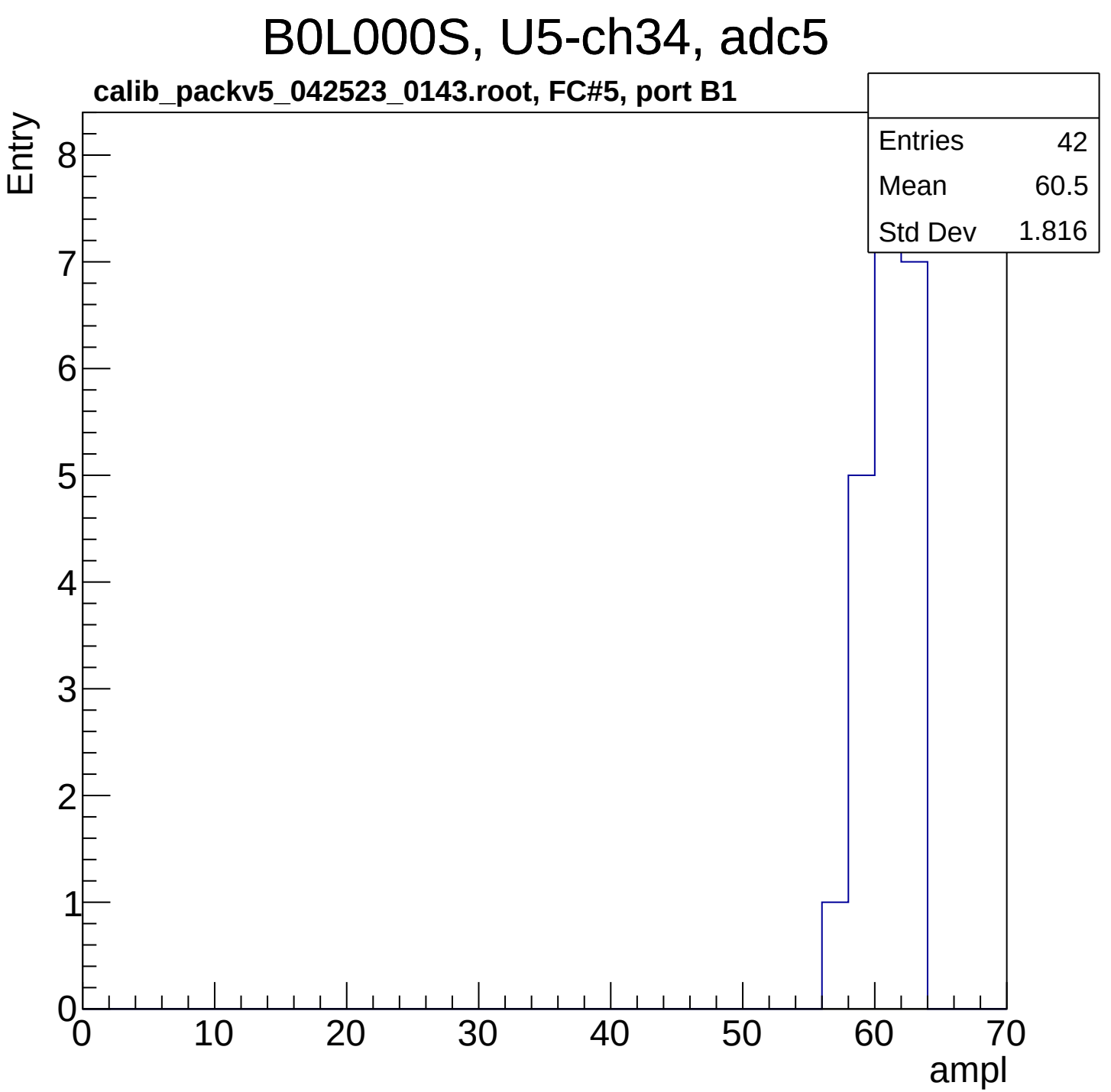
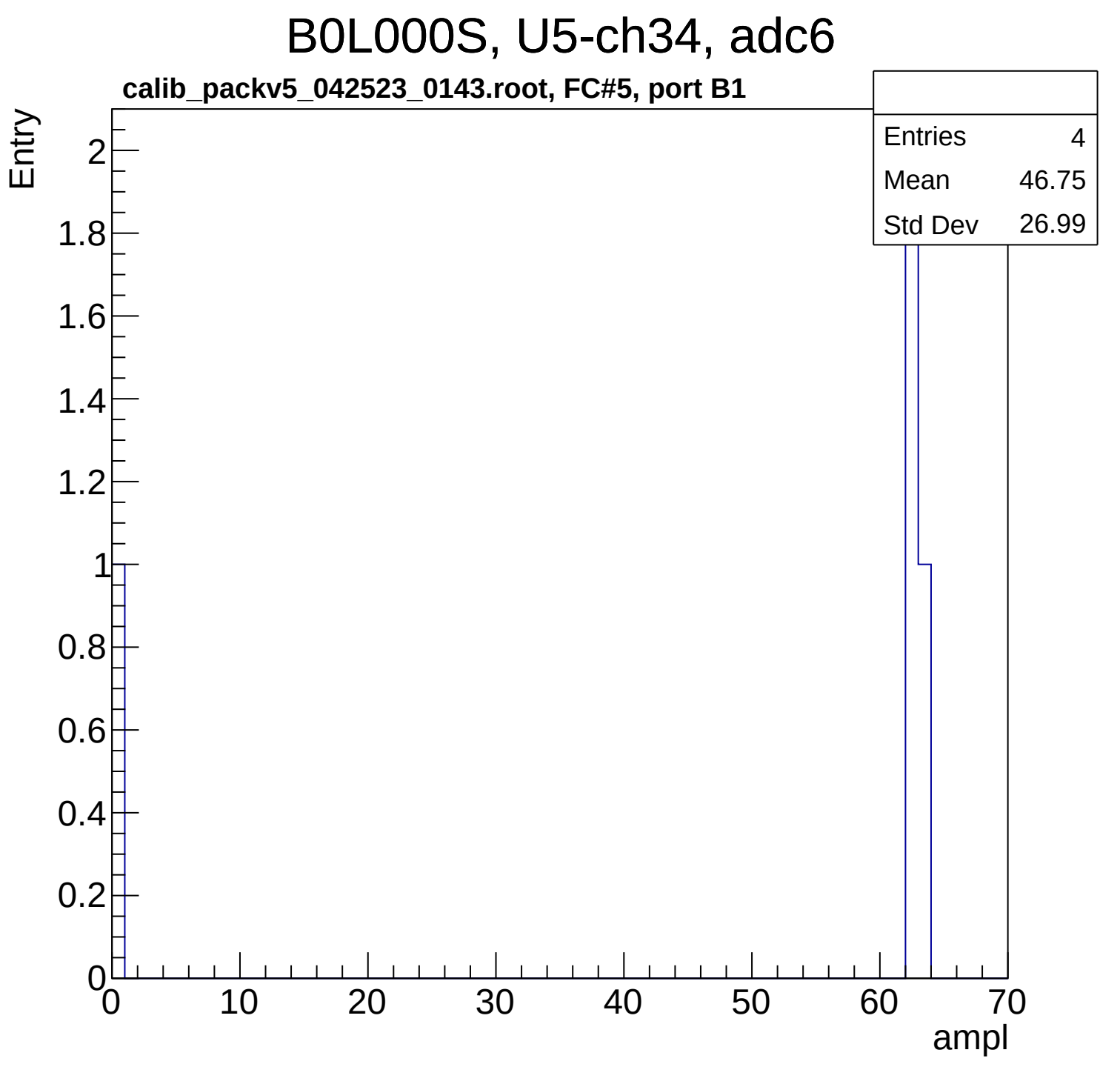

B0L000S, U5-ch34, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 60.5
Std Dev 1.816
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch34, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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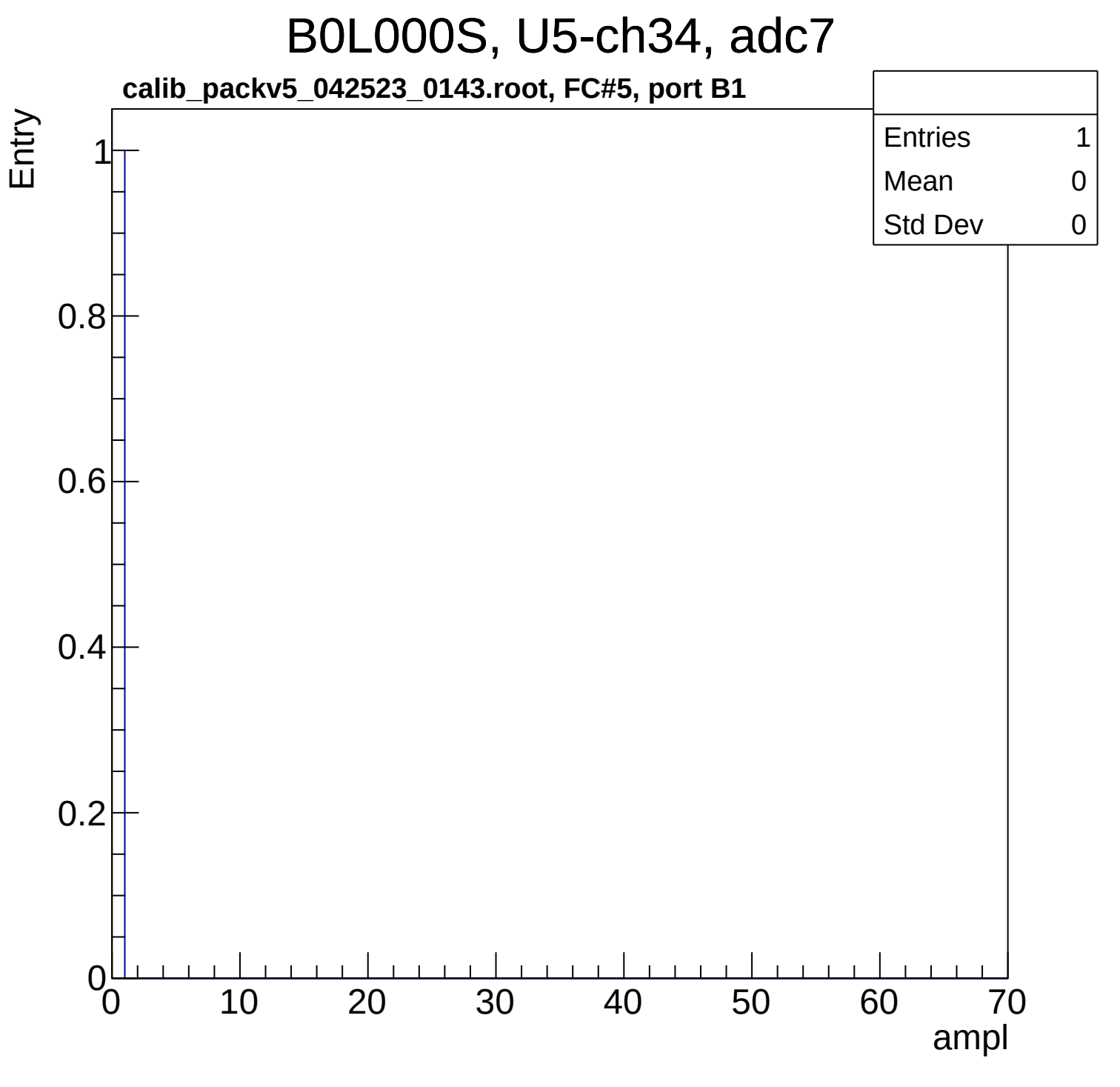
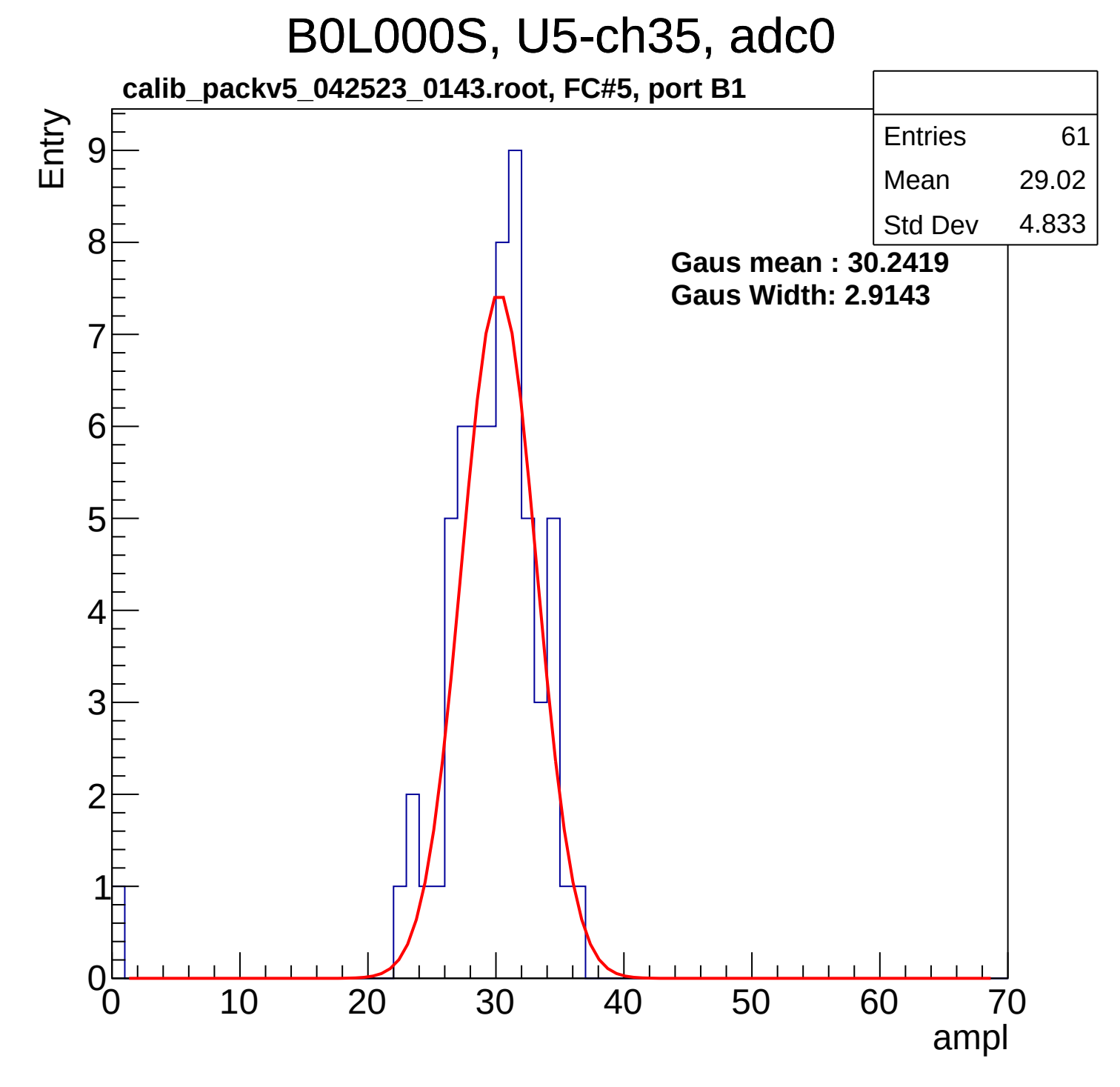
40
50
60
70


B0L000S, U5-ch34, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch35, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.02
Std Dev 4.833
Gaus mean : 30.2419
Gaus Width: 2.9143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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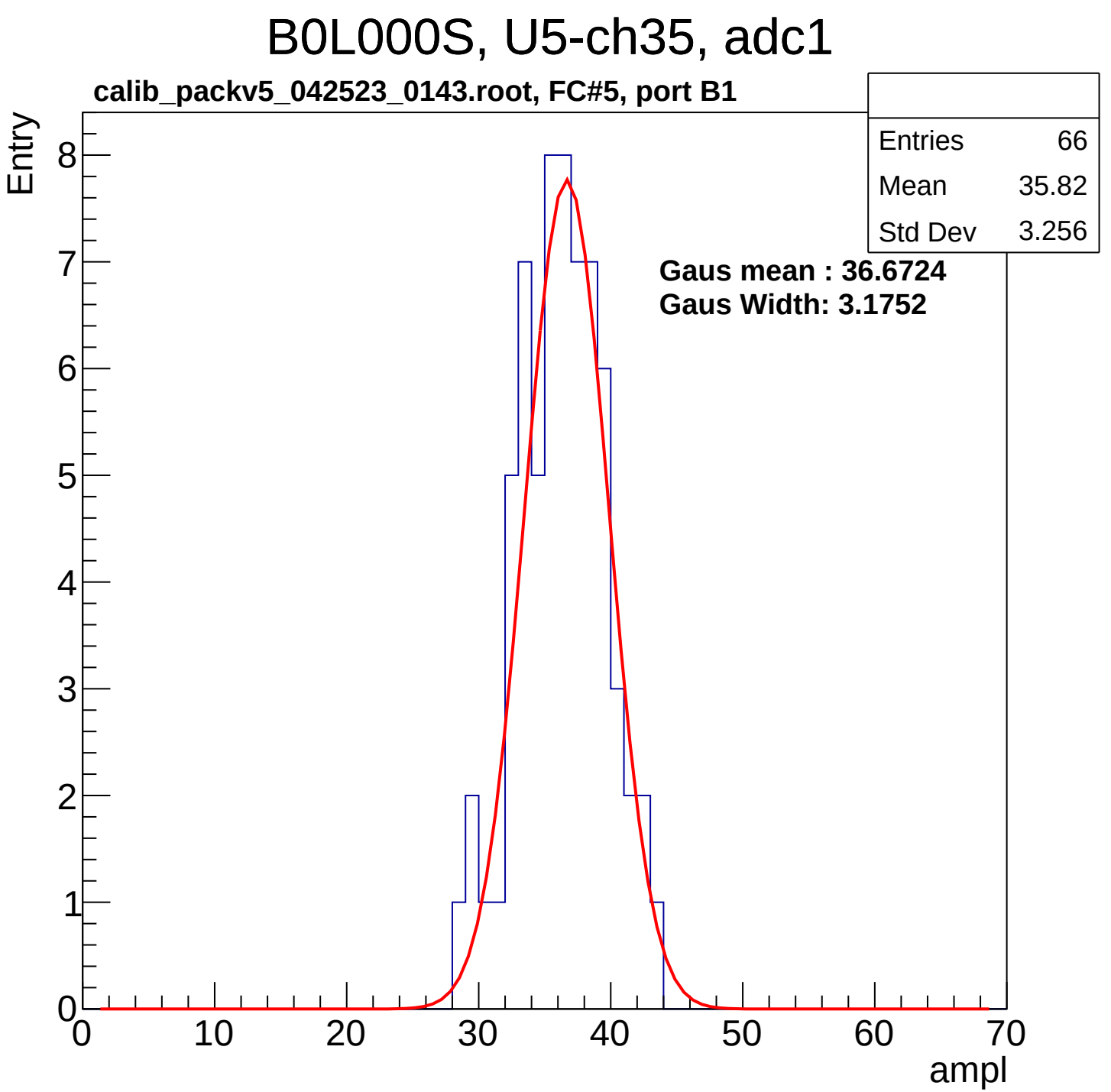
B0L000S, U5-ch35, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 35.82
Std Dev 3.256
Gaus mean : 36.6724
Gaus Width: 3.1752
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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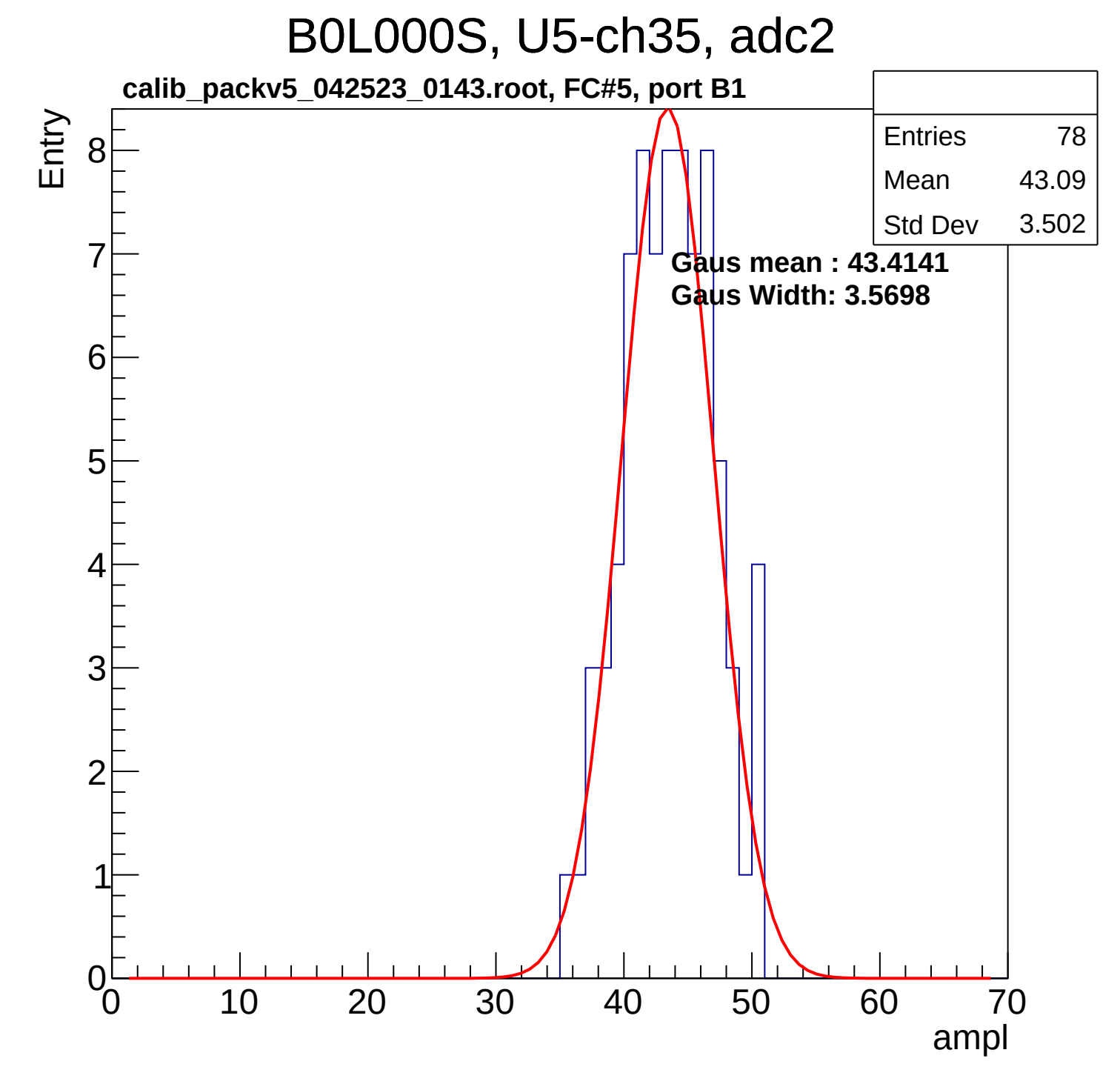
B0L000S, U5-ch35, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.09
Std Dev 3.502
Gaus mean : 43.4141
Gaus Width: 3.5698
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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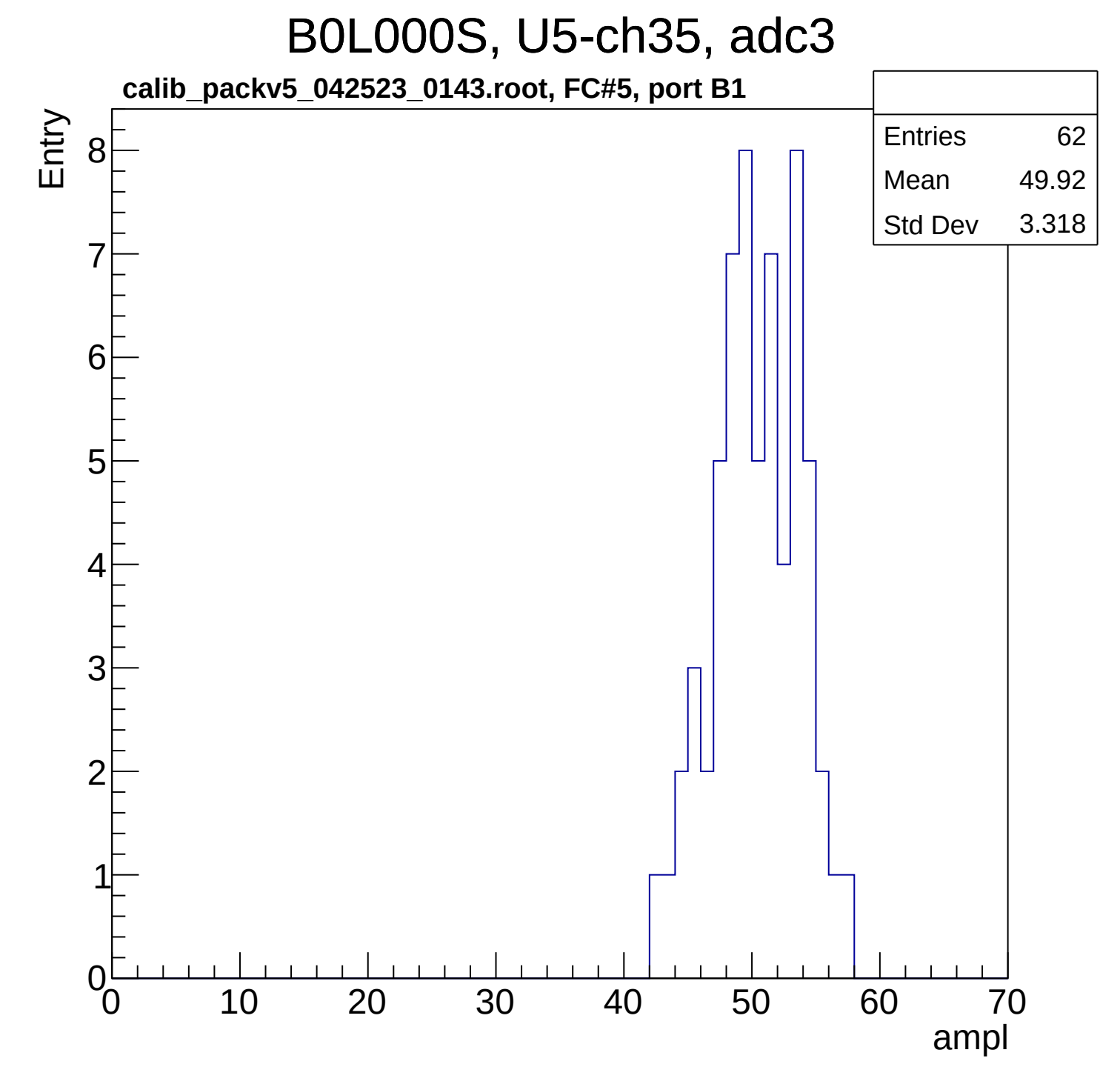
B0L000S, U5-ch35, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.92
Std Dev 3.318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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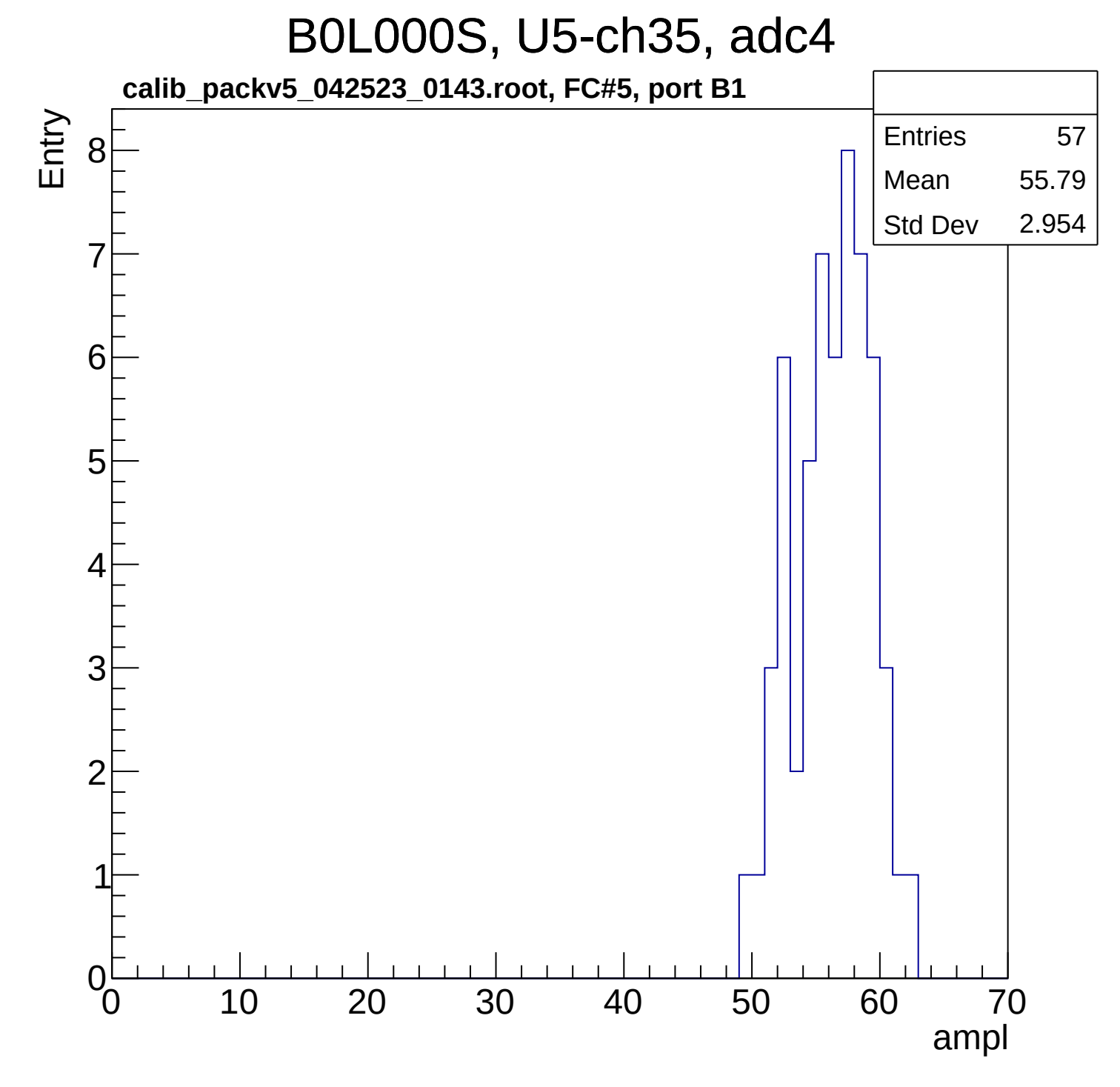
B0L000S, U5-ch35, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.79
Std Dev 2.954
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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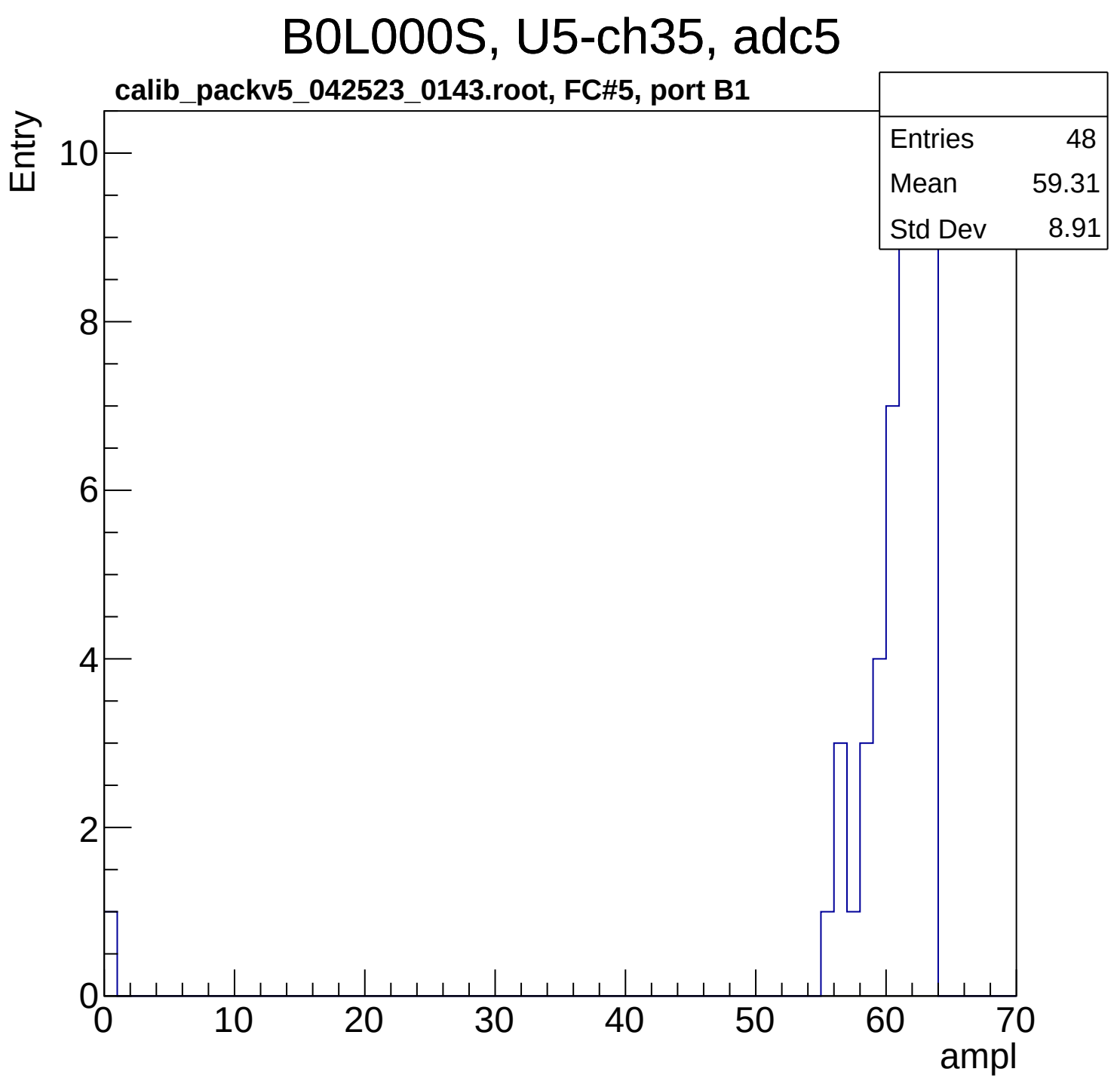
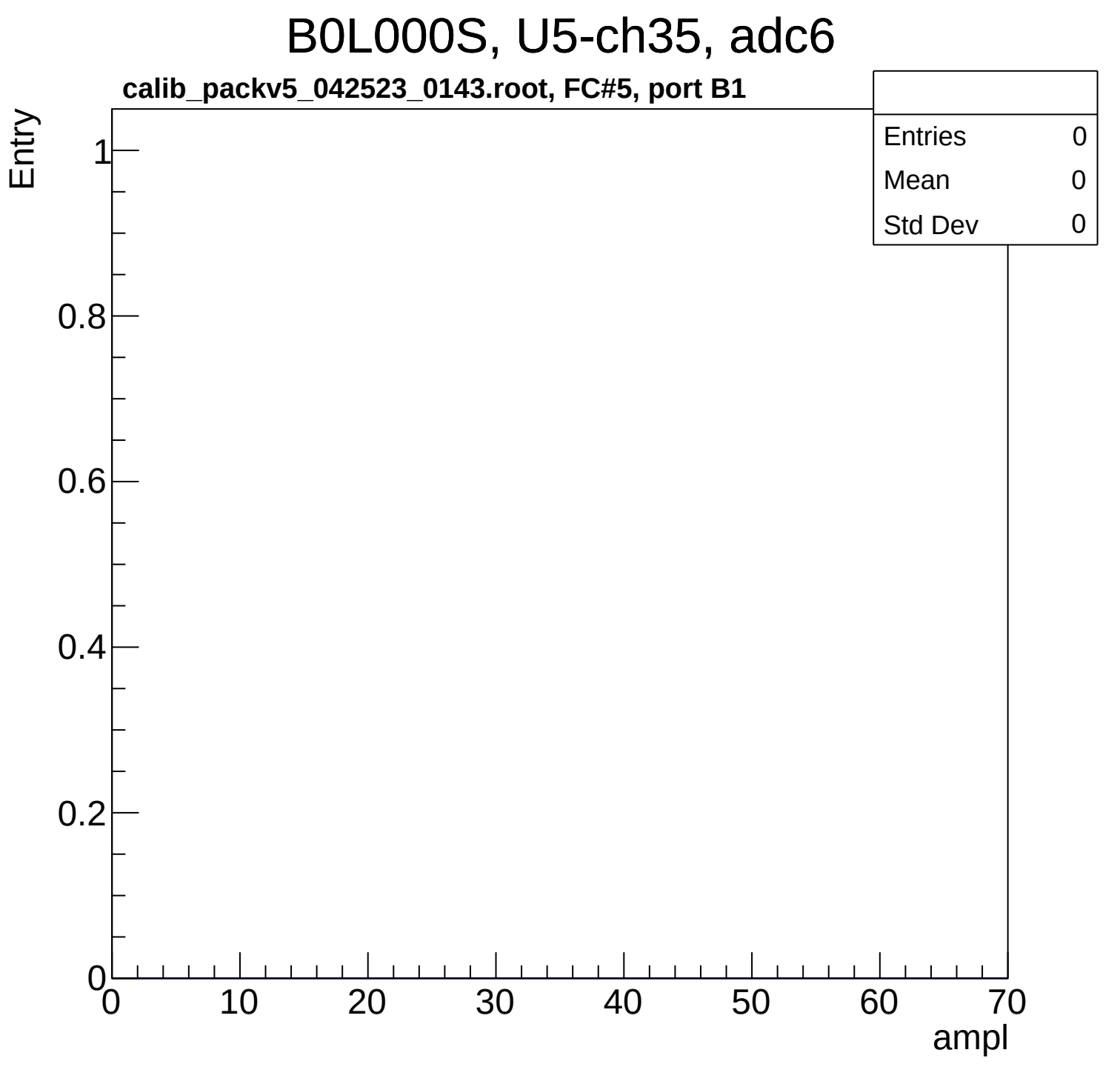
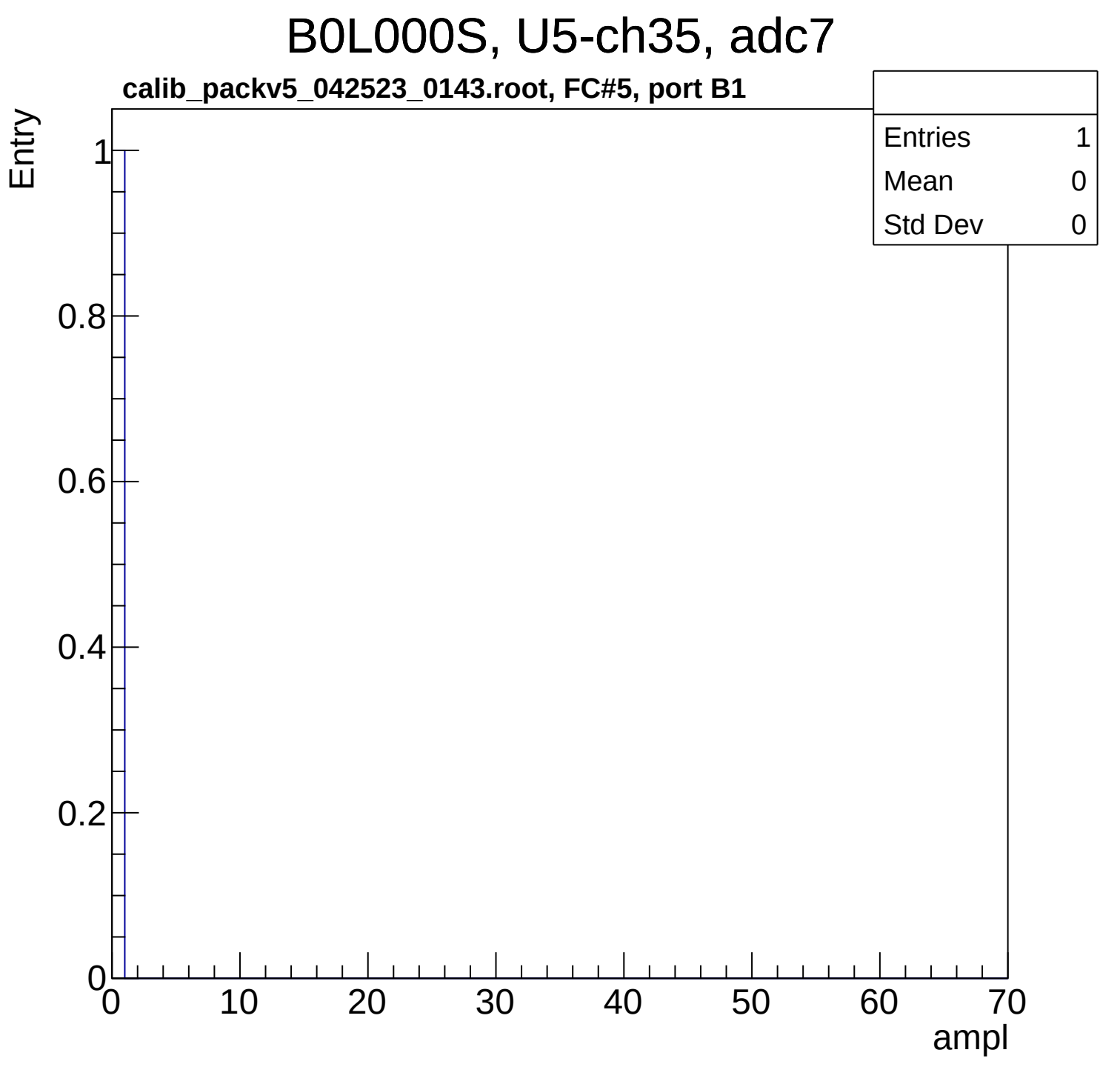

B0L000S, U5-ch35, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.31
Std Dev 8.91
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch35, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch35, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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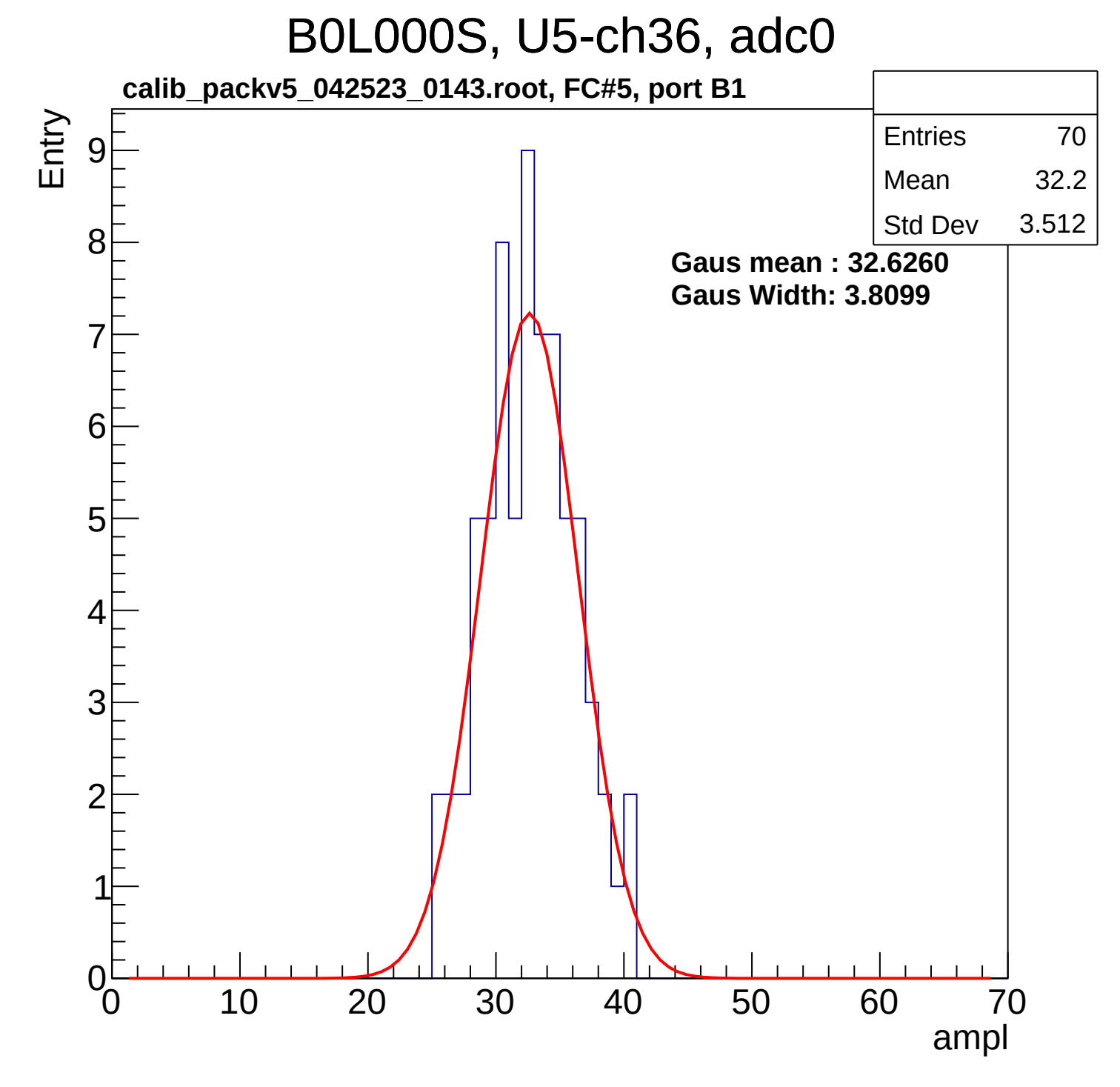
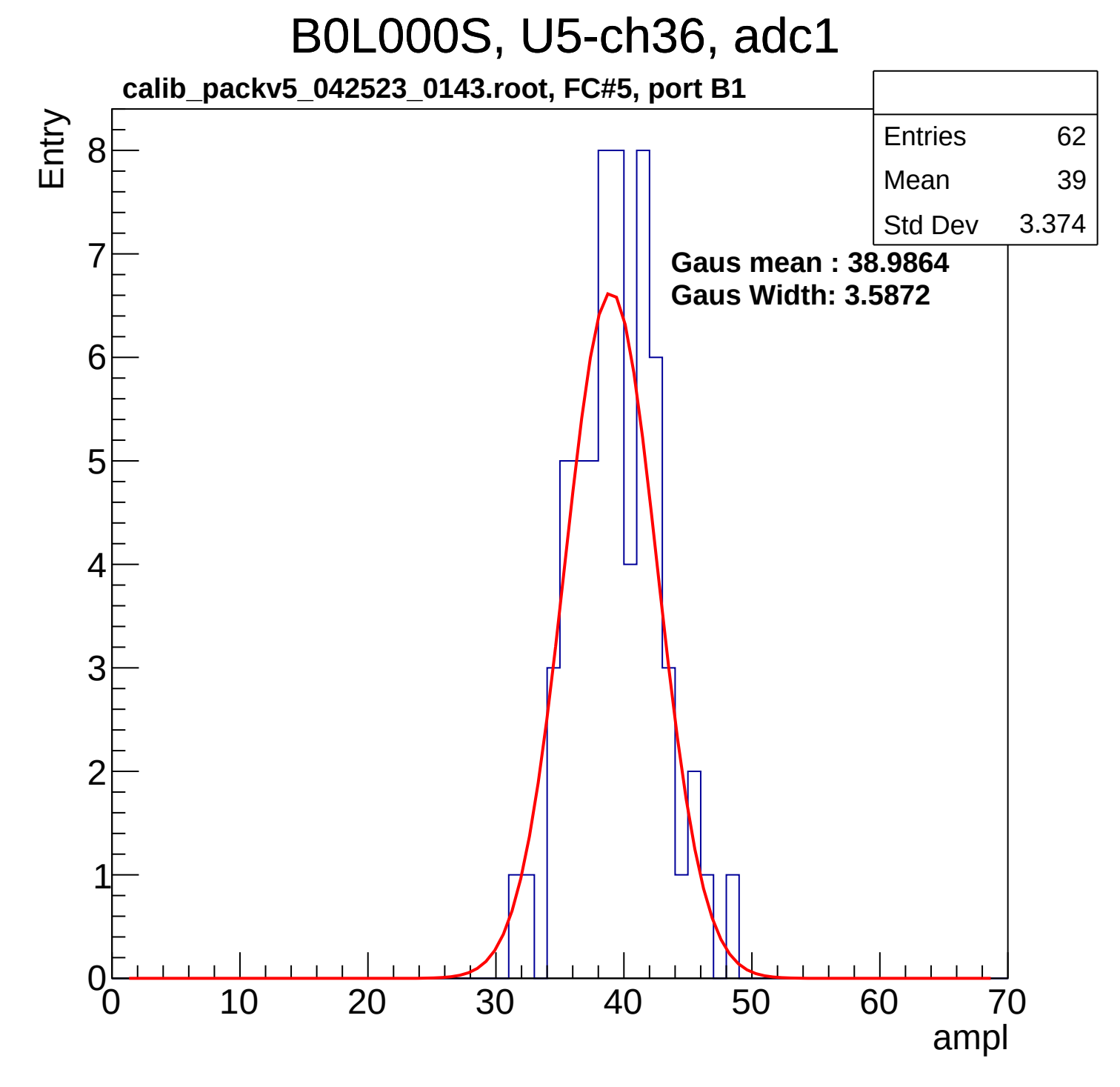
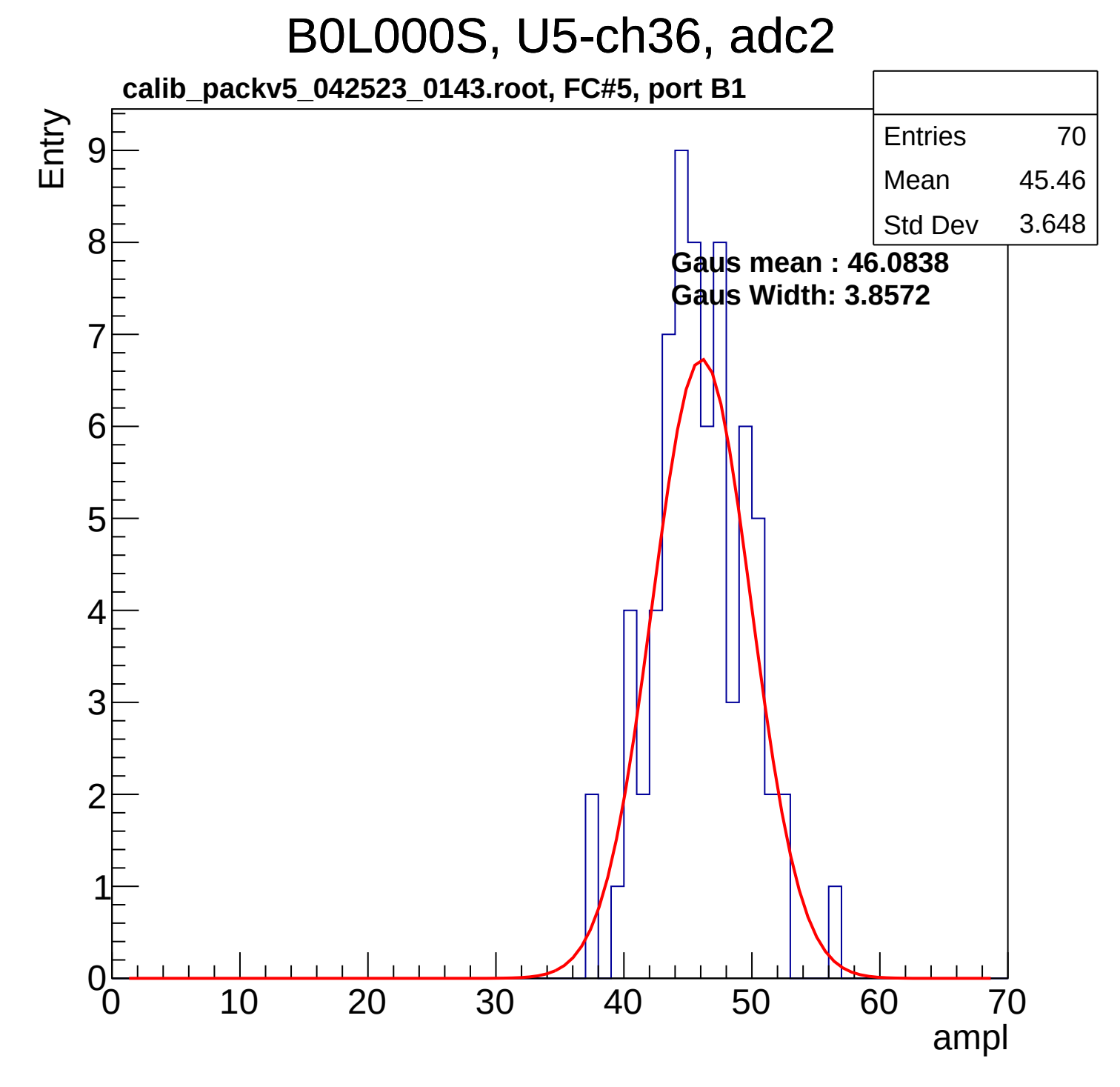
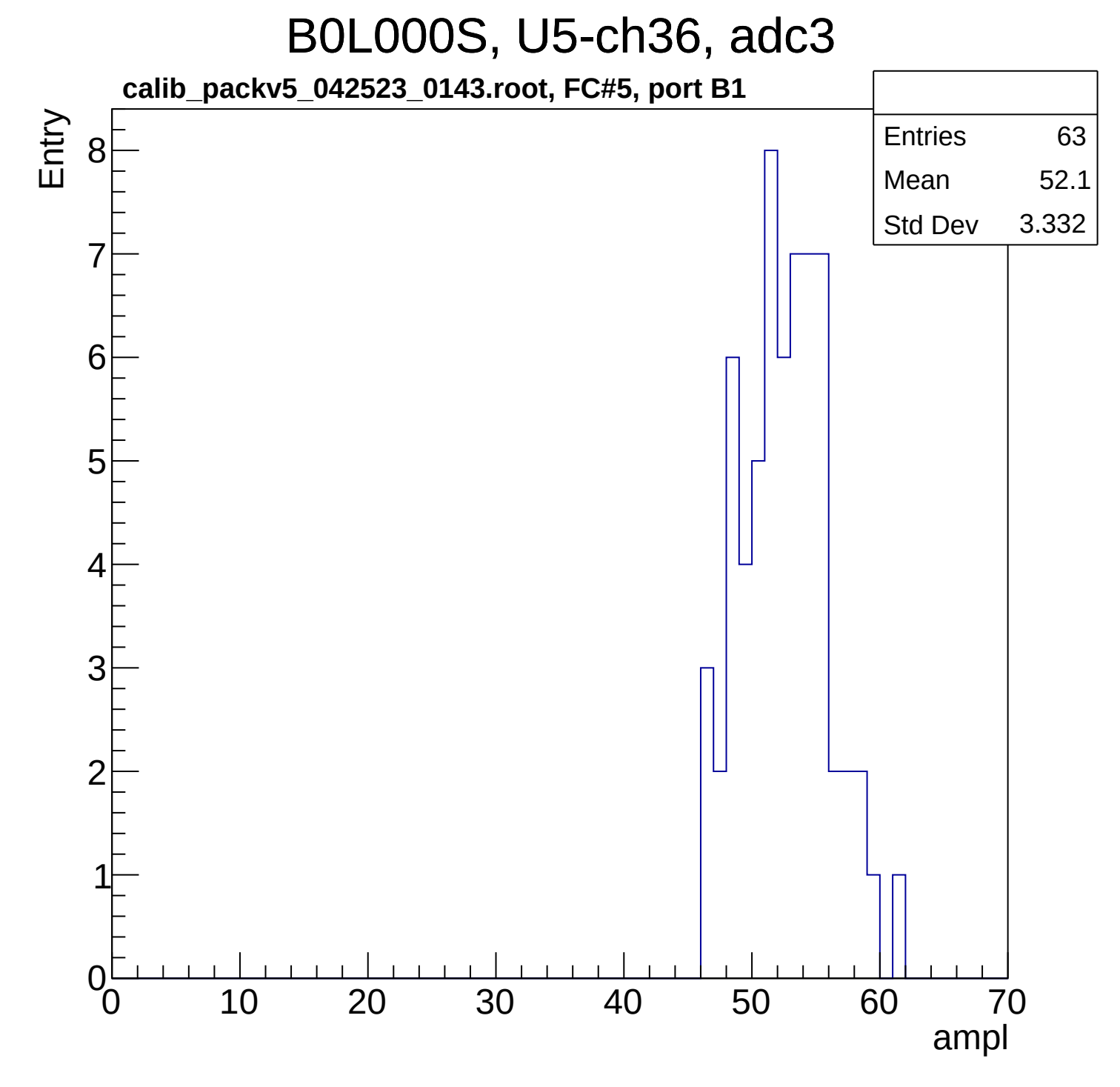
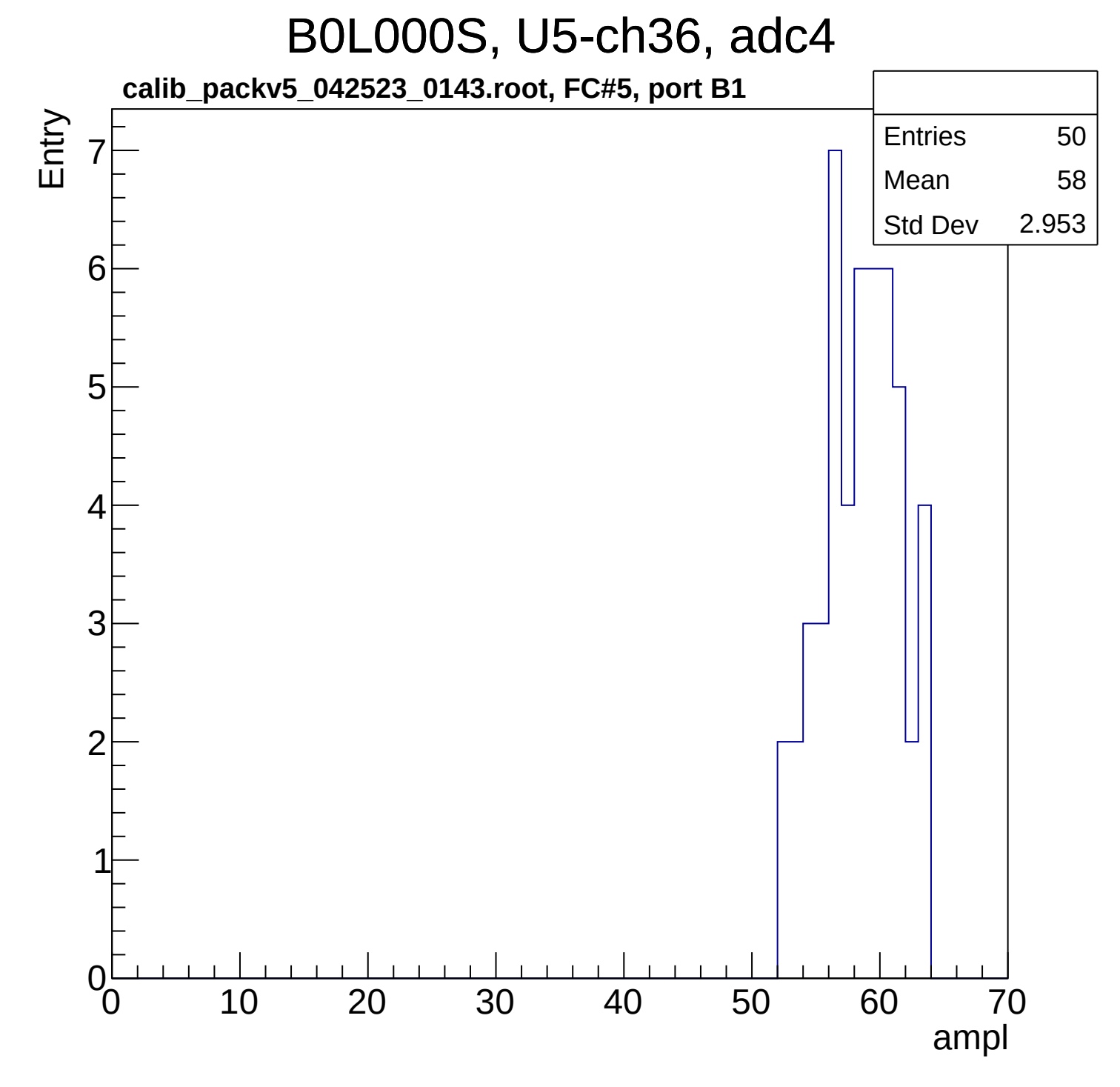
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

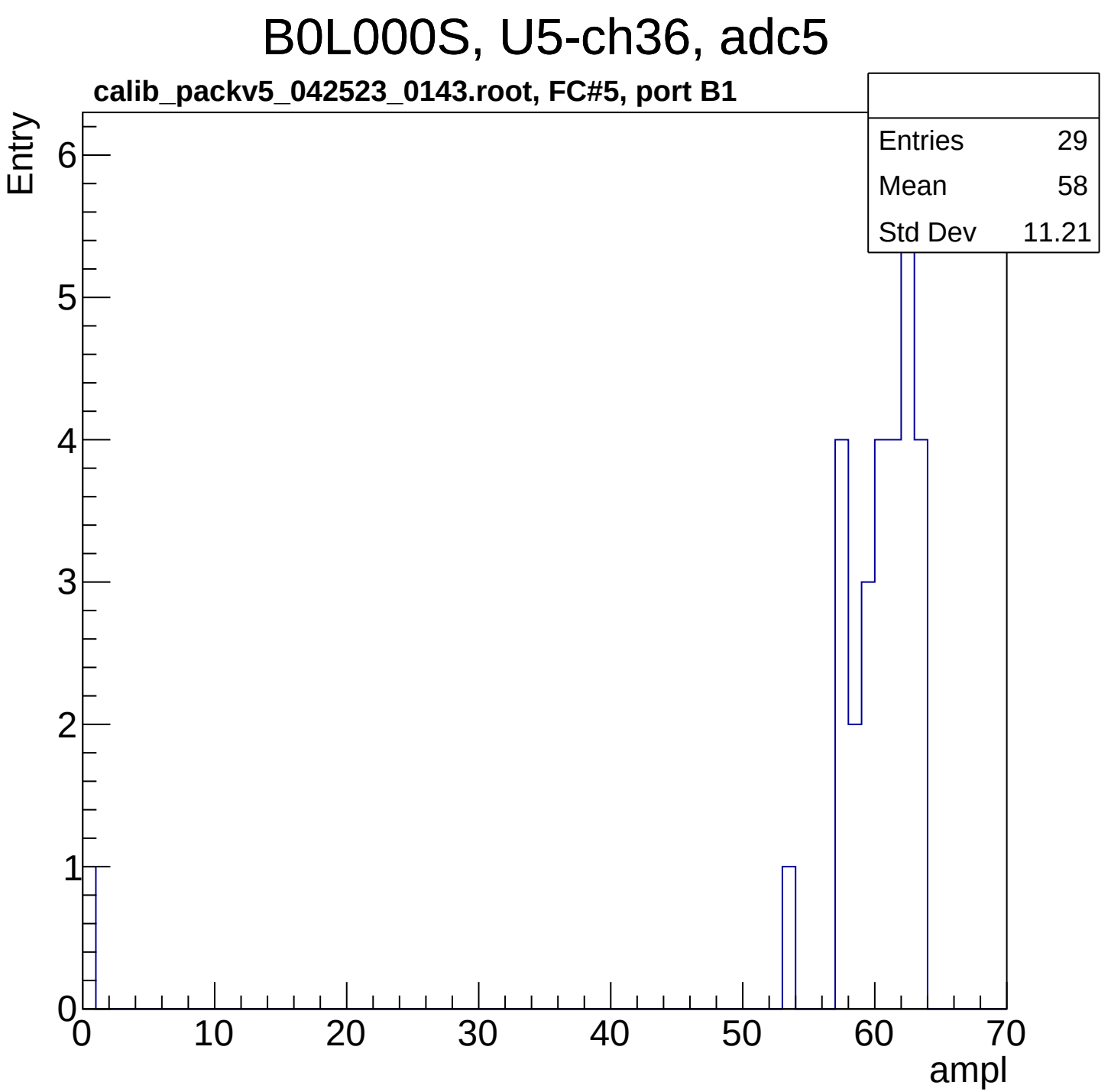

B0L000S, U5-ch36, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 32.2
Std Dev 3.512
Gaus mean : 32.6260
Gaus Width: 3.8099
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch36, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 39
Std Dev 3.374
Gaus mean : 38.9864
Gaus Width: 3.5872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch36, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 45.46
Std Dev 3.648
Gaus mean : 46.0838
Gaus Width: 3.8572
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch36, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 52.1
Std Dev 3.332
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch36, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch36, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 29
Mean 58
Std Dev 11.21
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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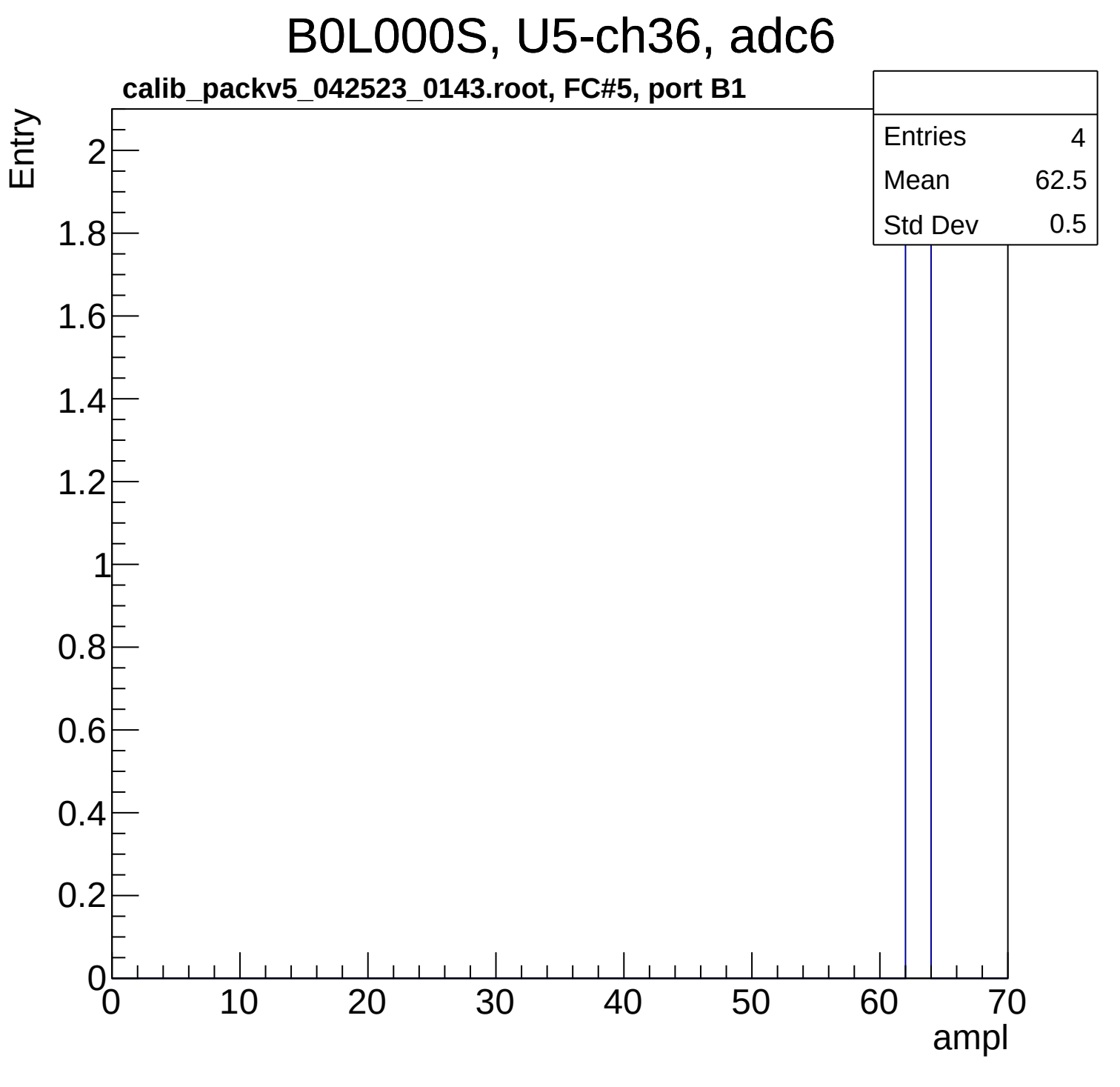
B0L000S, U5-ch36, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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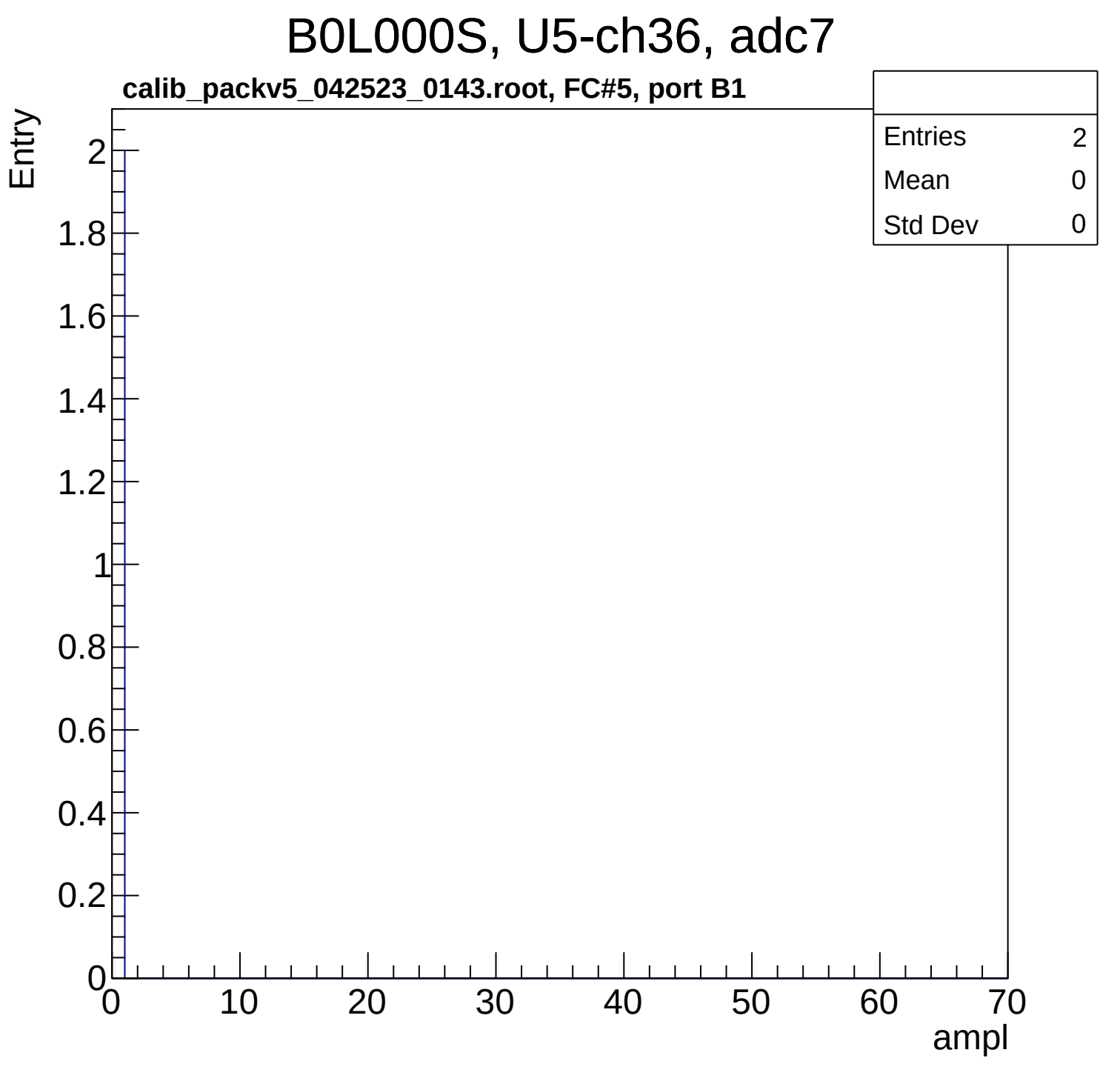
B0L000S, U5-ch36, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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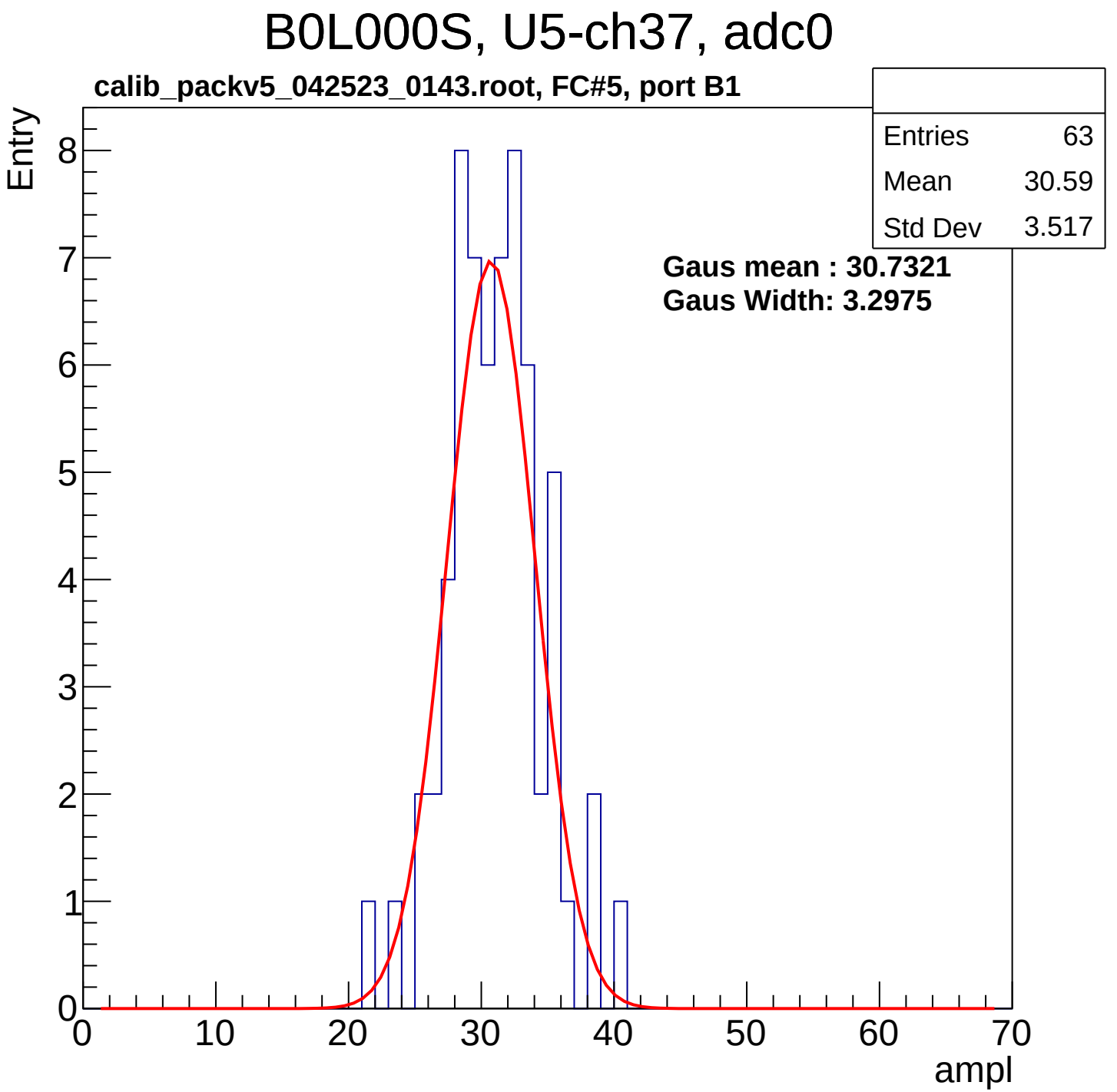
B0L000S, U5-ch37, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.59
Std Dev 3.517
Gaus mean : 30.7321
Gaus Width: 3.2975
Entry
ampl
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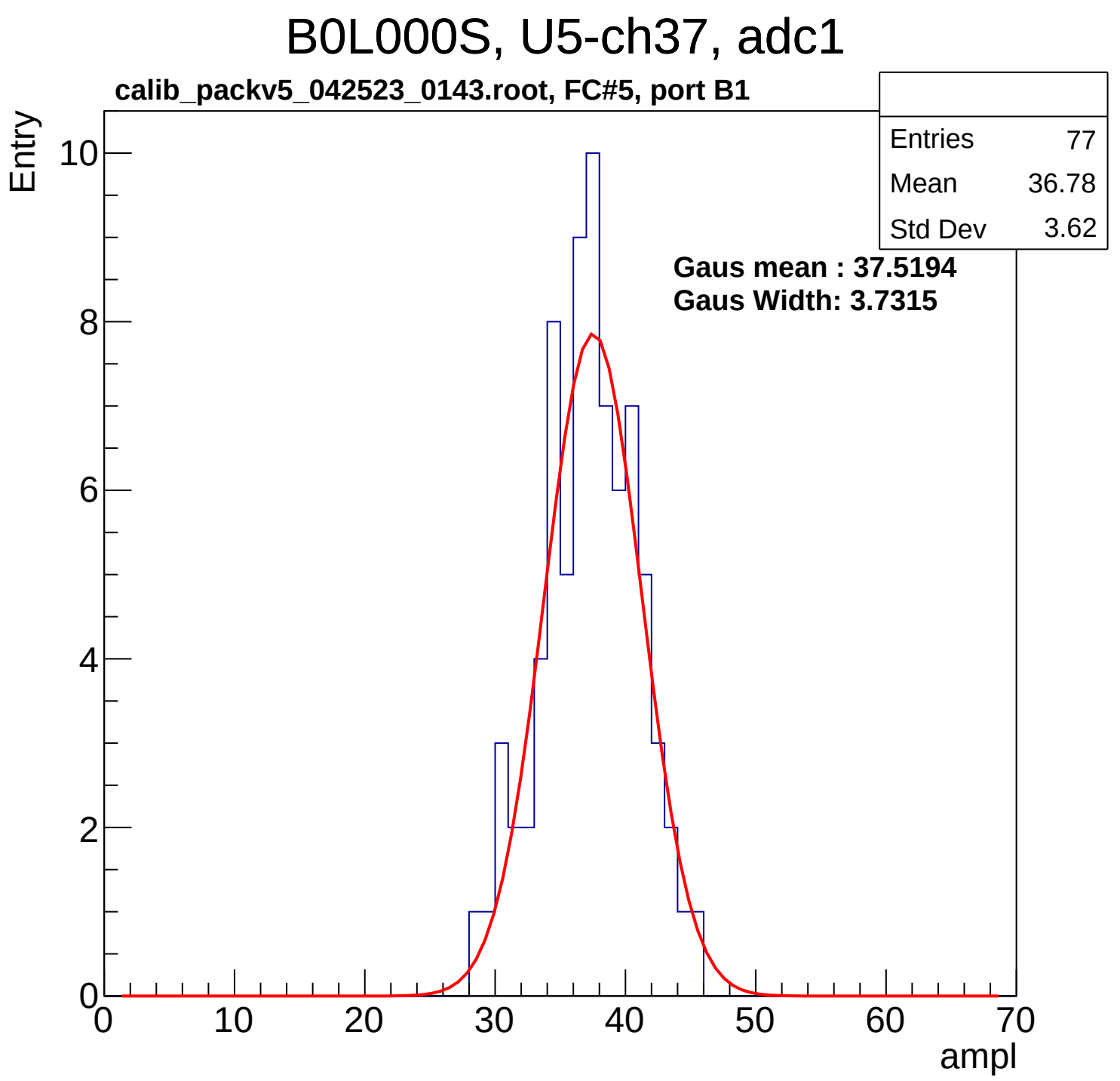
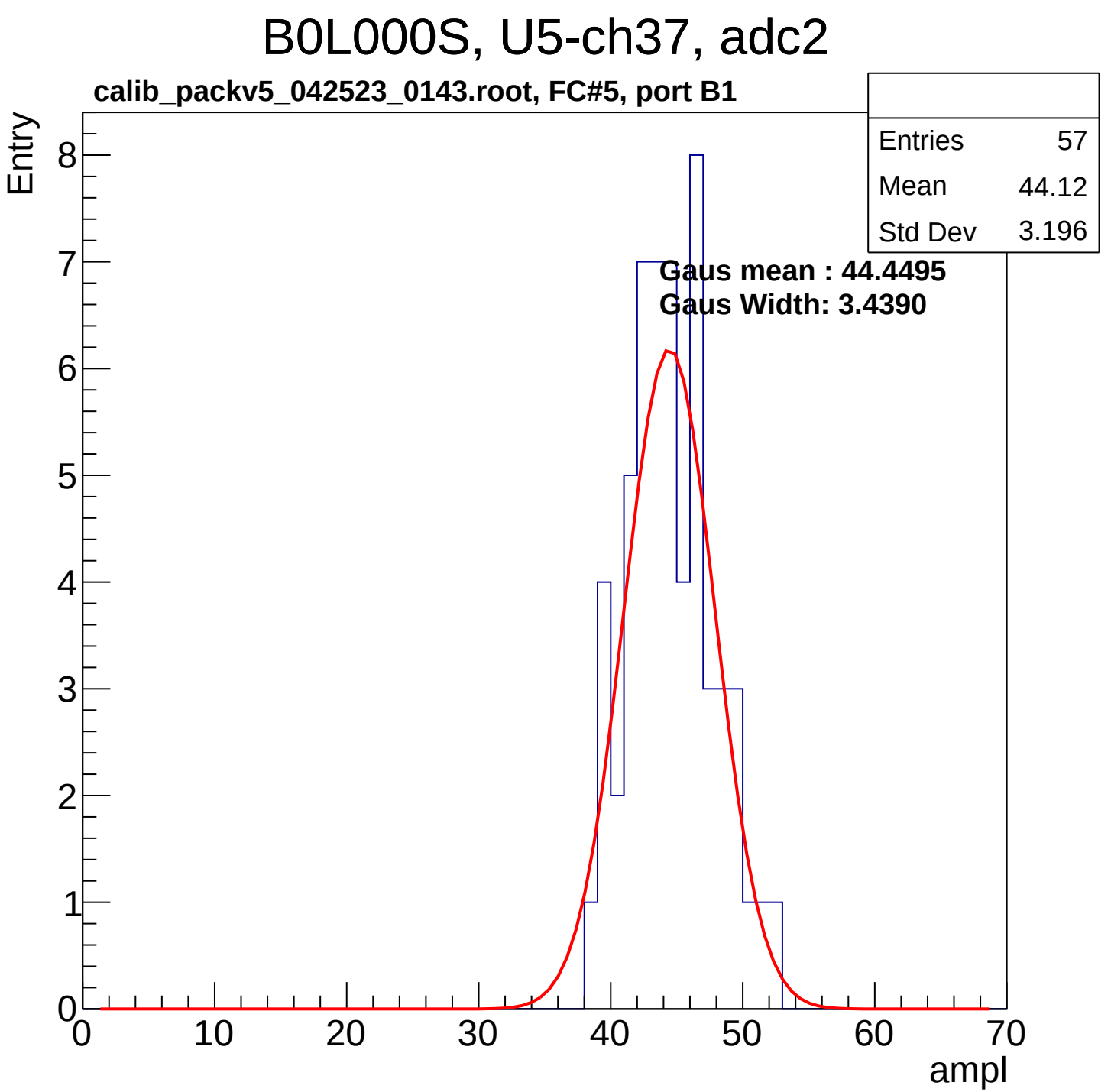
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch37, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.78
Std Dev 3.62
Gaus mean : 37.5194
Gaus Width: 3.7315
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch37, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 44.12
Std Dev 3.196
Gaus mean : 44.4495
Gaus Width: 3.4390
Entry
ampl
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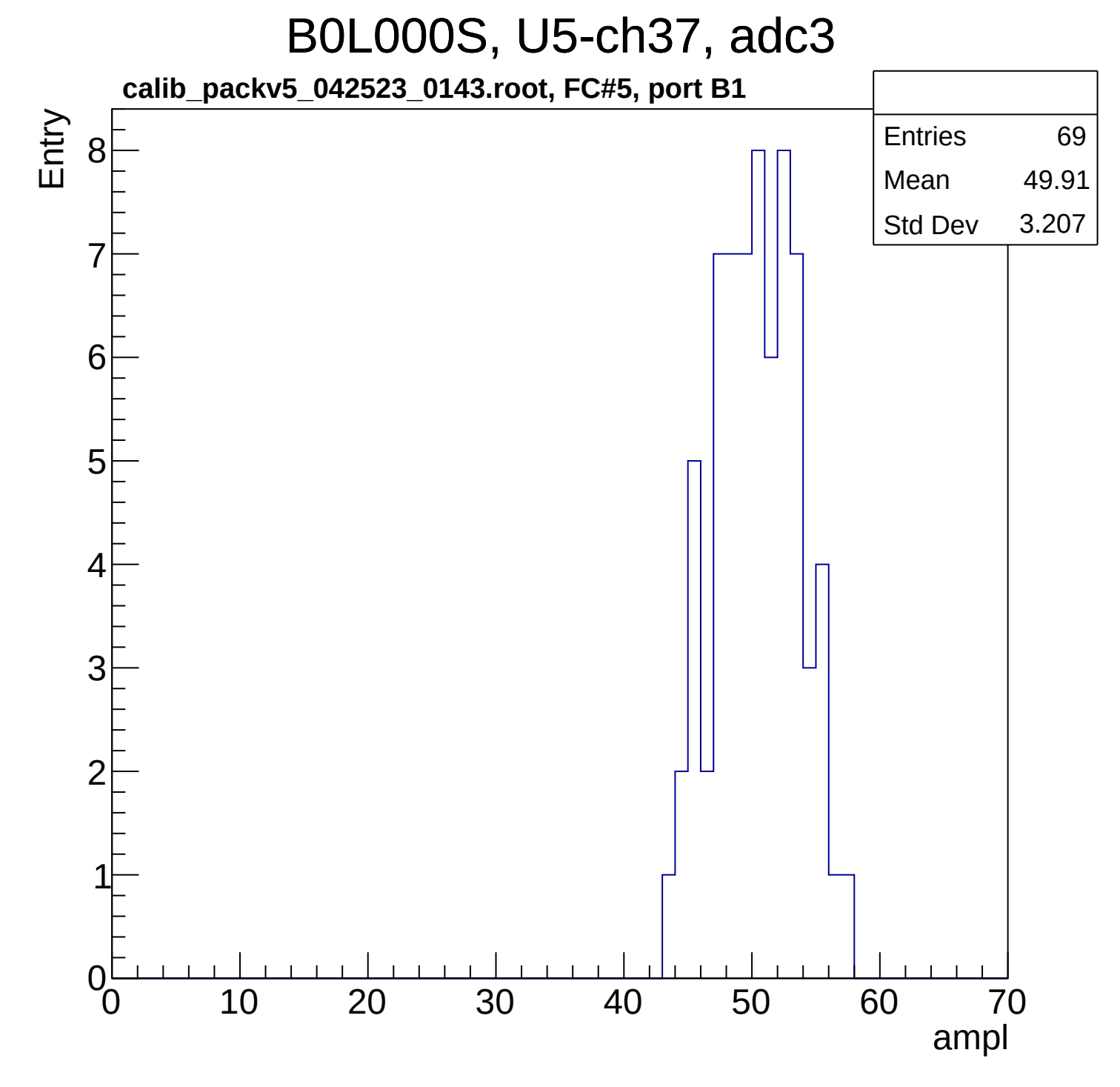
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch37, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.91
Std Dev 3.207
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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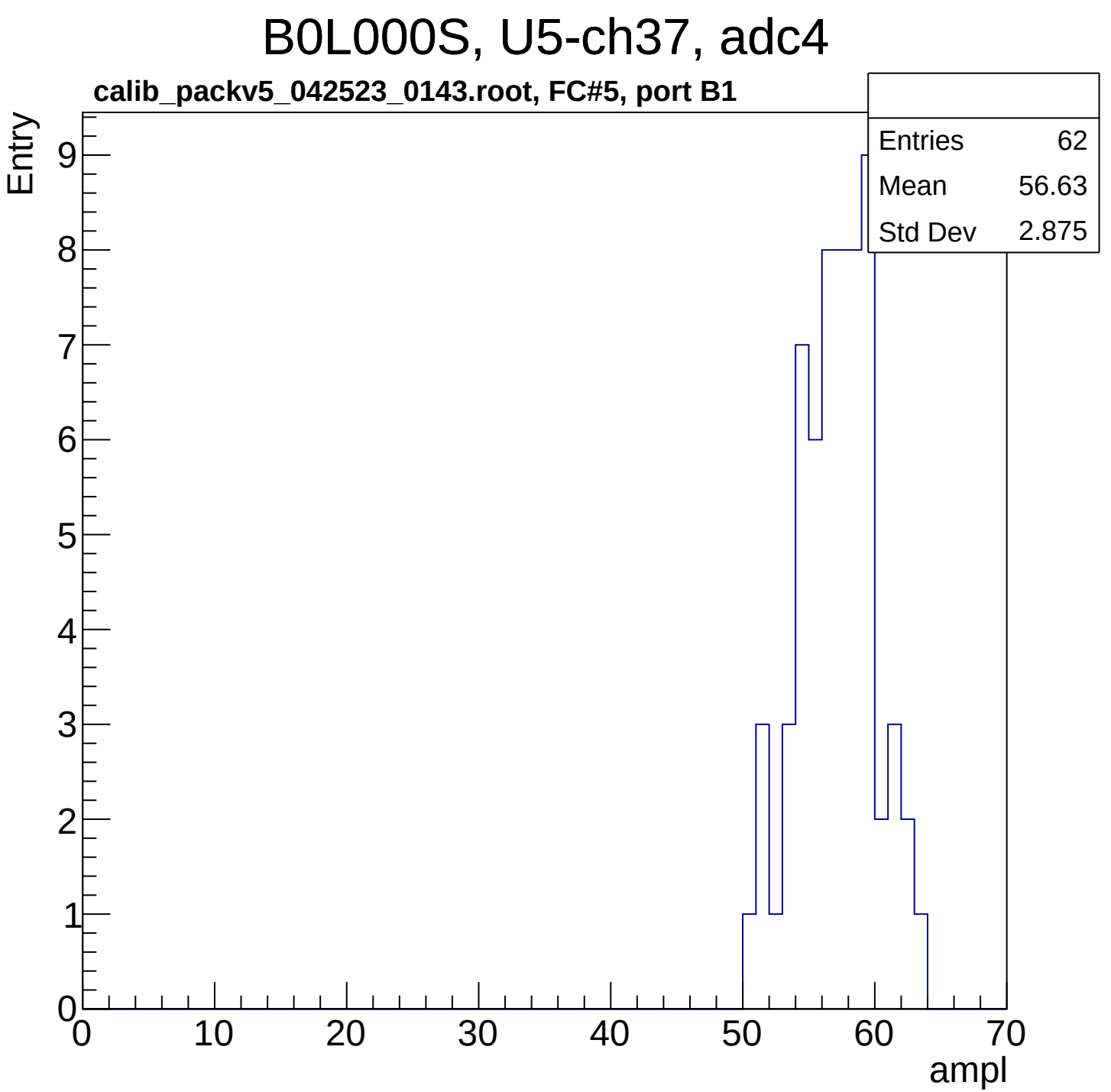
B0L000S, U5-ch37, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 56.63
Std Dev 2.875
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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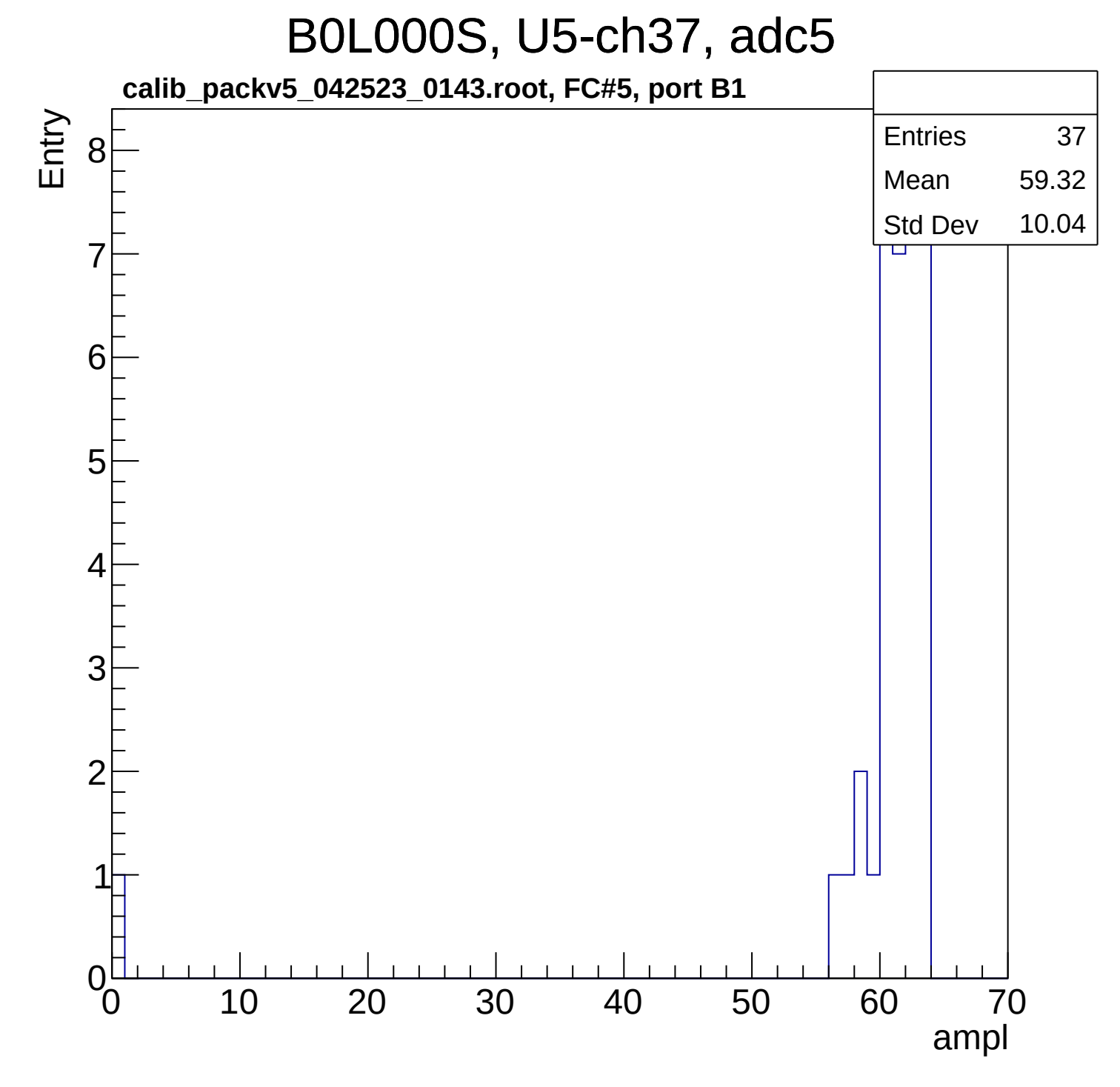
B0L000S, U5-ch37, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 59.32
Std Dev 10.04
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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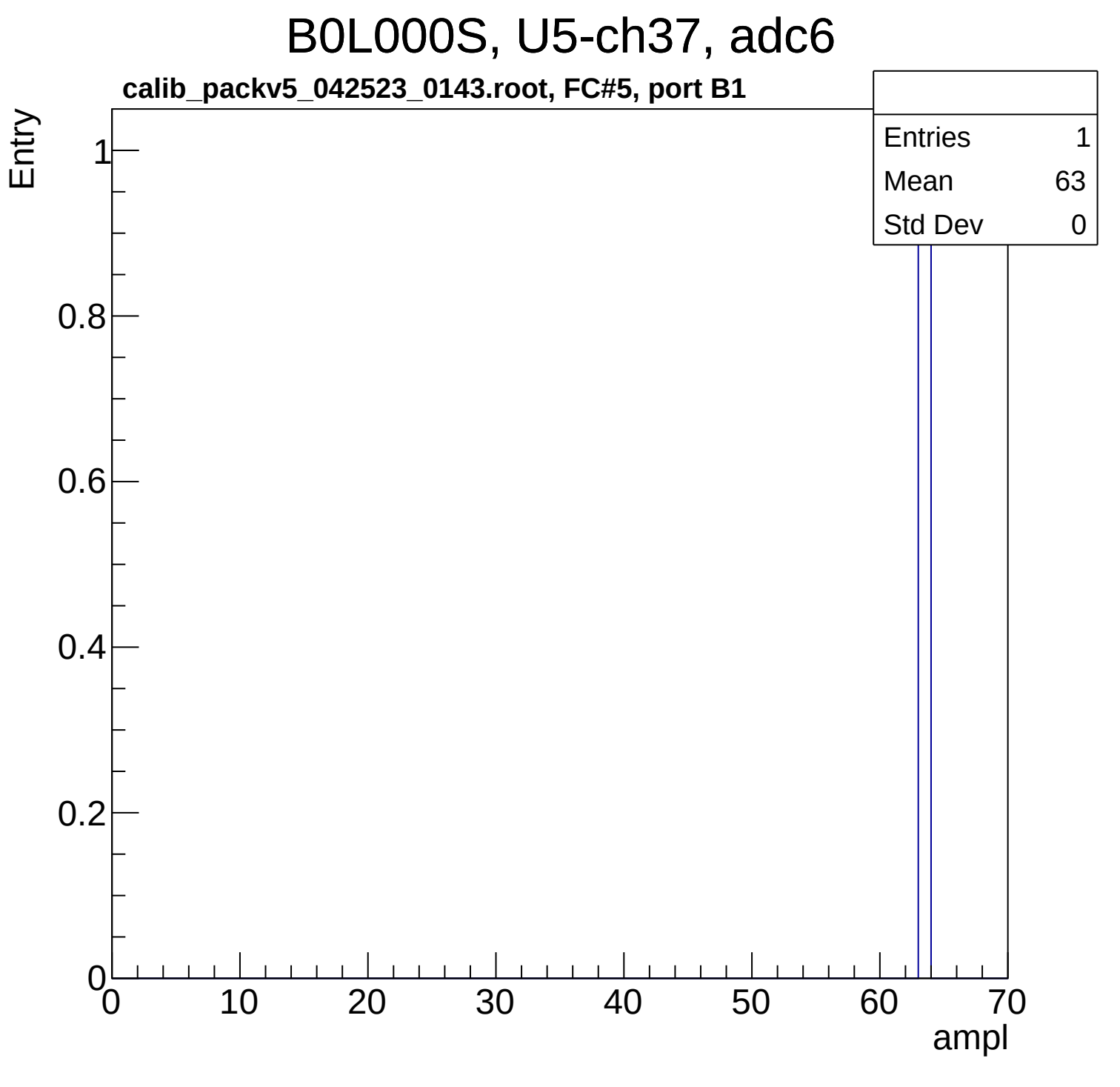
B0L000S, U5-ch37, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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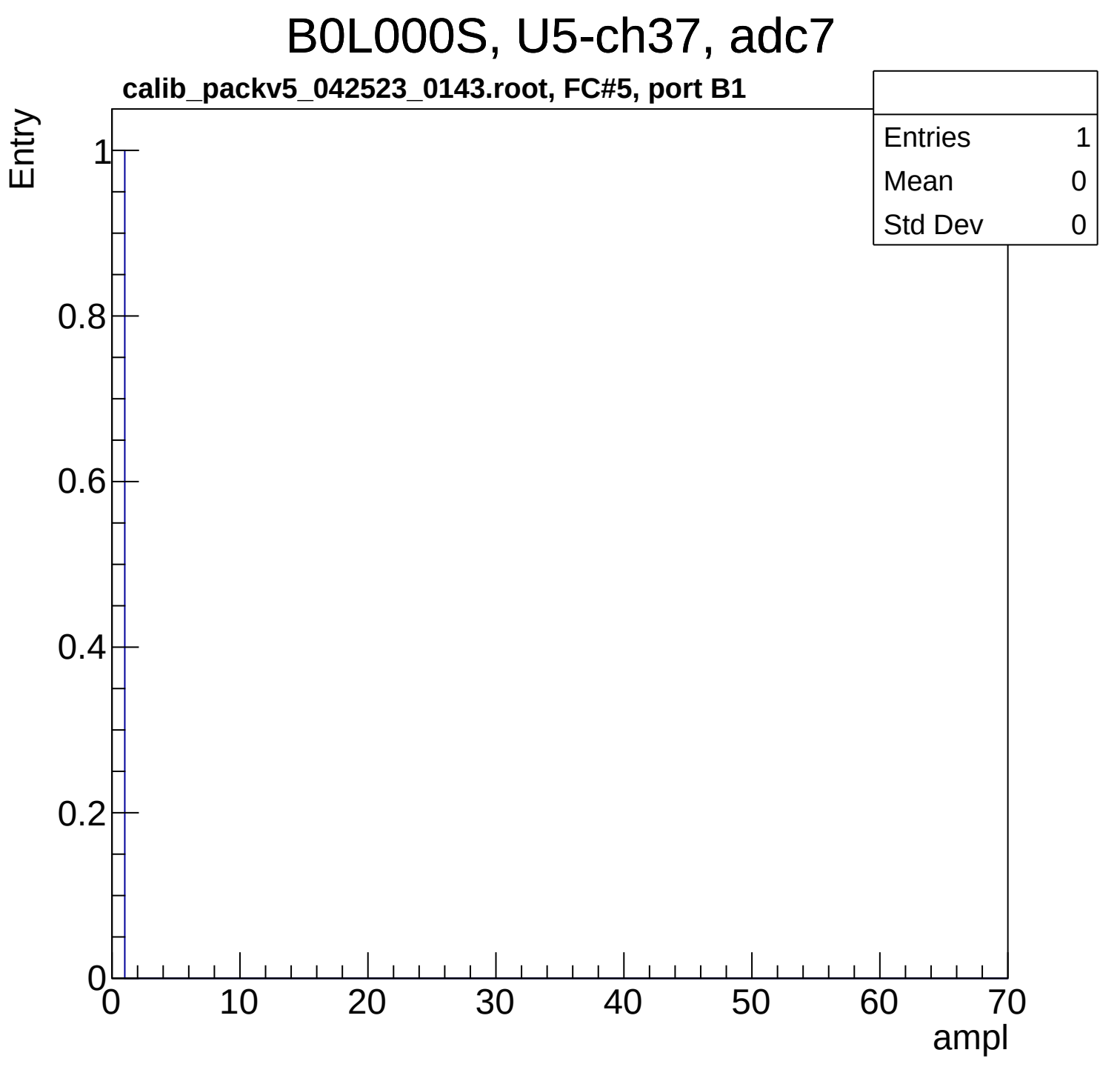
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch37, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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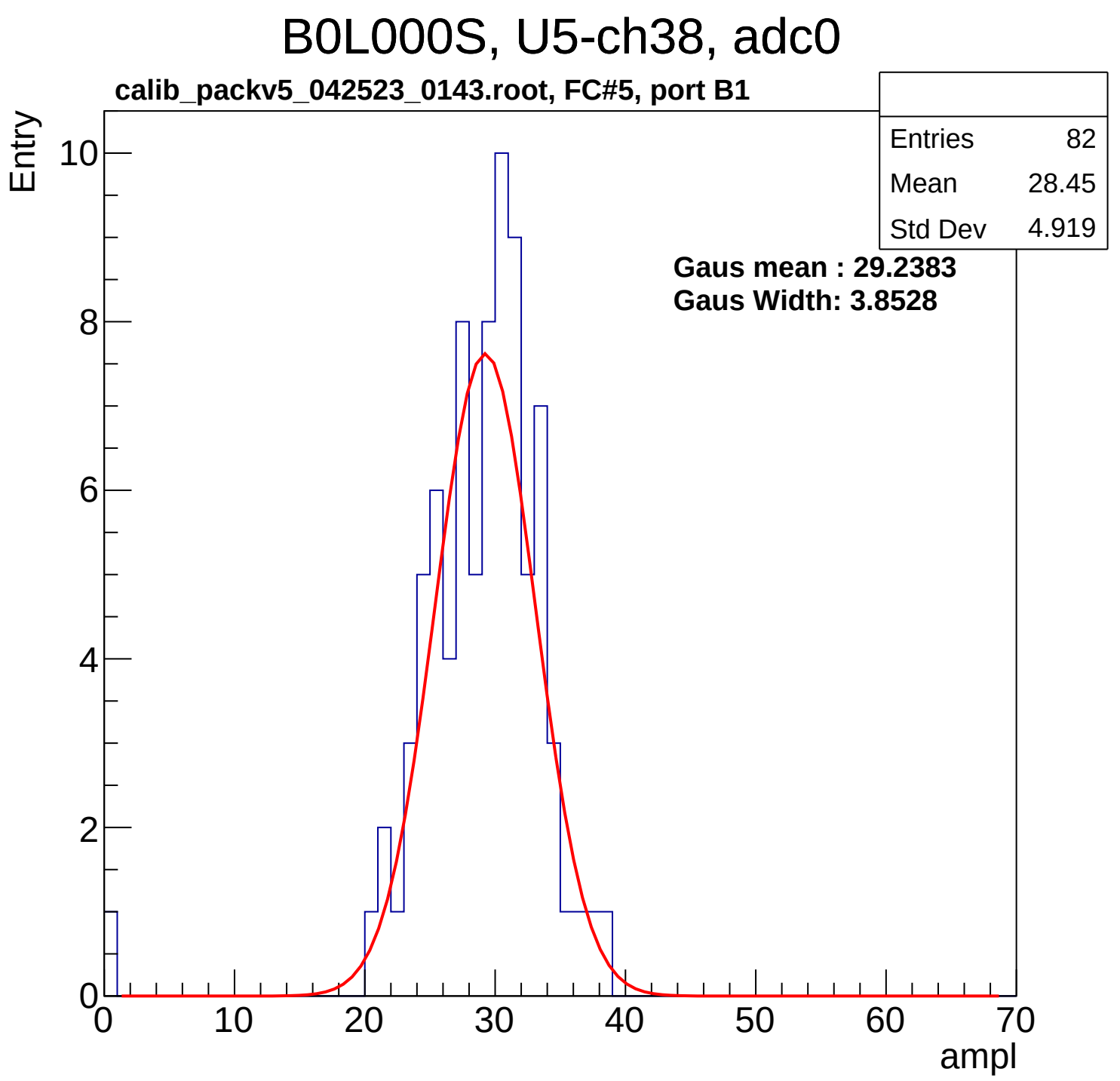
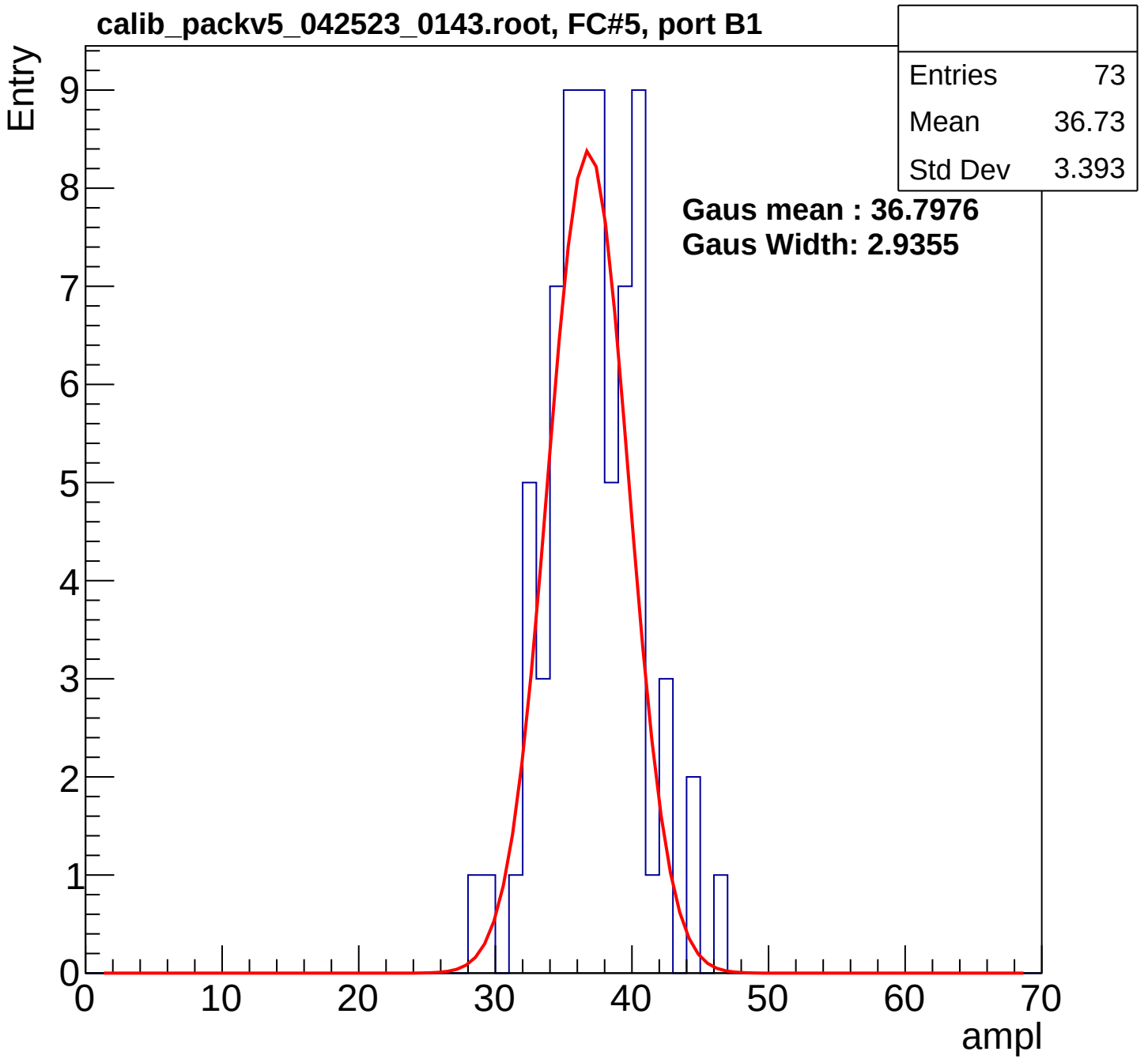
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch38, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 28.45
Std Dev 4.919
Gaus mean : 29.2383
Gaus Width: 3.8528
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch38, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.73
Std Dev 3.393
Gaus mean : 36.7976
Gaus Width: 2.9355
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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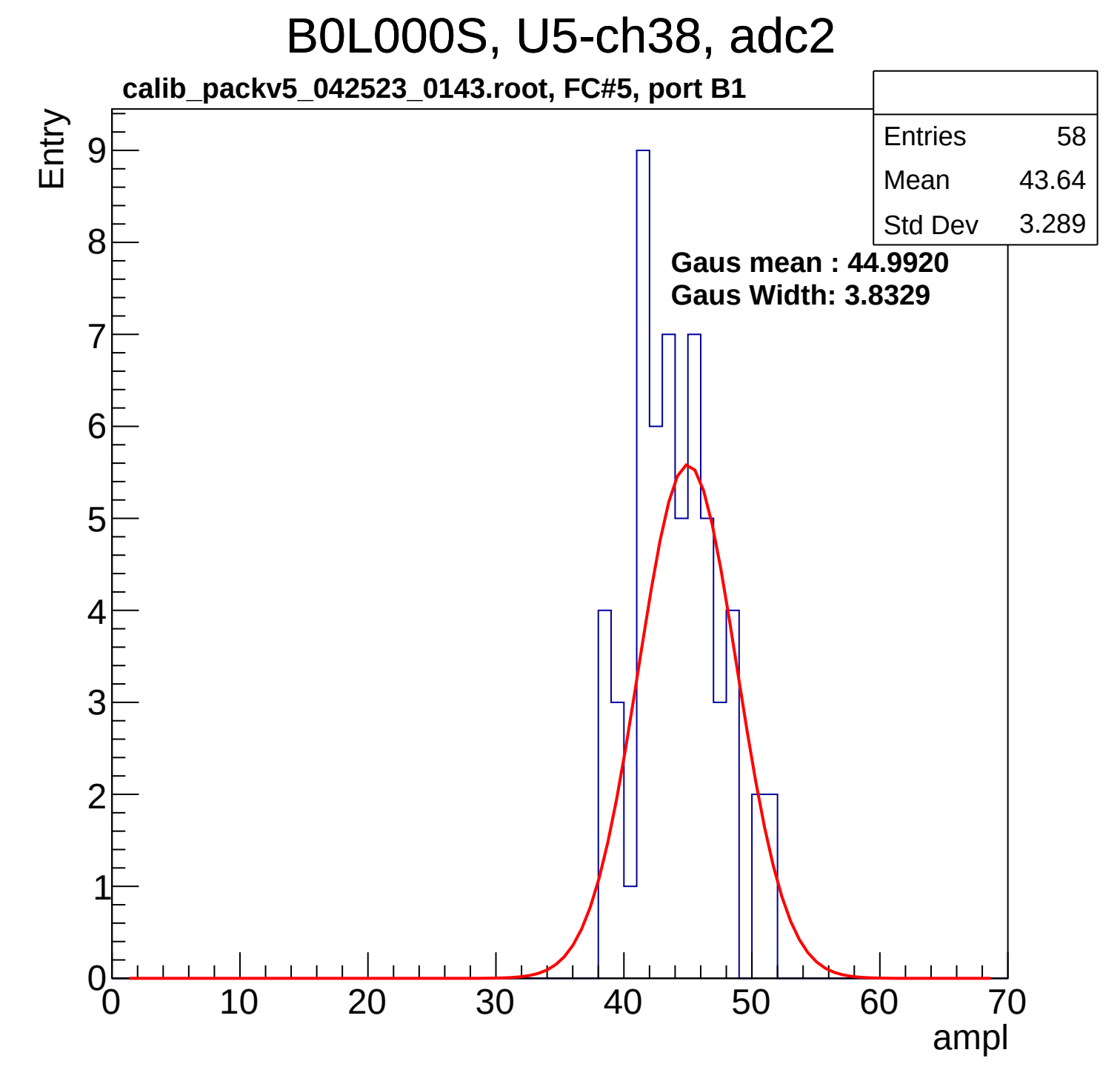
B0L000S, U5-ch38, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.64
Std Dev 3.289
Gaus mean : 44.9920
Gaus Width: 3.8329
Entry
ampl
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2
3
4
5
6
7
8
9
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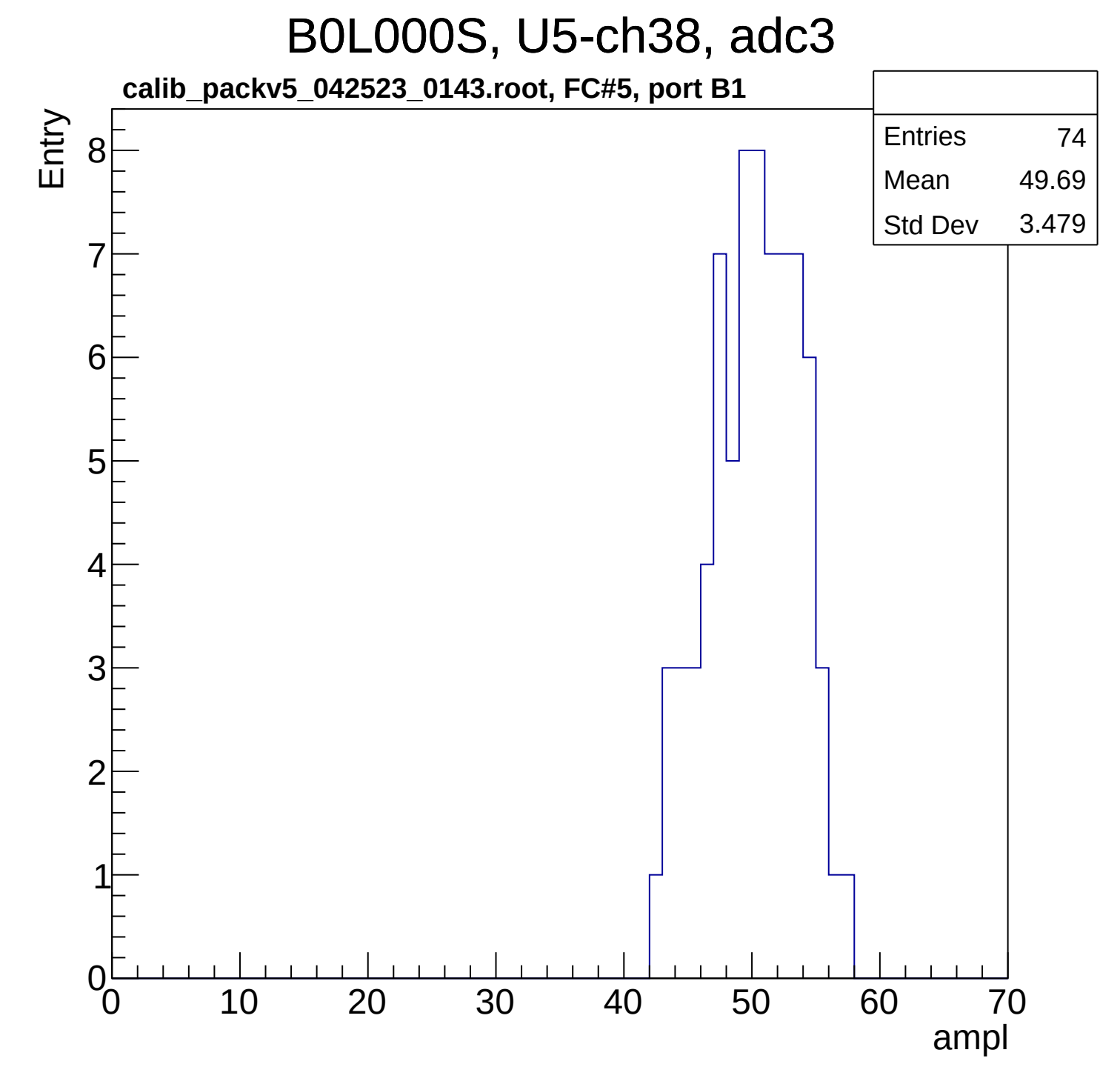
B0L000S, U5-ch38, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.69
Std Dev 3.479
Entry
ampl
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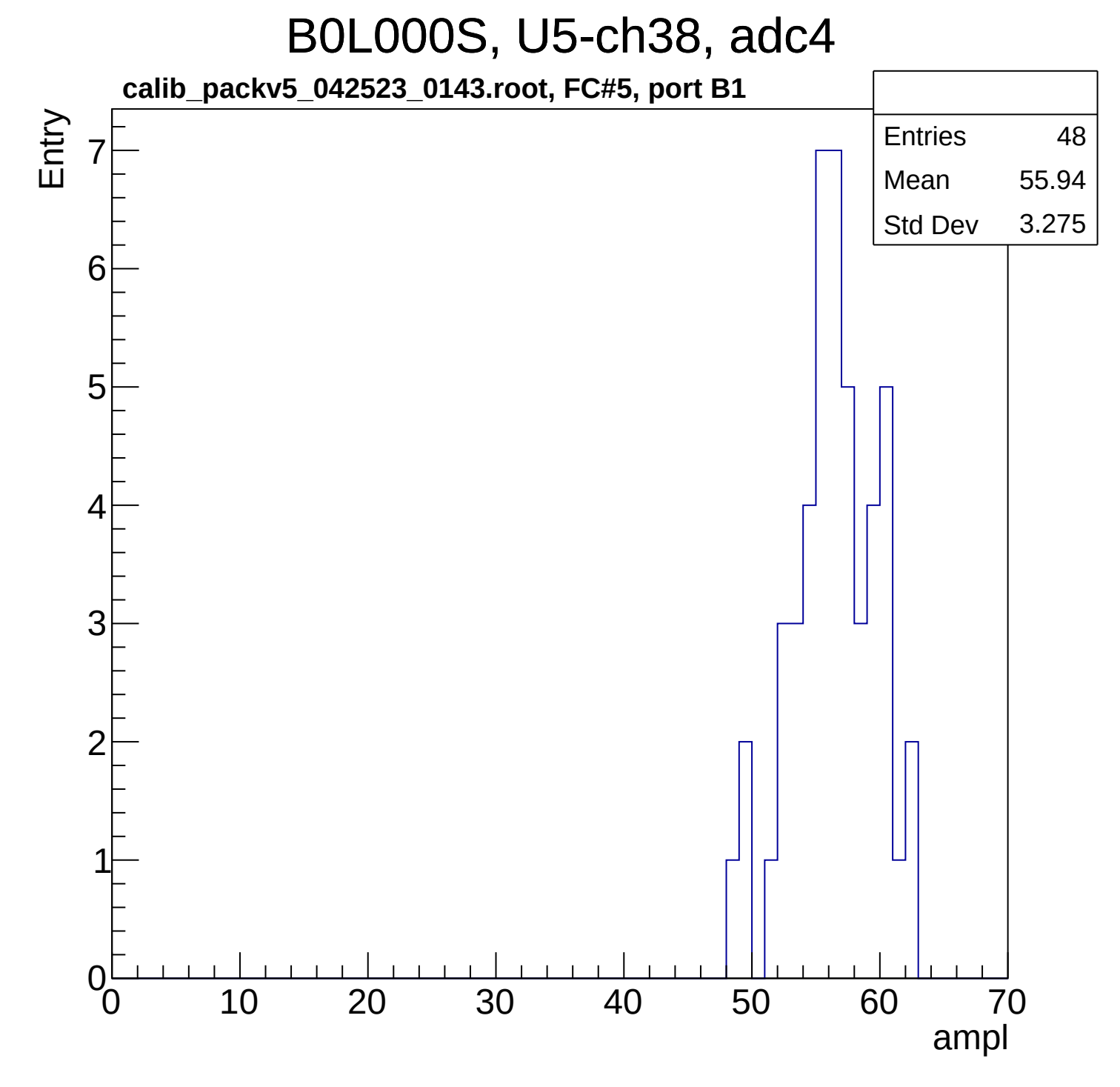
B0L000S, U5-ch38, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.94
Std Dev 3.275
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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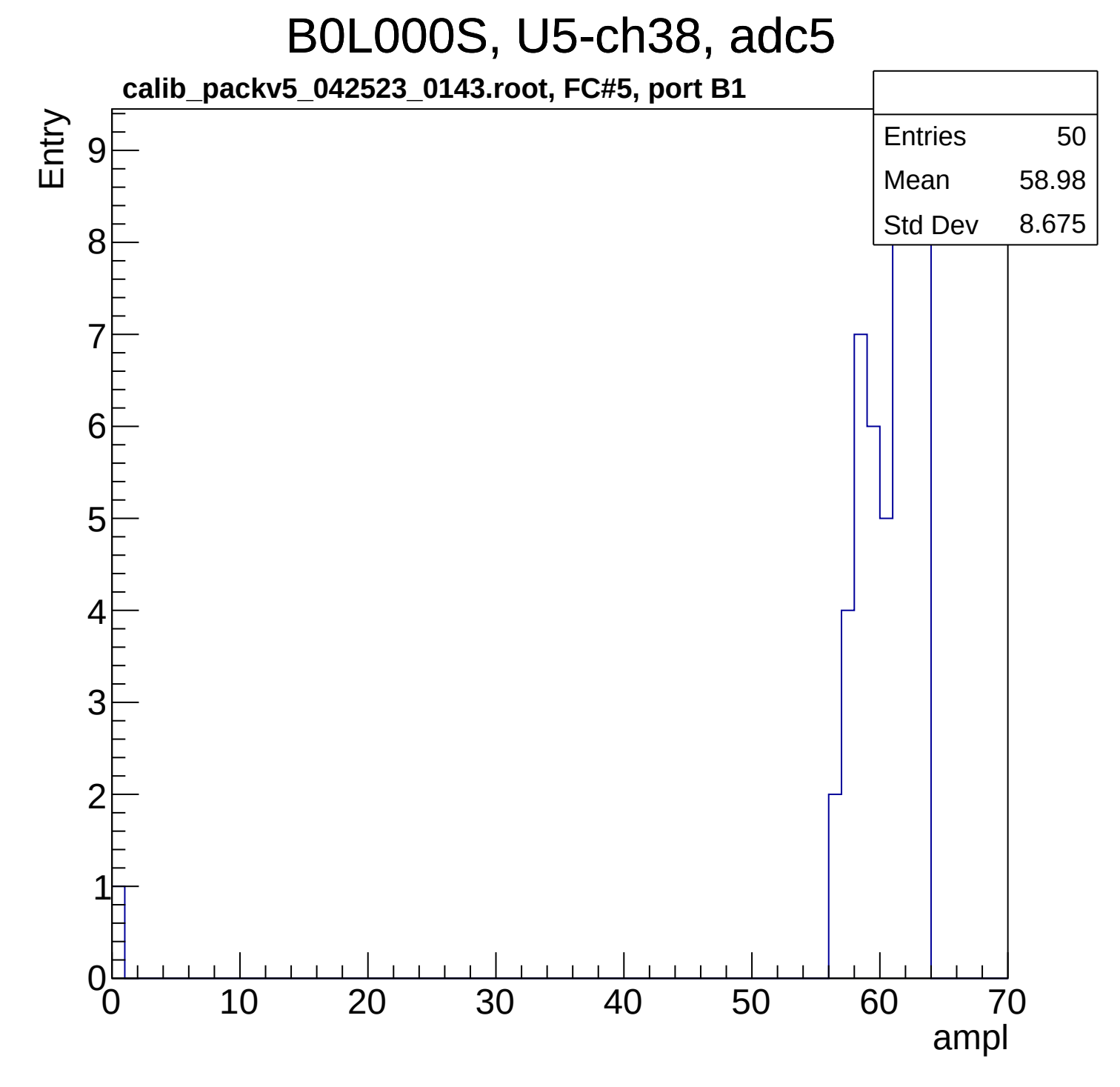
B0L000S, U5-ch38, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.98
Std Dev 8.675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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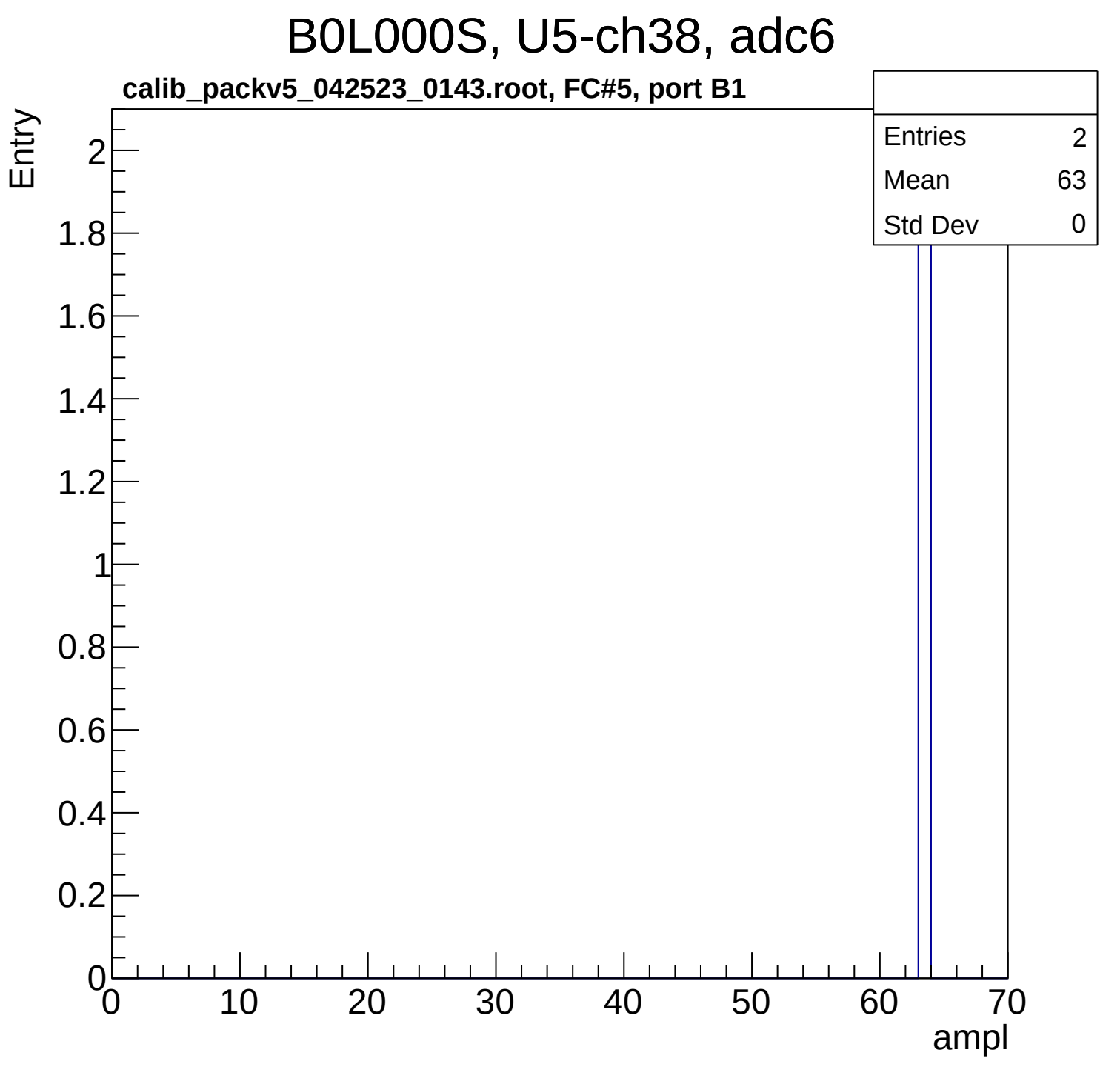
B0L000S, U5-ch38, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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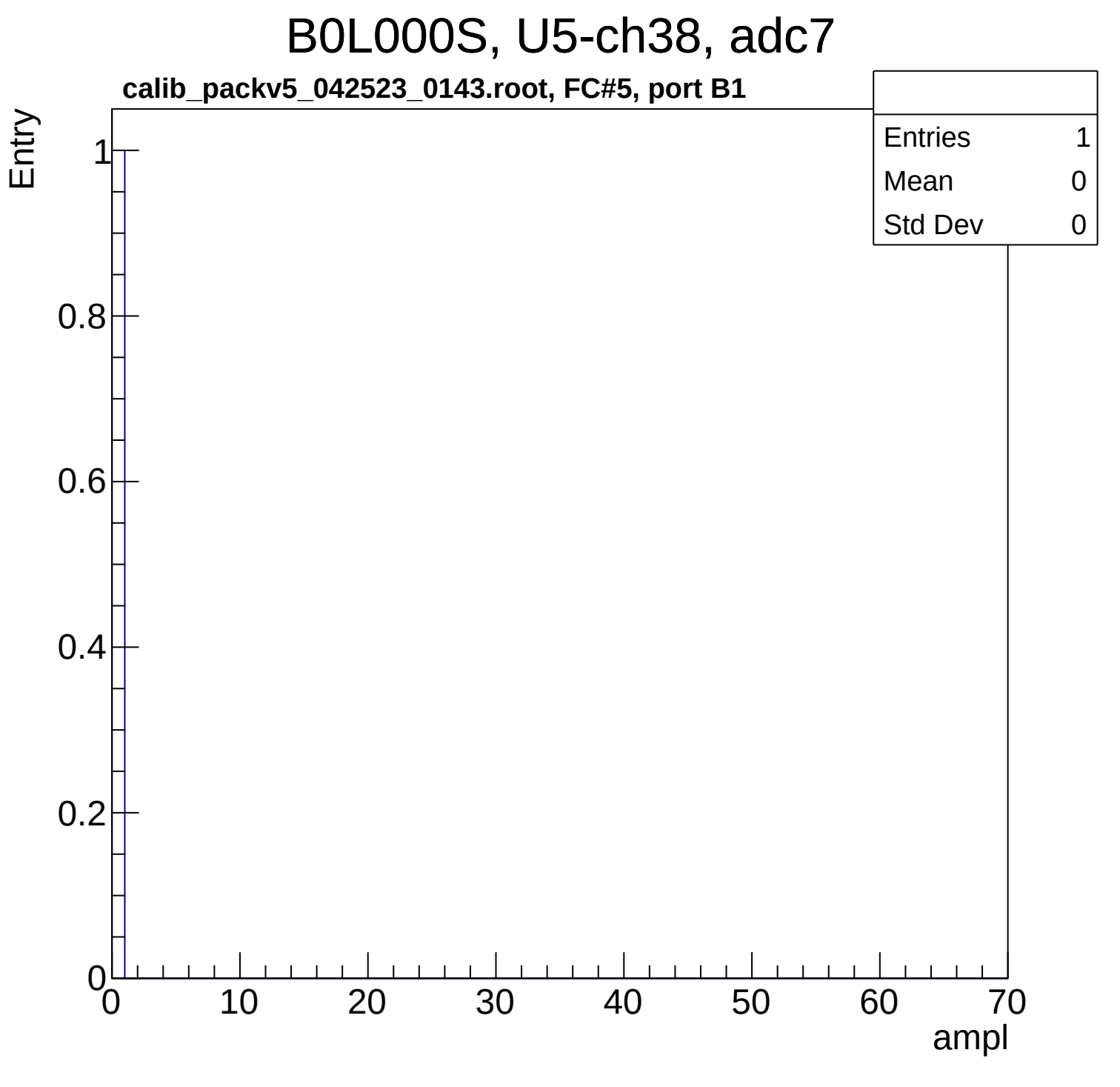
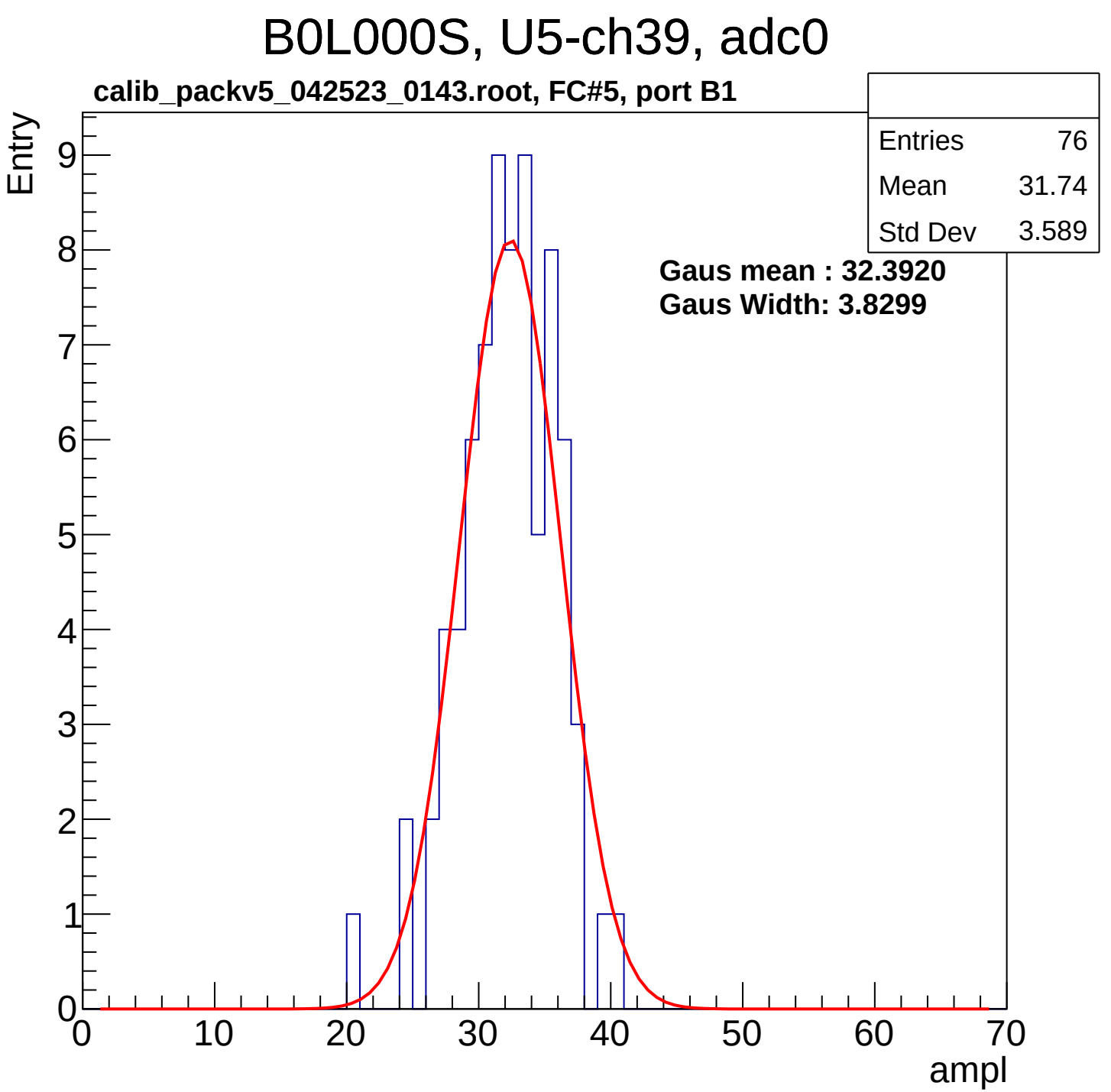

B0L000S, U5-ch38, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch39, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 31.74
Std Dev 3.589
Gaus mean : 32.3920
Gaus Width: 3.8299
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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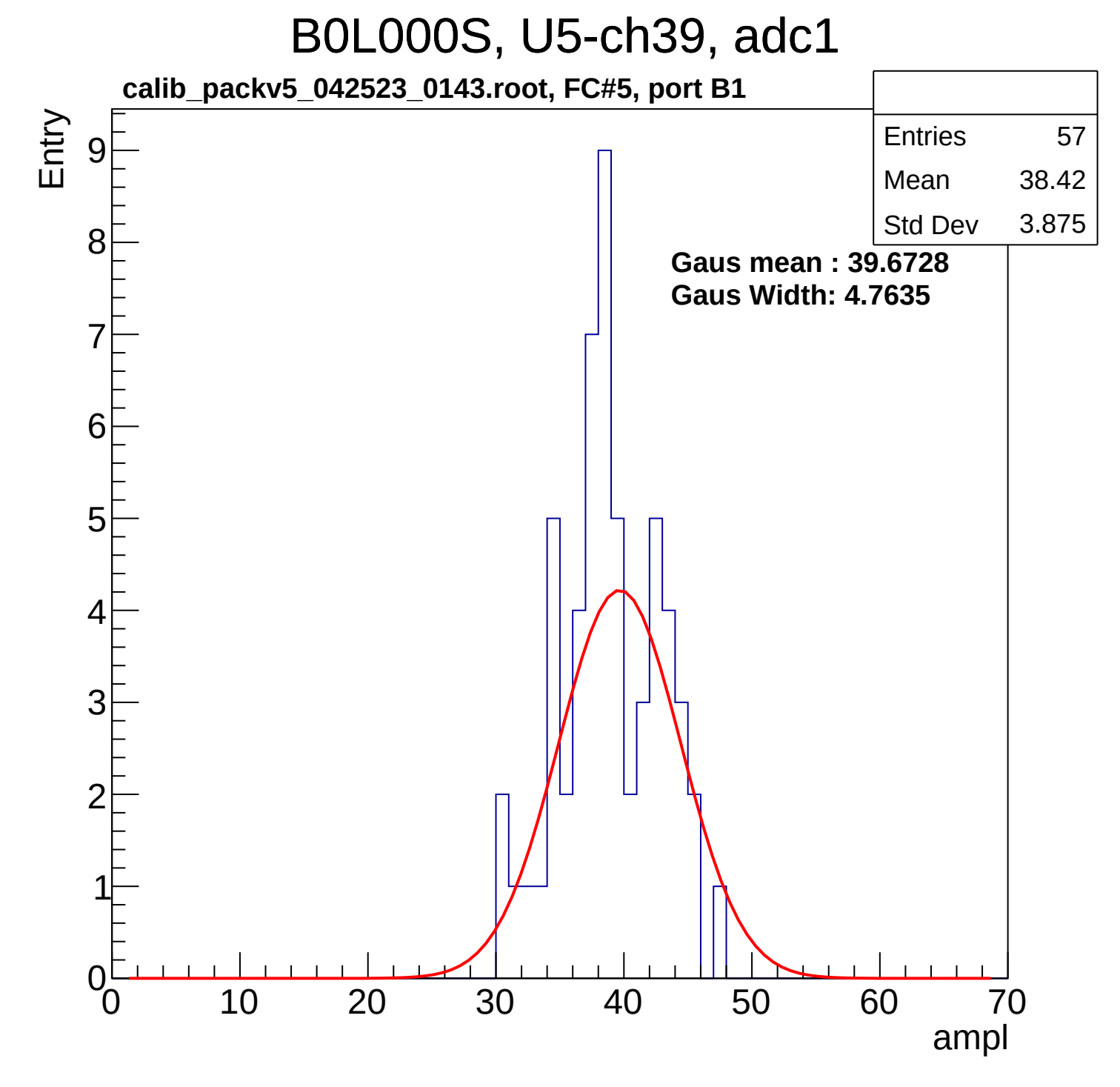
B0L000S, U5-ch39, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 38.42
Std Dev 3.875
Gaus mean : 39.6728
Gaus Width: 4.7635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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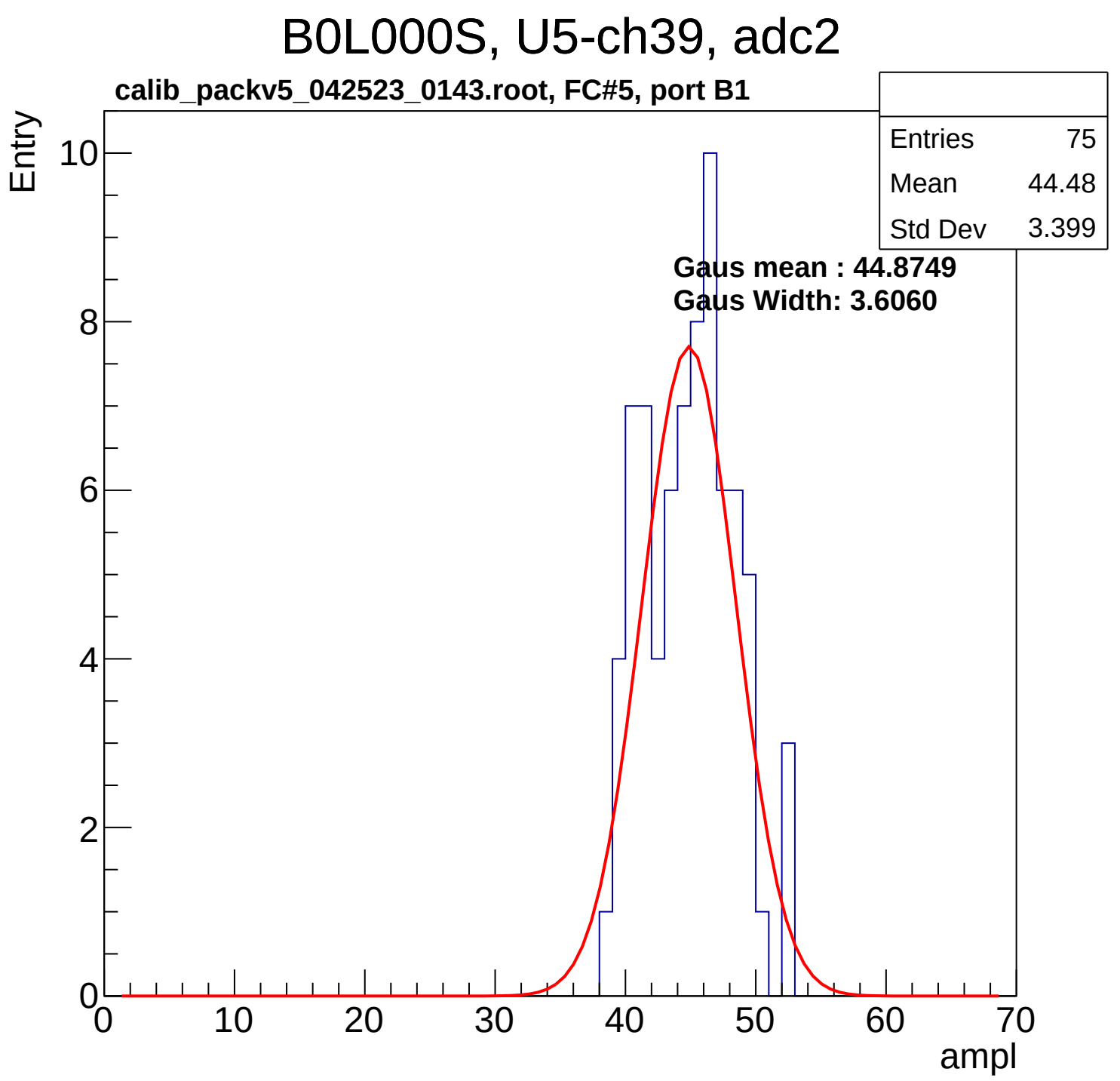
B0L000S, U5-ch39, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 44.48
Std Dev 3.399
Gaus mean : 44.8749
Gaus Width: 3.6060
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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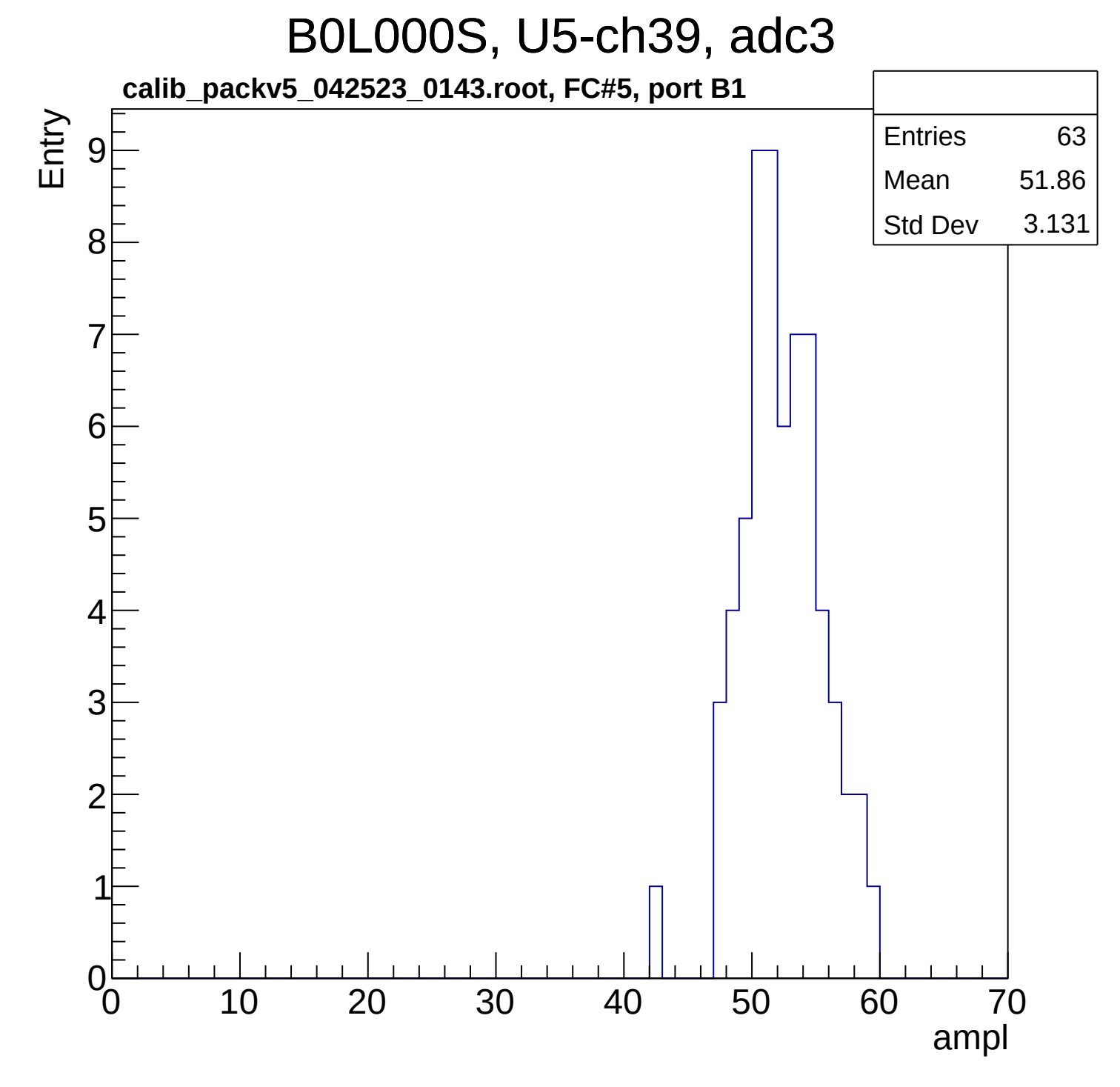
B0L000S, U5-ch39, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 51.86
Std Dev 3.131
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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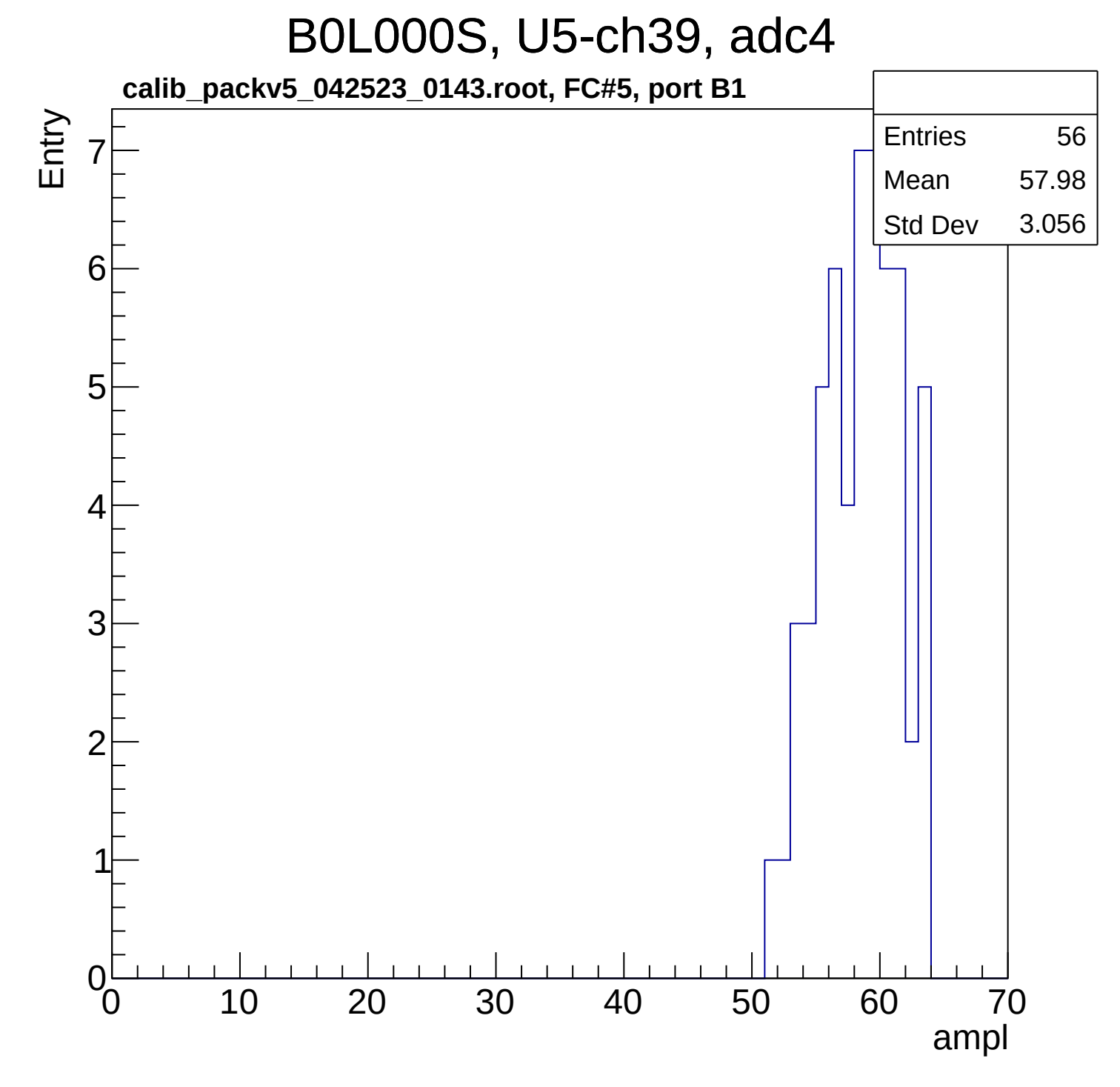
B0L000S, U5-ch39, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 57.98
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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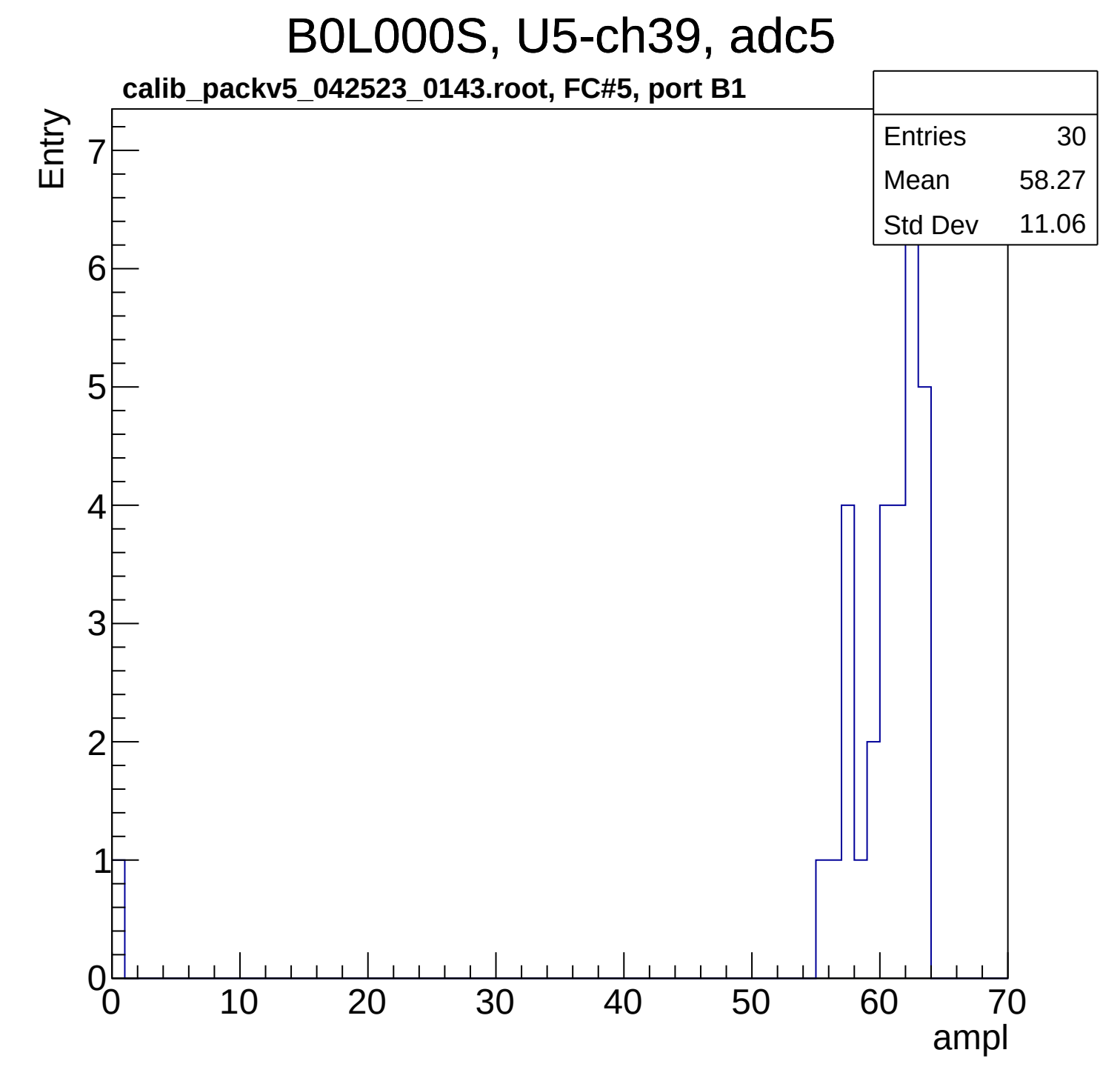
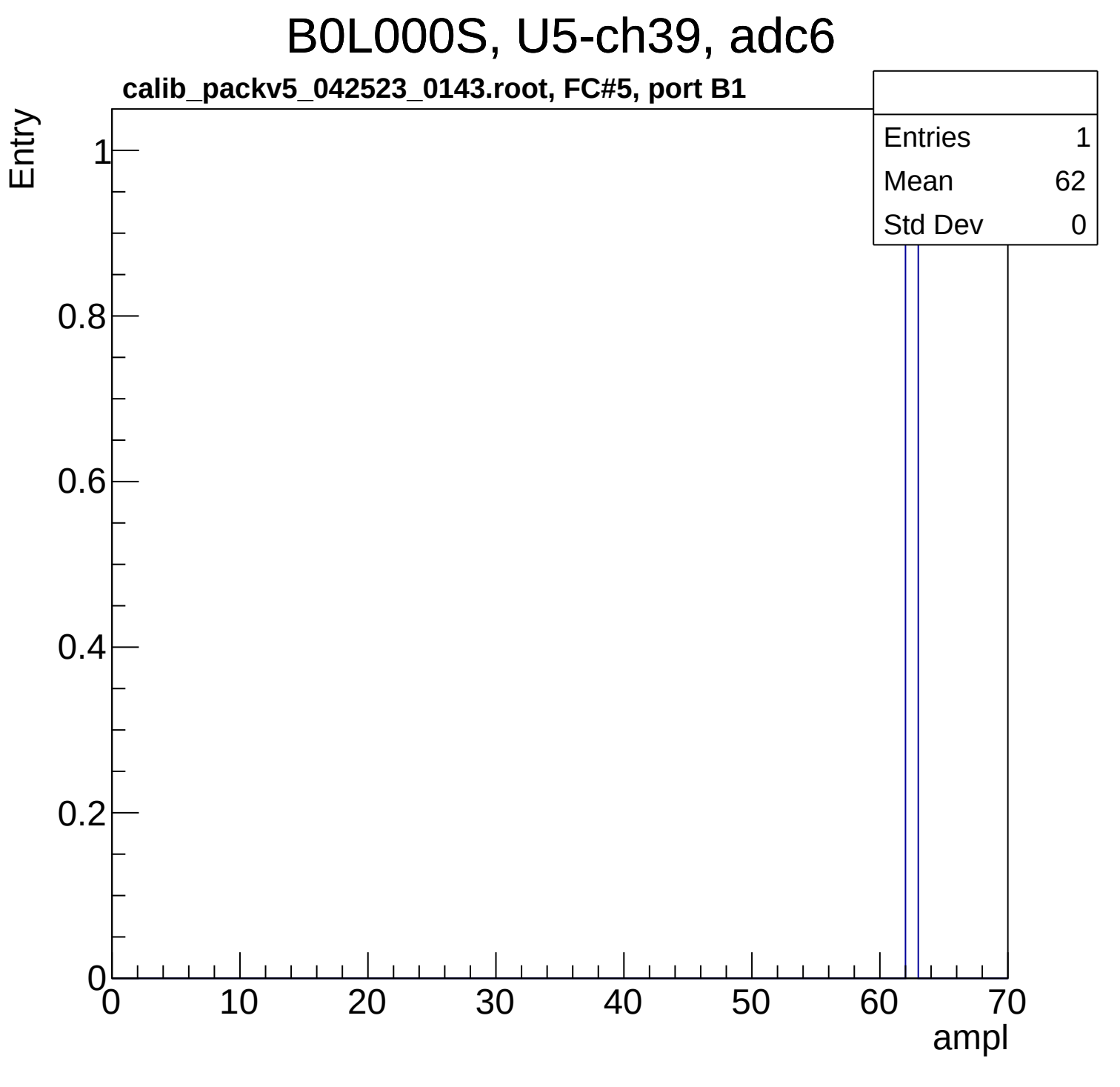
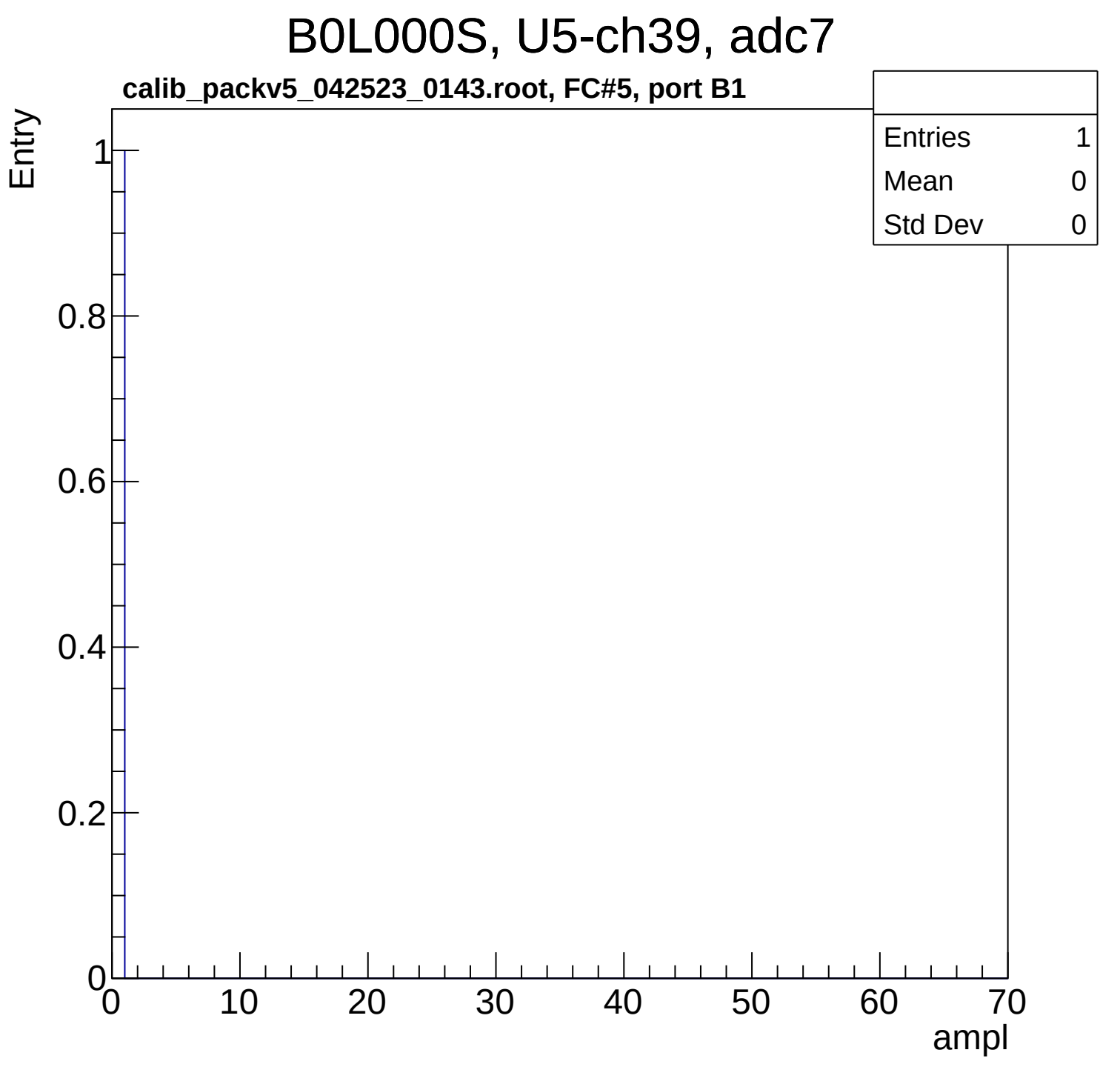
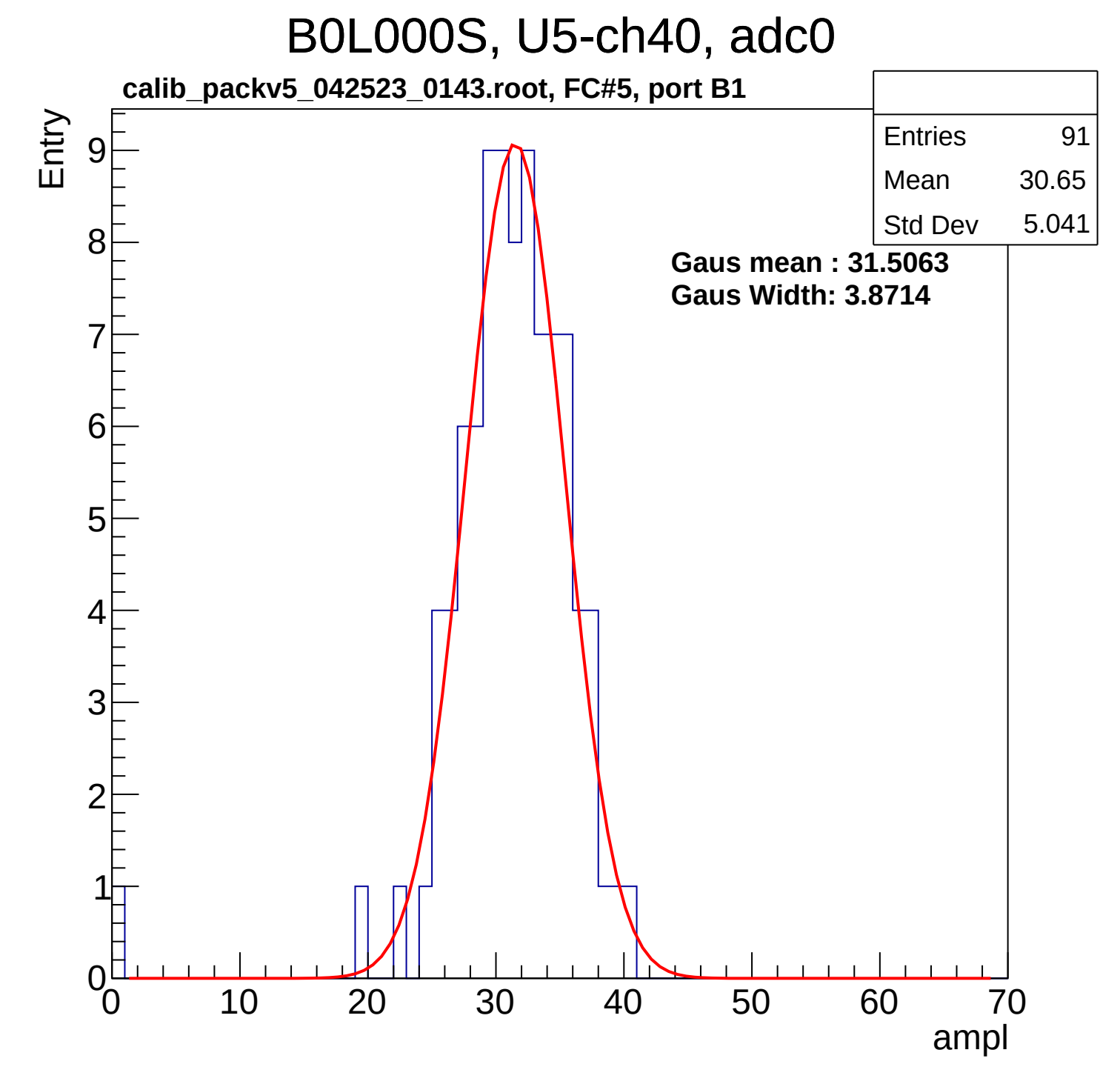
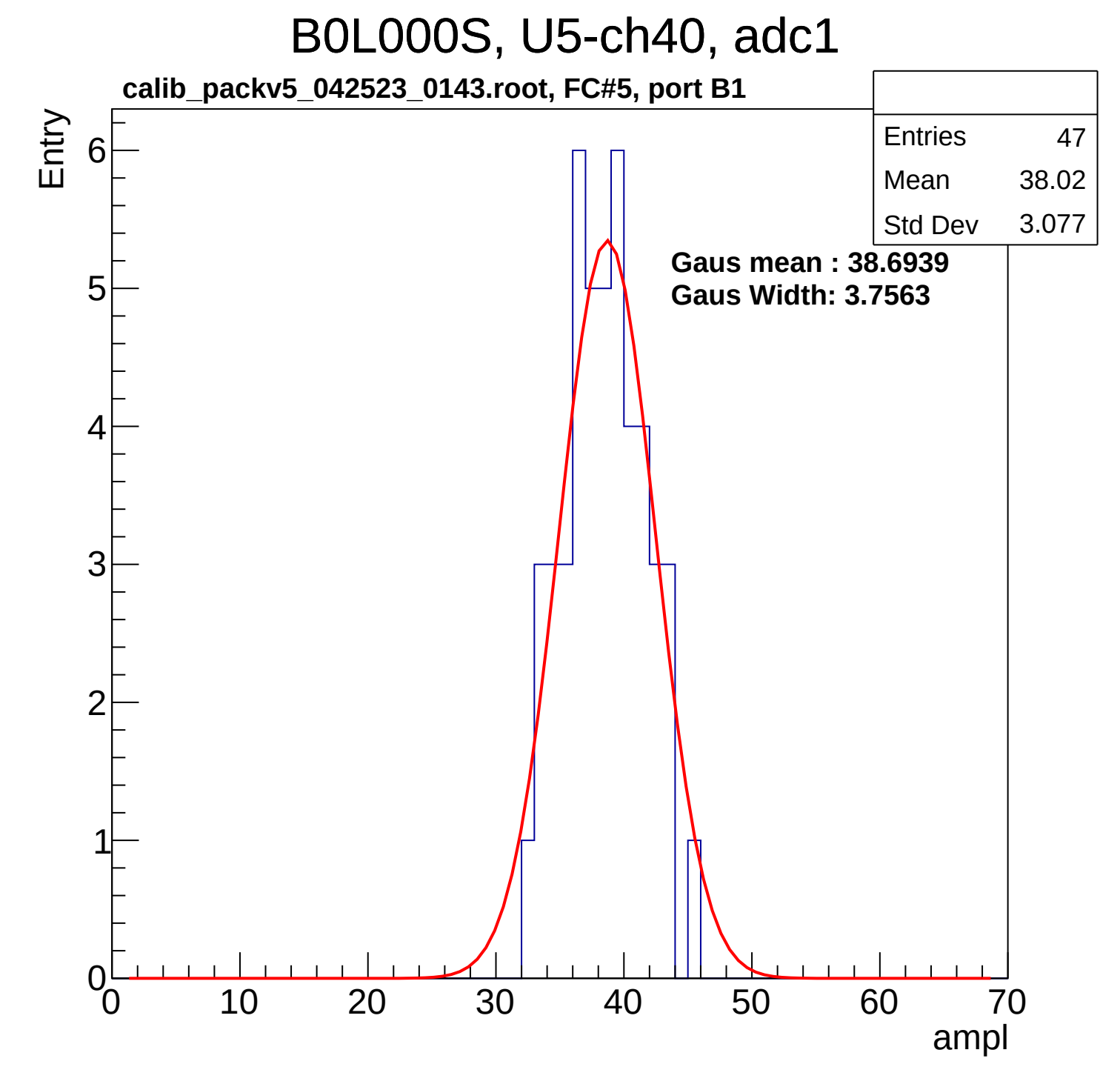
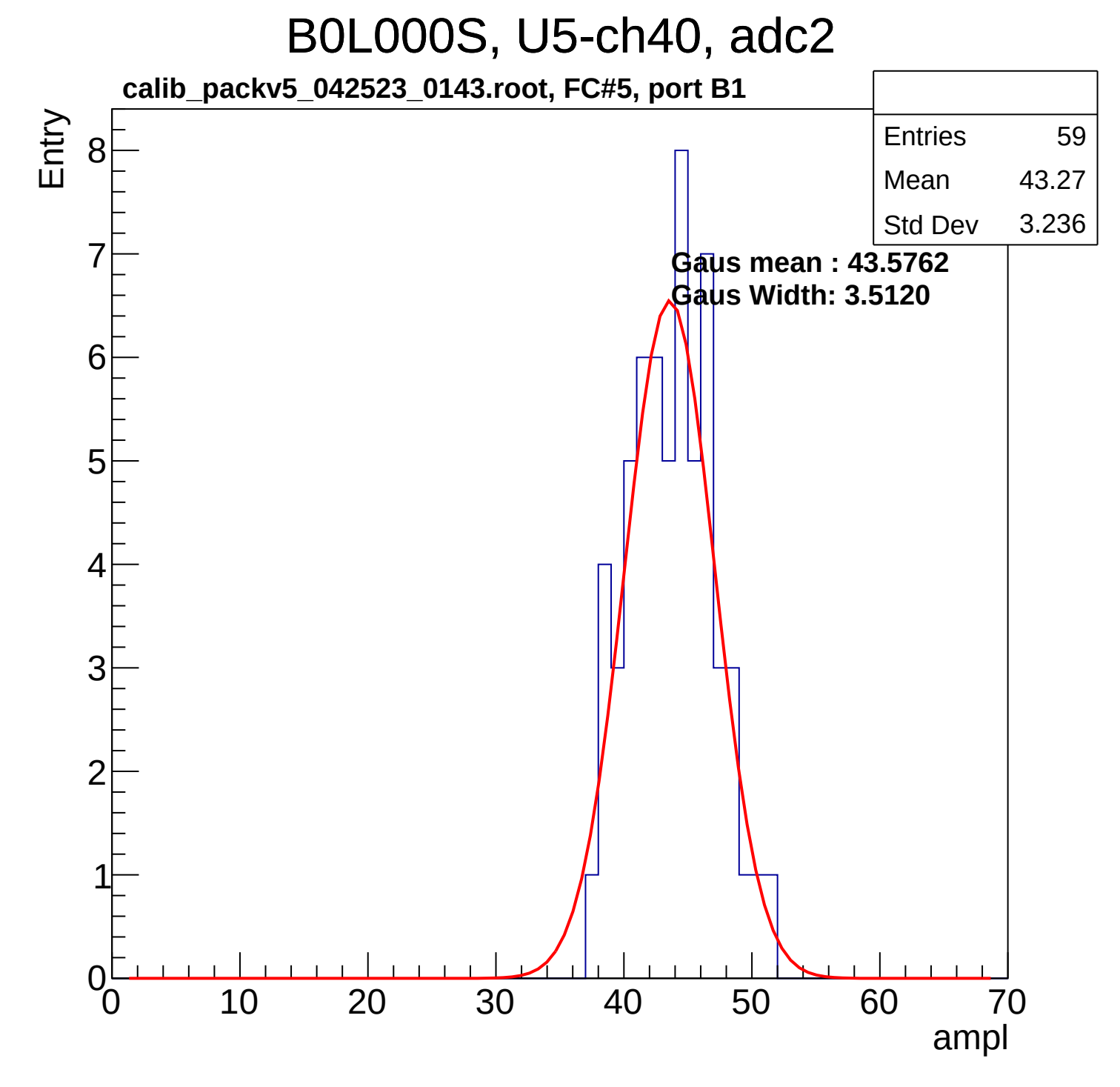

B0L000S, U5-ch39, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.27
Std Dev 11.06
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

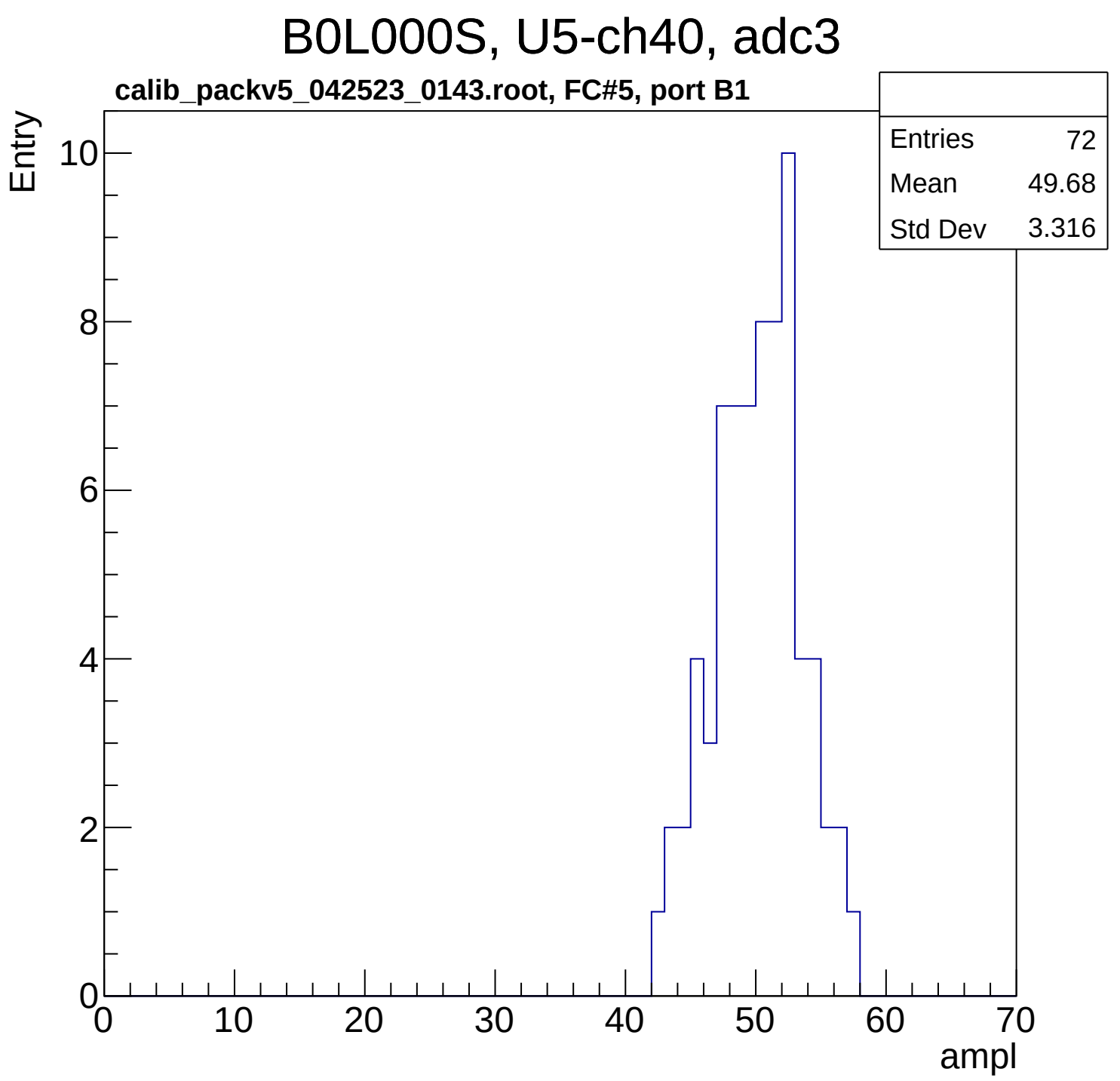

B0L000S, U5-ch39, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

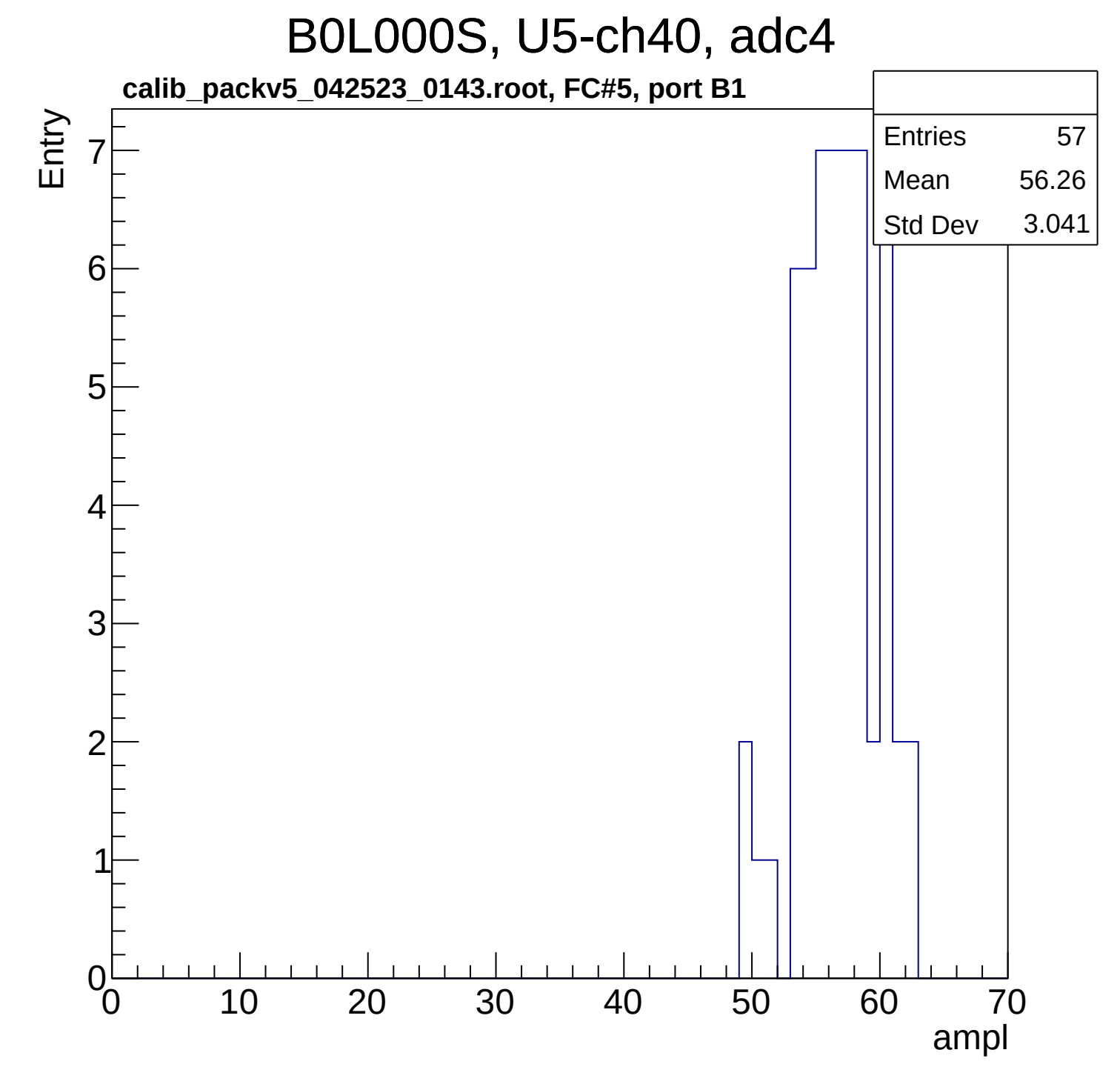

B0L000S, U5-ch39, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

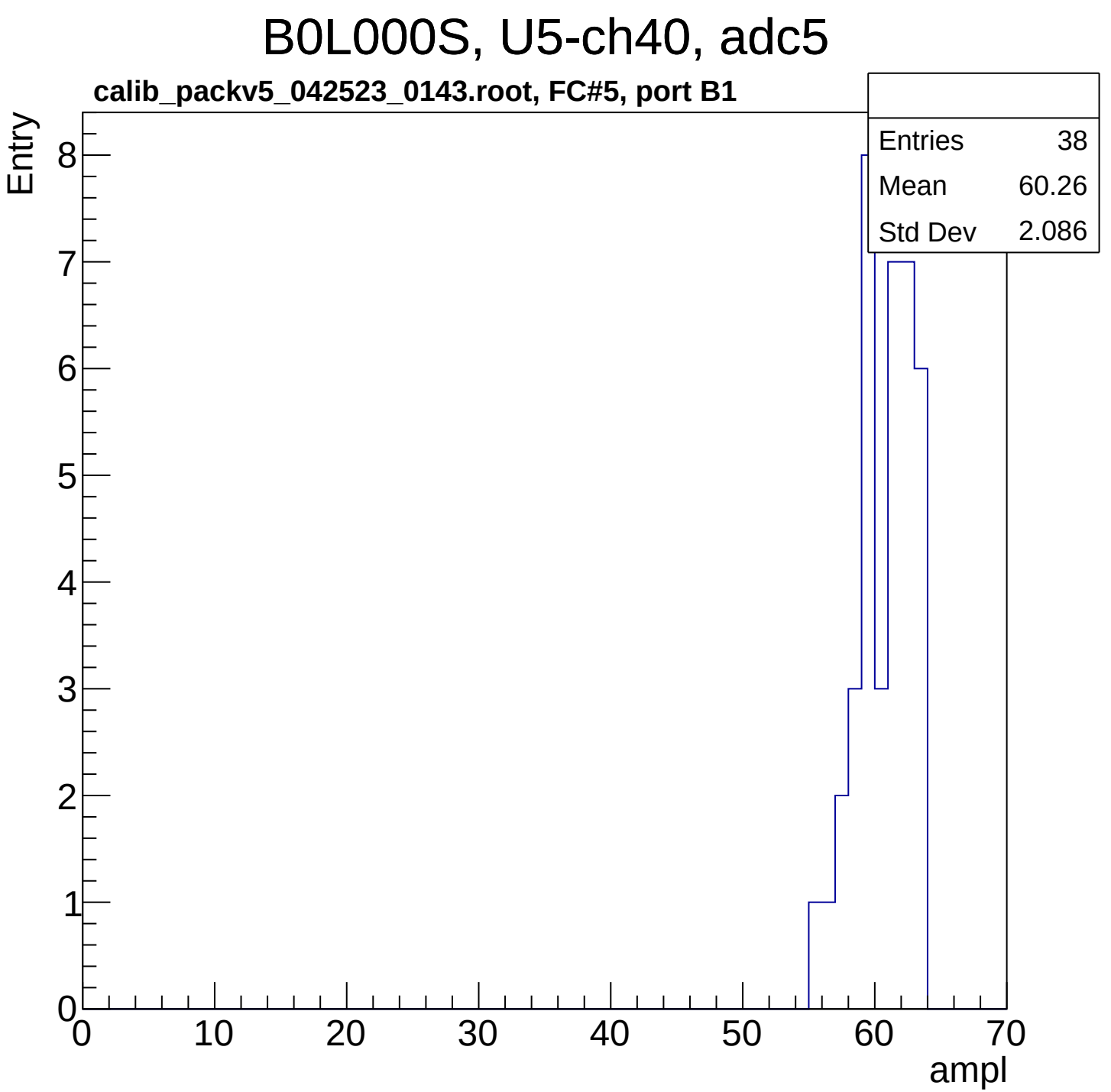

B0L000S, U5-ch40, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 91
Mean 30.65
Std Dev 5.041
Gaus mean : 31.5063
Gaus Width: 3.8714
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch40, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 38.02
Std Dev 3.077
Gaus mean : 38.6939
Gaus Width: 3.7563
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch40, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.27
Std Dev 3.236
Gaus mean : 43.5762
Gaus Width: 3.5120
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch40, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.68
Std Dev 3.316
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch40, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 56.26
Std Dev 3.041
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch40, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 60.26
Std Dev 2.086
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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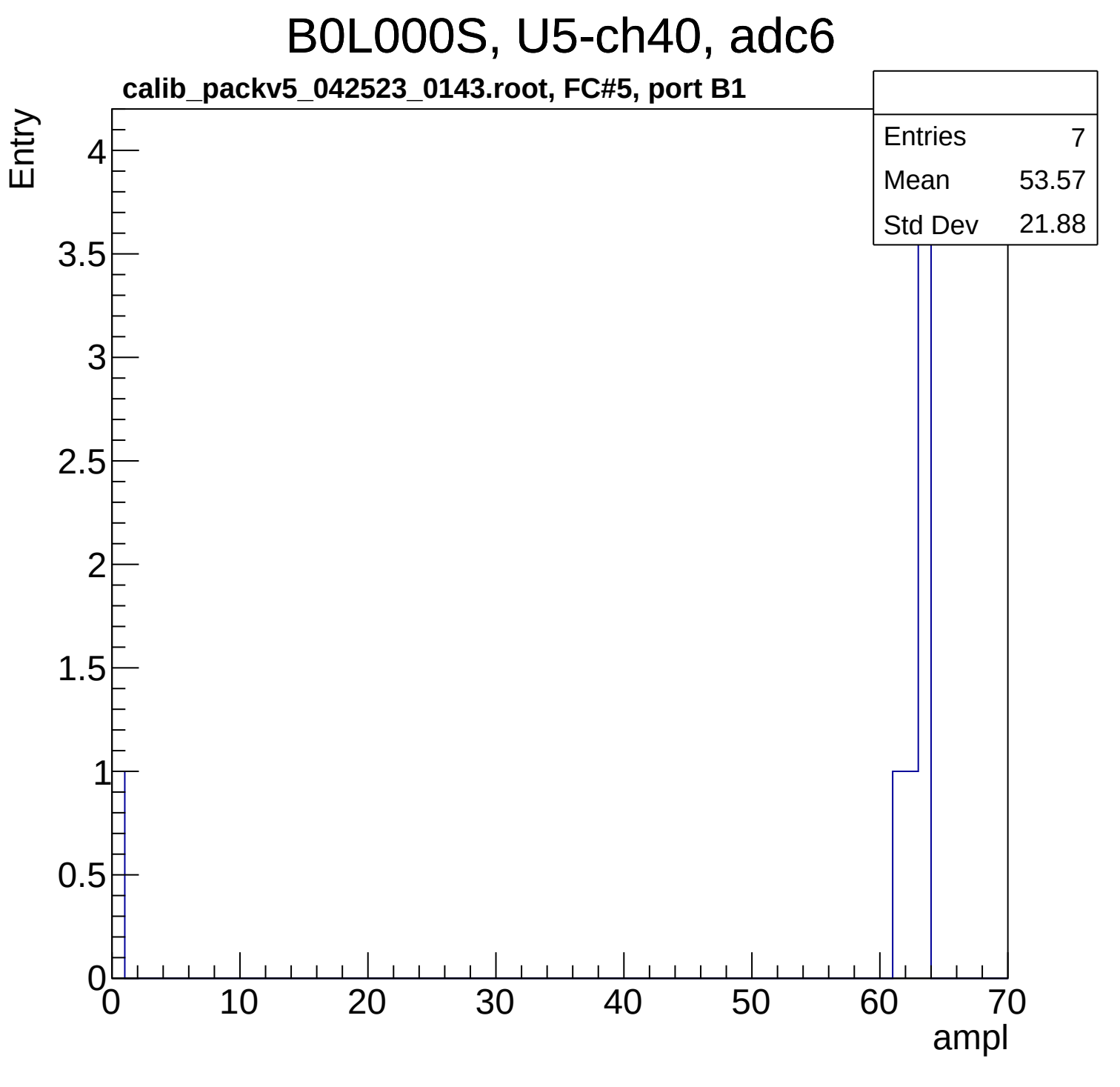
B0L000S, U5-ch40, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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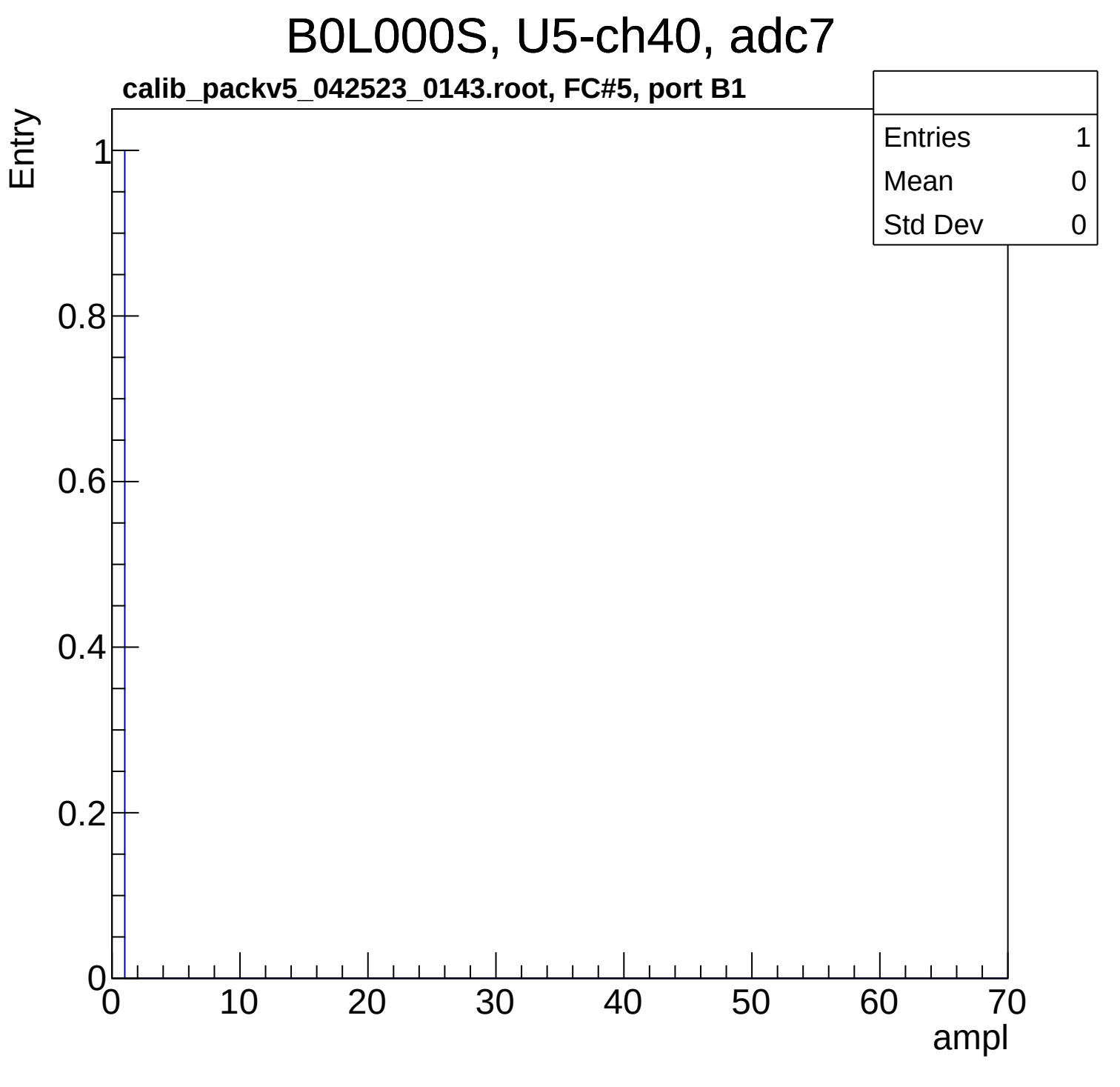
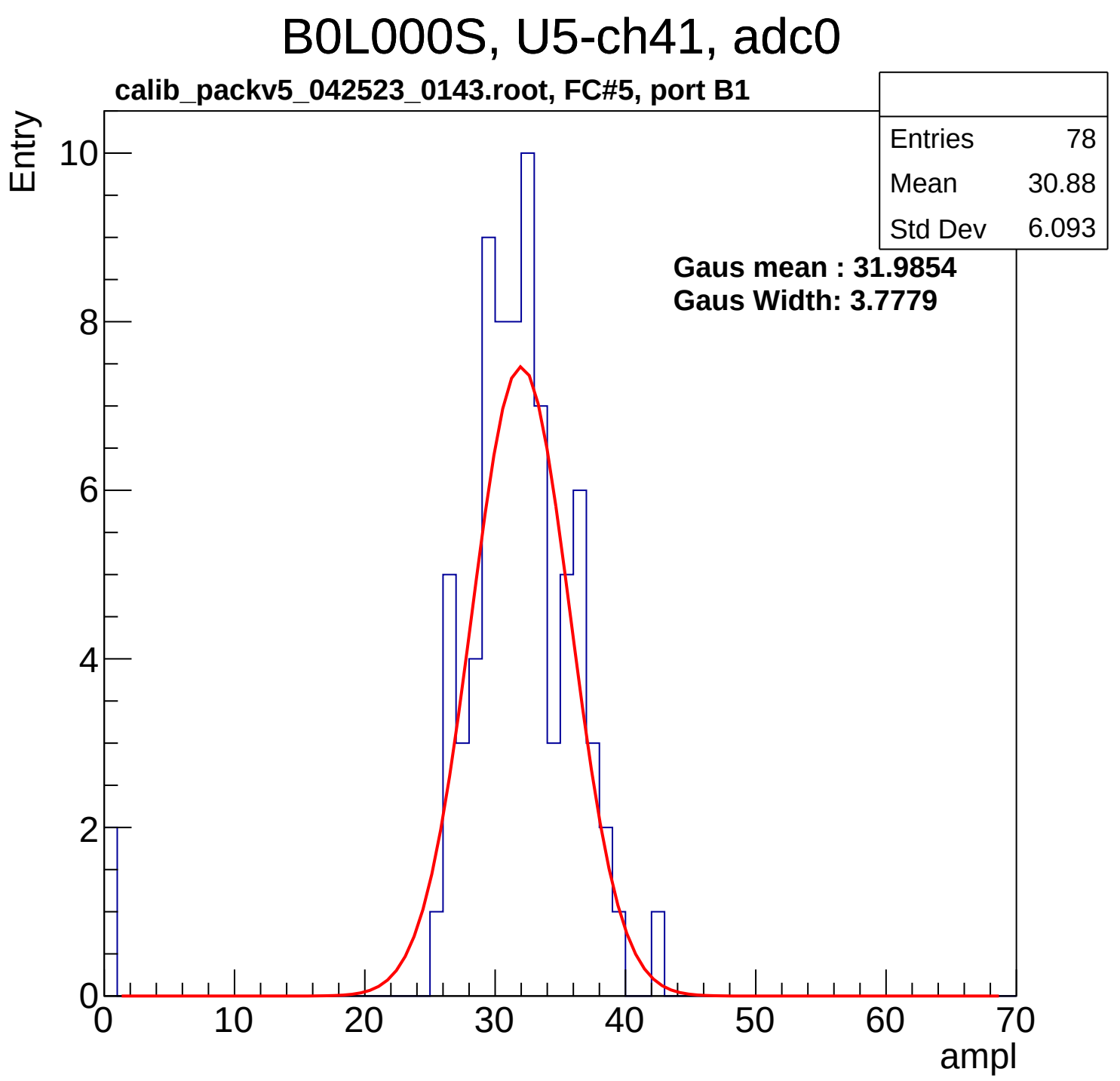
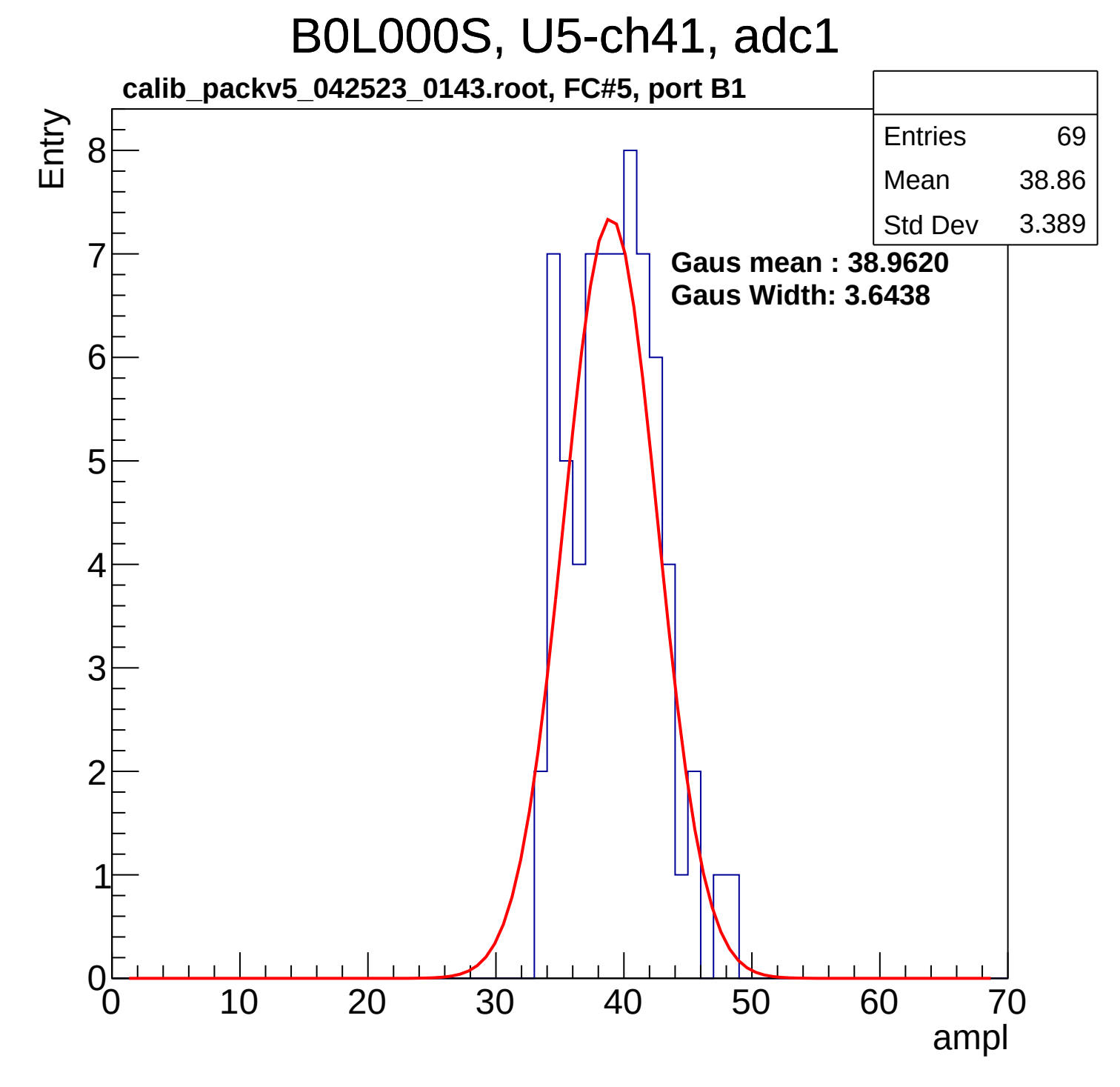

B0L000S, U5-ch40, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch41, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 30.88
Std Dev 6.093
Gaus mean : 31.9854
Gaus Width: 3.7779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch41, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 38.86
Std Dev 3.389
Gaus mean : 38.9620
Gaus Width: 3.6438
Entry
ampl
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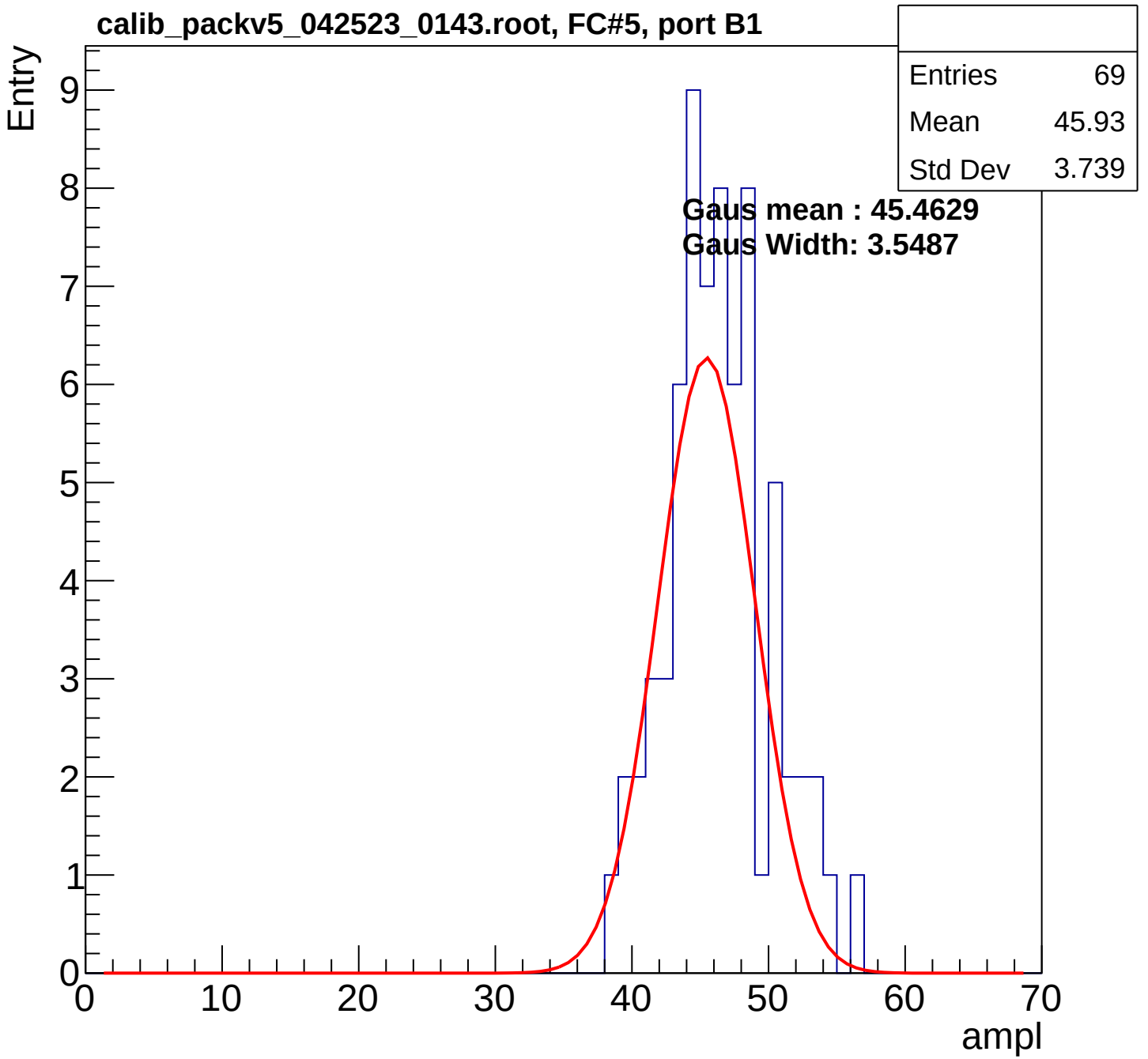
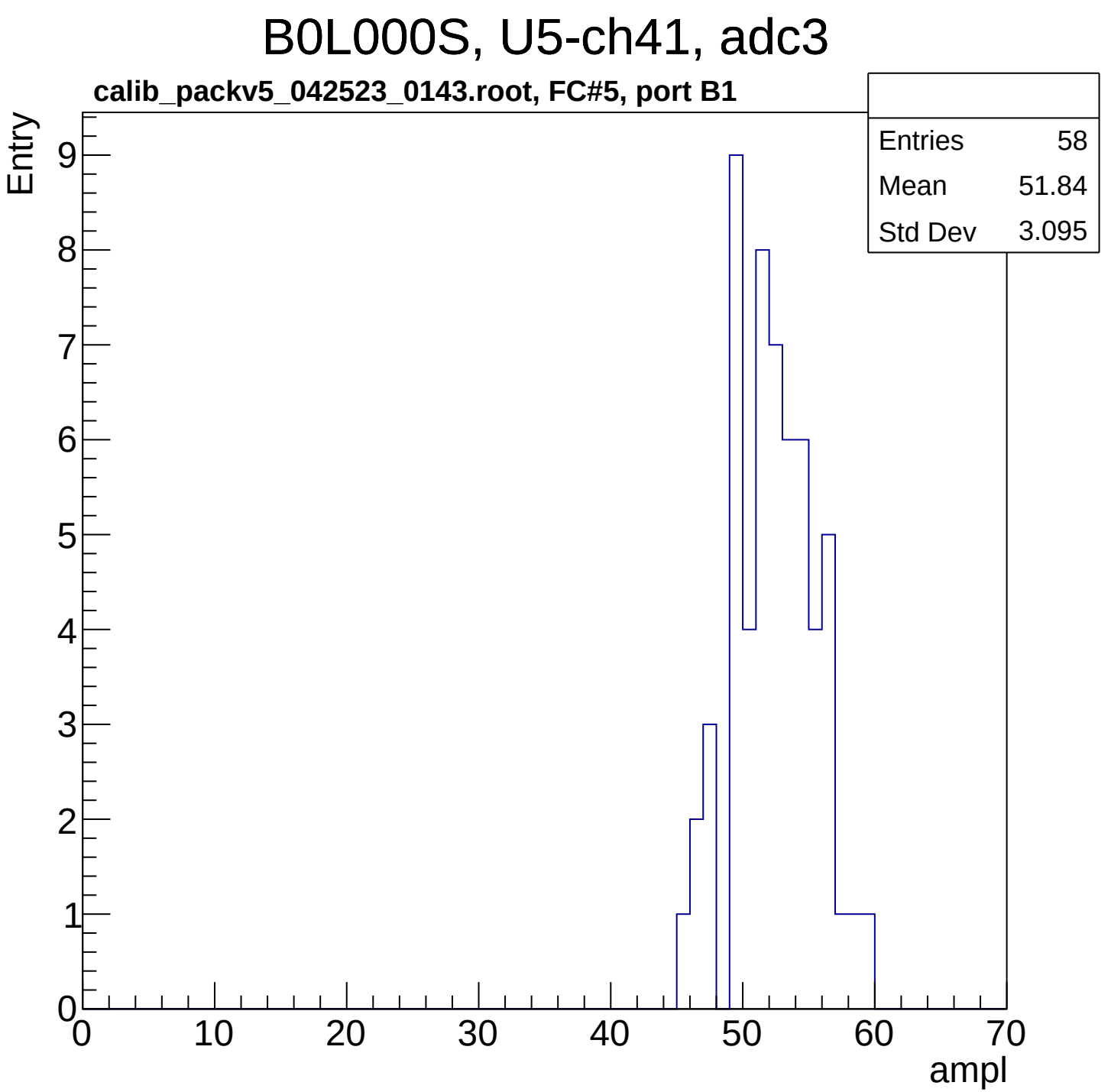
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch41, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 45.93
Std Dev 3.739
Gaus mean : 45.4629
Gaus Width: 3.5487
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch41, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 51.84
Std Dev 3.095
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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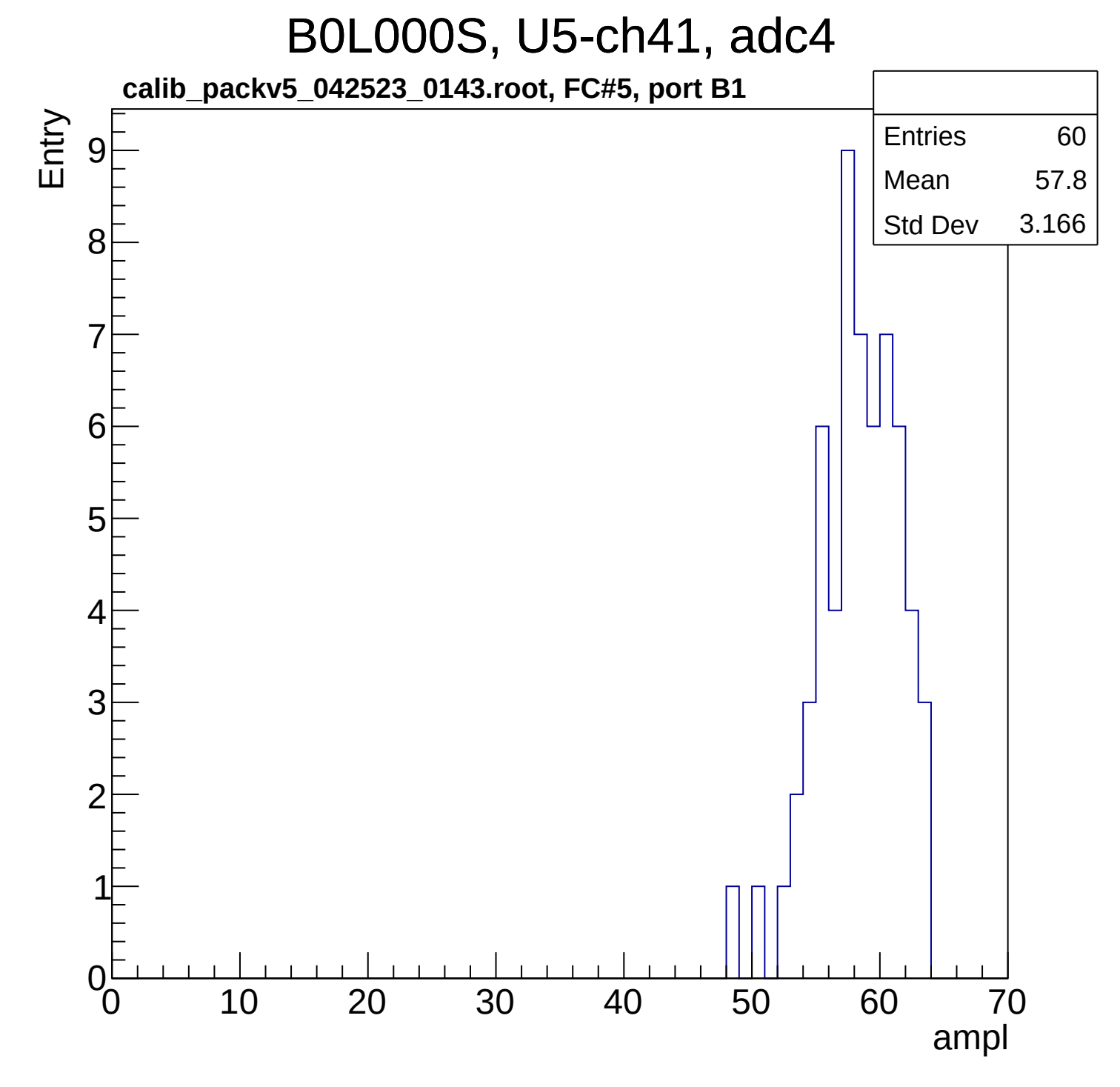
B0L000S, U5-ch41, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.8
Std Dev 3.166
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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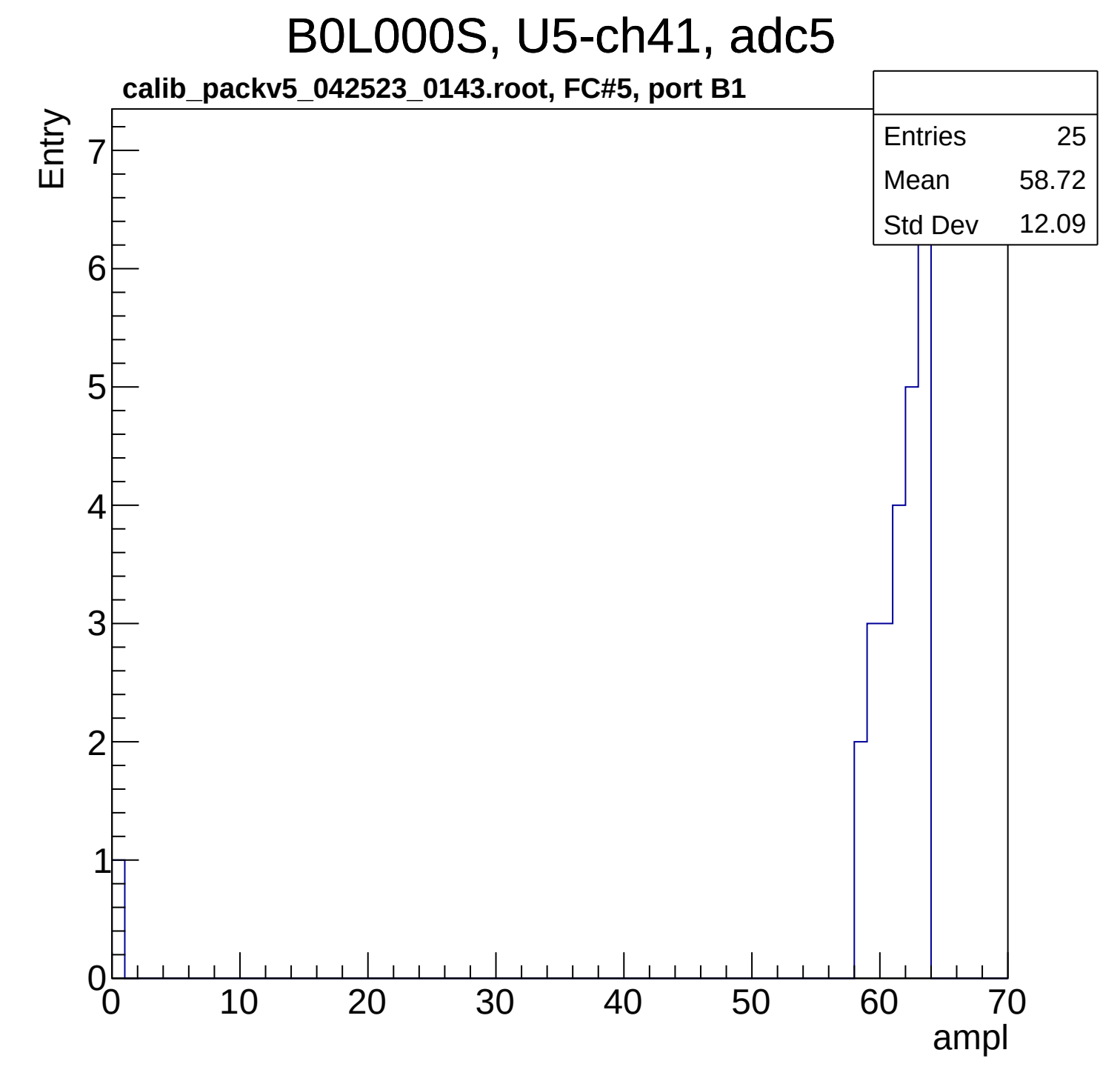
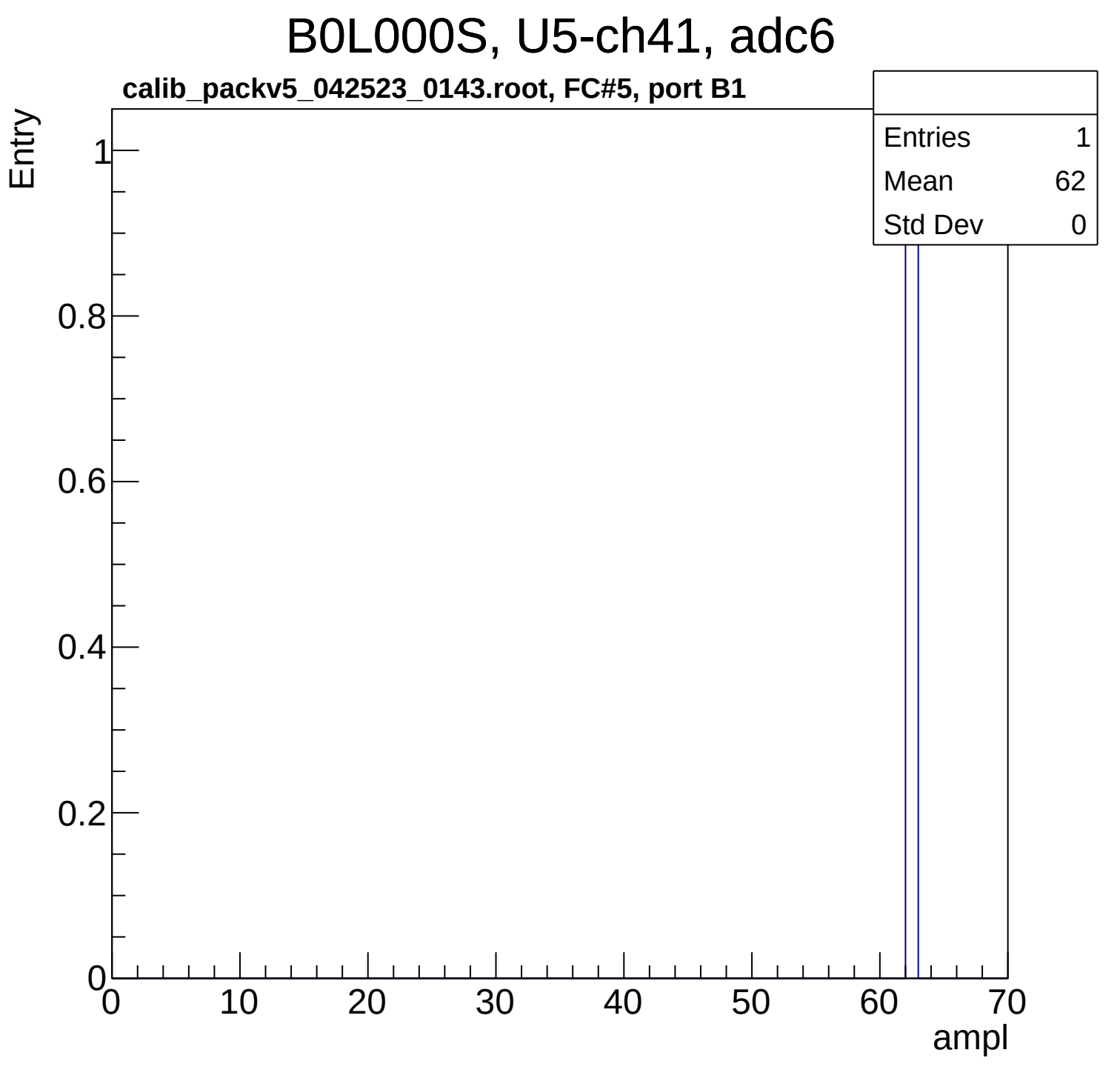
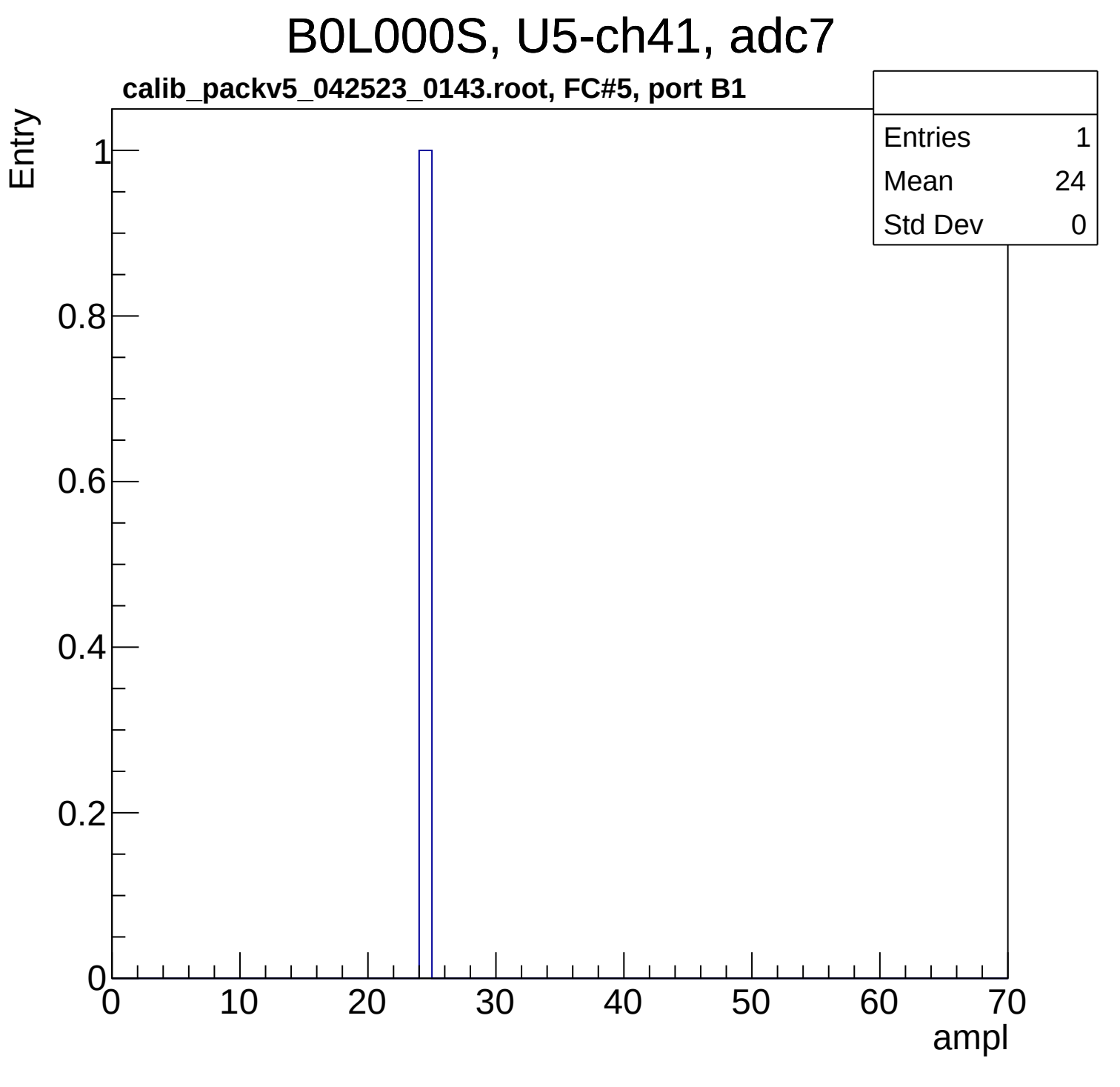

B0L000S, U5-ch41, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 25
Mean 58.72
Std Dev 12.09
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch41, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch41, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
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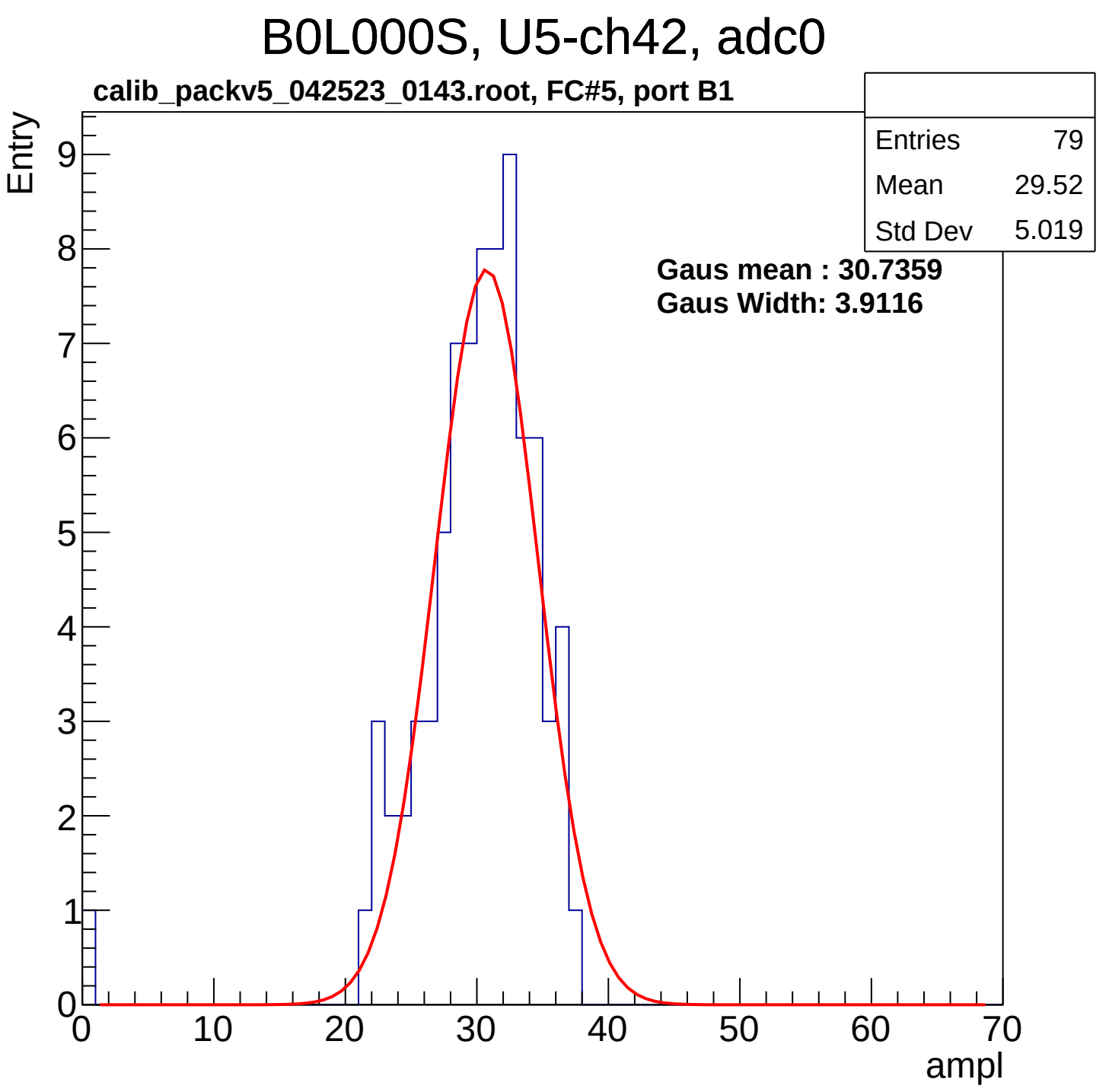
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch42, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 29.52
Std Dev 5.019
Gaus mean : 30.7359
Gaus Width: 3.9116
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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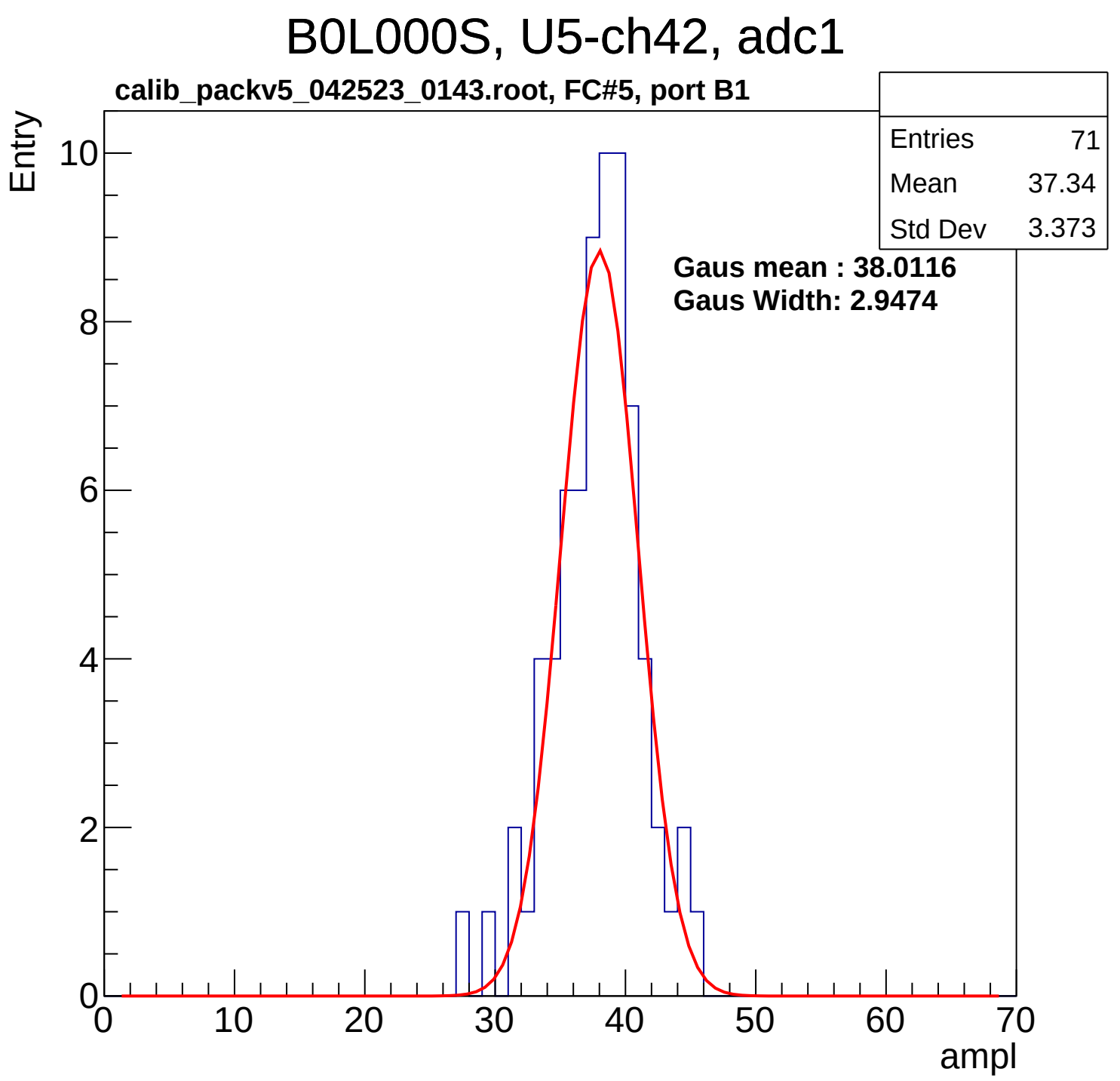
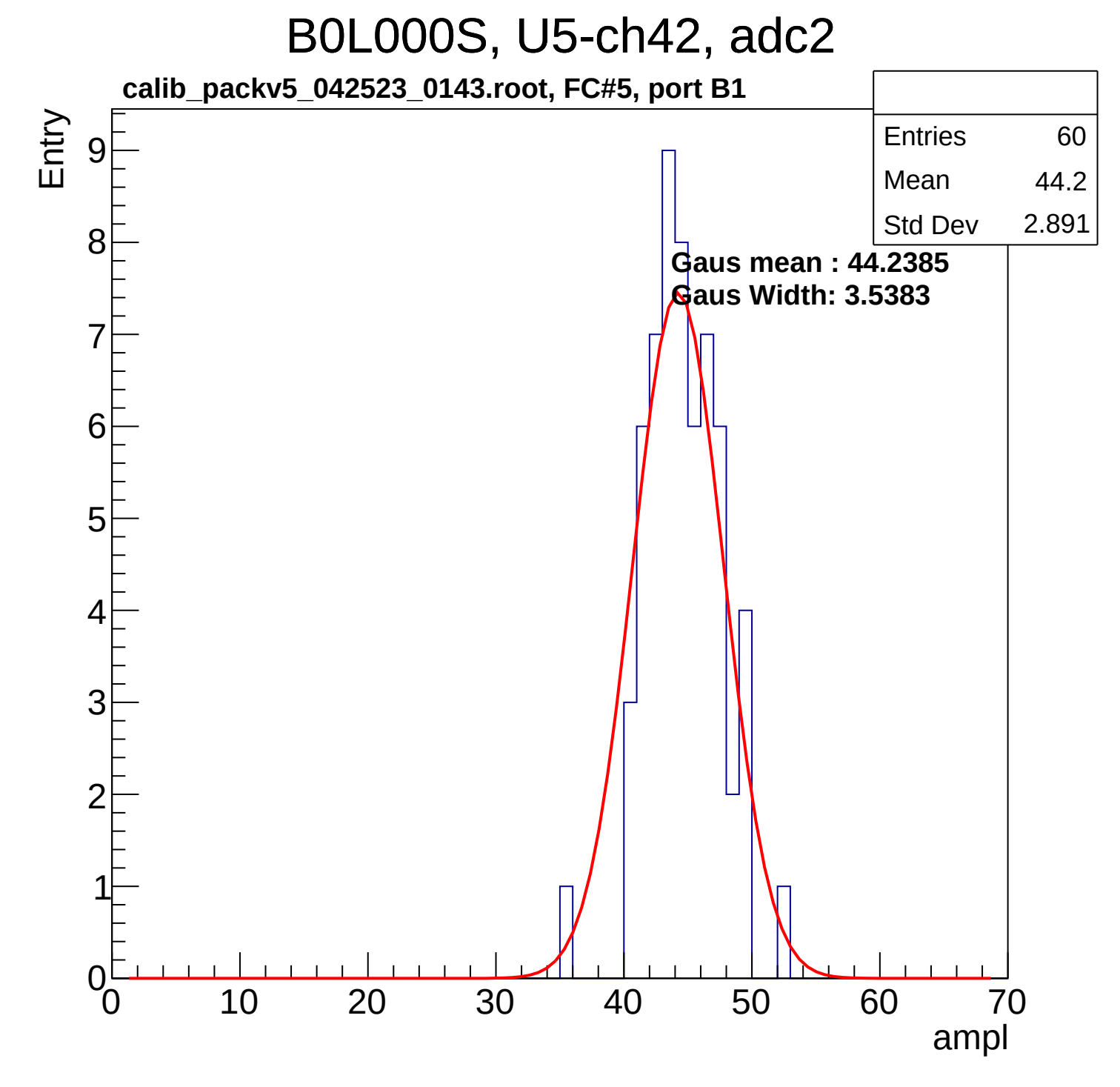
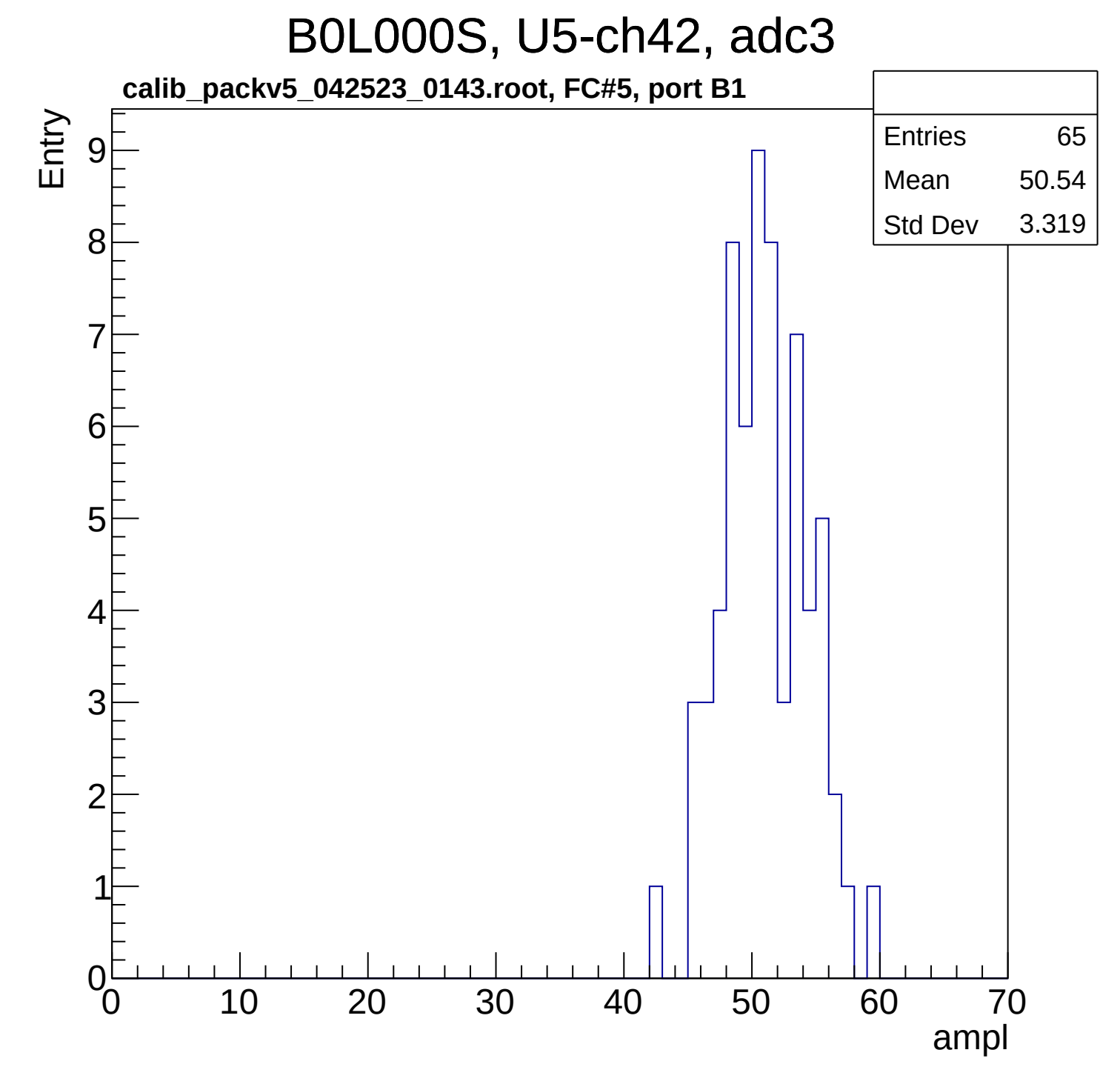

B0L000S, U5-ch42, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.34
Std Dev 3.373
Gaus mean : 38.0116
Gaus Width: 2.9474
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch42, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 44.2
Std Dev 2.891
Gaus mean : 44.2385
Gaus Width: 3.5383
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch42, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 50.54
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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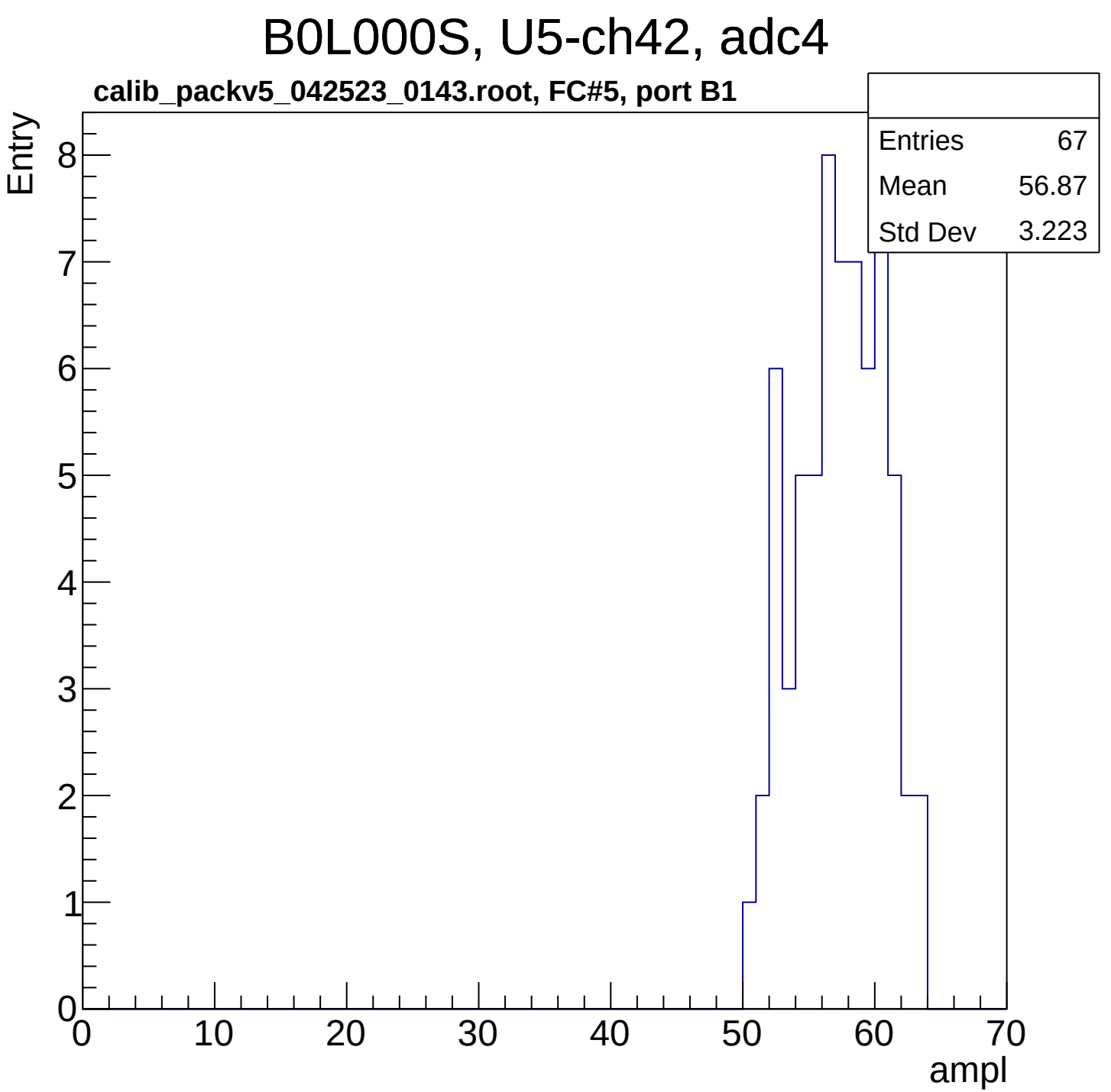
B0L000S, U5-ch42, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 56.87
Std Dev 3.223
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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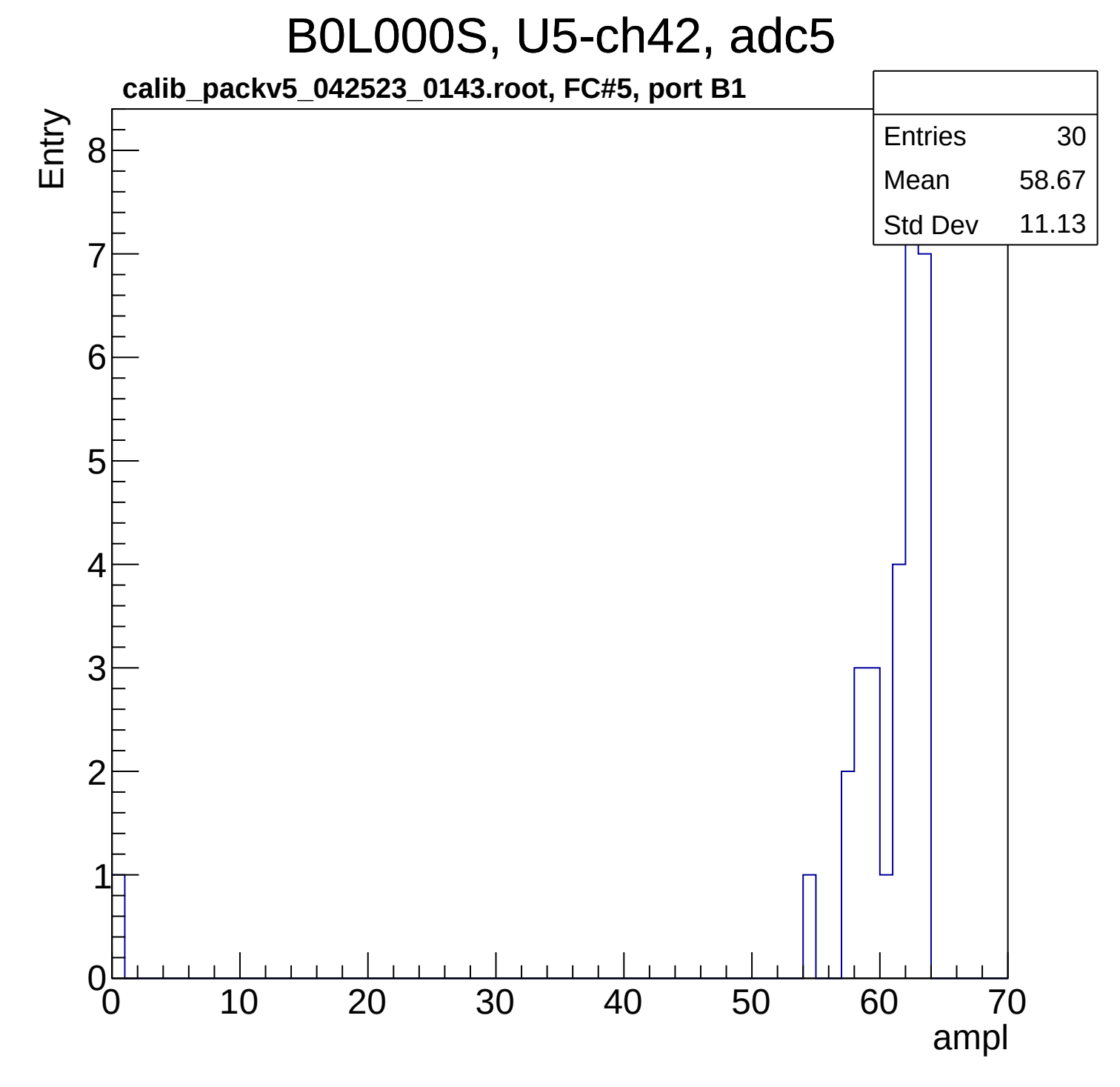
B0L000S, U5-ch42, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.67
Std Dev 11.13
Entry
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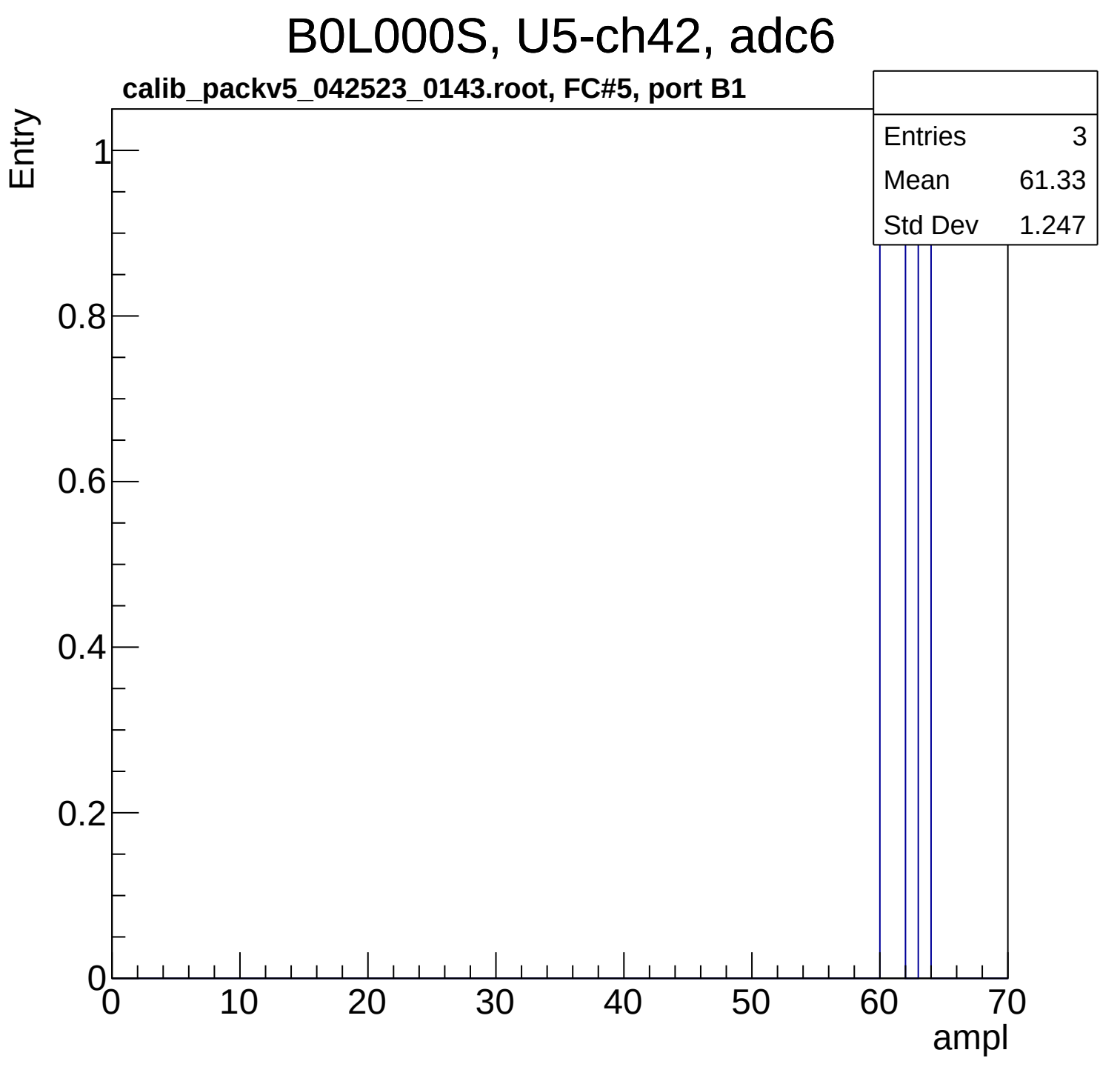
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch42, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 1.247
Entry
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0.8
0.6
0.4
0.2
0
0
10
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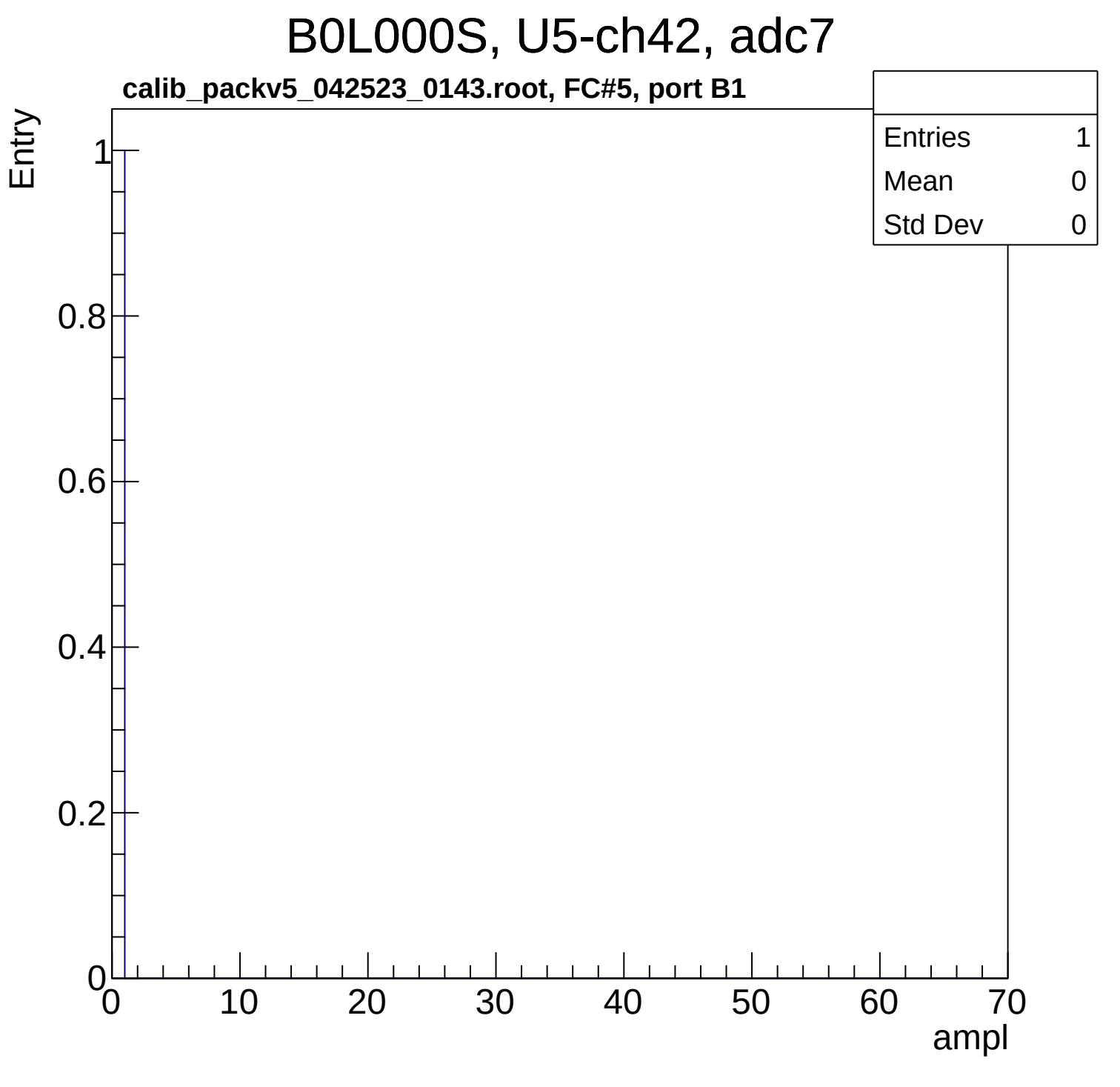
50
60
70
ampl


B0L000S, U5-ch42, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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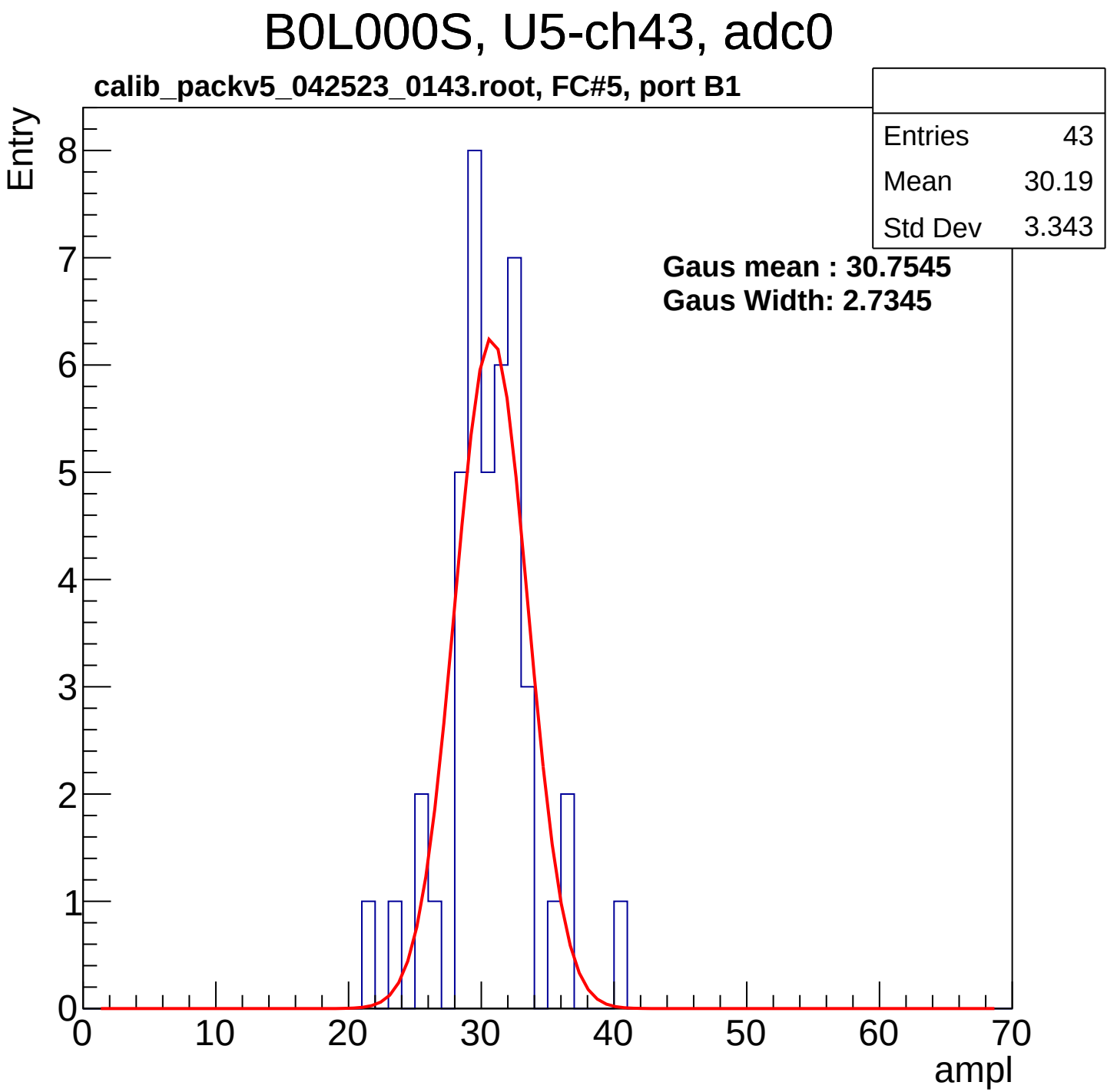
40
50
60
70
ampl


B0L000S, U5-ch43, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 30.19
Std Dev 3.343
Gaus mean : 30.7545
Gaus Width: 2.7345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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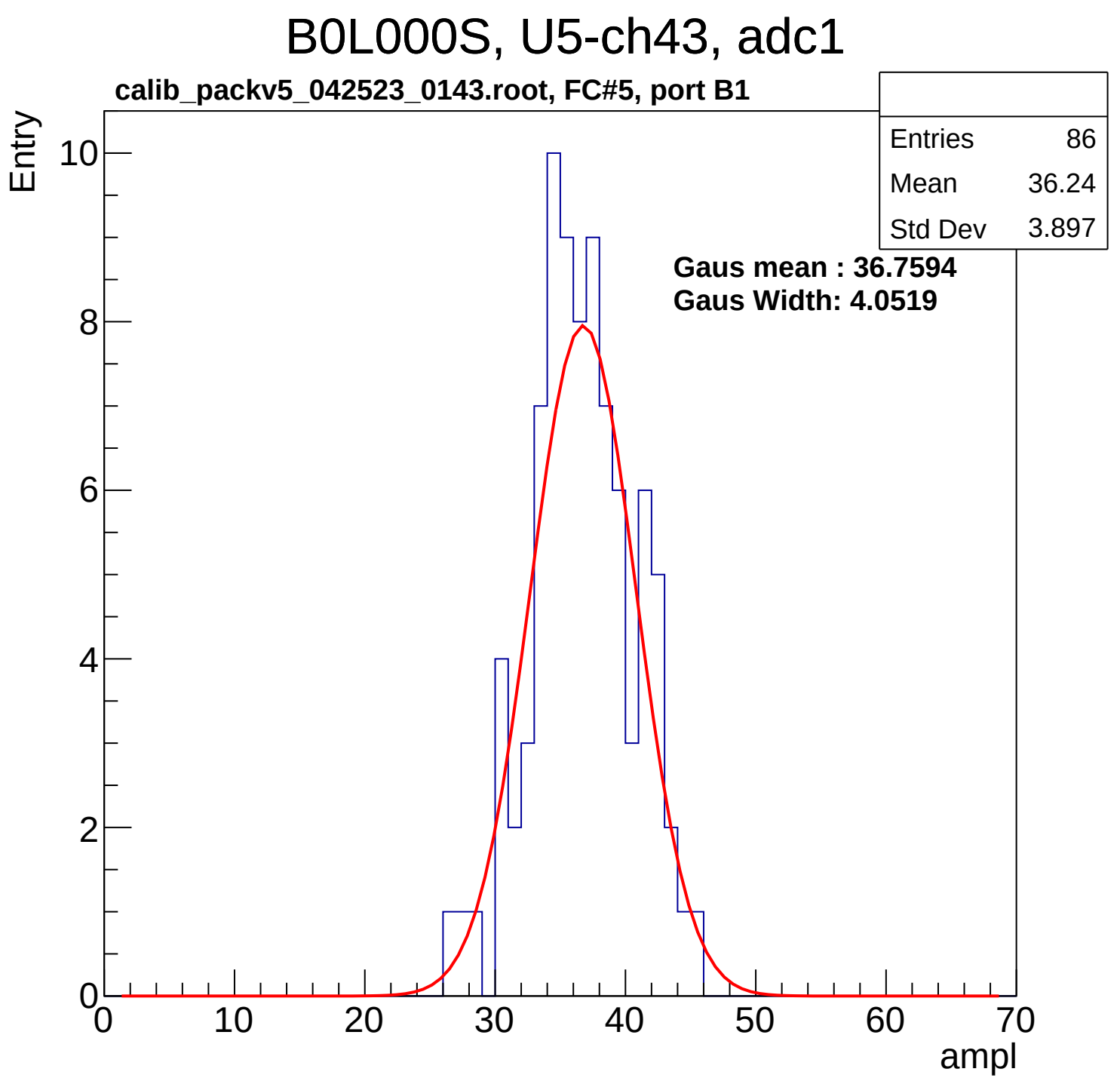
B0L000S, U5-ch43, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 36.24
Std Dev 3.897
Gaus mean : 36.7594
Gaus Width: 4.0519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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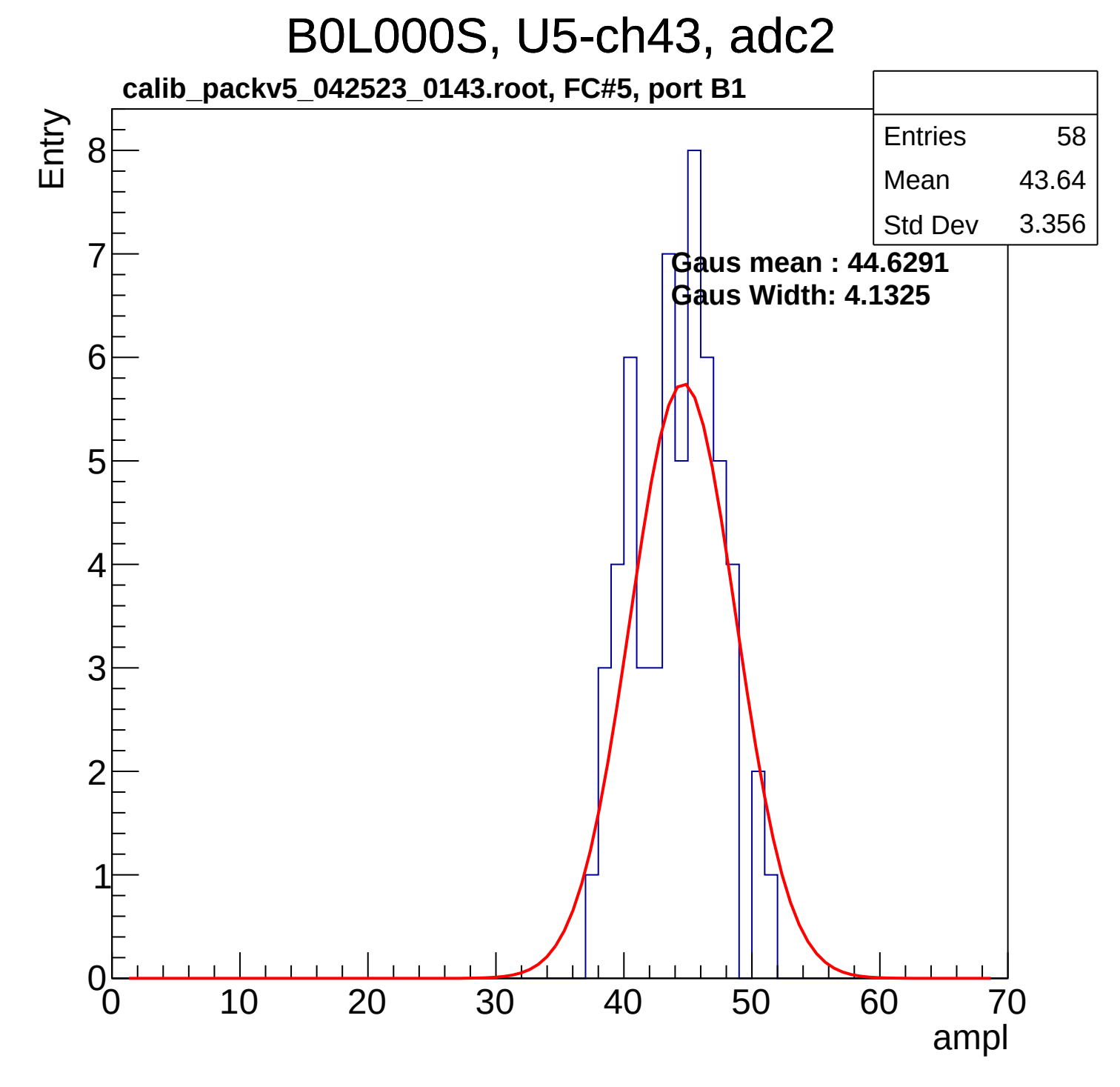
B0L000S, U5-ch43, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.64
Std Dev 3.356
Gaus mean : 44.6291
Gaus Width: 4.1325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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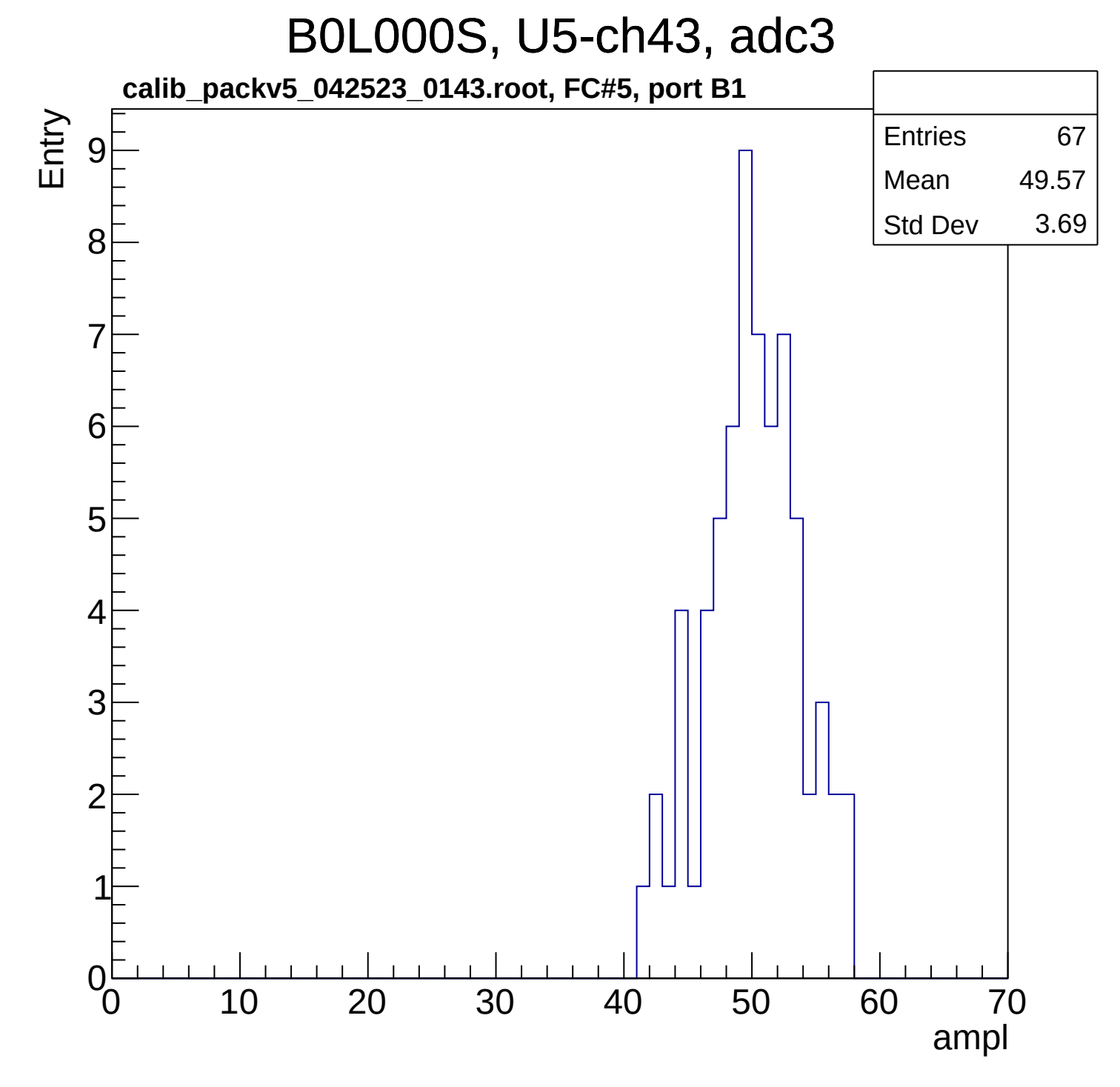
B0L000S, U5-ch43, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.57
Std Dev 3.69
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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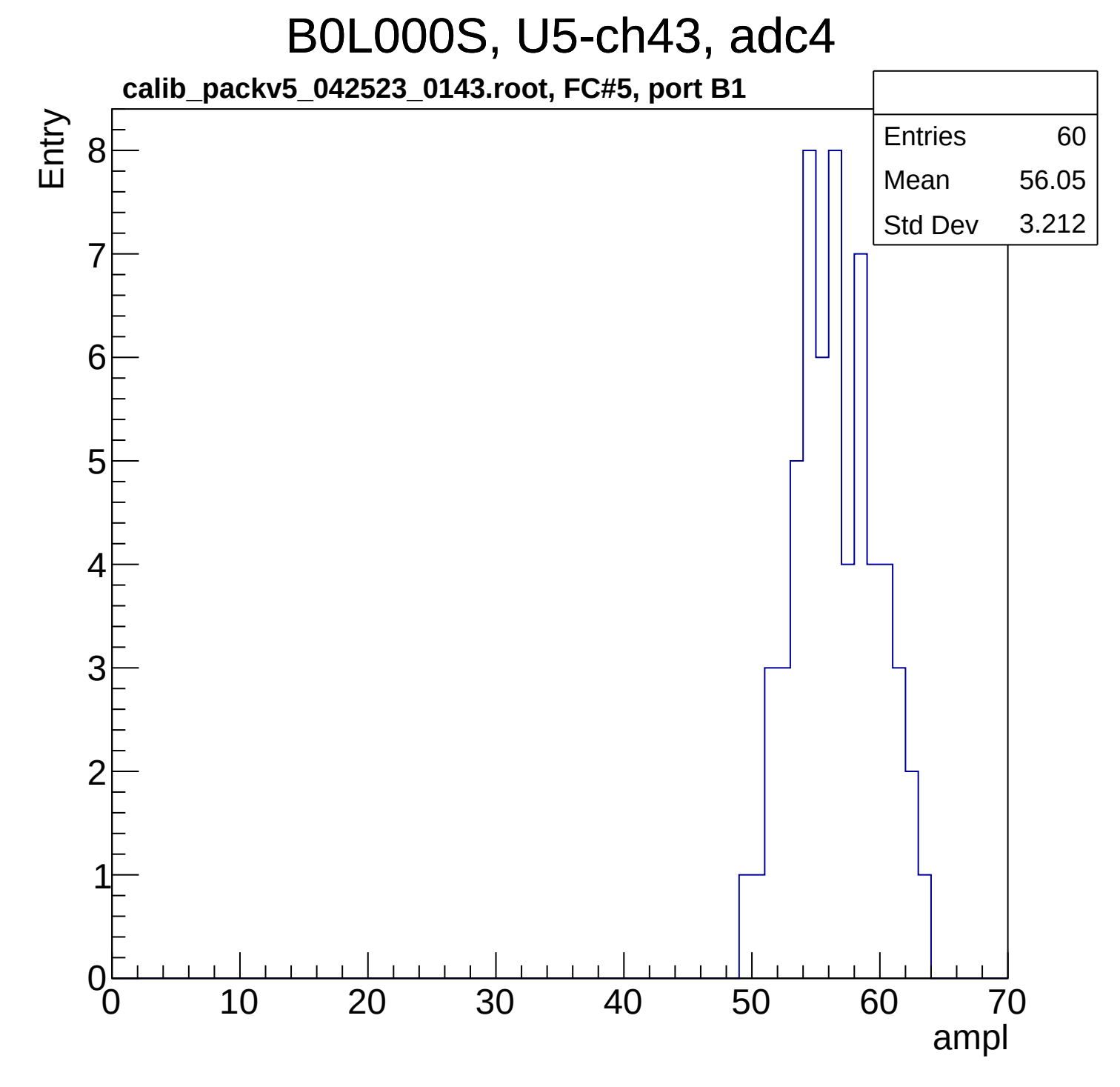
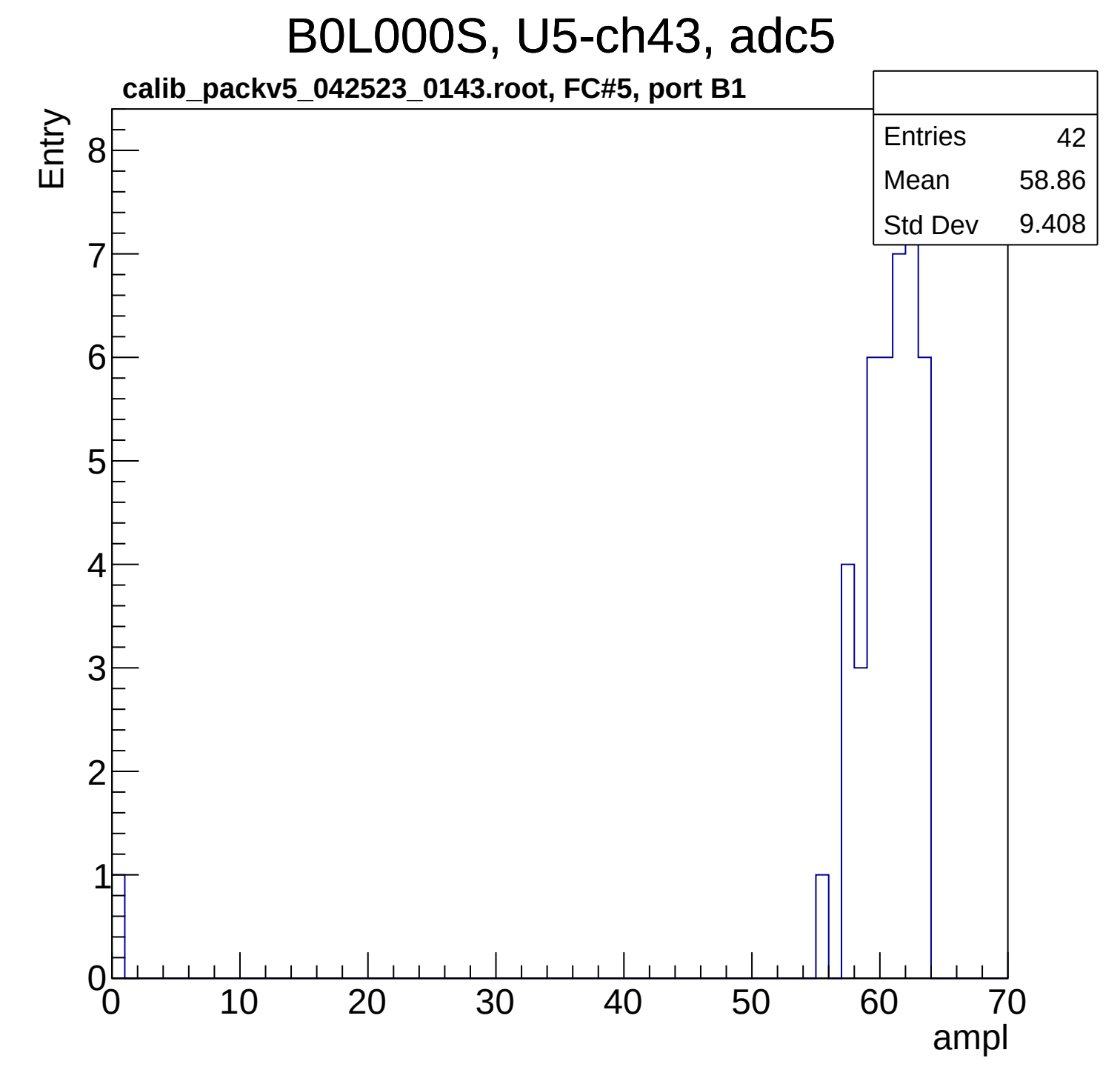

B0L000S, U5-ch43, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 56.05
Std Dev 3.212
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch43, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.86
Std Dev 9.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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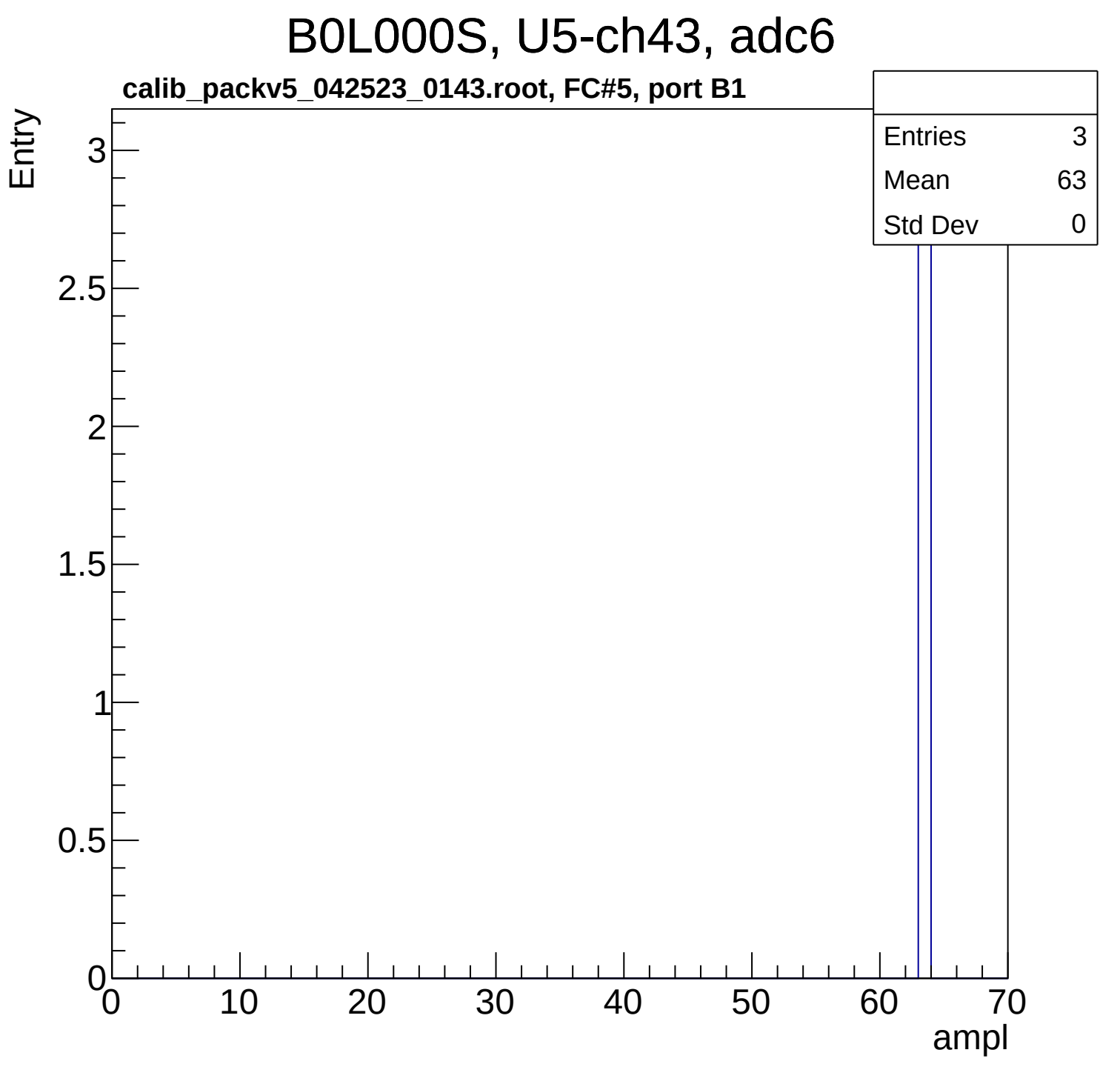
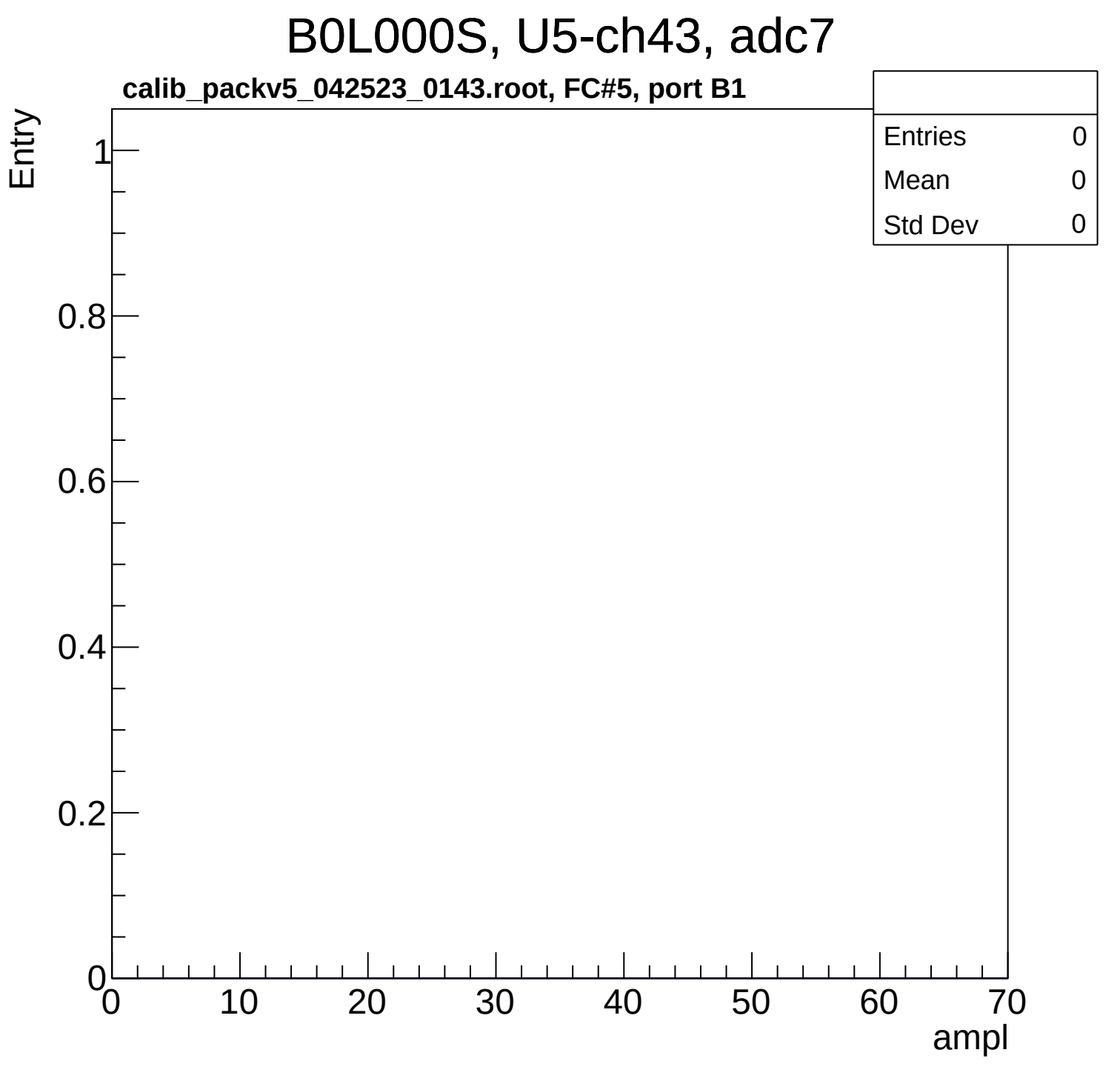
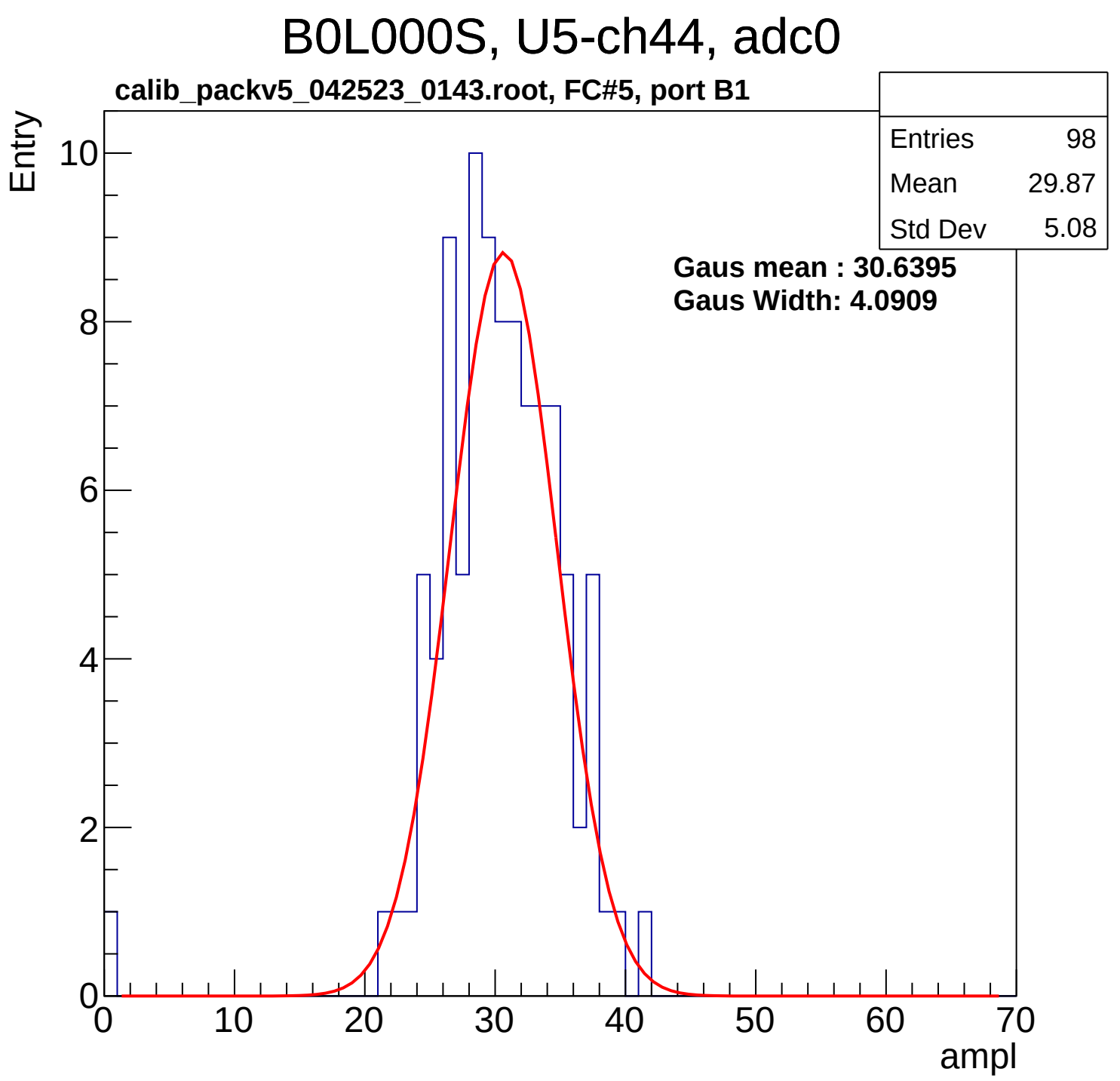
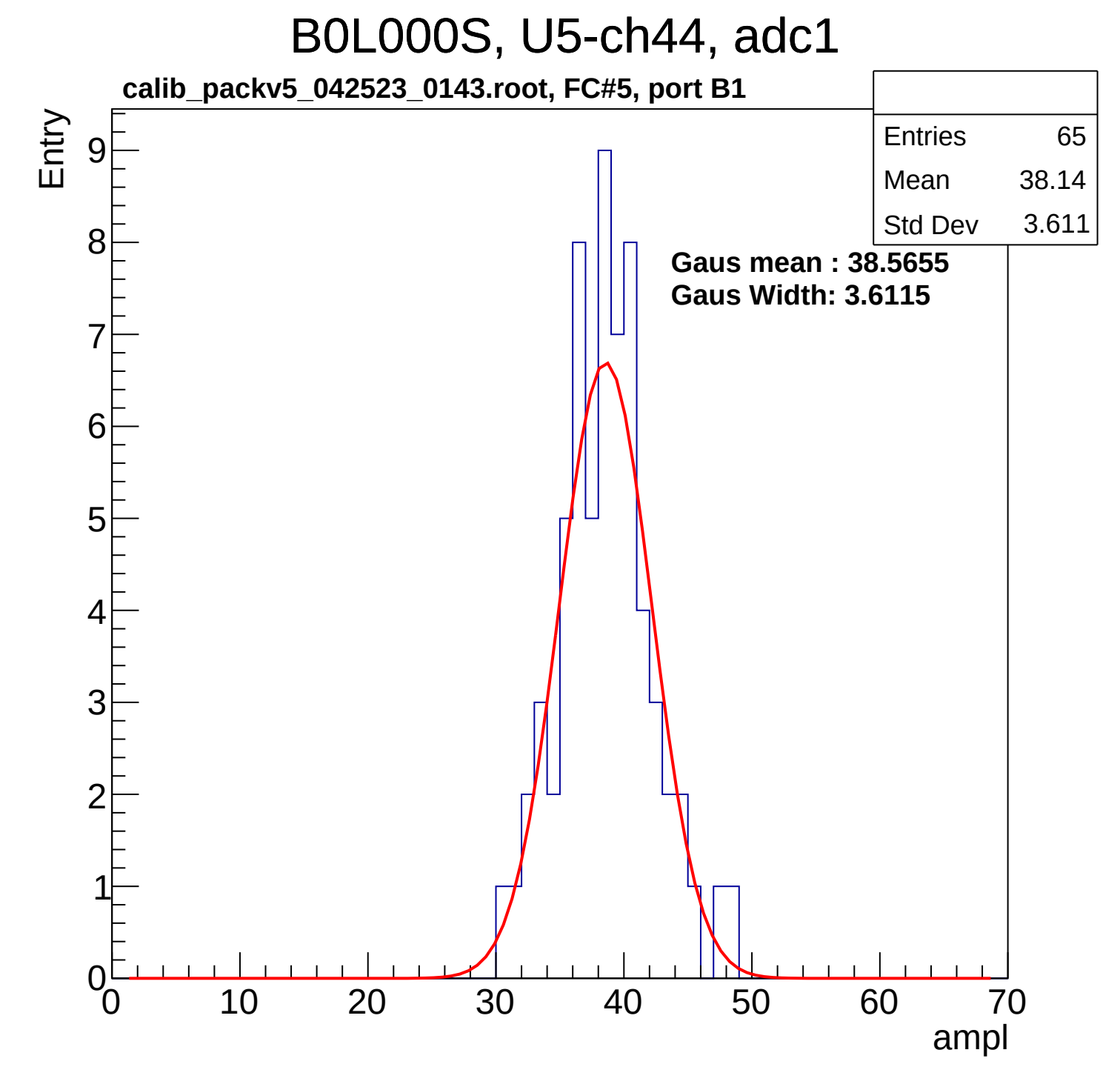
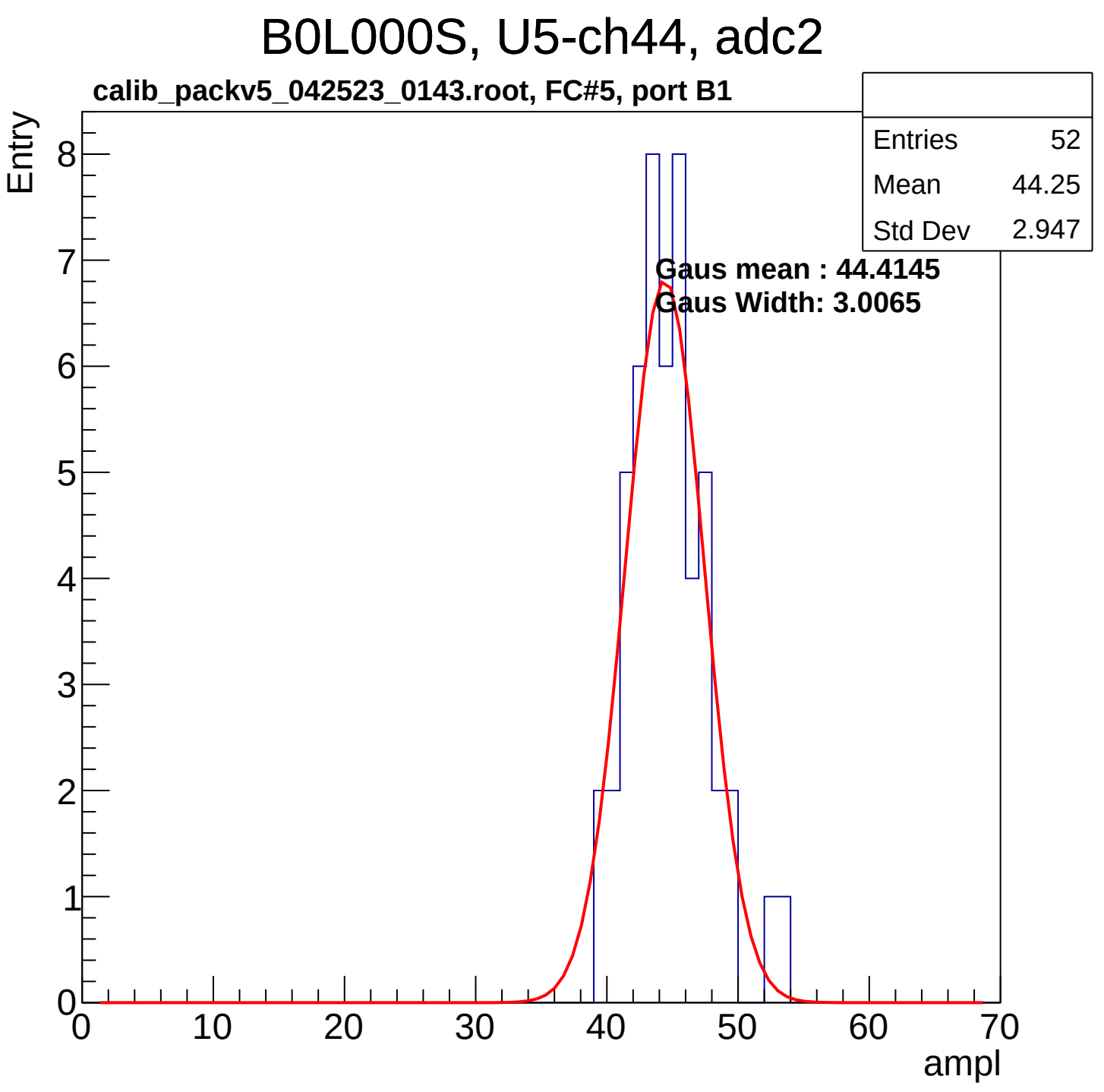

B0L000S, U5-ch43, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch43, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch44, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 98
Mean 29.87
Std Dev 5.08
Gaus mean : 30.6395
Gaus Width: 4.0909
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch44, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 38.14
Std Dev 3.611
Gaus mean : 38.5655
Gaus Width: 3.6115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch44, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 44.25
Std Dev 2.947
Gaus mean : 44.4145
Gaus Width: 3.0065
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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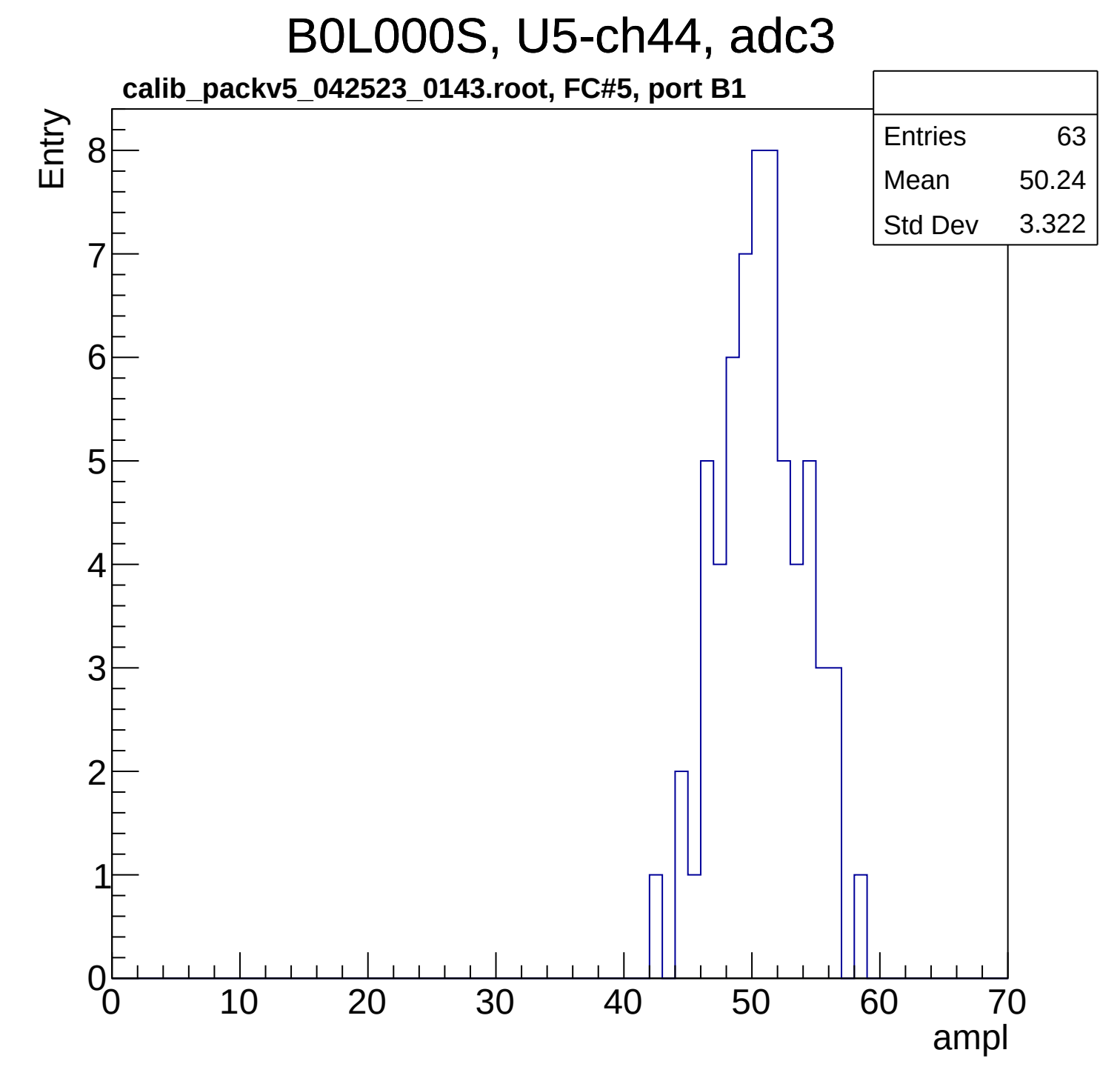
B0L000S, U5-ch44, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 50.24
Std Dev 3.322
Entry
ampl
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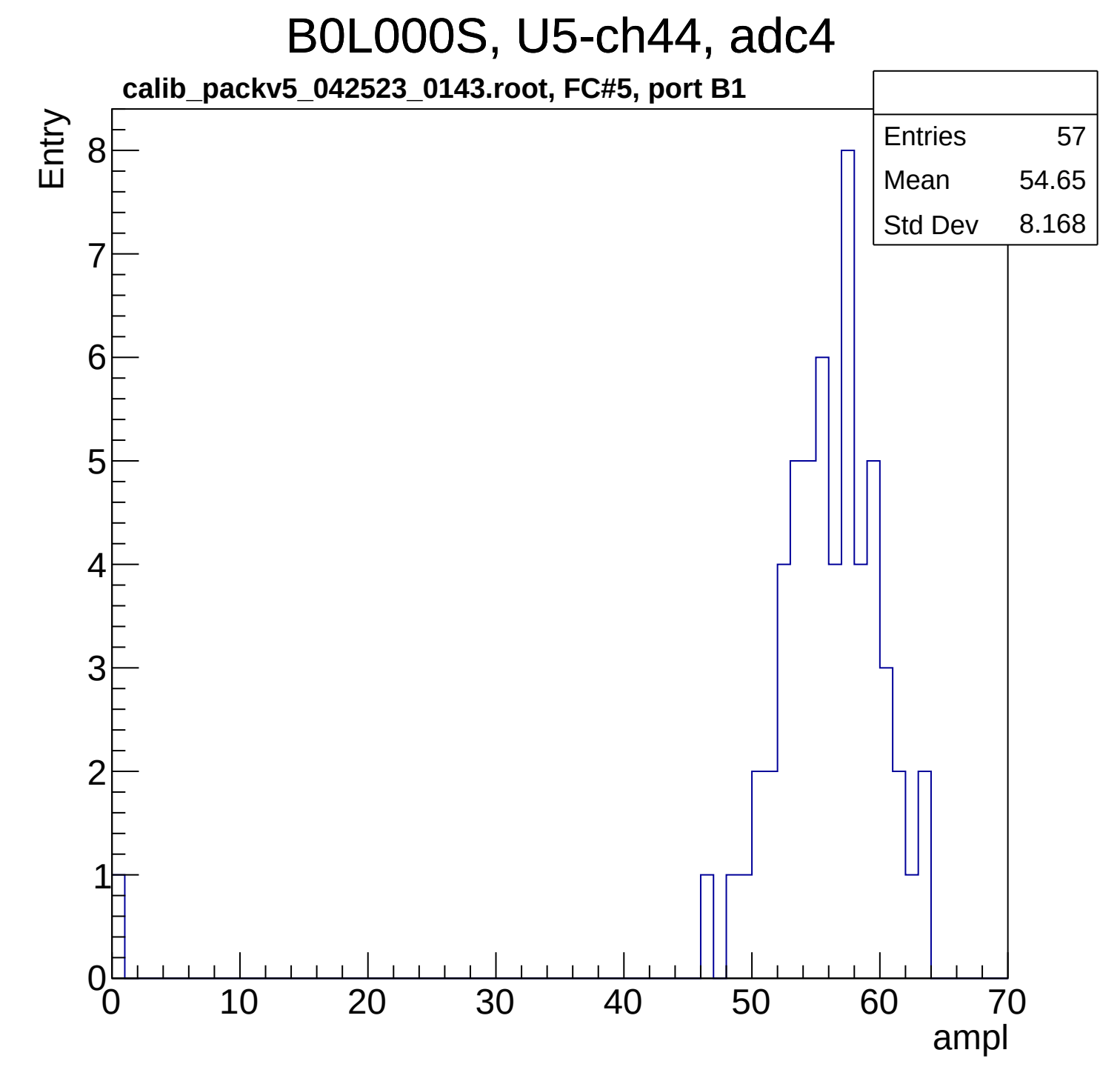
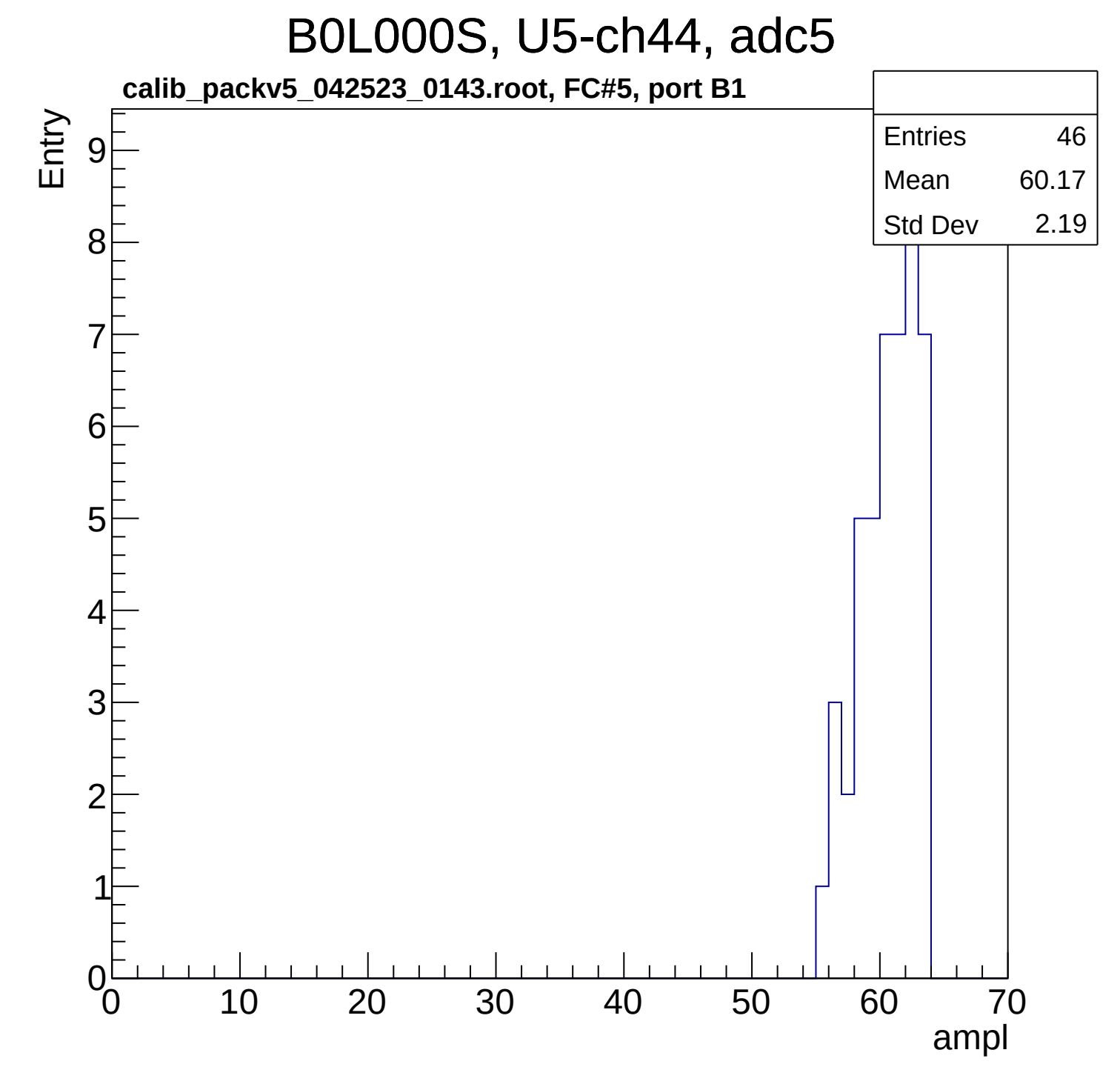
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch44, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 54.65
Std Dev 8.168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch44, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 60.17
Std Dev 2.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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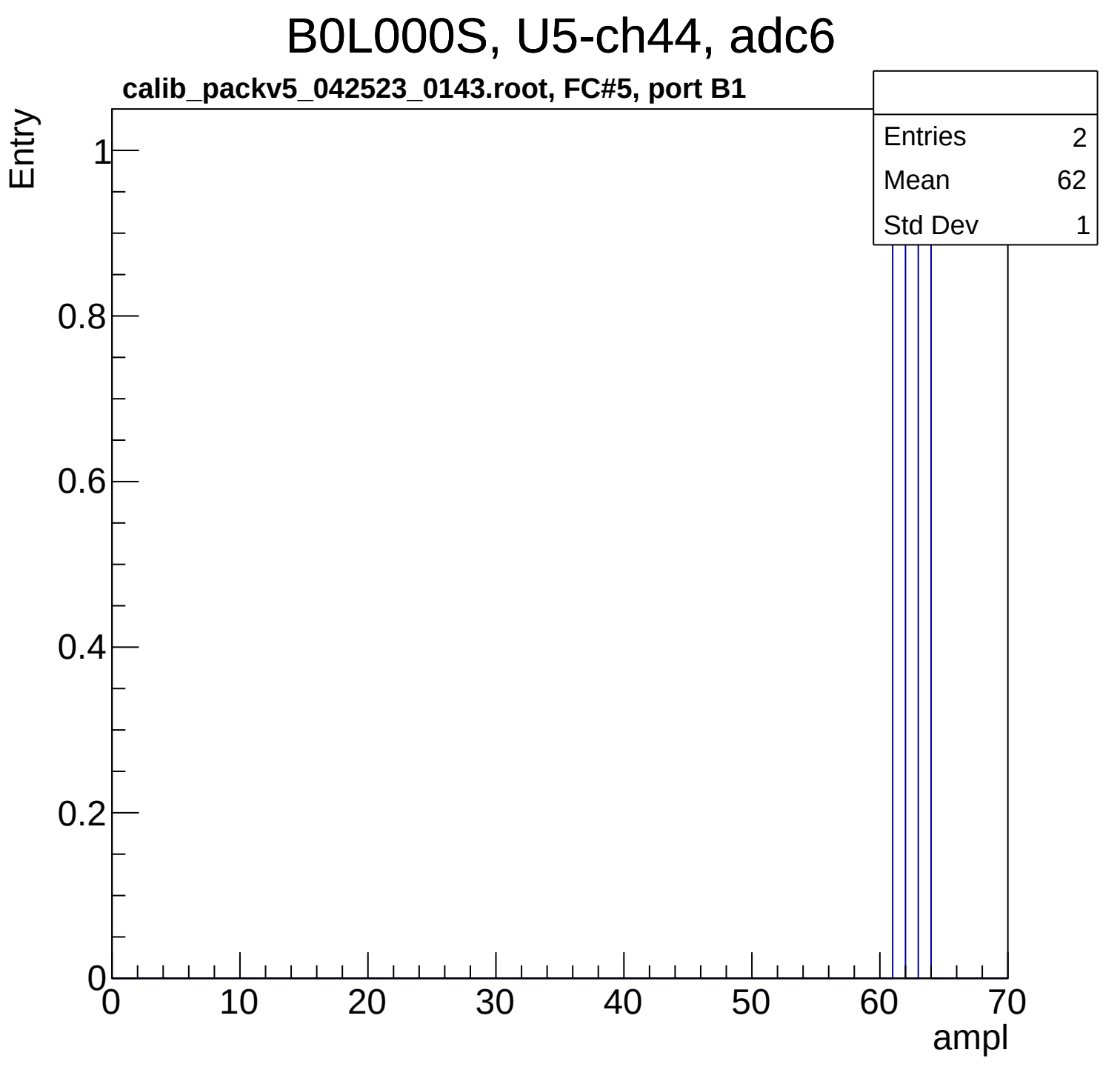
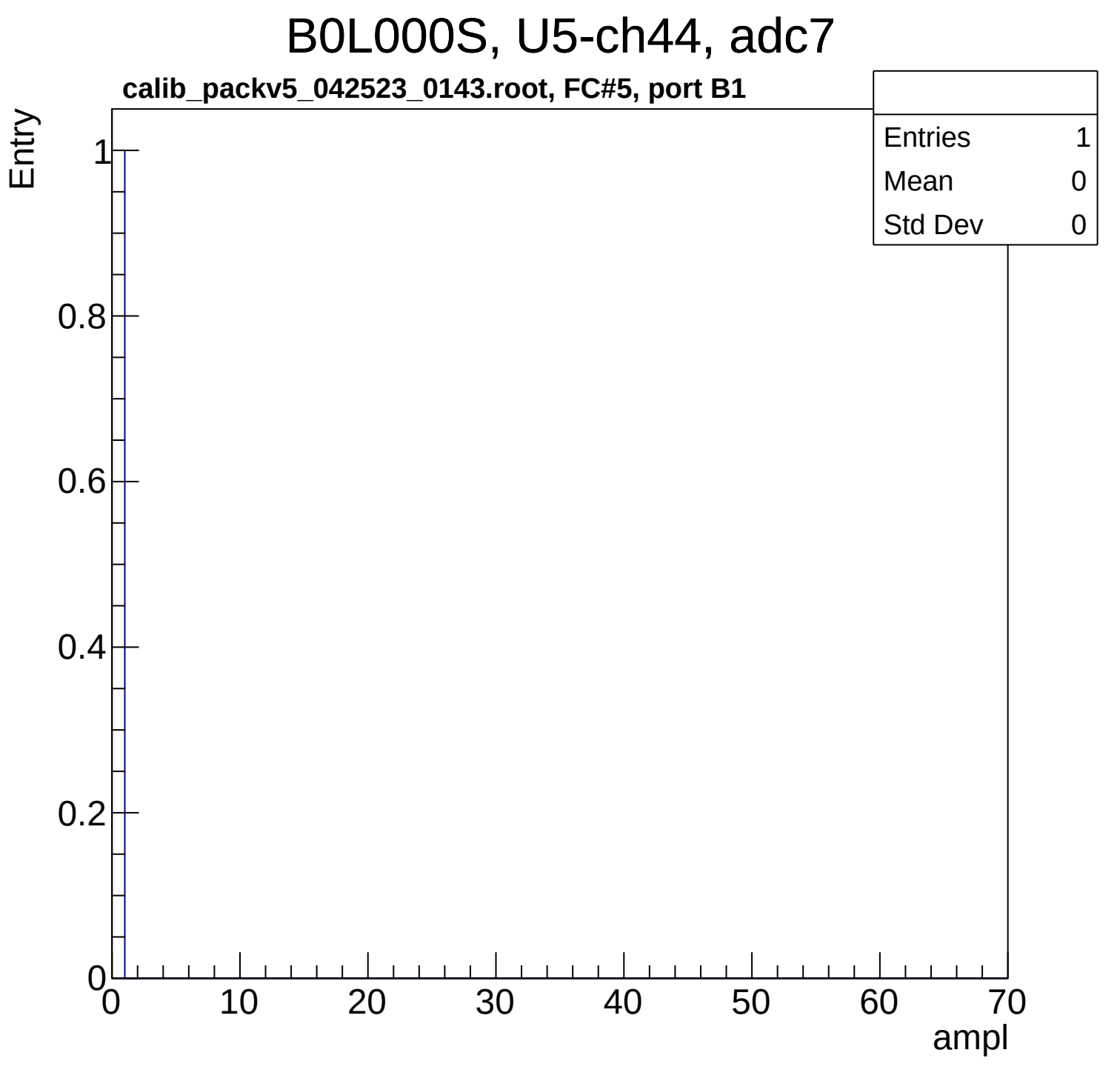

B0L000S, U5-ch44, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch44, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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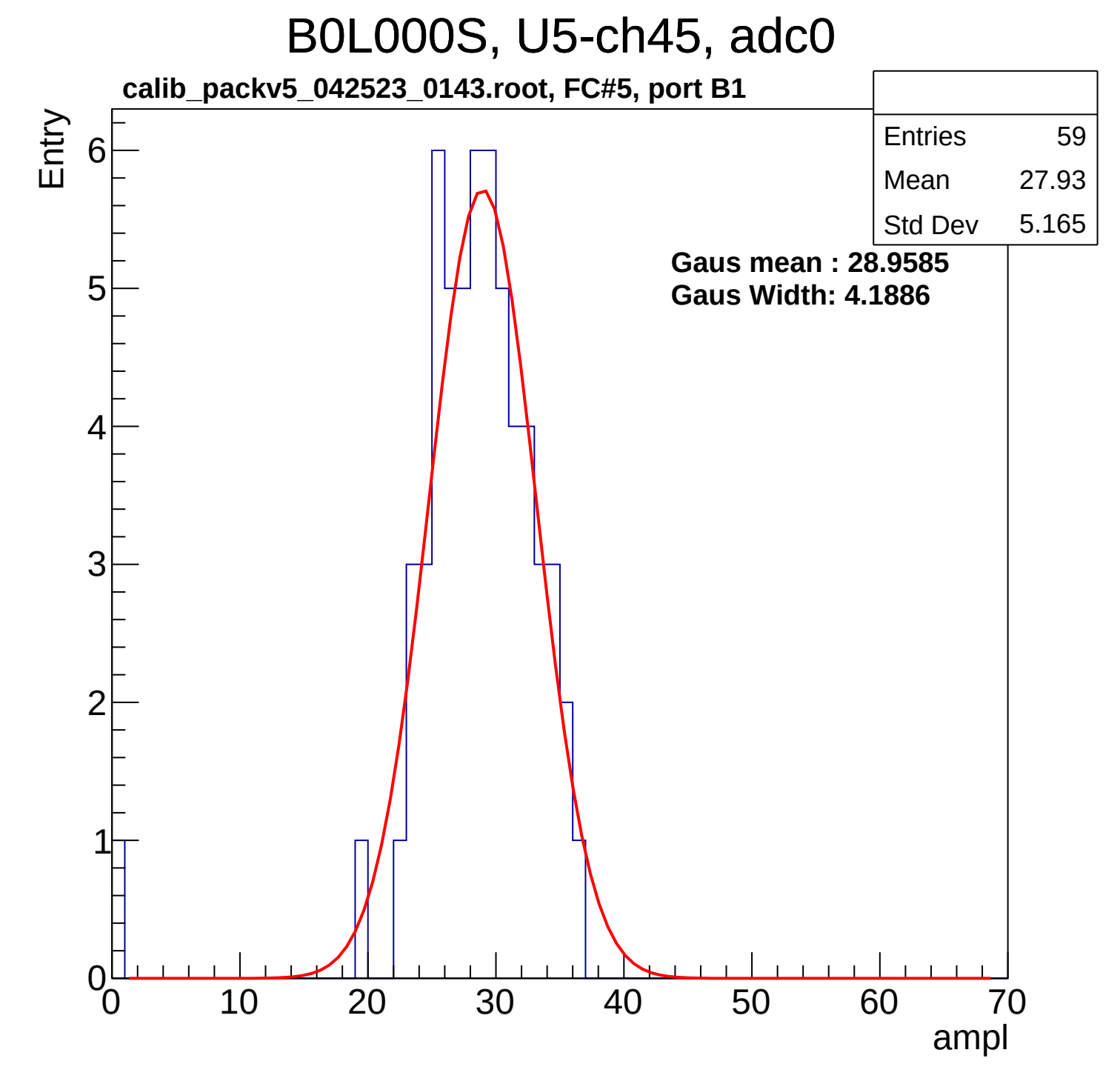
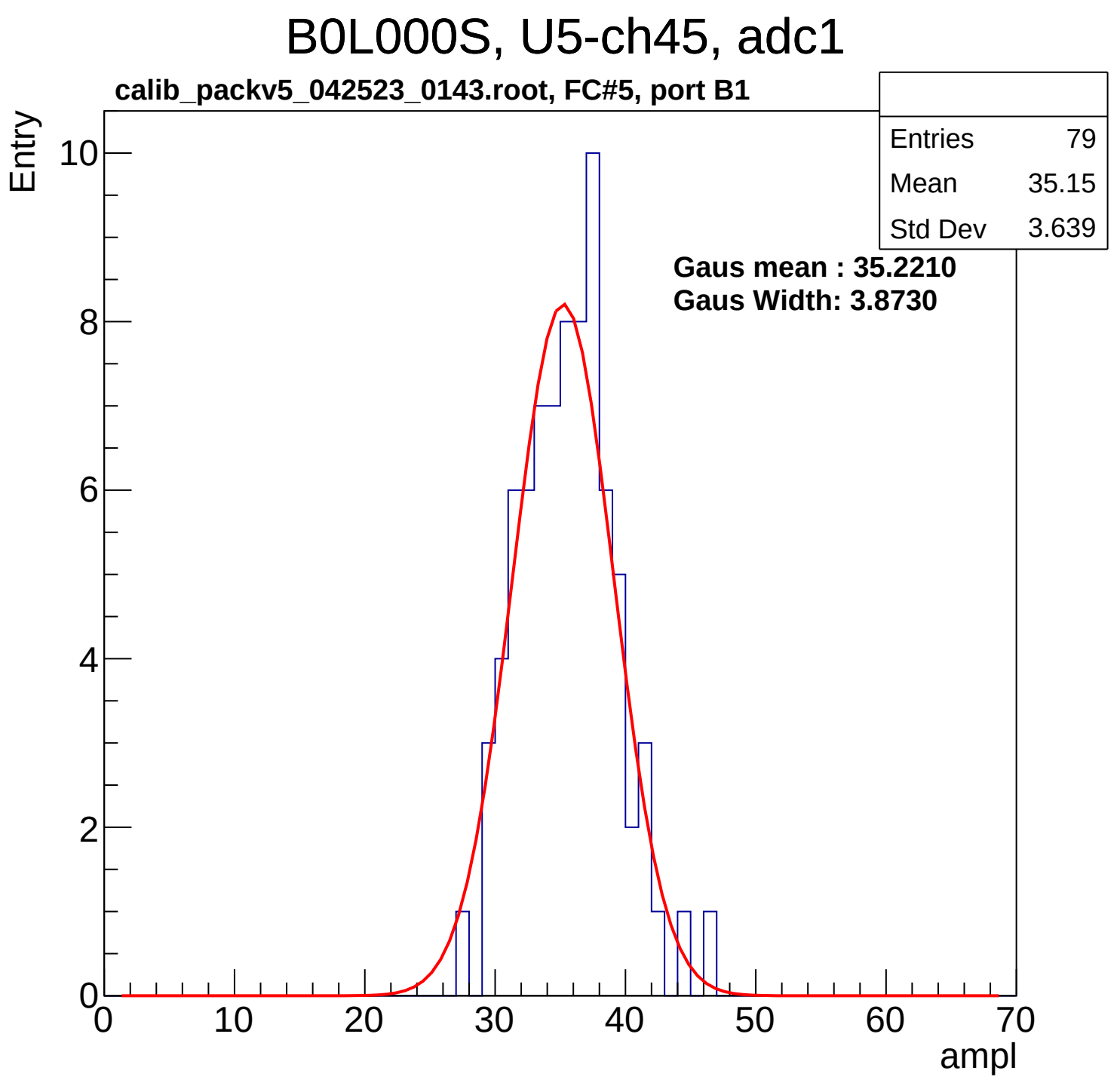
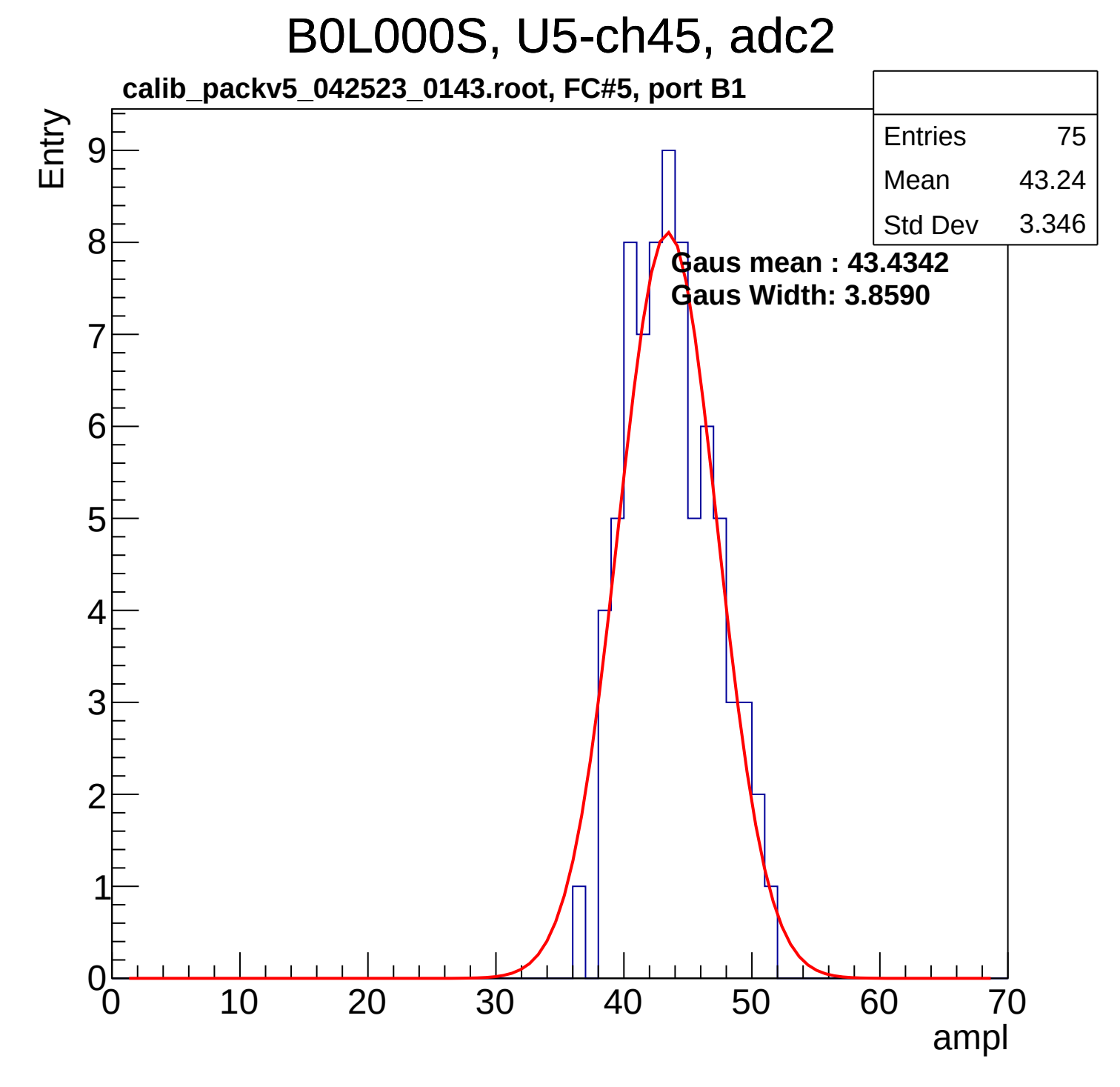
30
40
50
60
70
ampl


B0L000S, U5-ch45, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 27.93
Std Dev 5.165
Gaus mean : 28.9585
Gaus Width: 4.1886
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch45, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 35.15
Std Dev 3.639
Gaus mean : 35.2210
Gaus Width: 3.8730
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch45, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 43.24
Std Dev 3.346
Gaus mean : 43.4342
Gaus Width: 3.8590
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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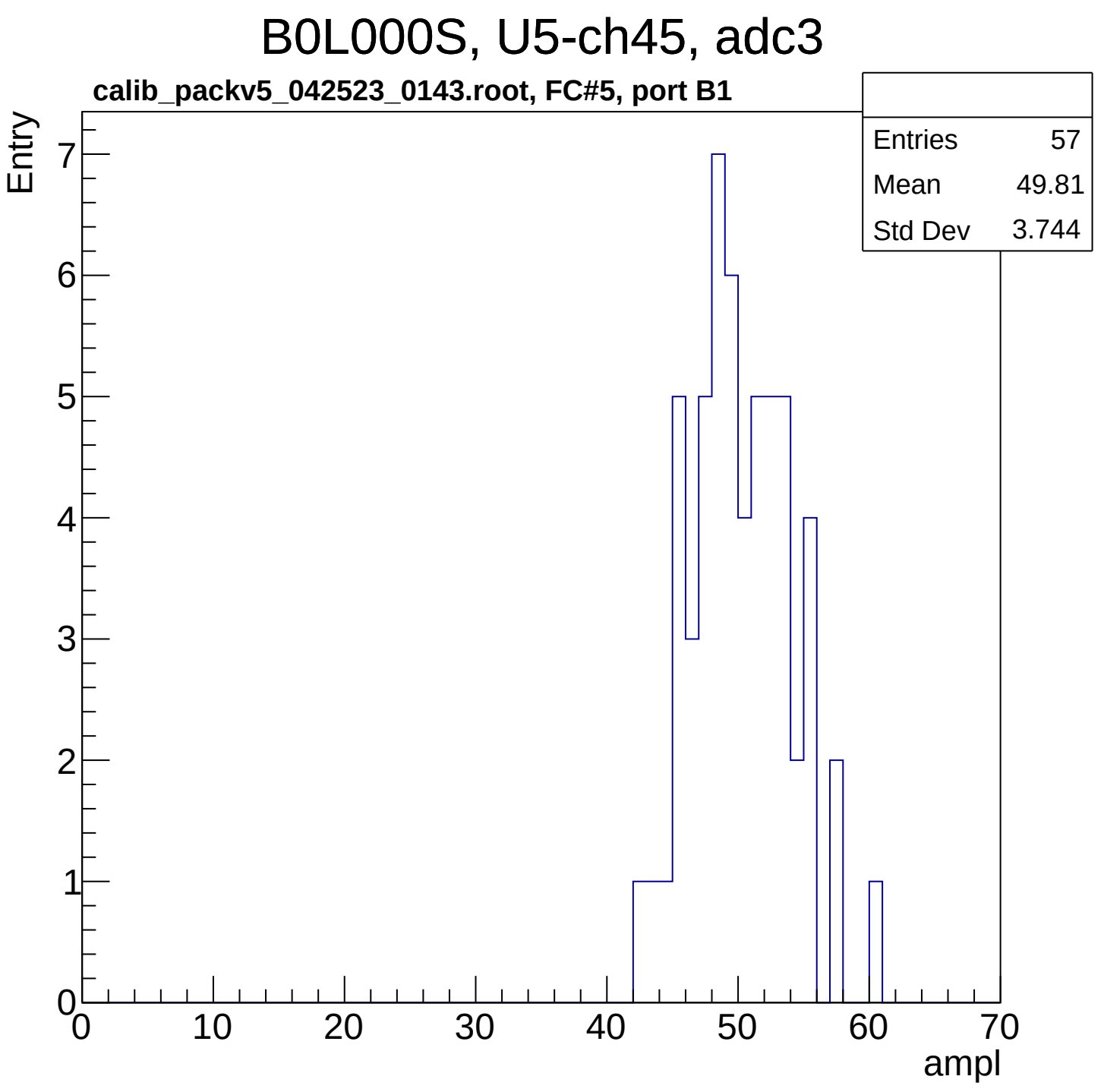
B0L000S, U5-ch45, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.81
Std Dev 3.744
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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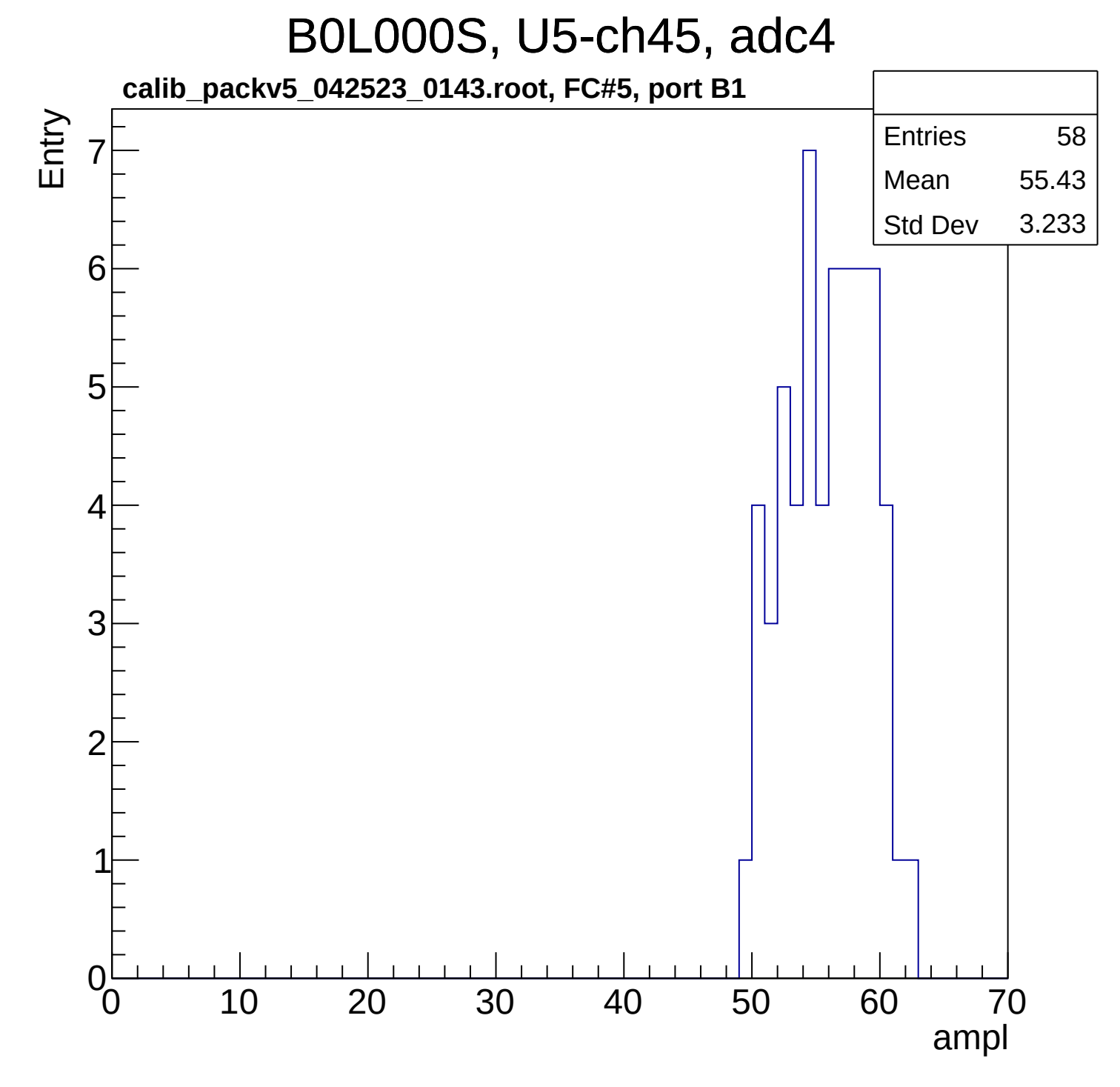
B0L000S, U5-ch45, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.43
Std Dev 3.233
Entry
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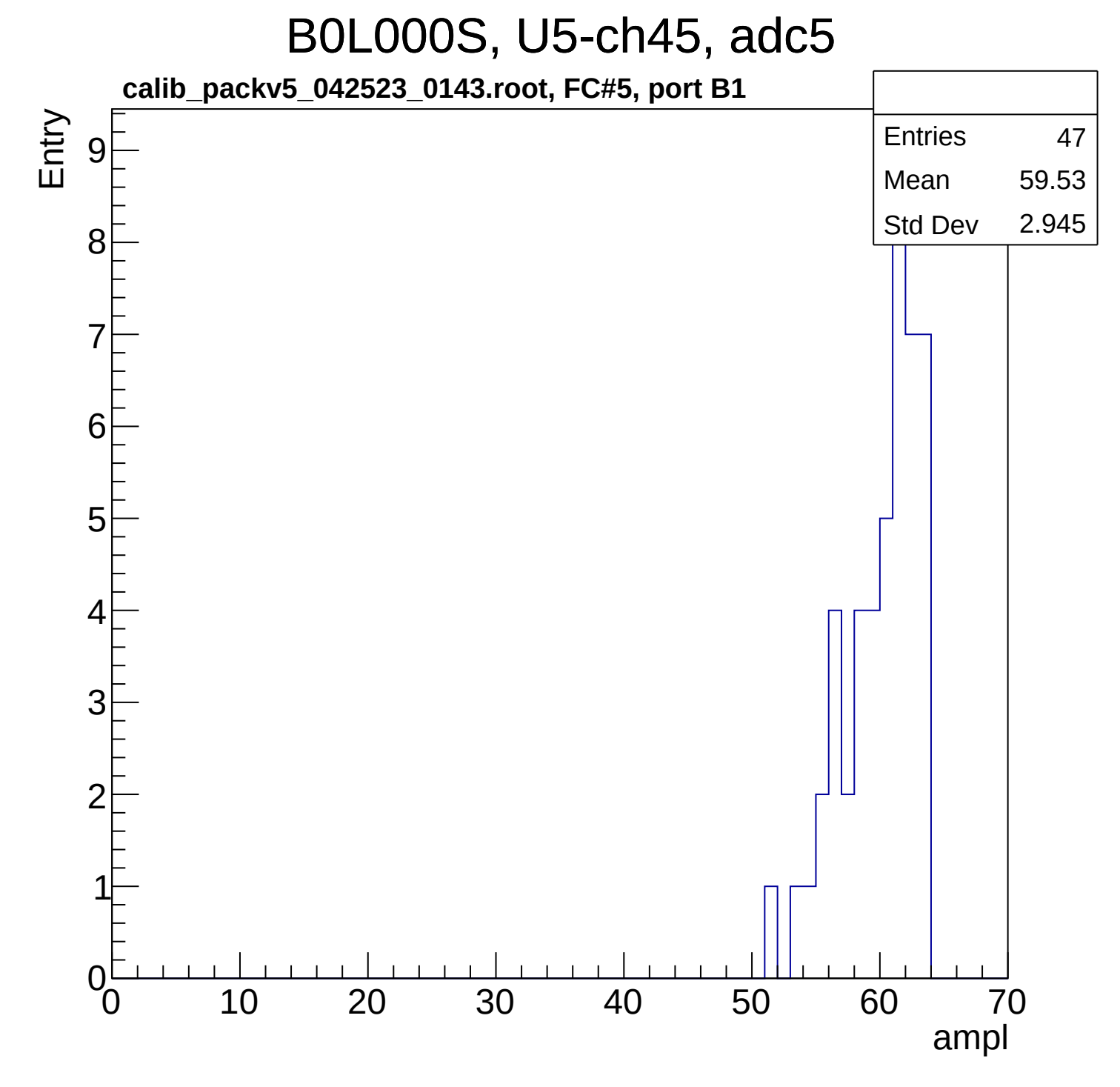
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch45, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.53
Std Dev 2.945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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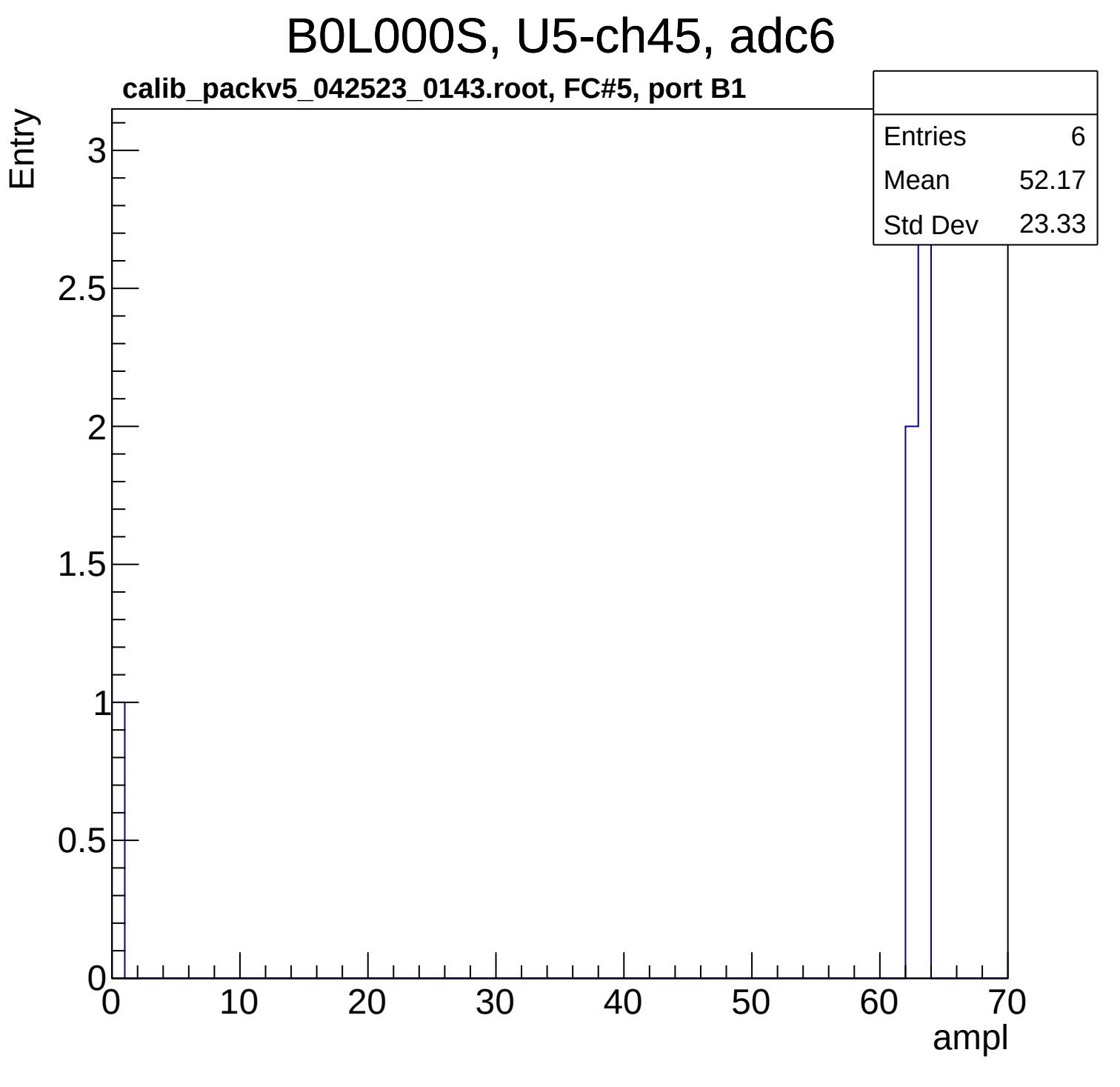
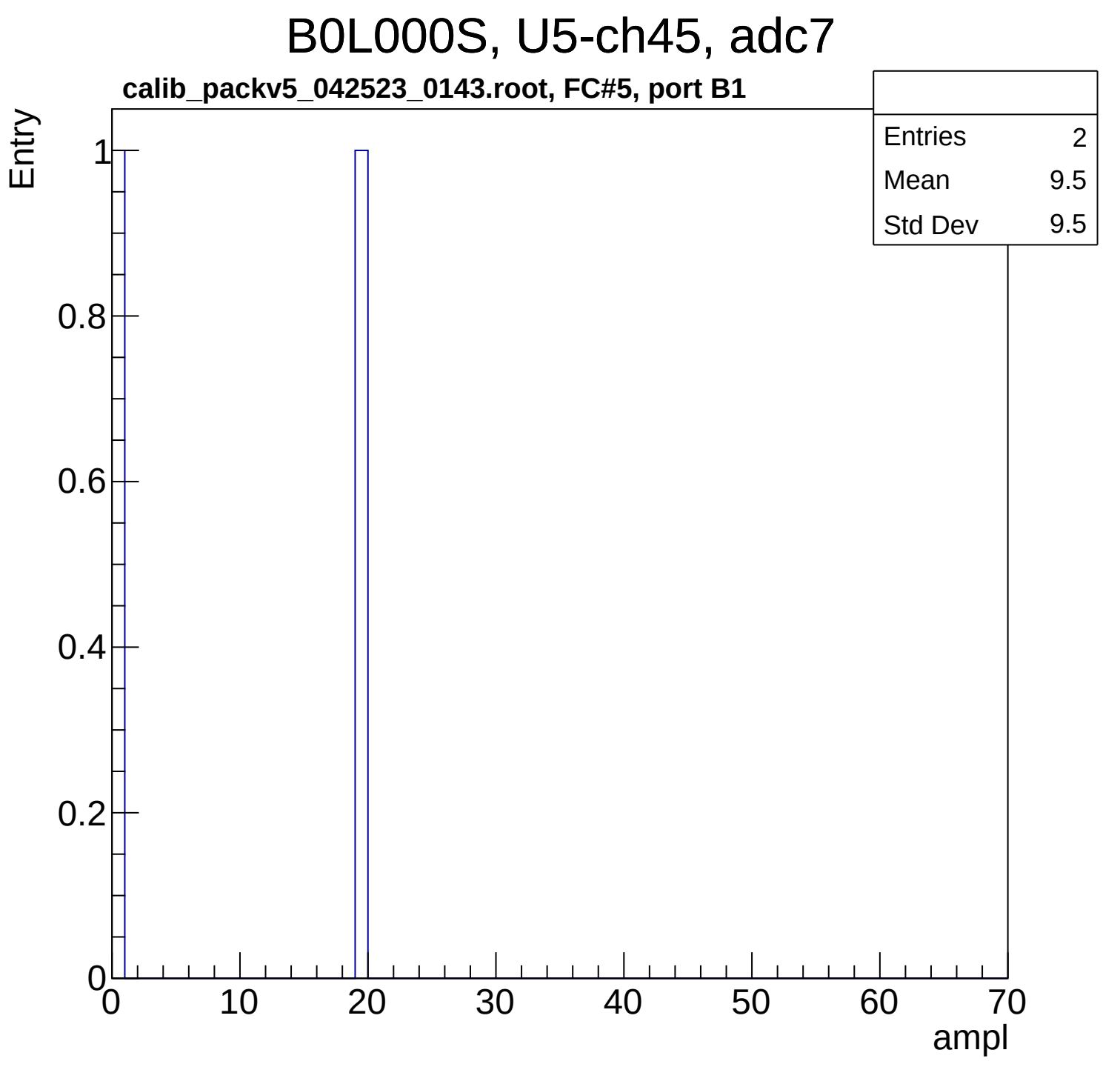

B0L000S, U5-ch45, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 52.17
Std Dev 23.33
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch45, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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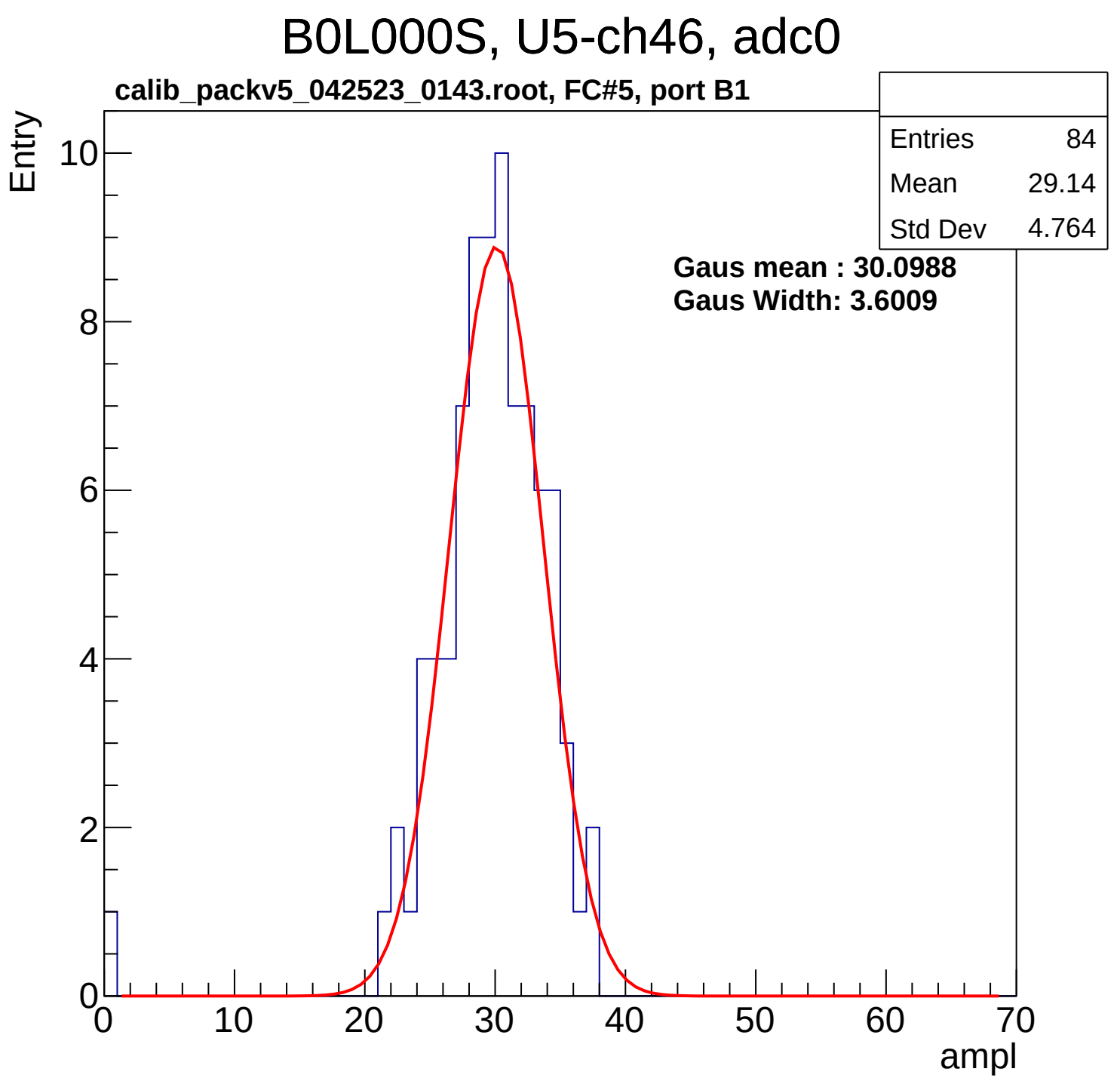
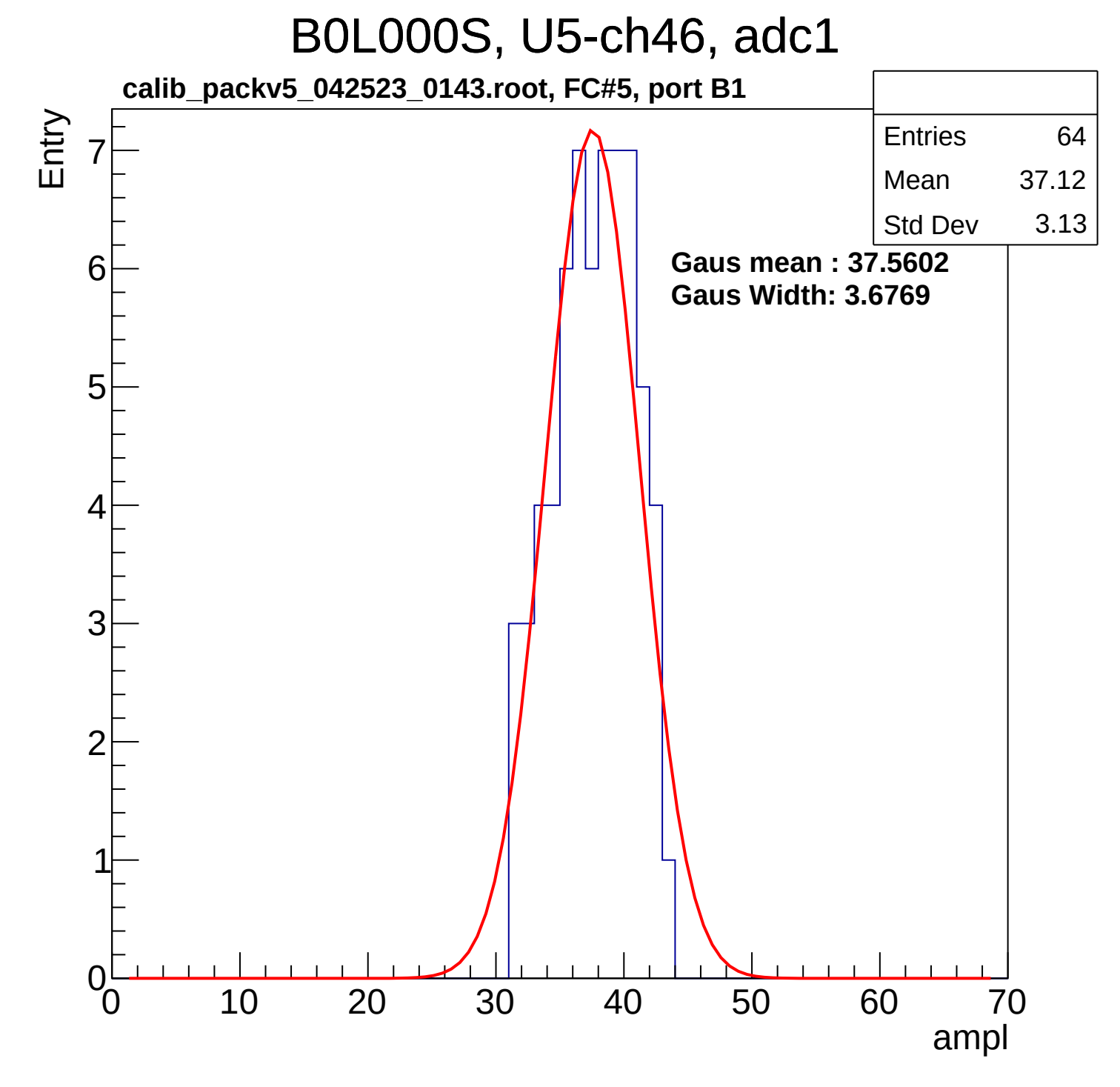
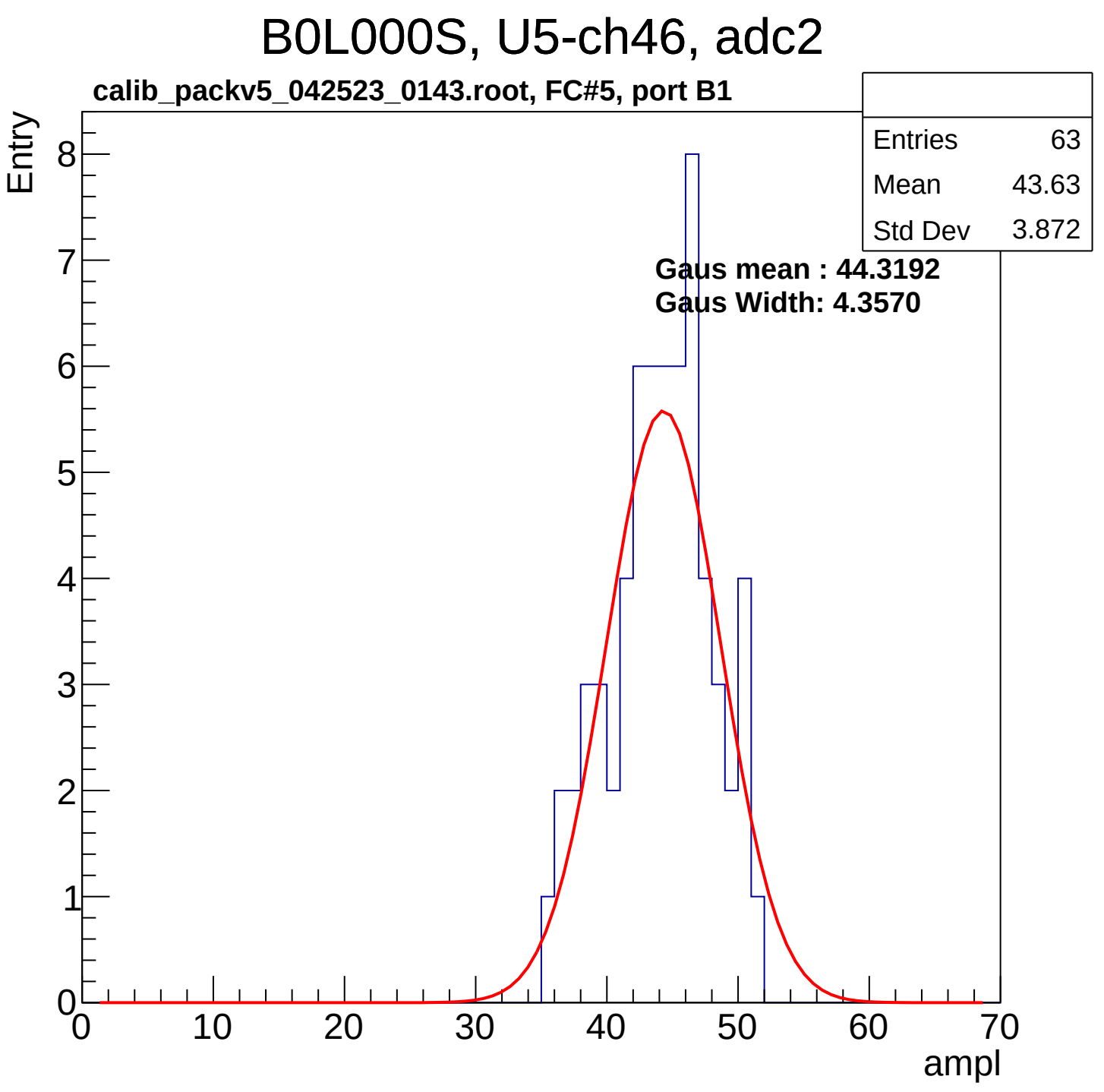
50
60
70
ampl


B0L000S, U5-ch46, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 29.14
Std Dev 4.764
Gaus mean : 30.0988
Gaus Width: 3.6009
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch46, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 37.12
Std Dev 3.13
Gaus mean : 37.5602
Gaus Width: 3.6769
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch46, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.63
Std Dev 3.872
Gaus mean : 44.3192
Gaus Width: 4.3570
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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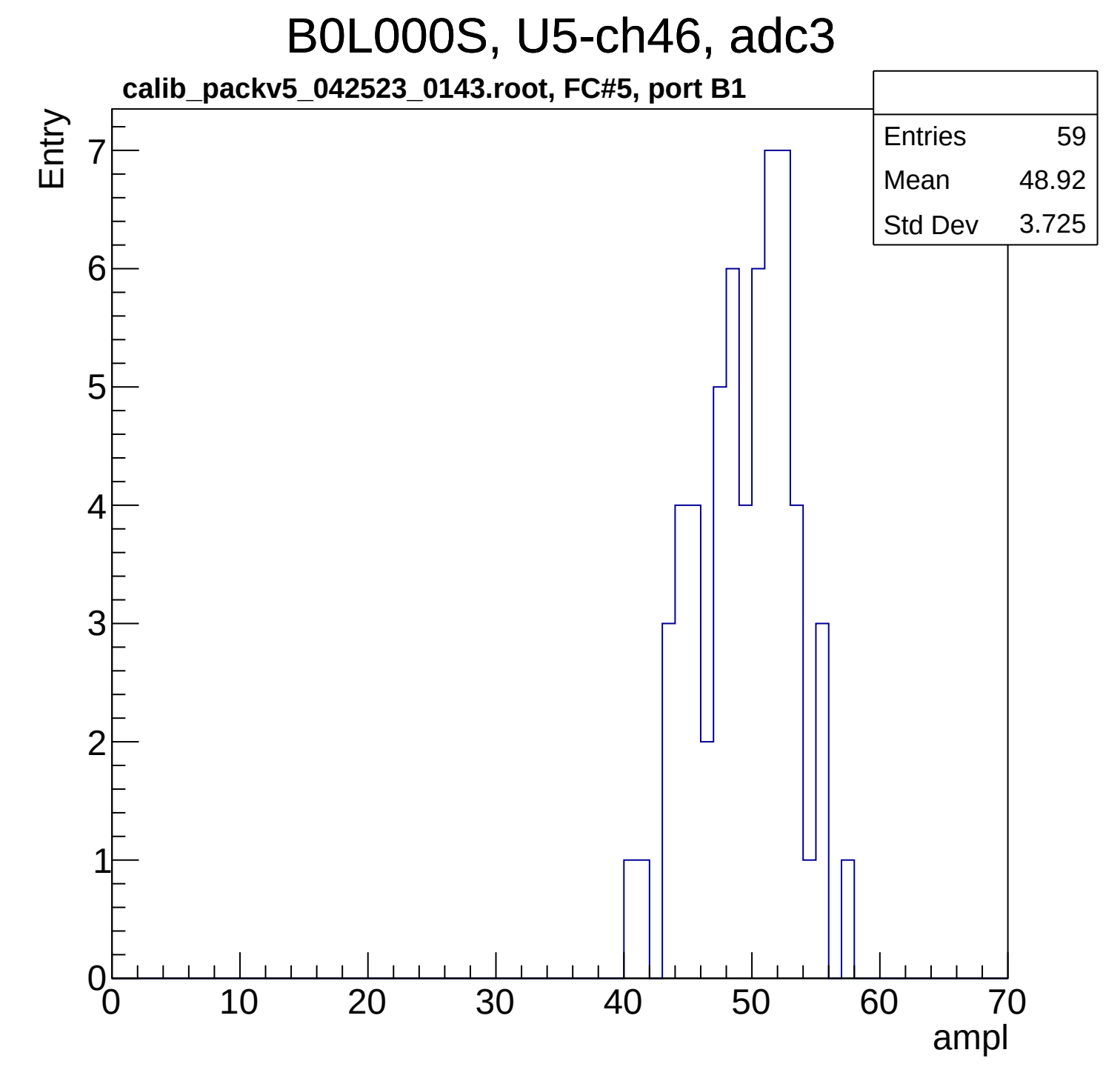
B0L000S, U5-ch46, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 48.92
Std Dev 3.725
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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60
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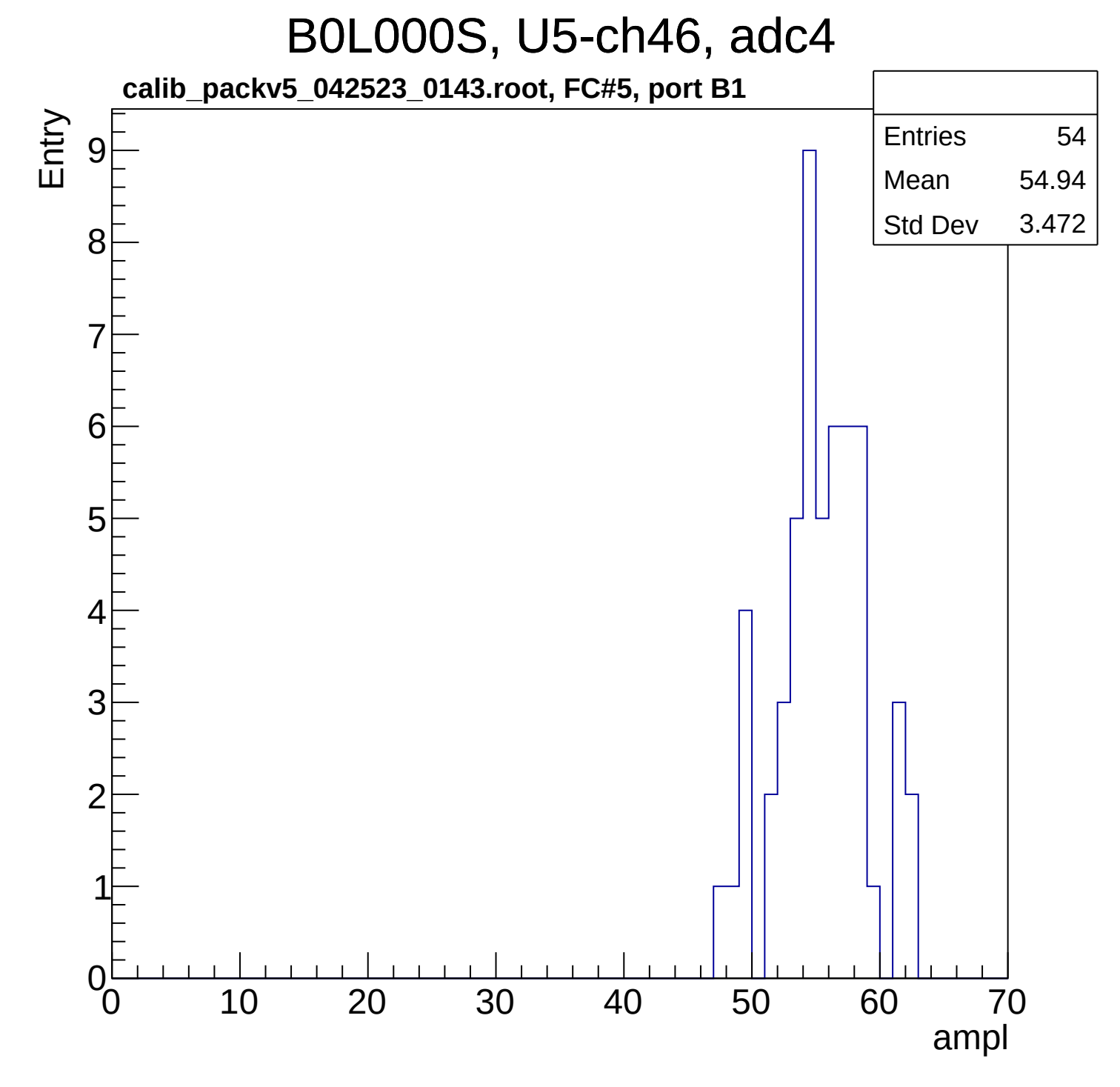
B0L000S, U5-ch46, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.94
Std Dev 3.472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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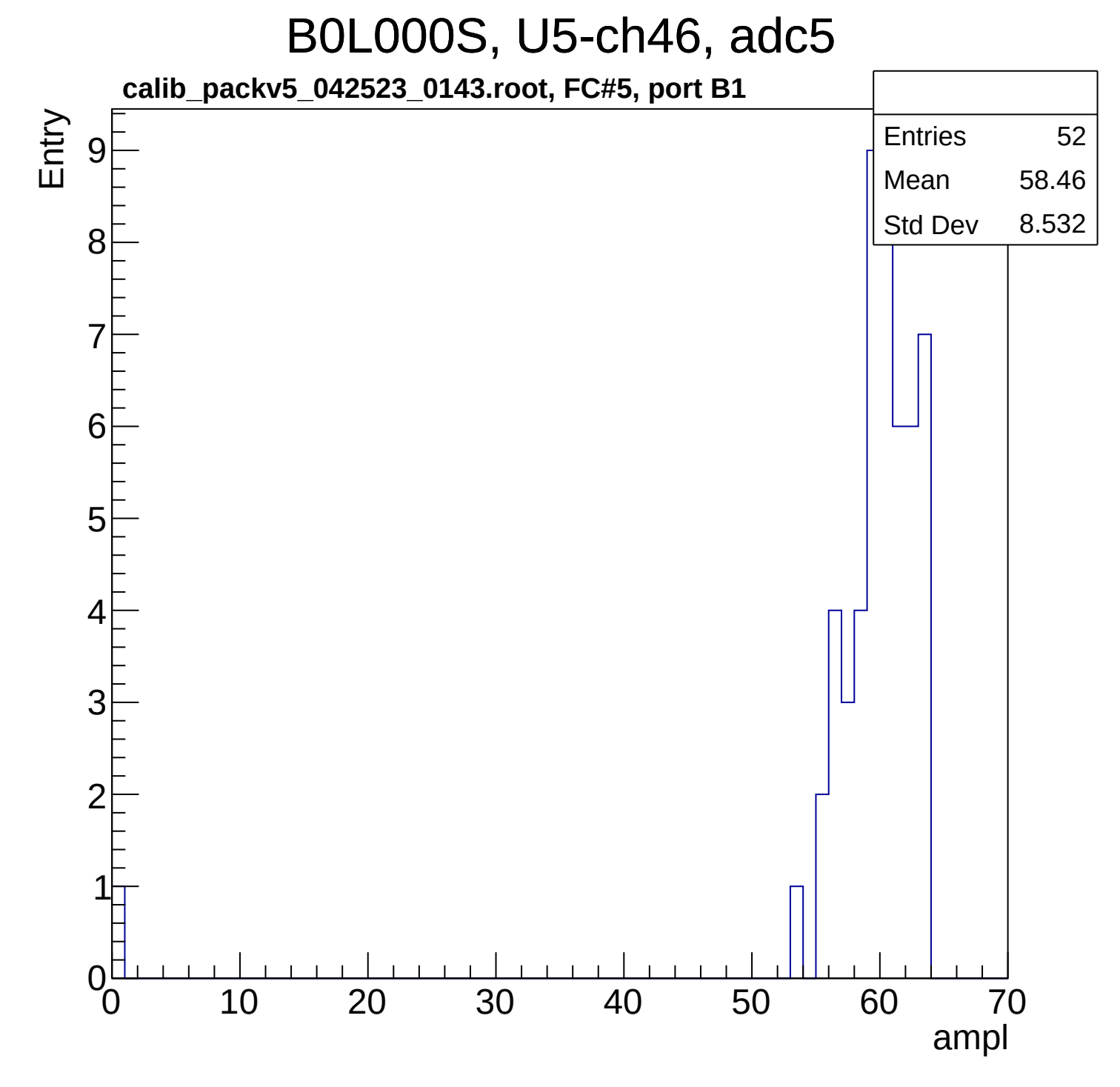
B0L000S, U5-ch46, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 58.46
Std Dev 8.532
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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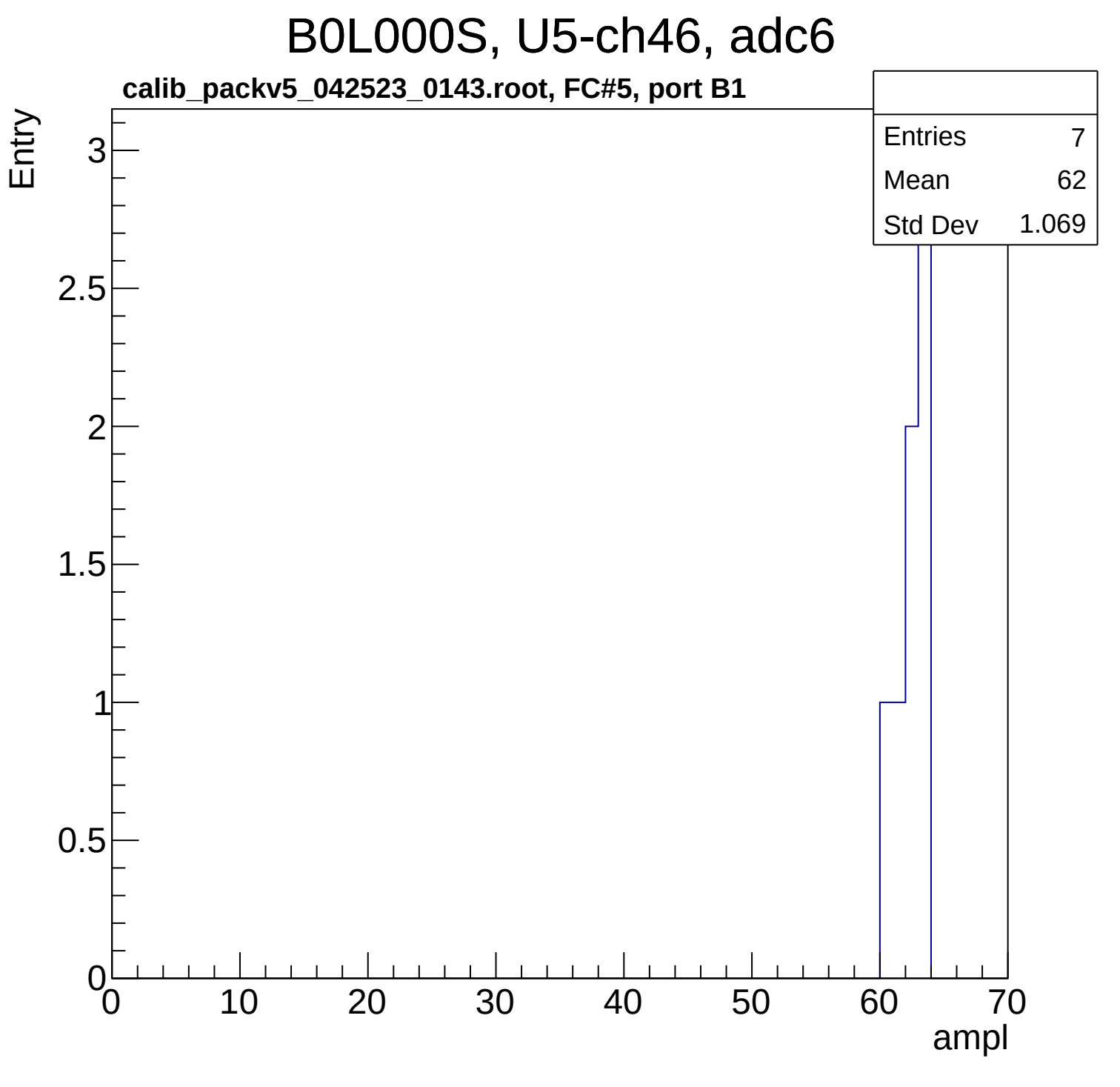
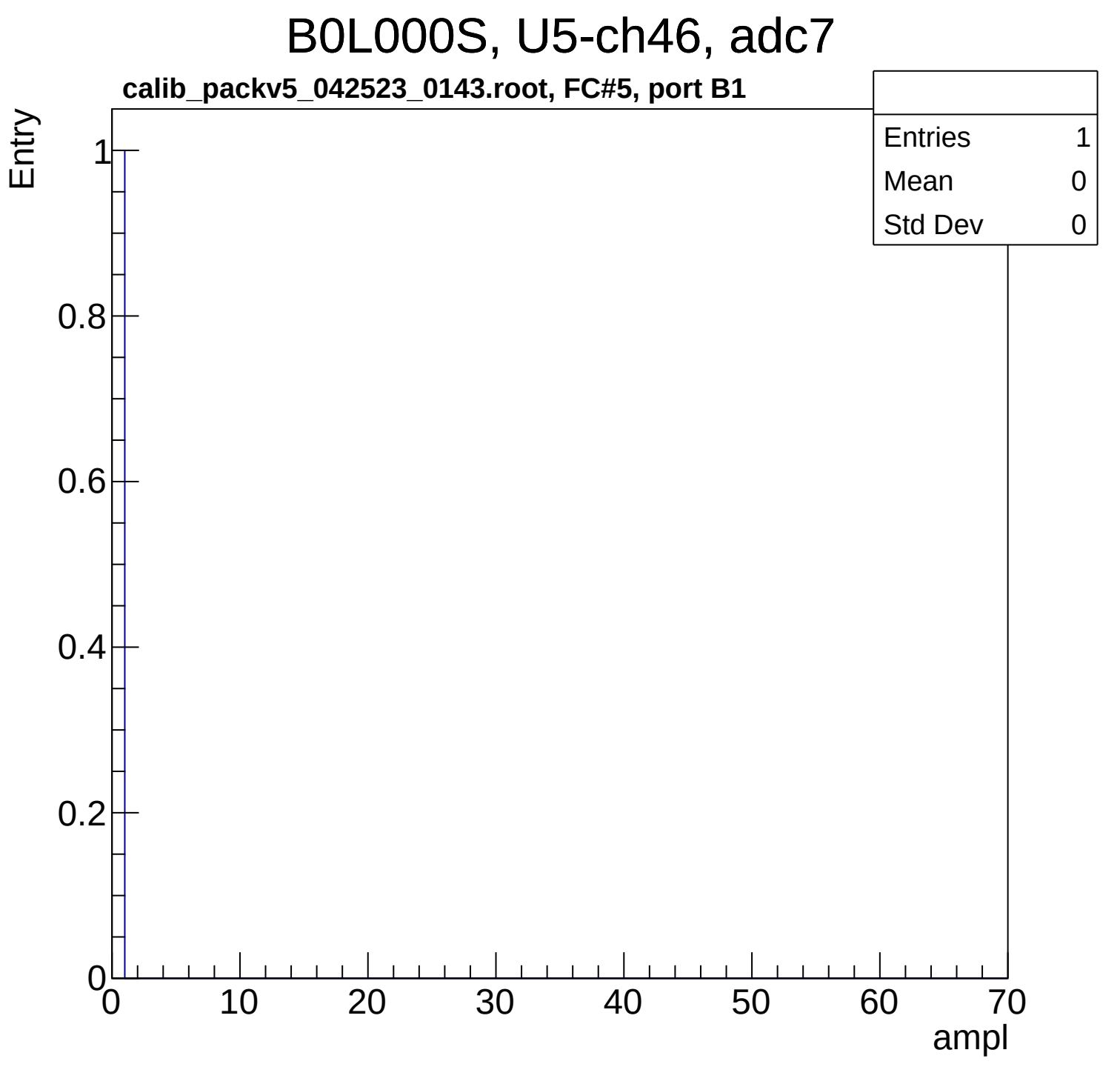
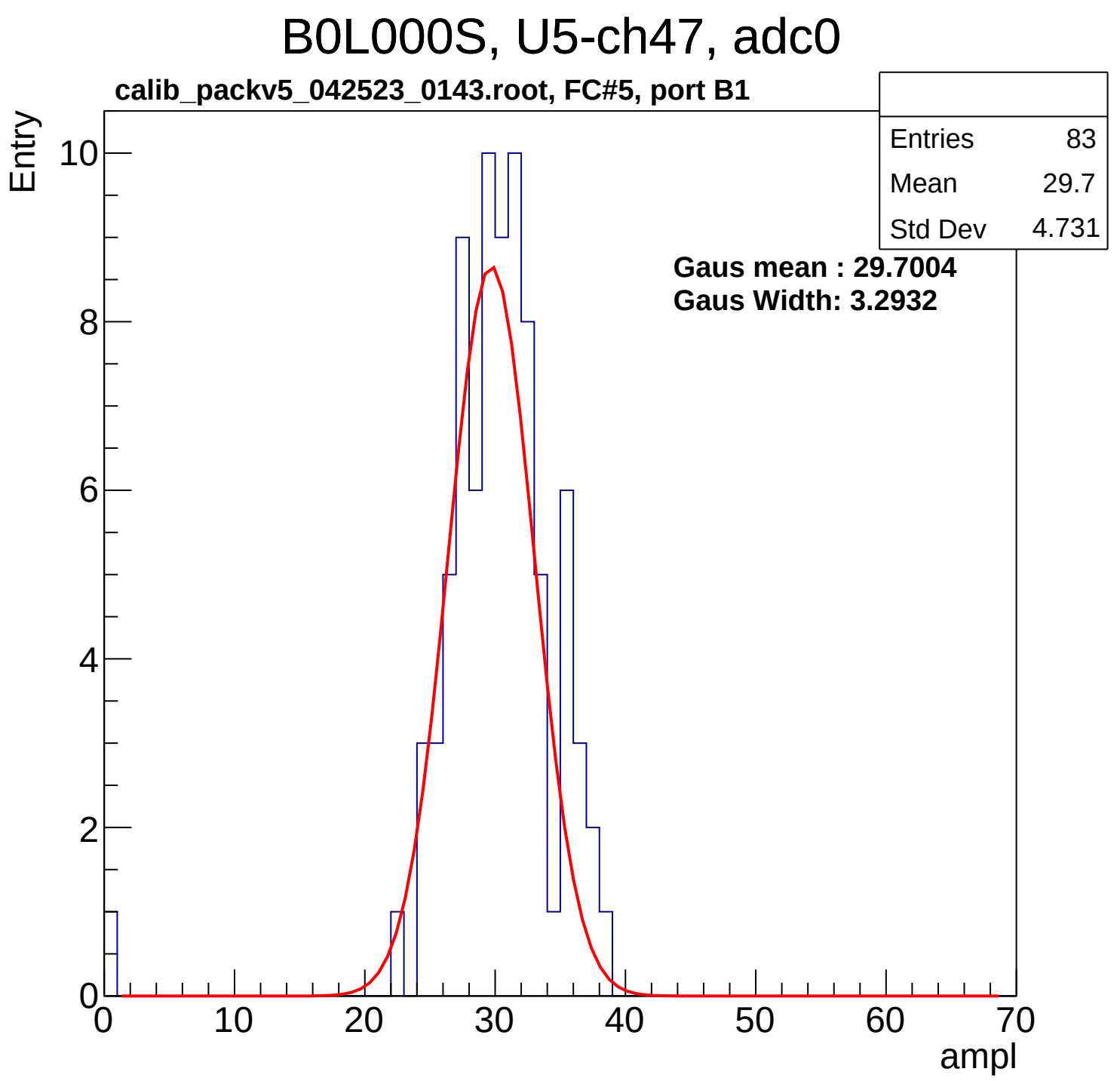
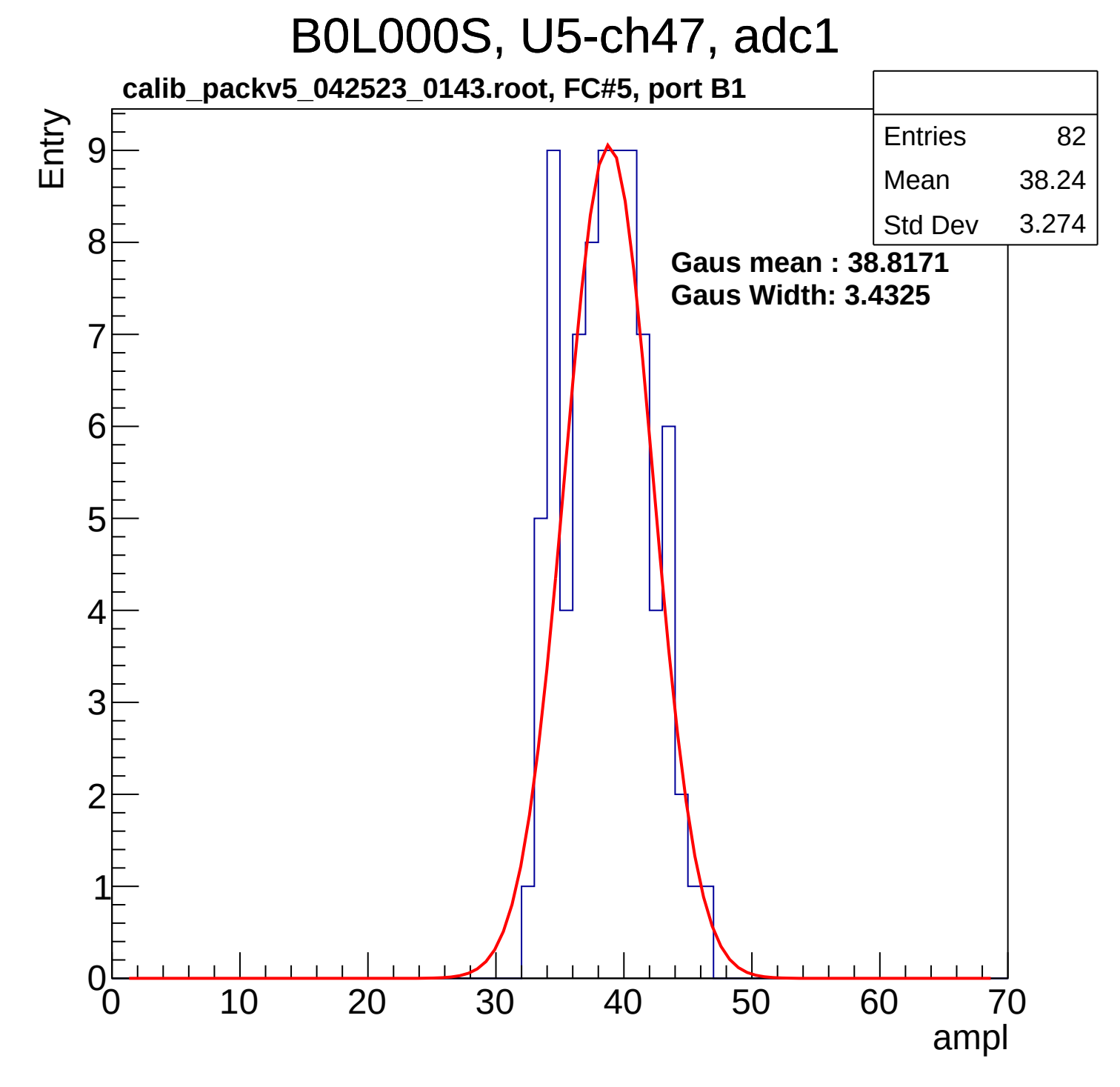

B0L000S, U5-ch46, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62
Std Dev 1.069
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch46, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch47, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 29.7
Std Dev 4.731
Gaus mean : 29.7004
Gaus Width: 3.2932
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 38.24
Std Dev 3.274
Gaus mean : 38.8171
Gaus Width: 3.4325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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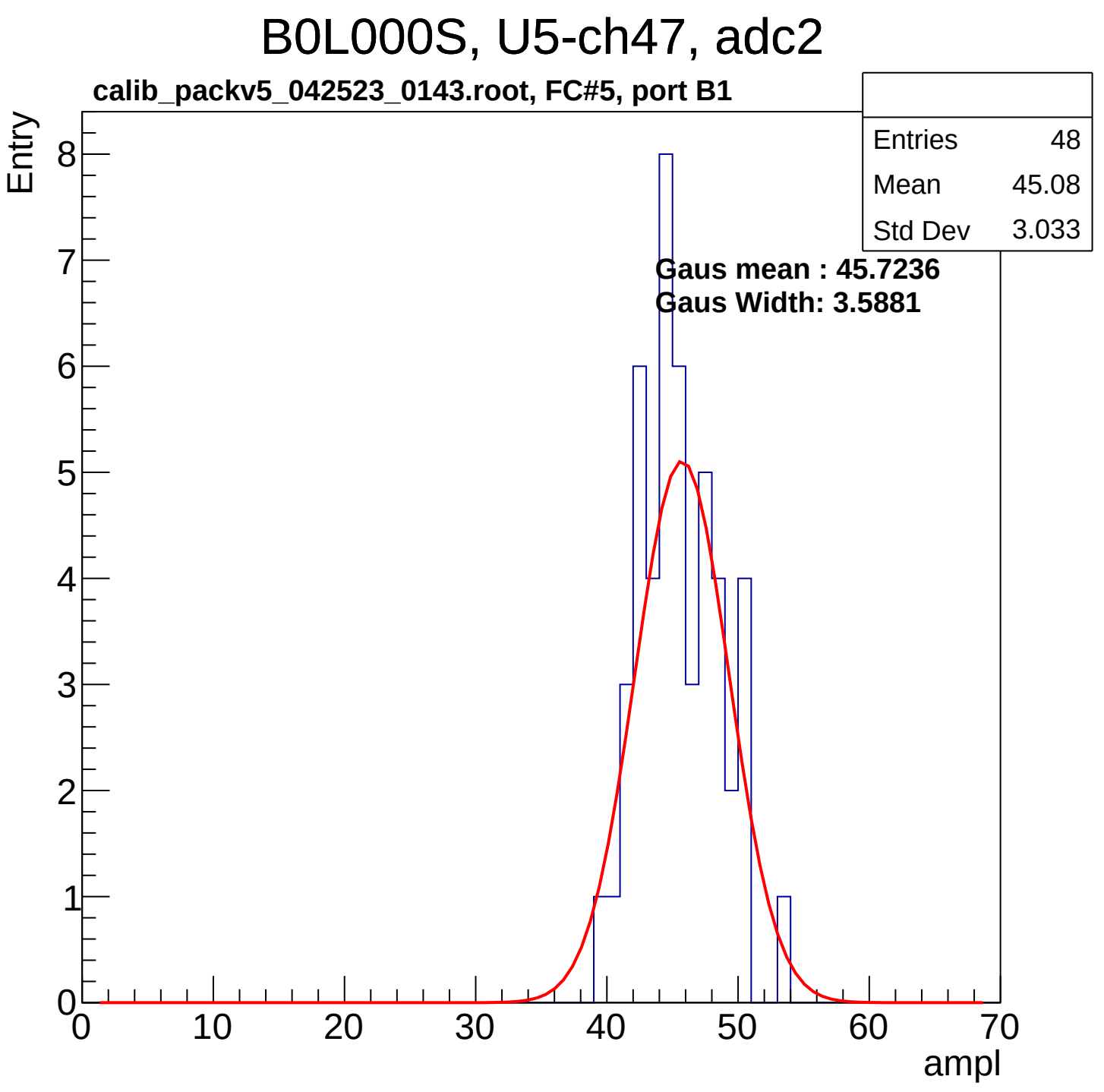
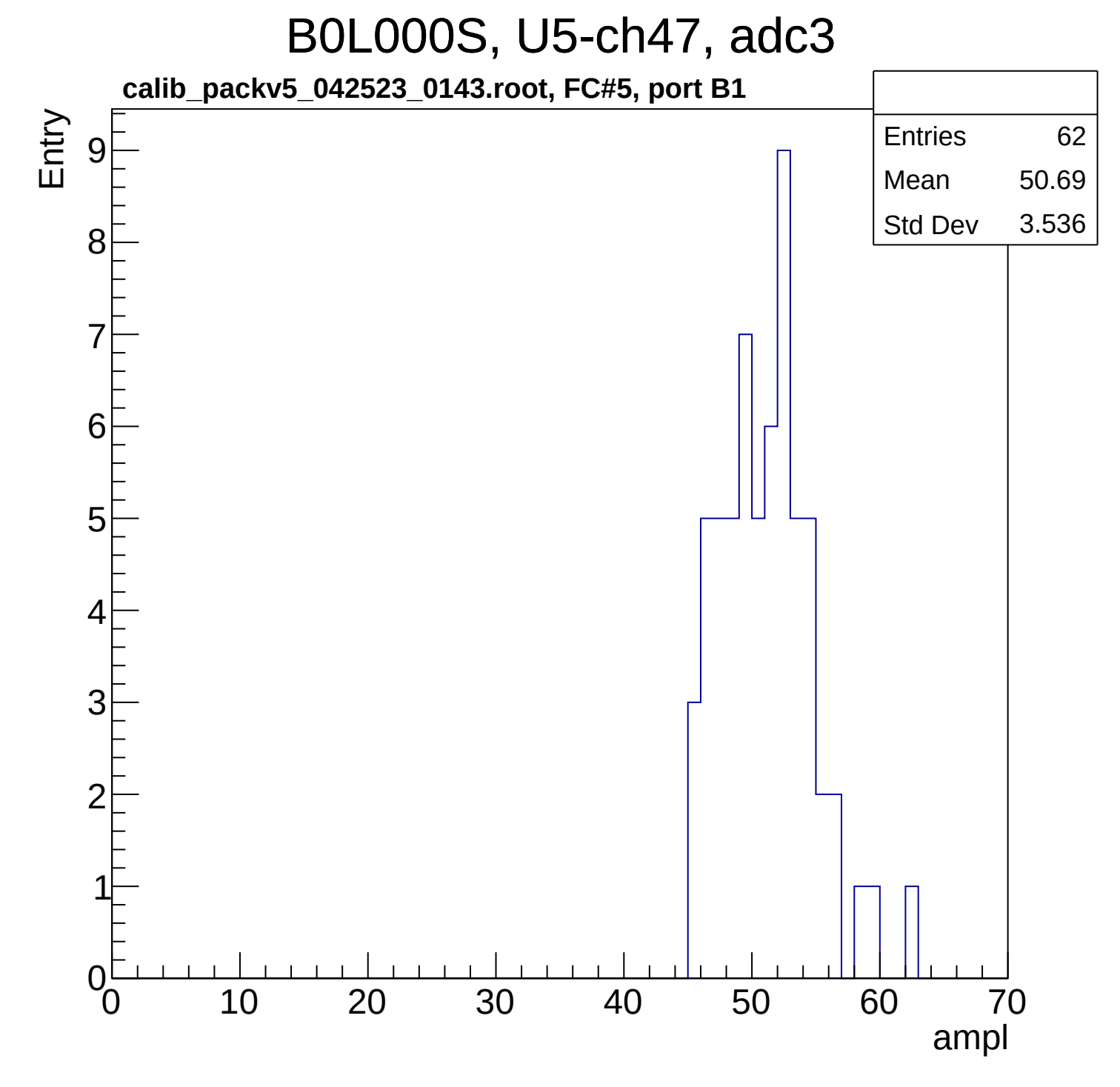
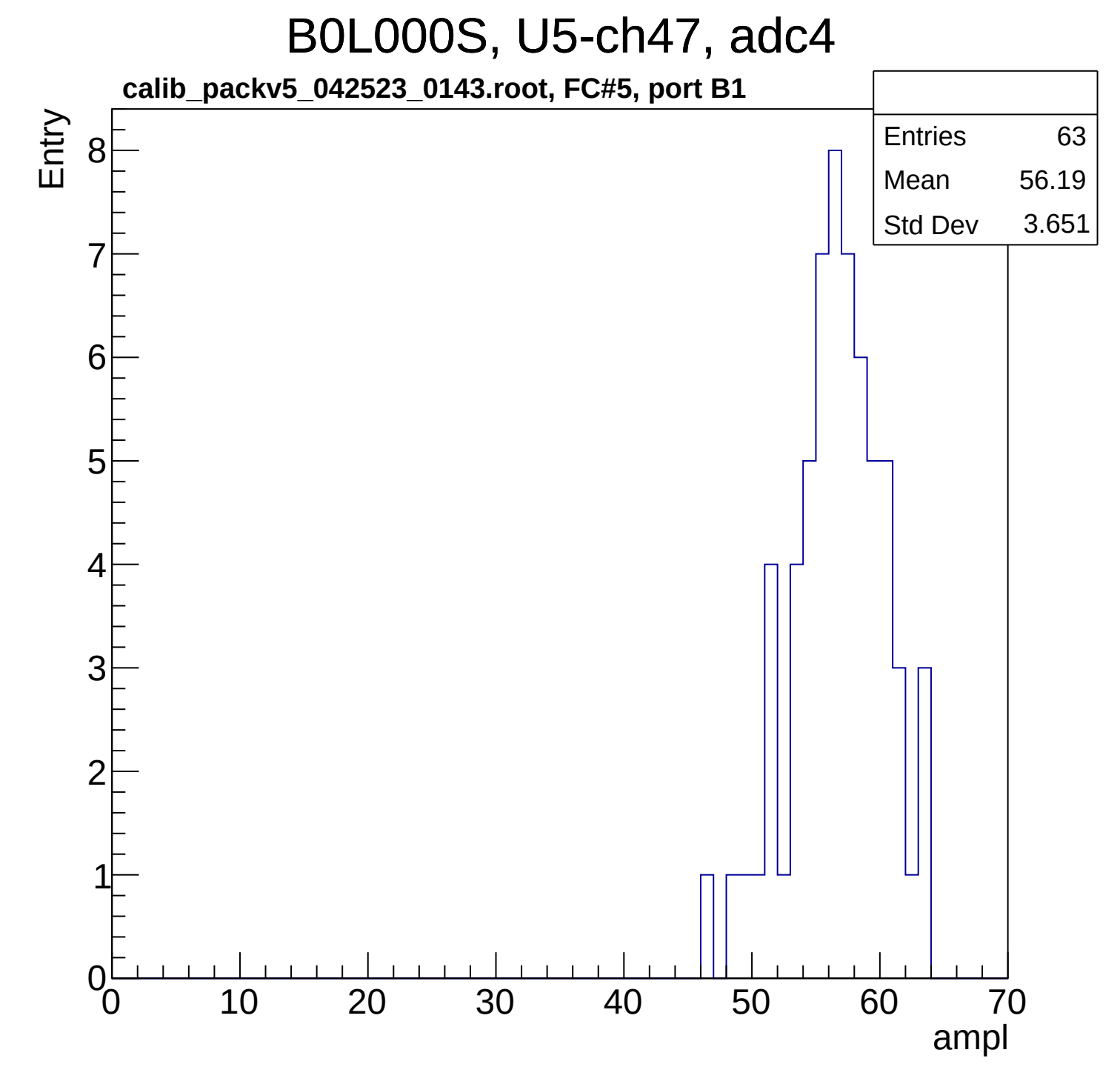
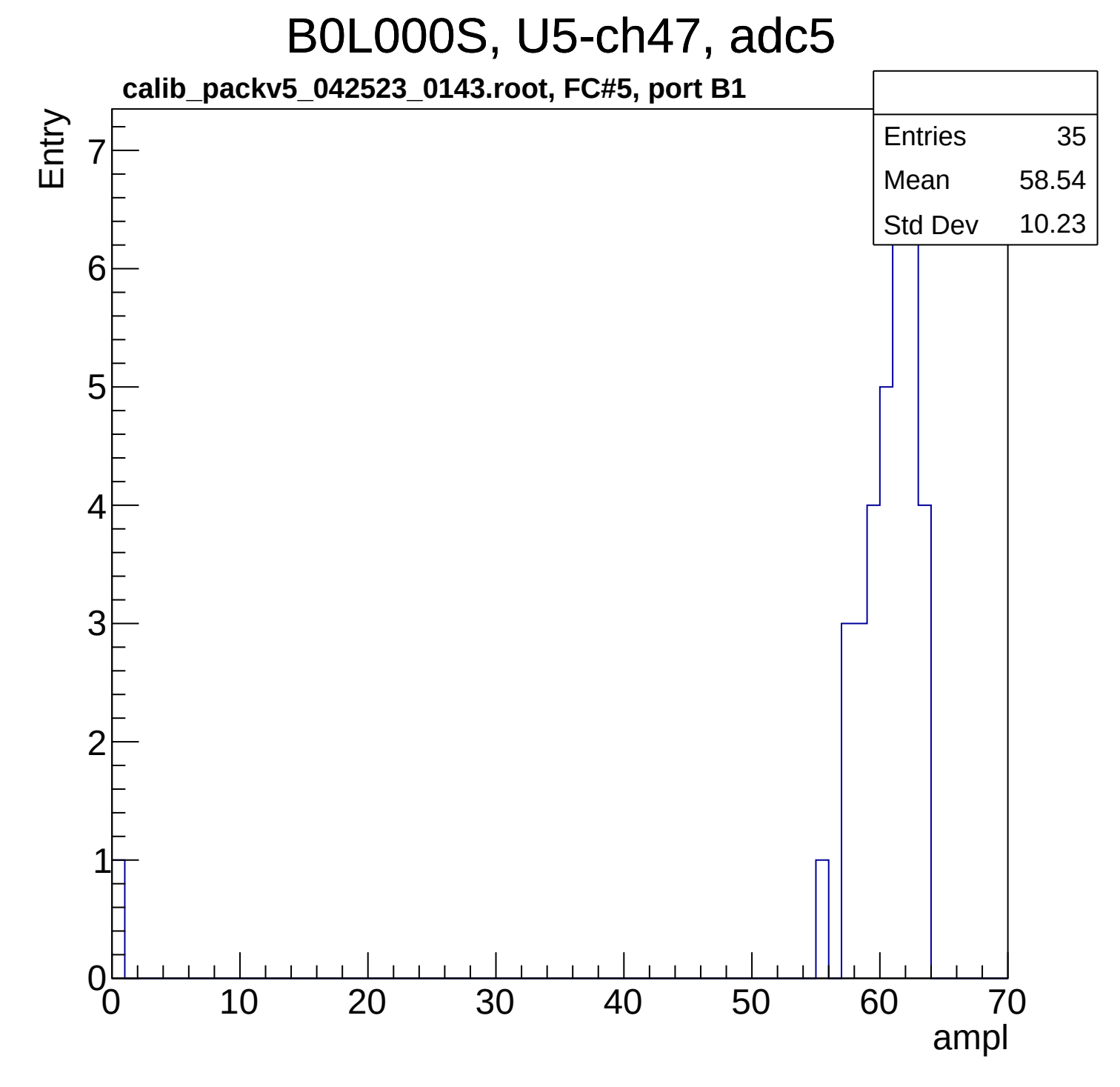
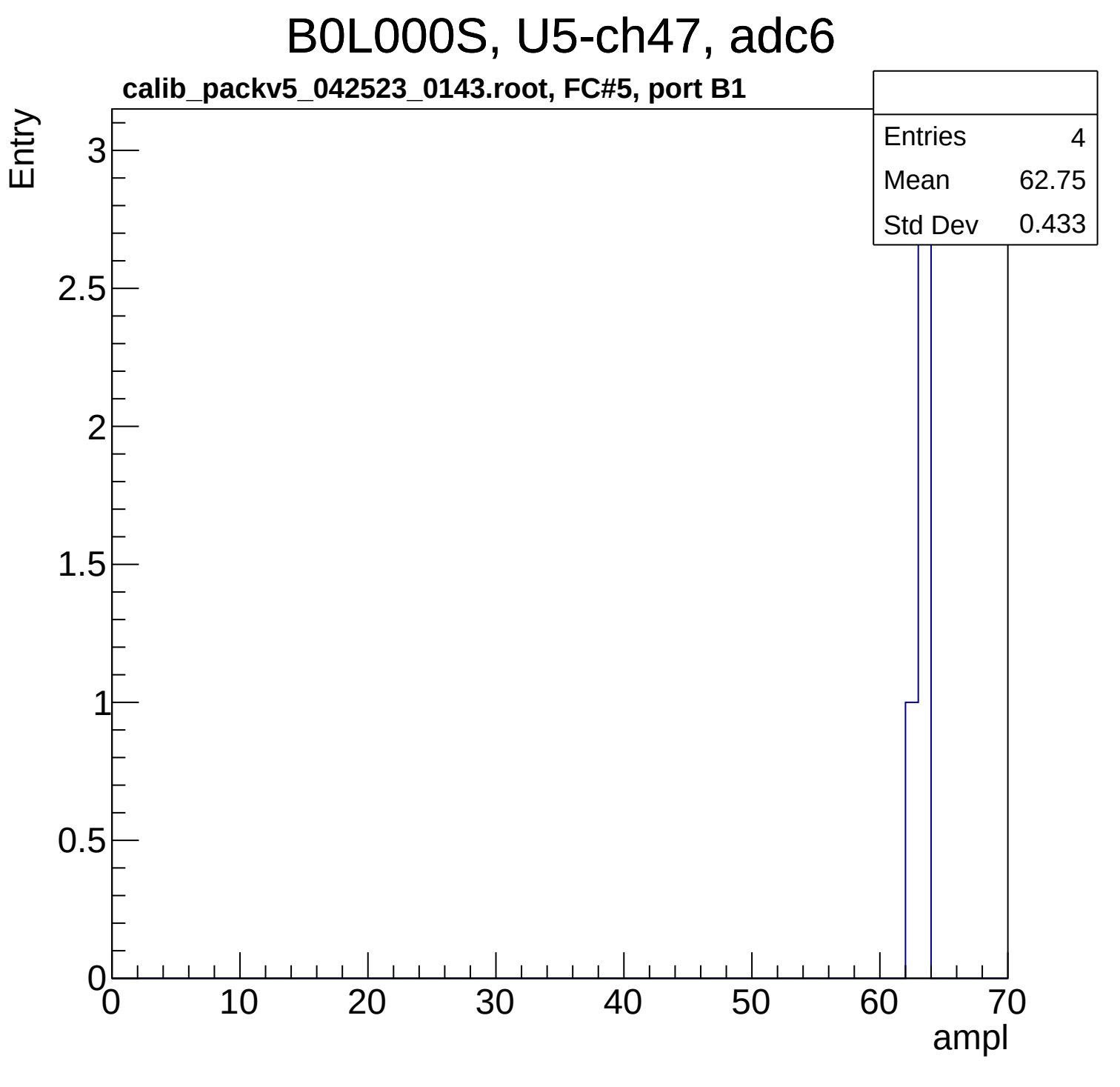

B0L000S, U5-ch47, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 45.08
Std Dev 3.033
Gaus mean : 45.7236
Gaus Width: 3.5881
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 50.69
Std Dev 3.536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.19
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.54
Std Dev 10.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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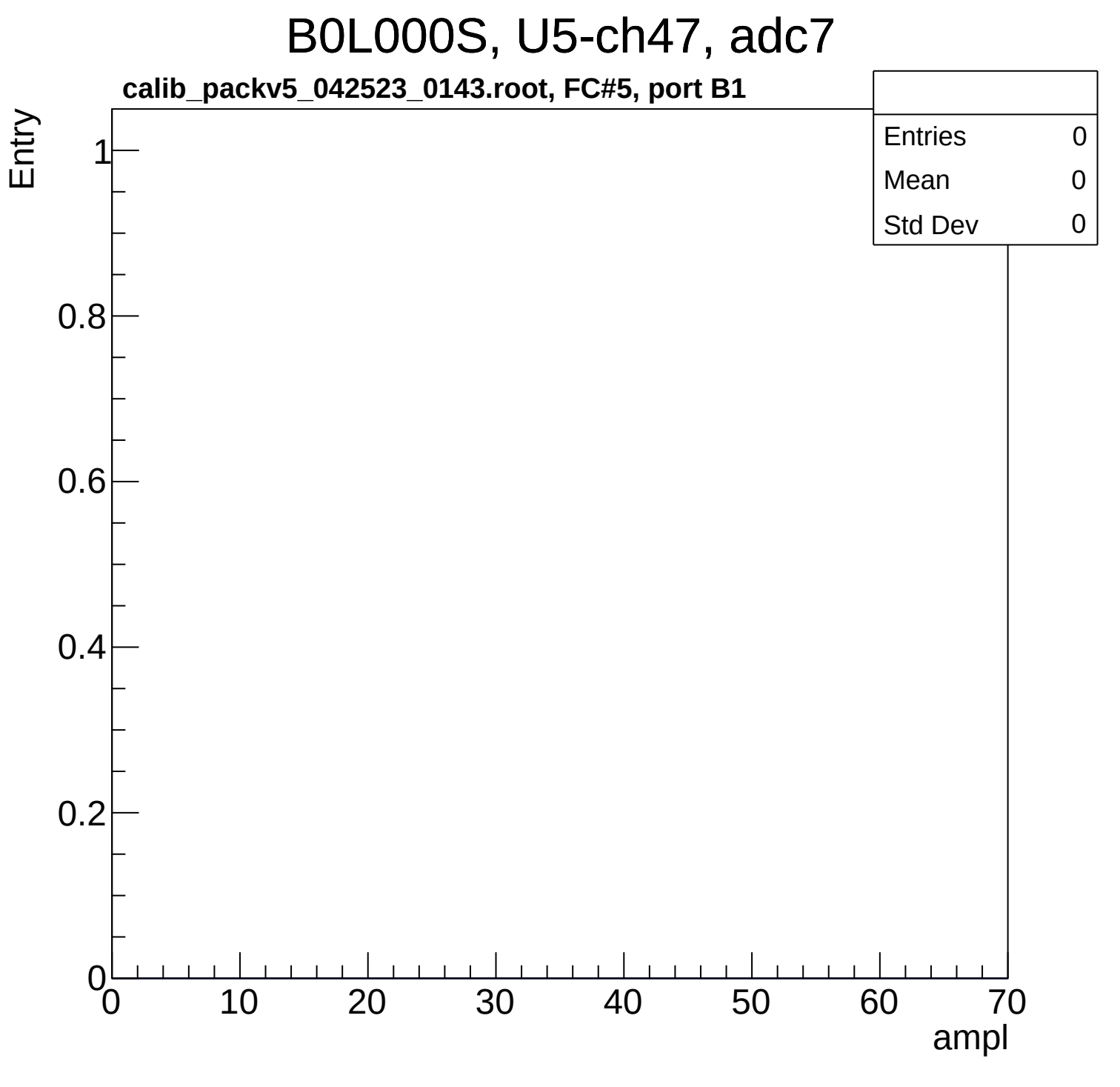
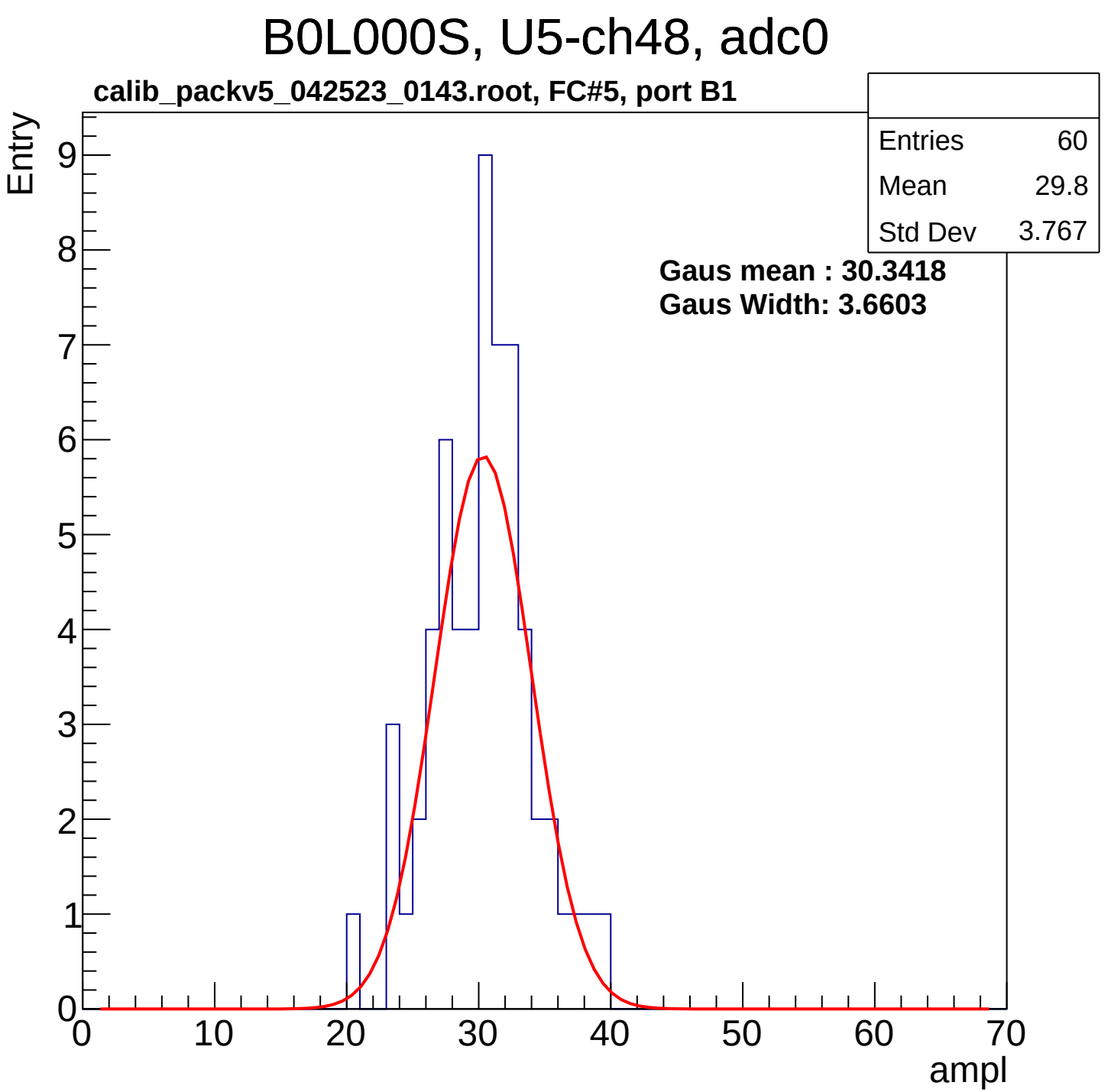
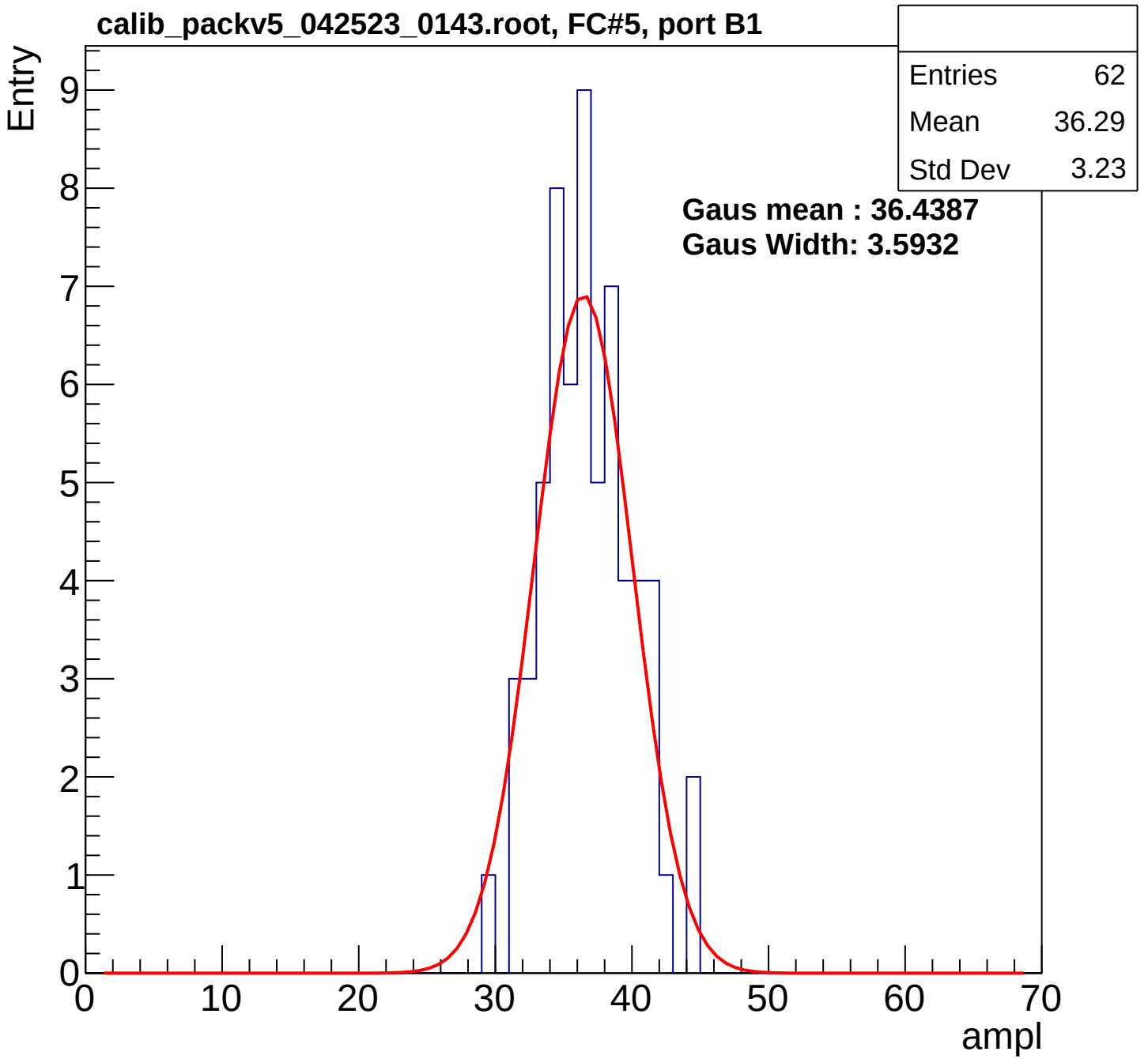
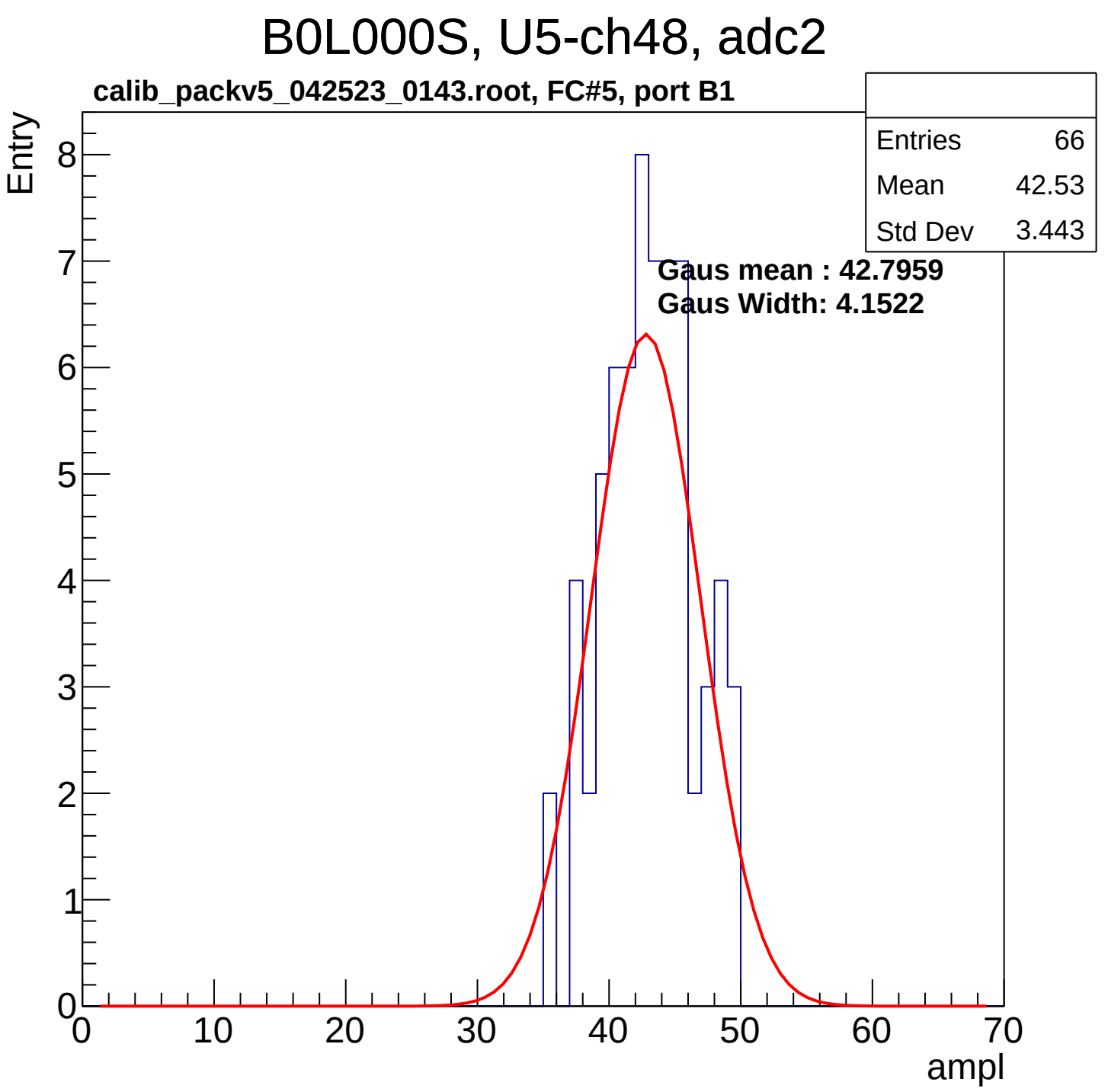
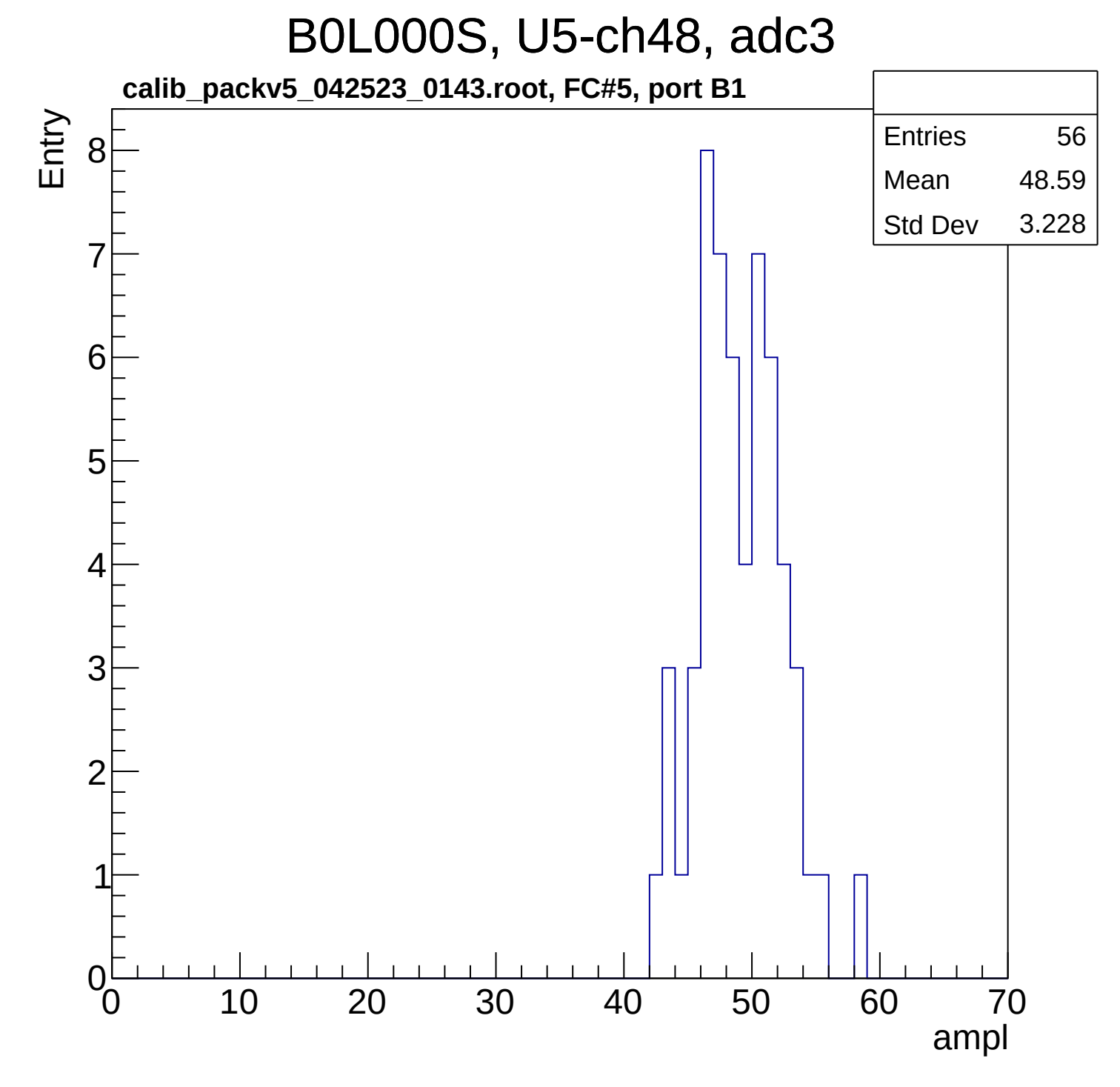
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch47, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch48, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 29.8
Std Dev 3.767
Gaus mean : 30.3418
Gaus Width: 3.6603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch48, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.29
Std Dev 3.23
Gaus mean : 36.4387
Gaus Width: 3.5932
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch48, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 42.53
Std Dev 3.443
Gaus mean : 42.7959
Gaus Width: 4.1522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch48, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 48.59
Std Dev 3.228
Entry
ampl
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1
2
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4
5
6
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0
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20
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60
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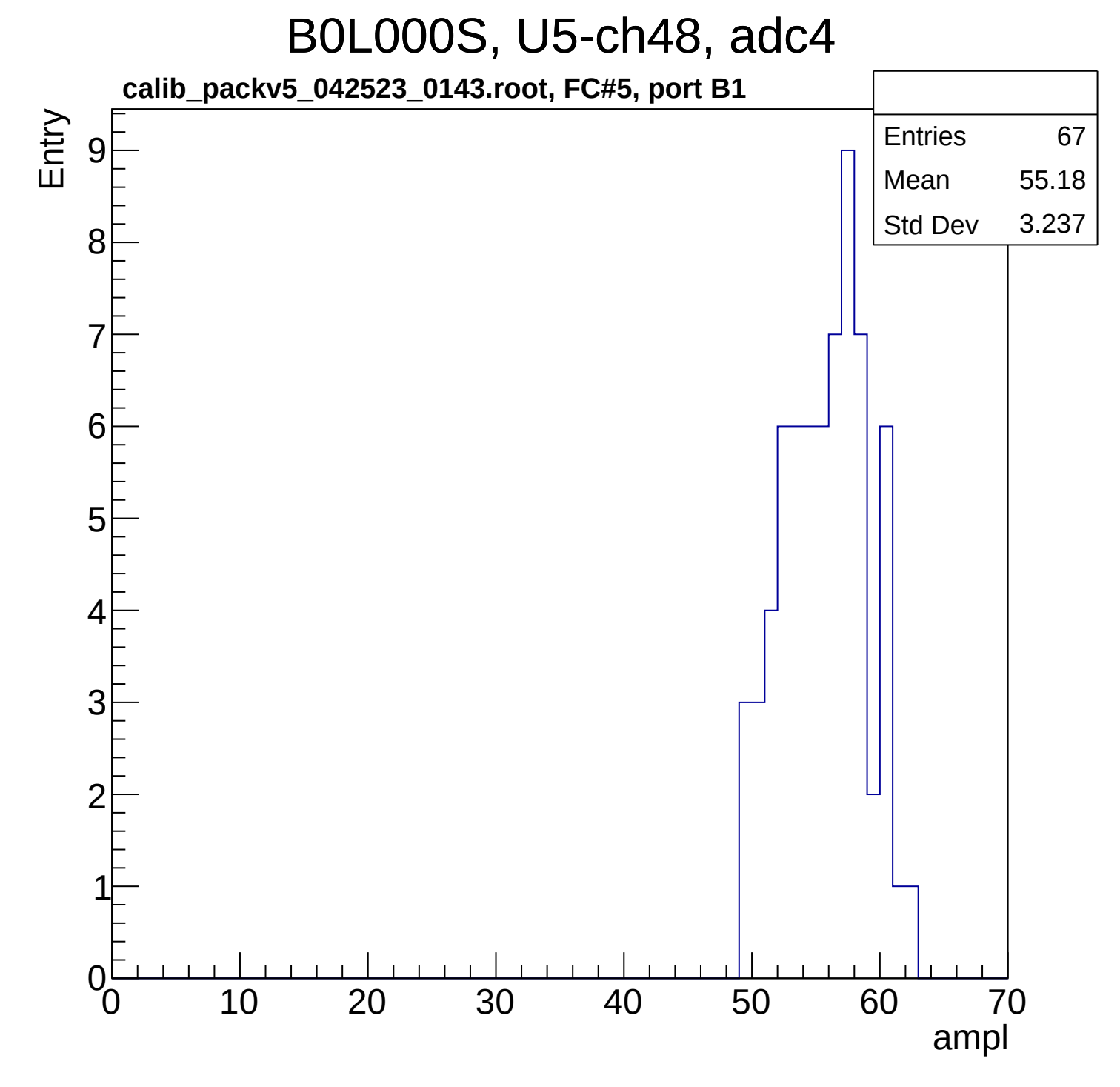
B0L000S, U5-ch48, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 55.18
Std Dev 3.237
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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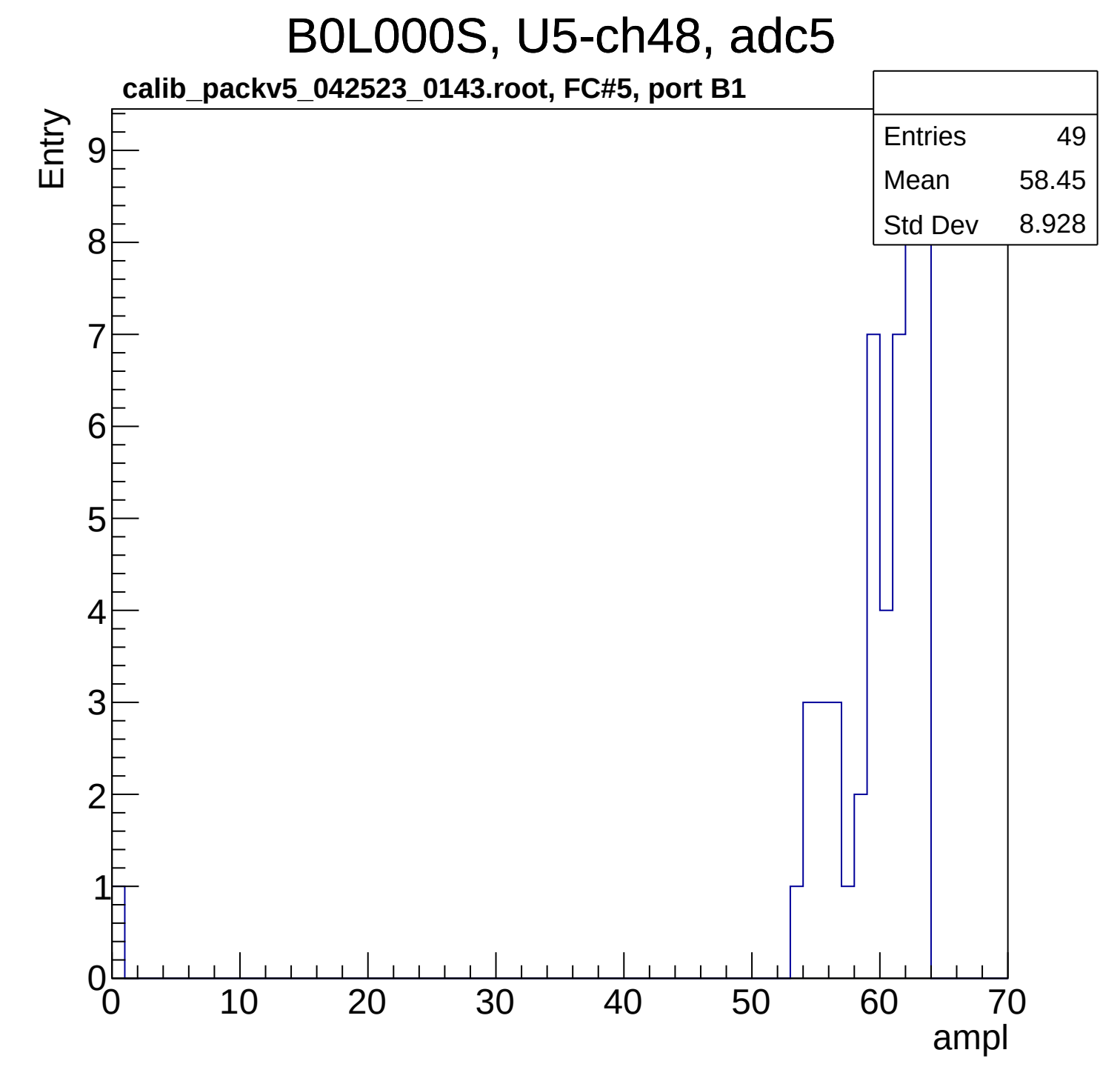
B0L000S, U5-ch48, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.45
Std Dev 8.928
Entry
ampl
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3
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5
6
7
8
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0
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60
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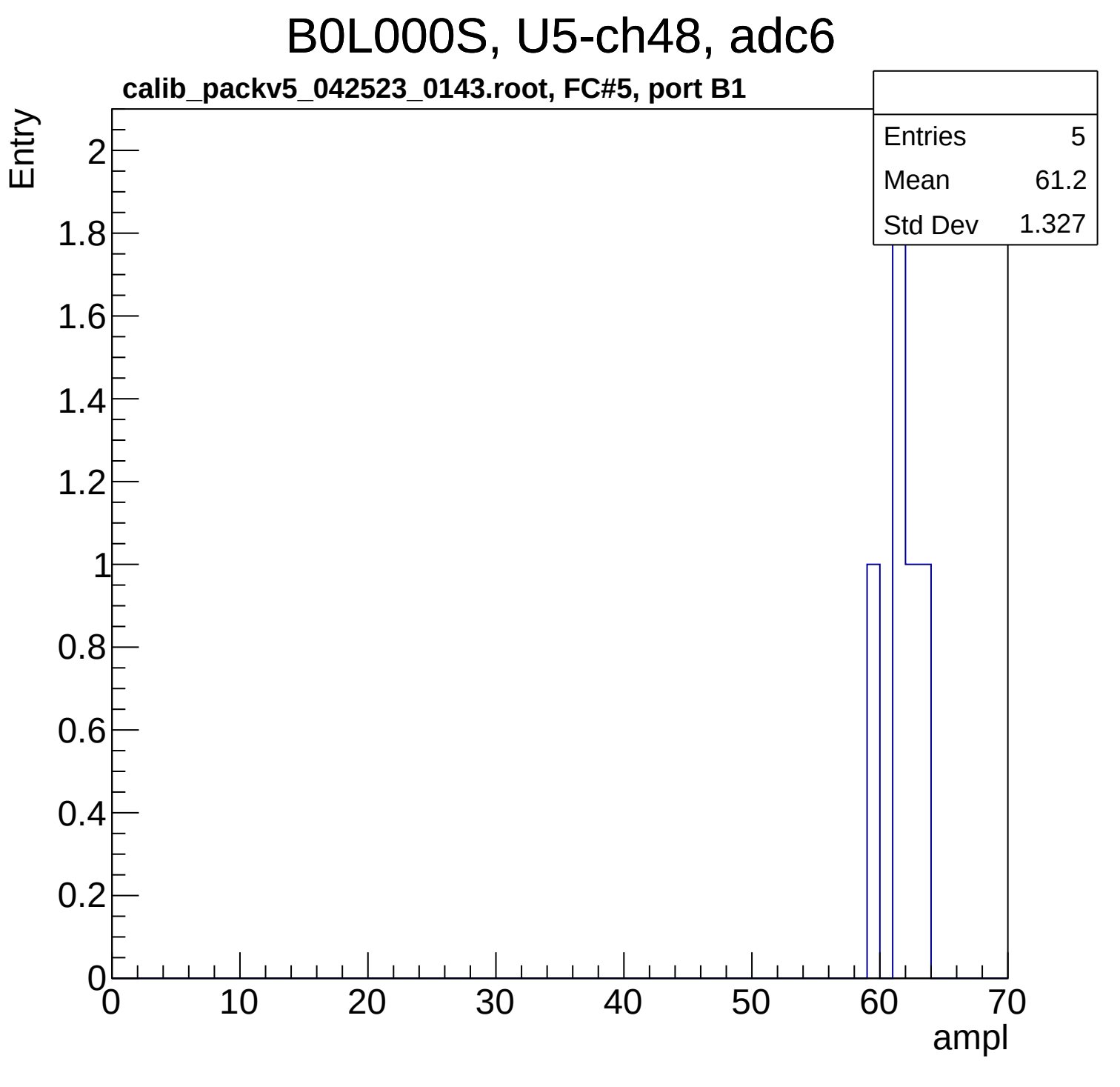
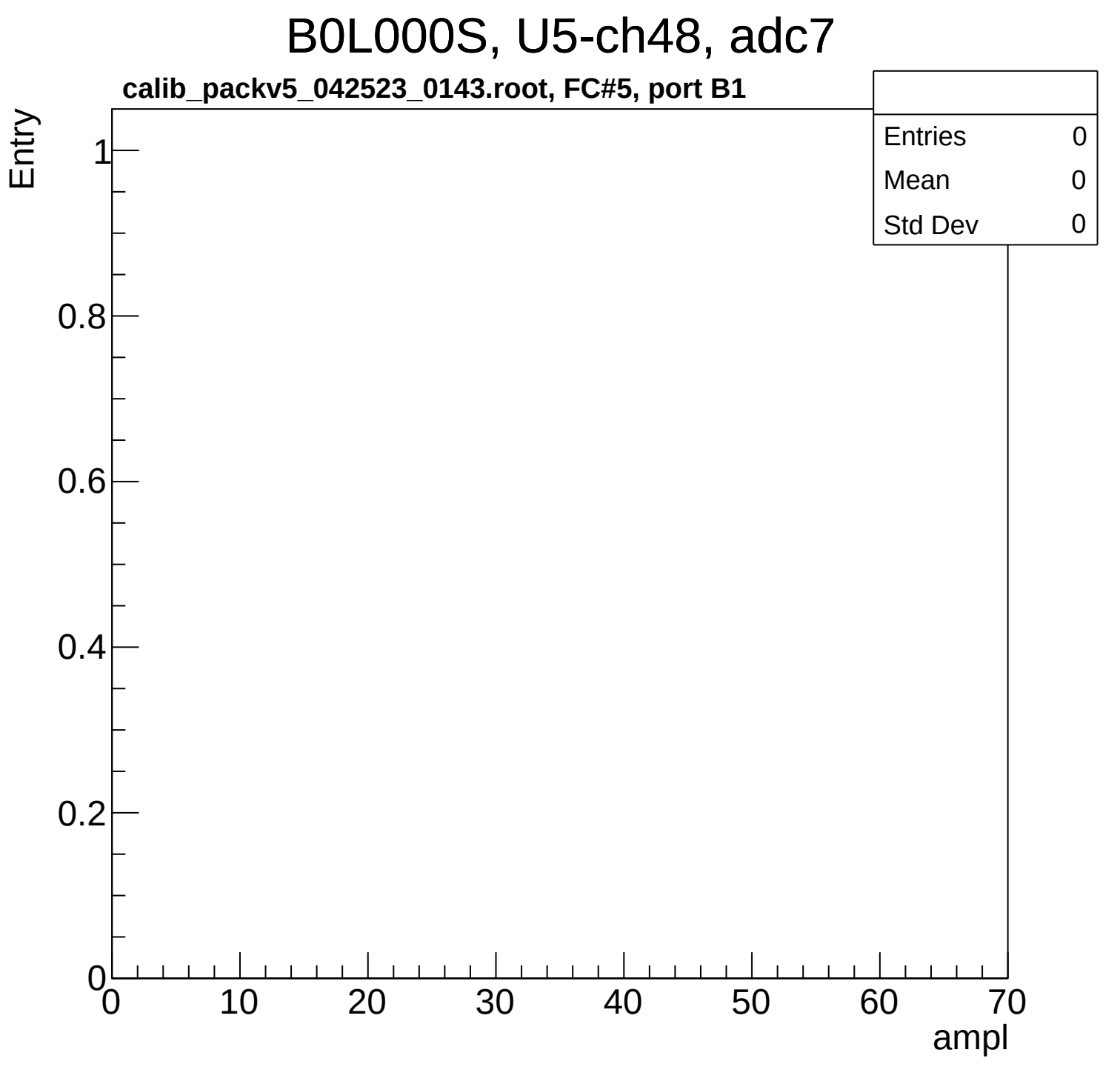
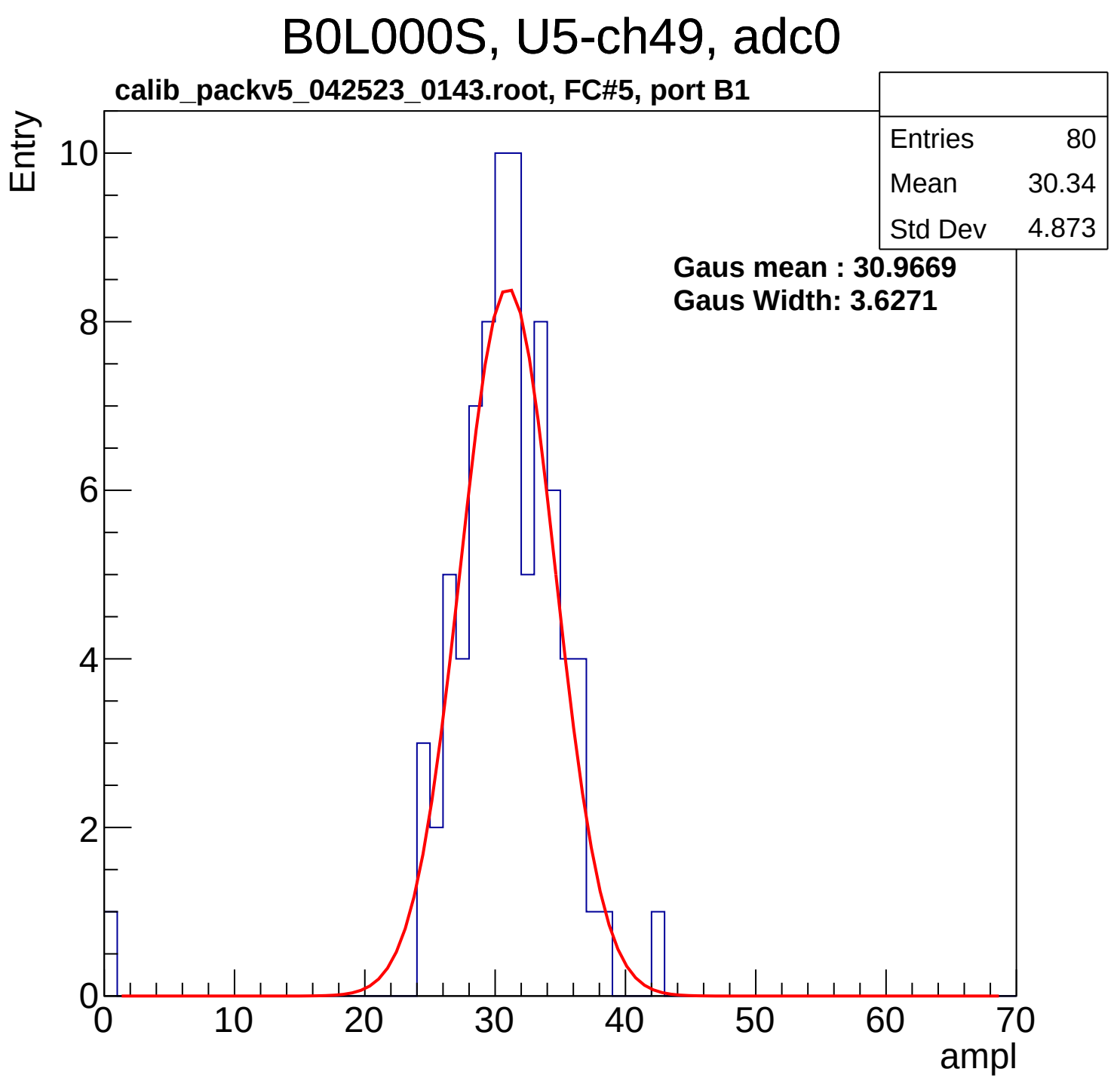
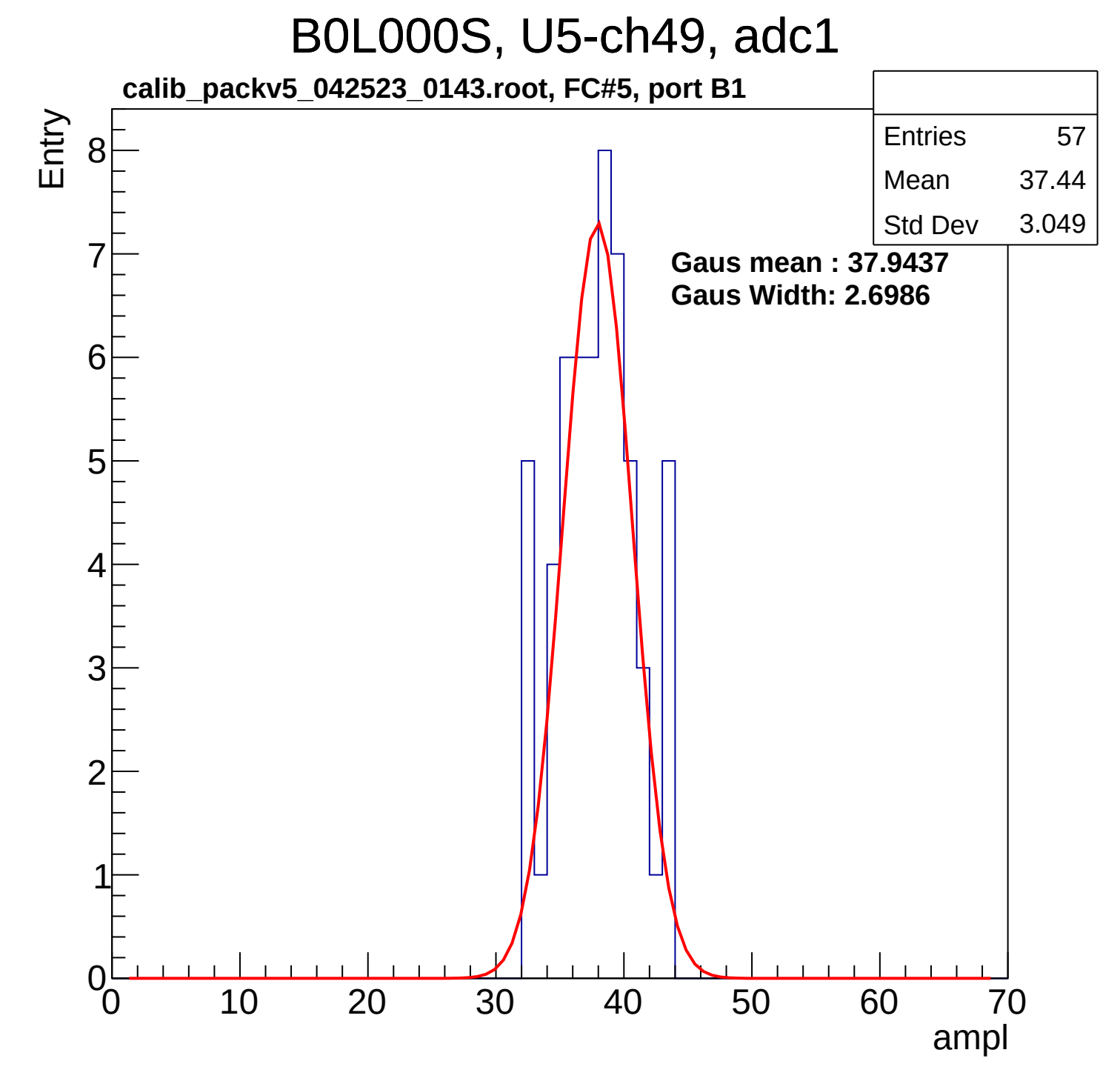
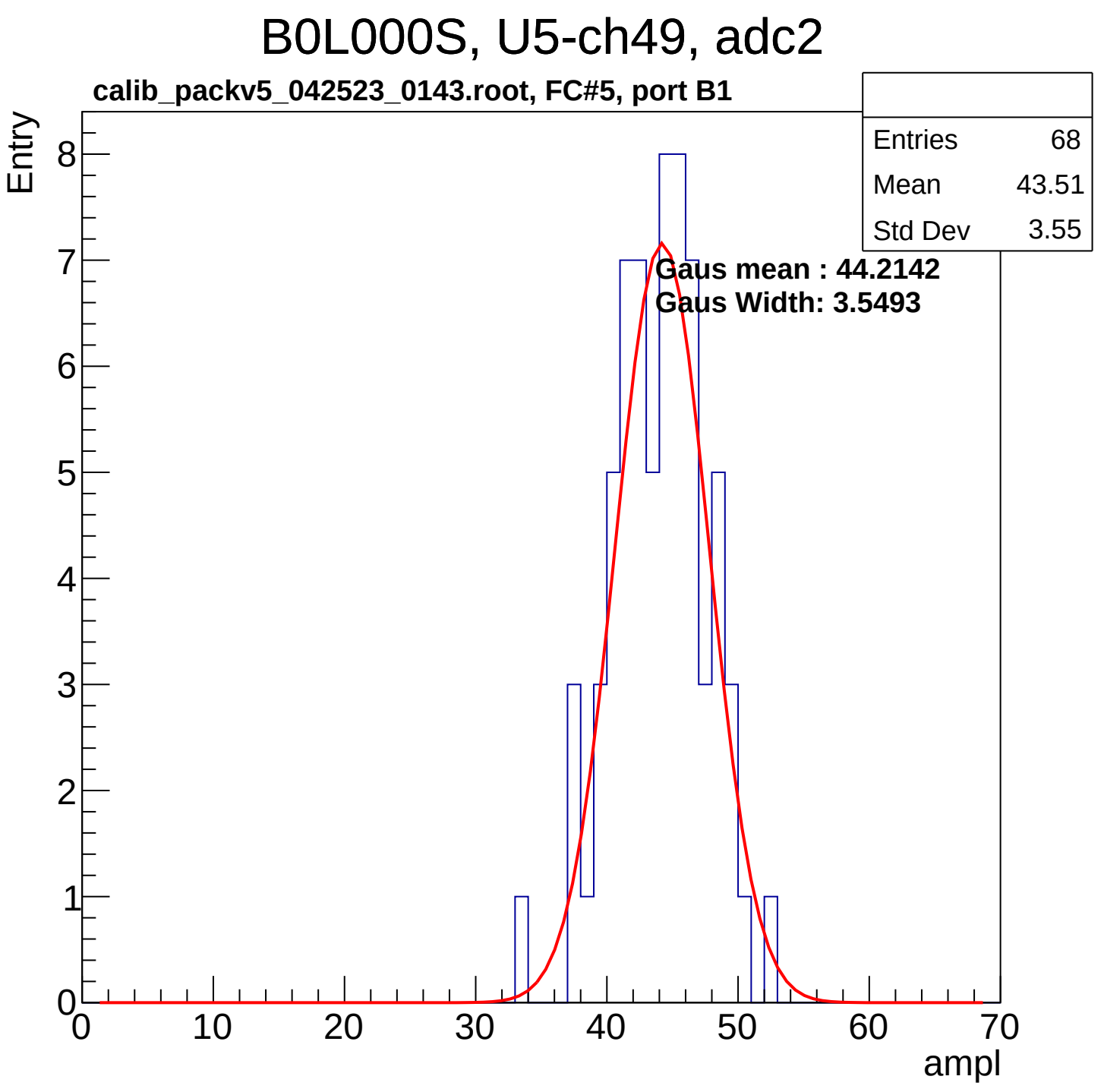

B0L000S, U5-ch48, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch48, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch49, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 30.34
Std Dev 4.873
Gaus mean : 30.9669
Gaus Width: 3.6271
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch49, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 37.44
Std Dev 3.049
Gaus mean : 37.9437
Gaus Width: 2.6986
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch49, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 43.51
Std Dev 3.55
Gaus mean : 44.2142
Gaus Width: 3.5493
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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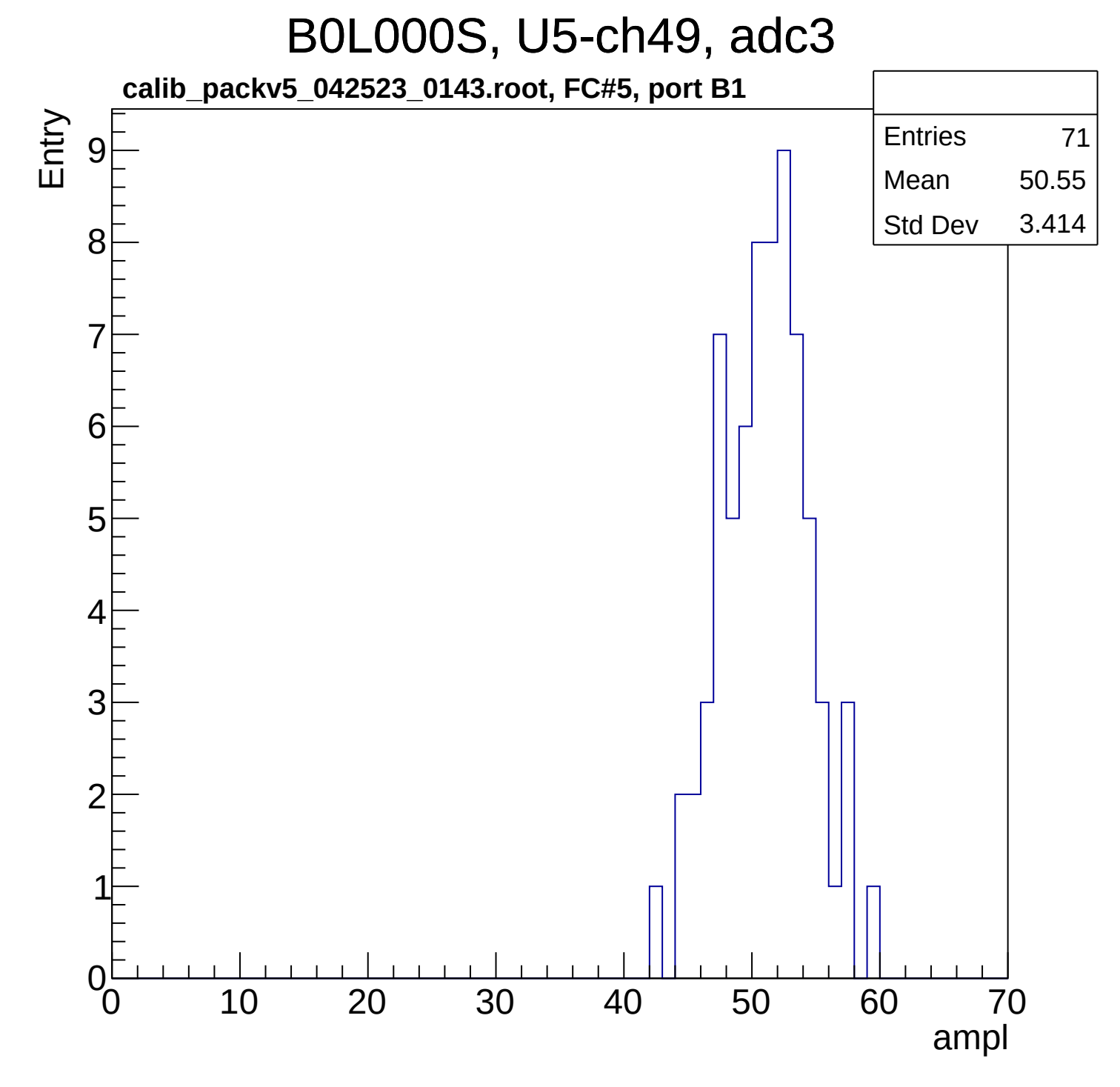
B0L000S, U5-ch49, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 50.55
Std Dev 3.414
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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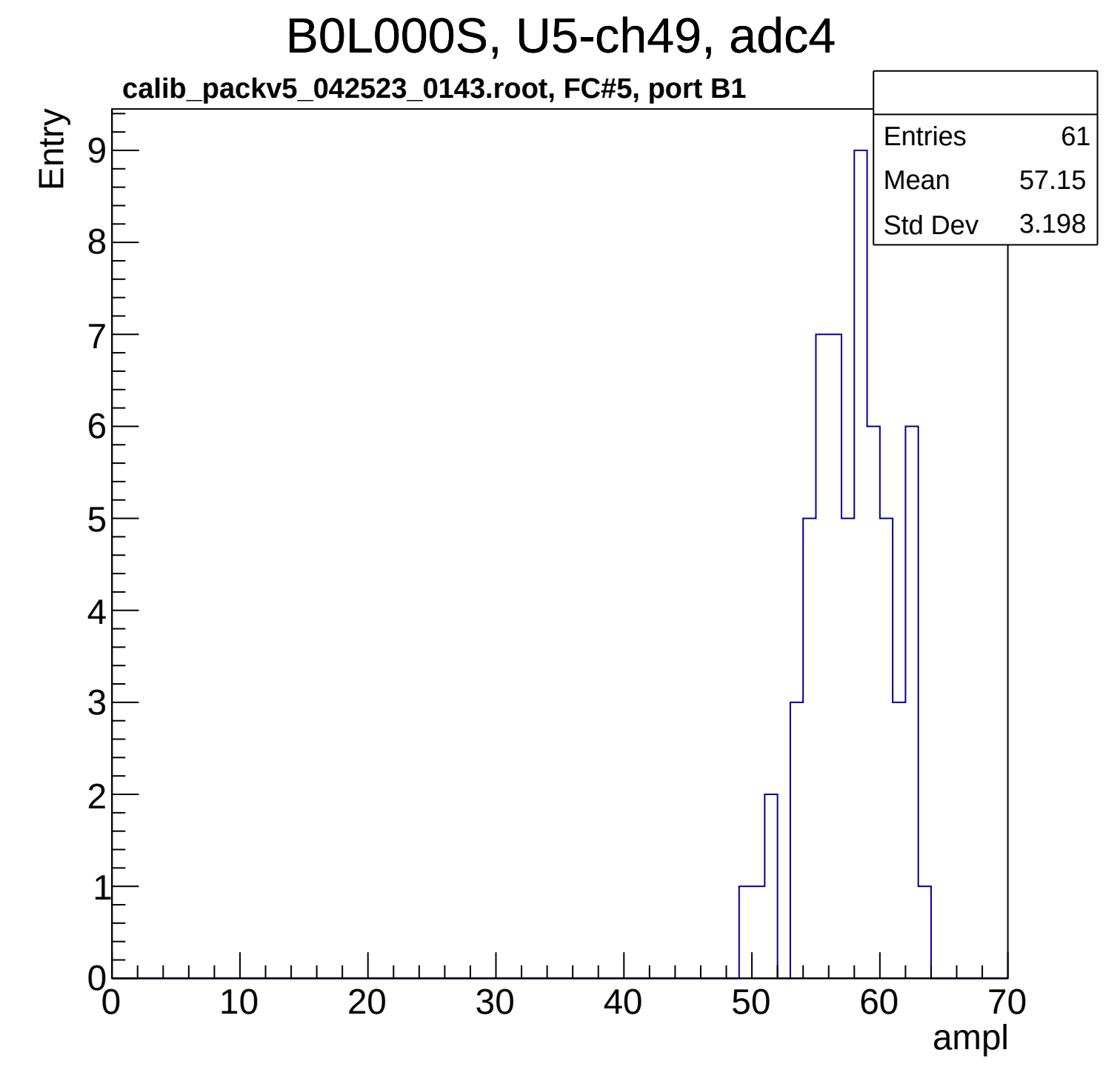
B0L000S, U5-ch49, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 57.15
Std Dev 3.198
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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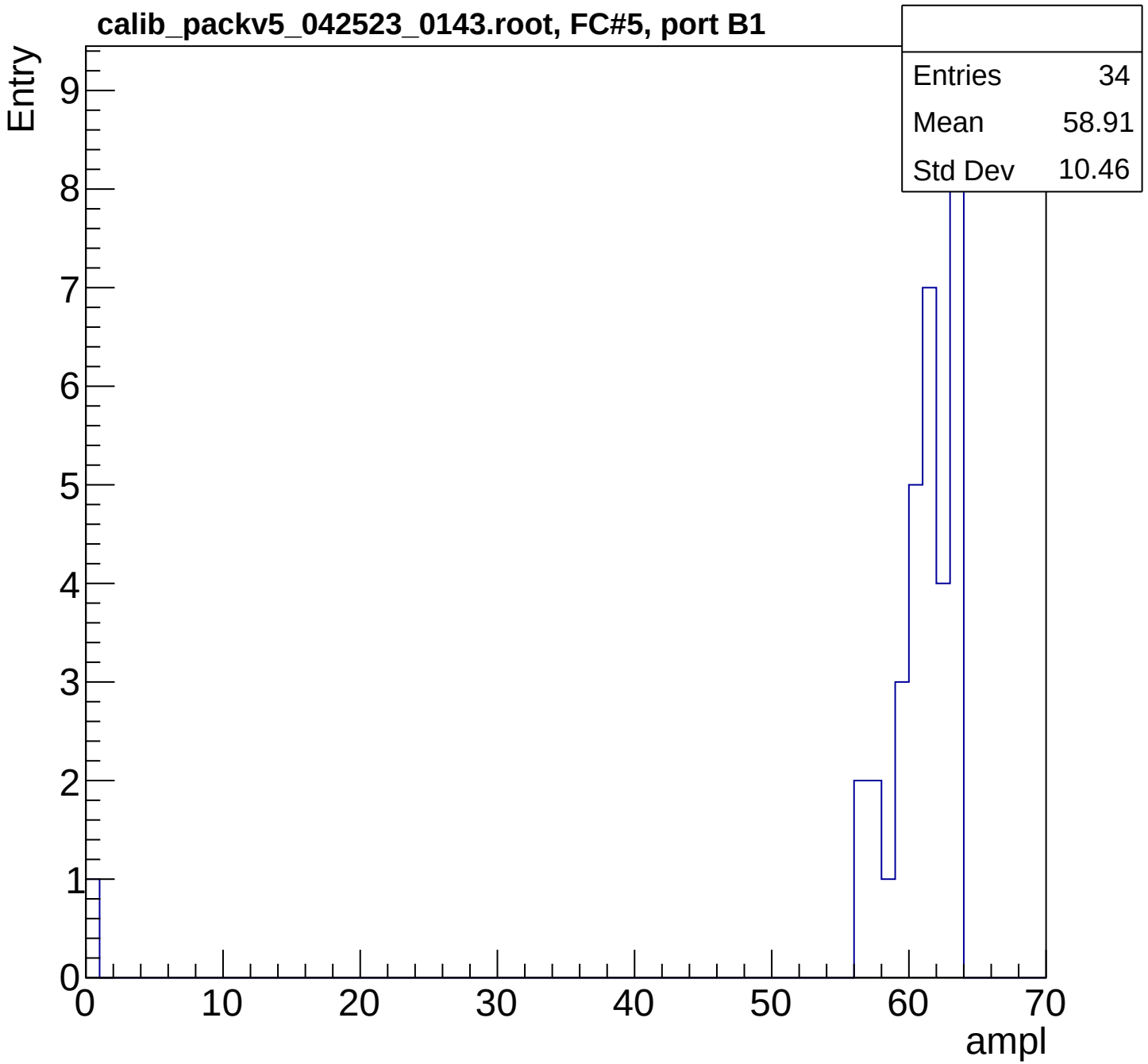
B0L000S, U5-ch49, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 58.91
Std Dev 10.46
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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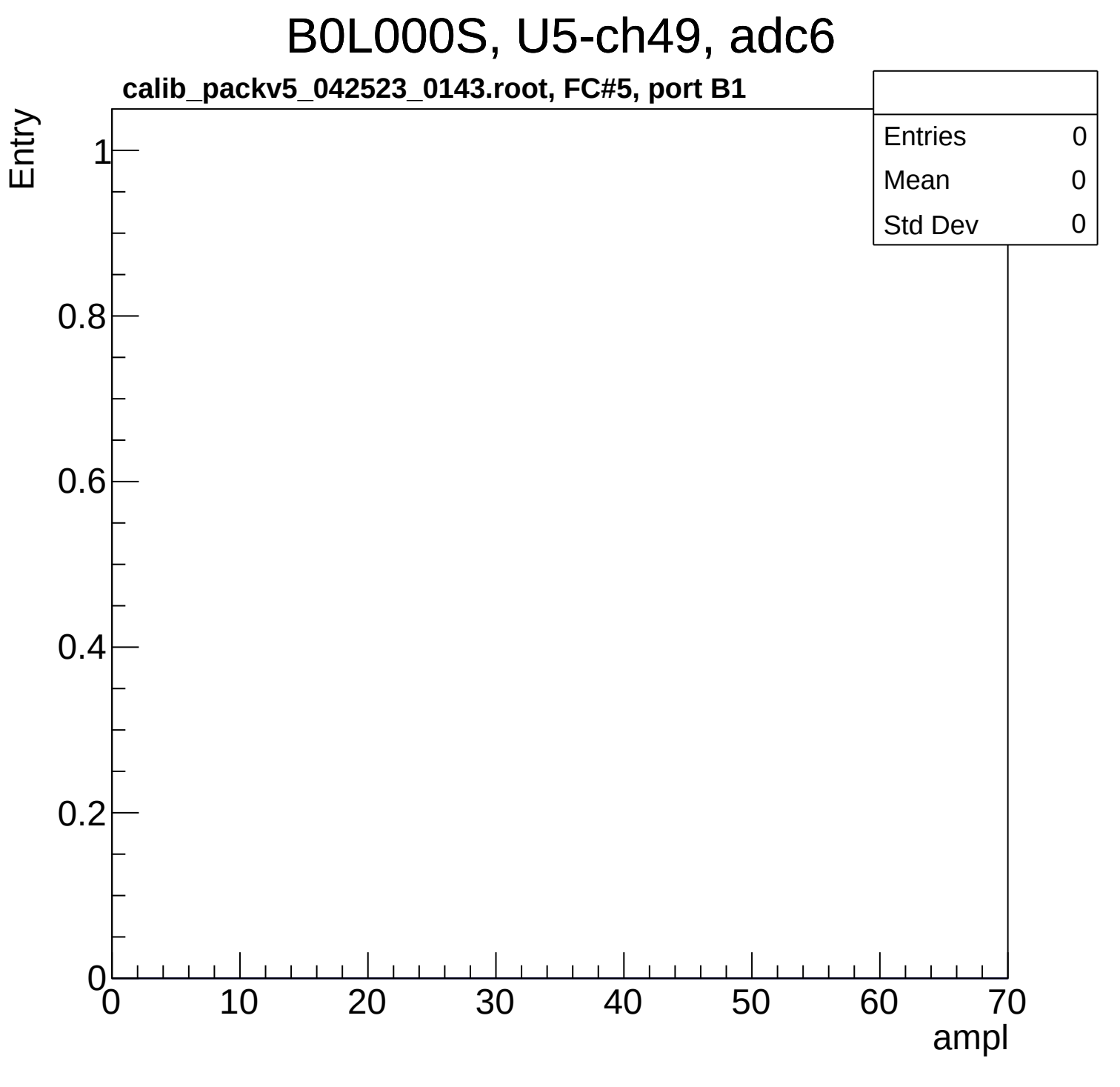
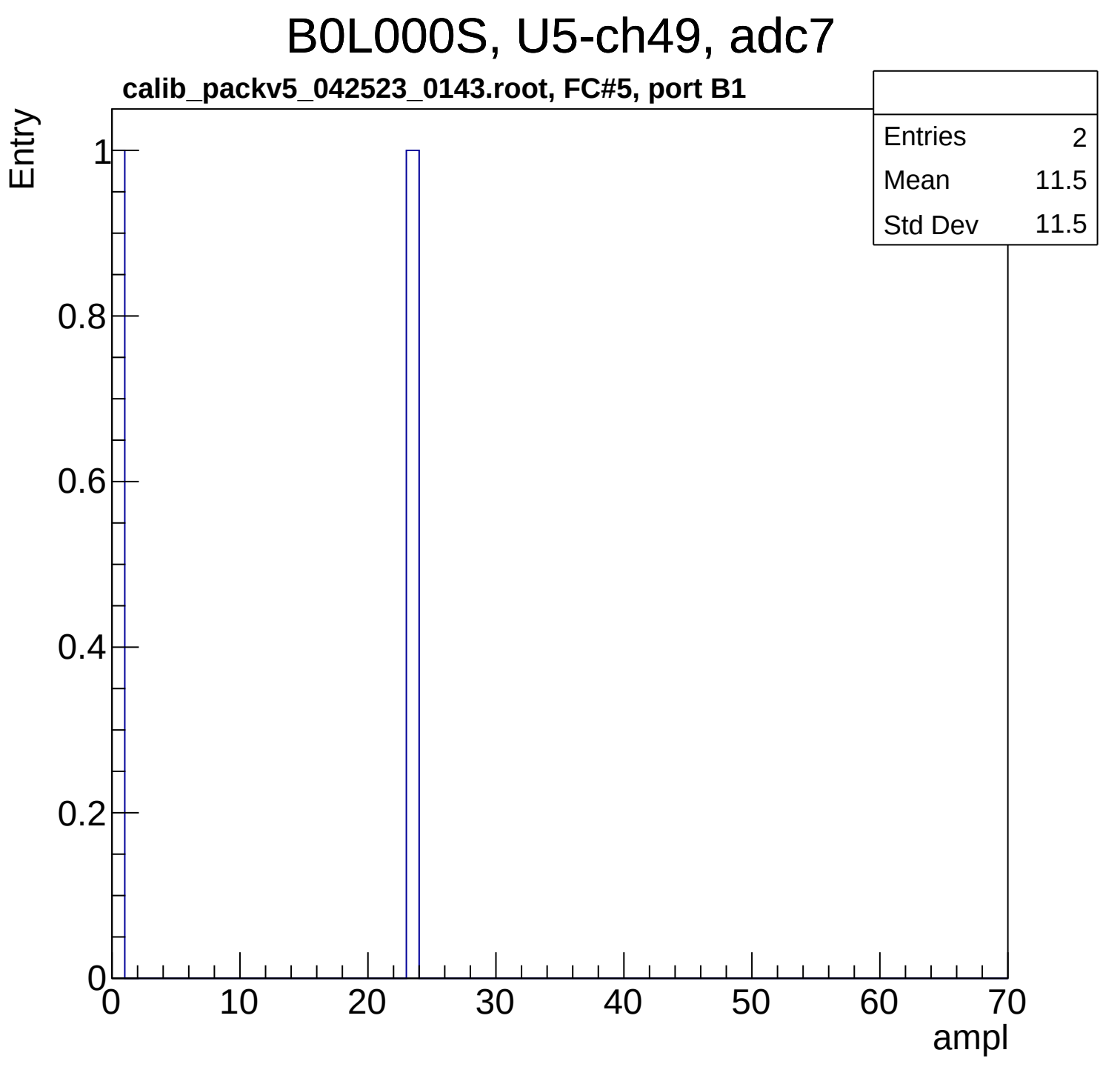

B0L000S, U5-ch49, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch49, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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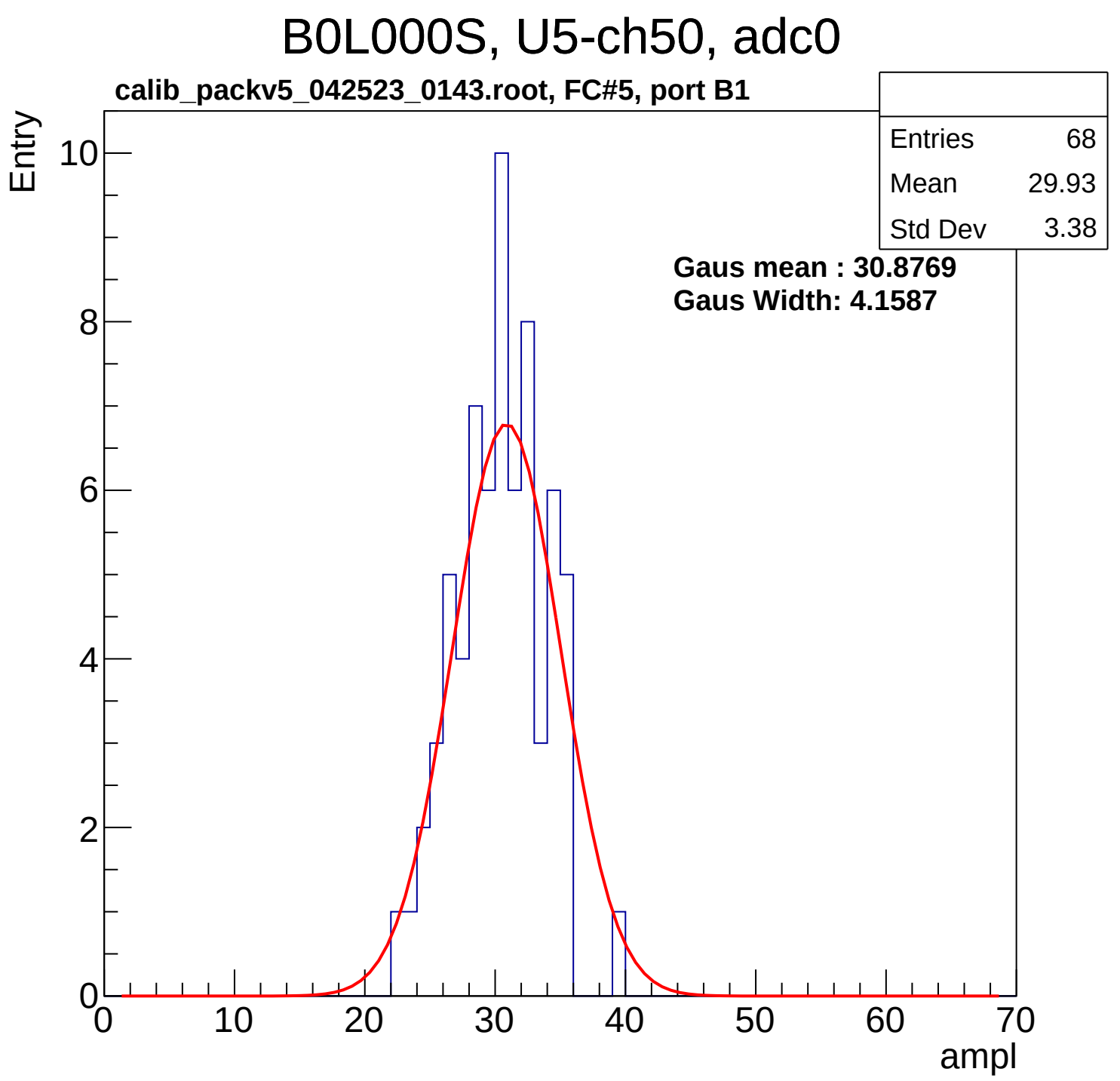
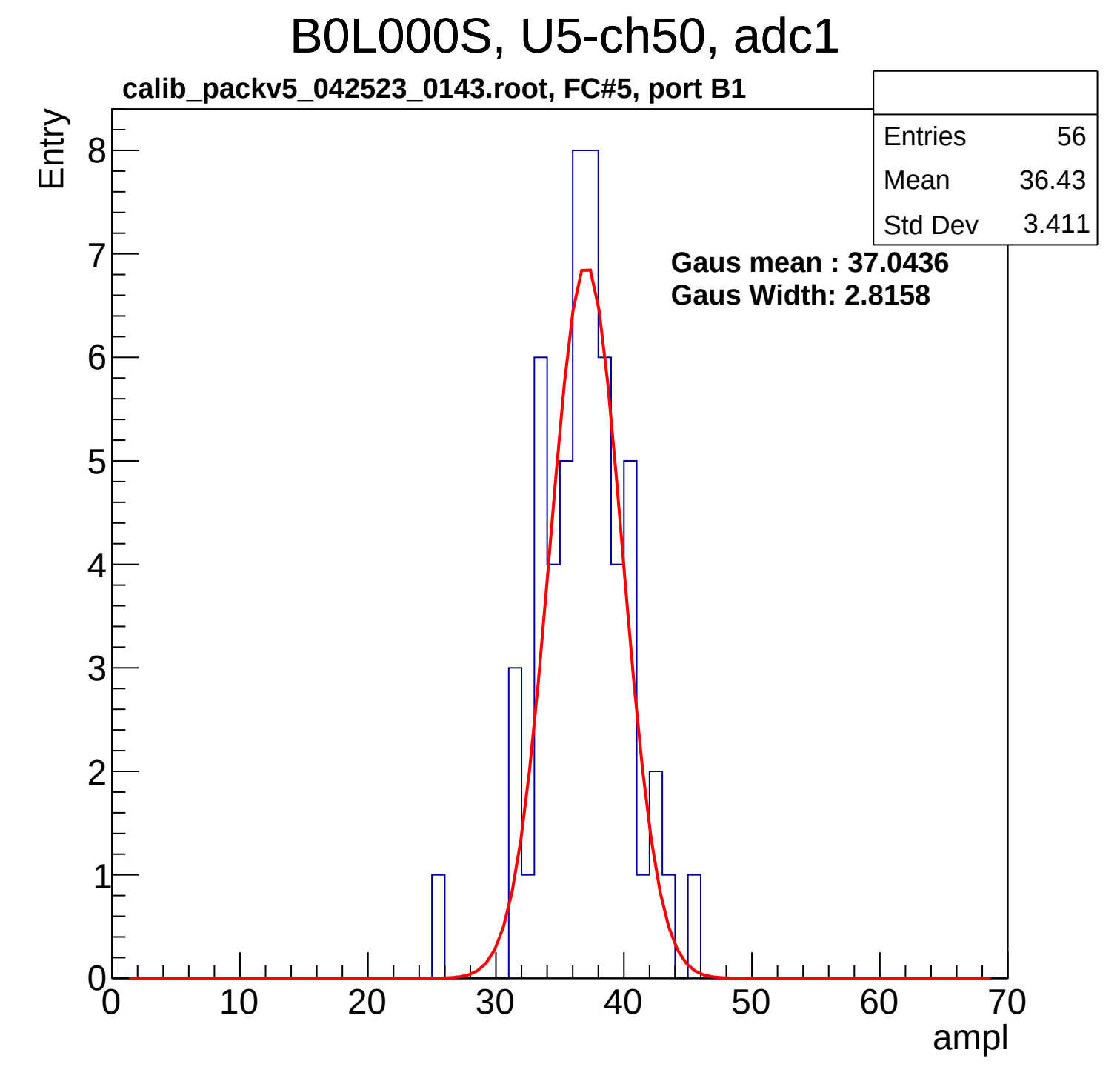
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch50, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.93
Std Dev 3.38
Gaus mean : 30.8769
Gaus Width: 4.1587
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch50, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 36.43
Std Dev 3.411
Gaus mean : 37.0436
Gaus Width: 2.8158
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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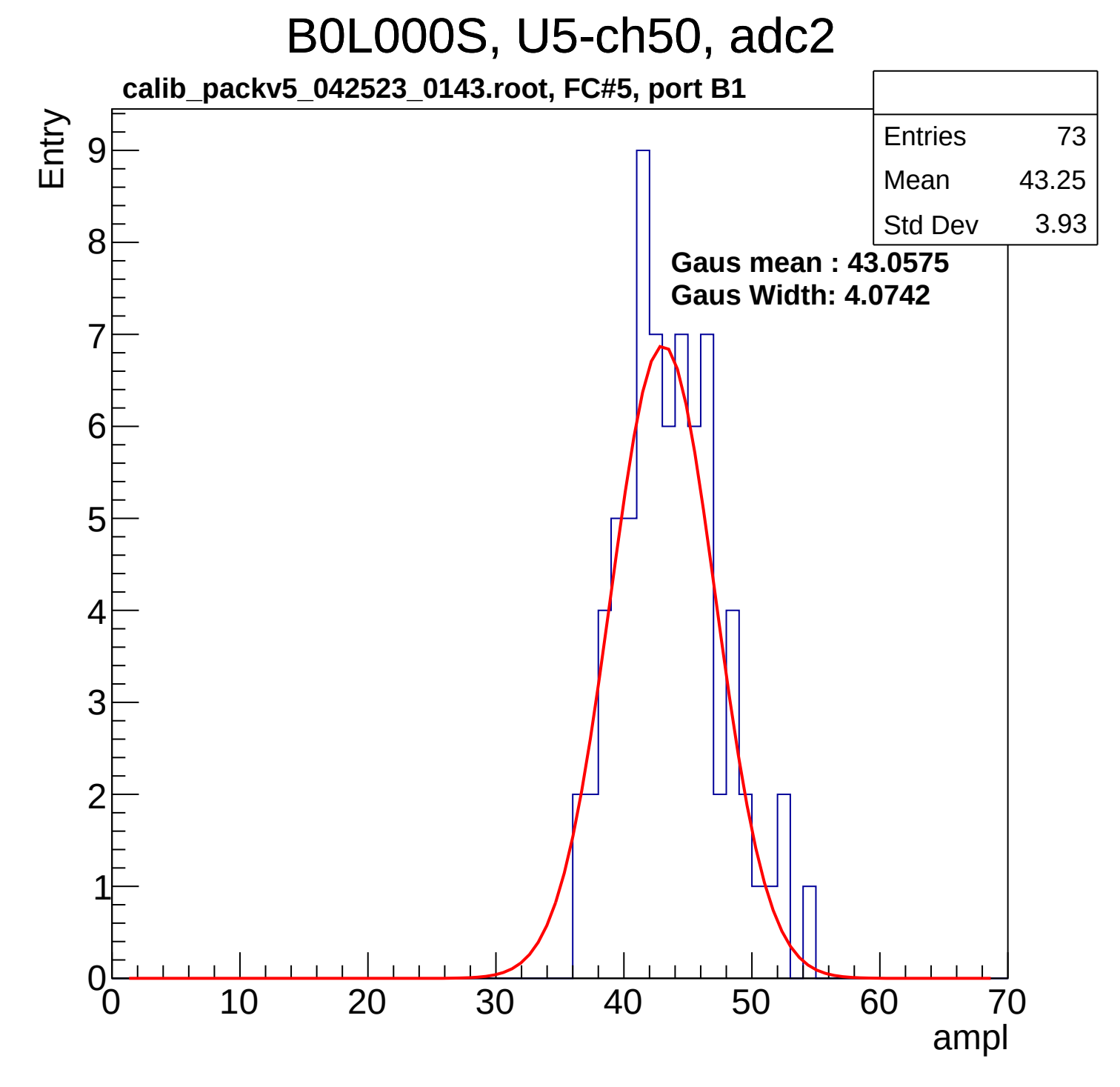
B0L000S, U5-ch50, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 43.25
Std Dev 3.93
Gaus mean : 43.0575
Gaus Width: 4.0742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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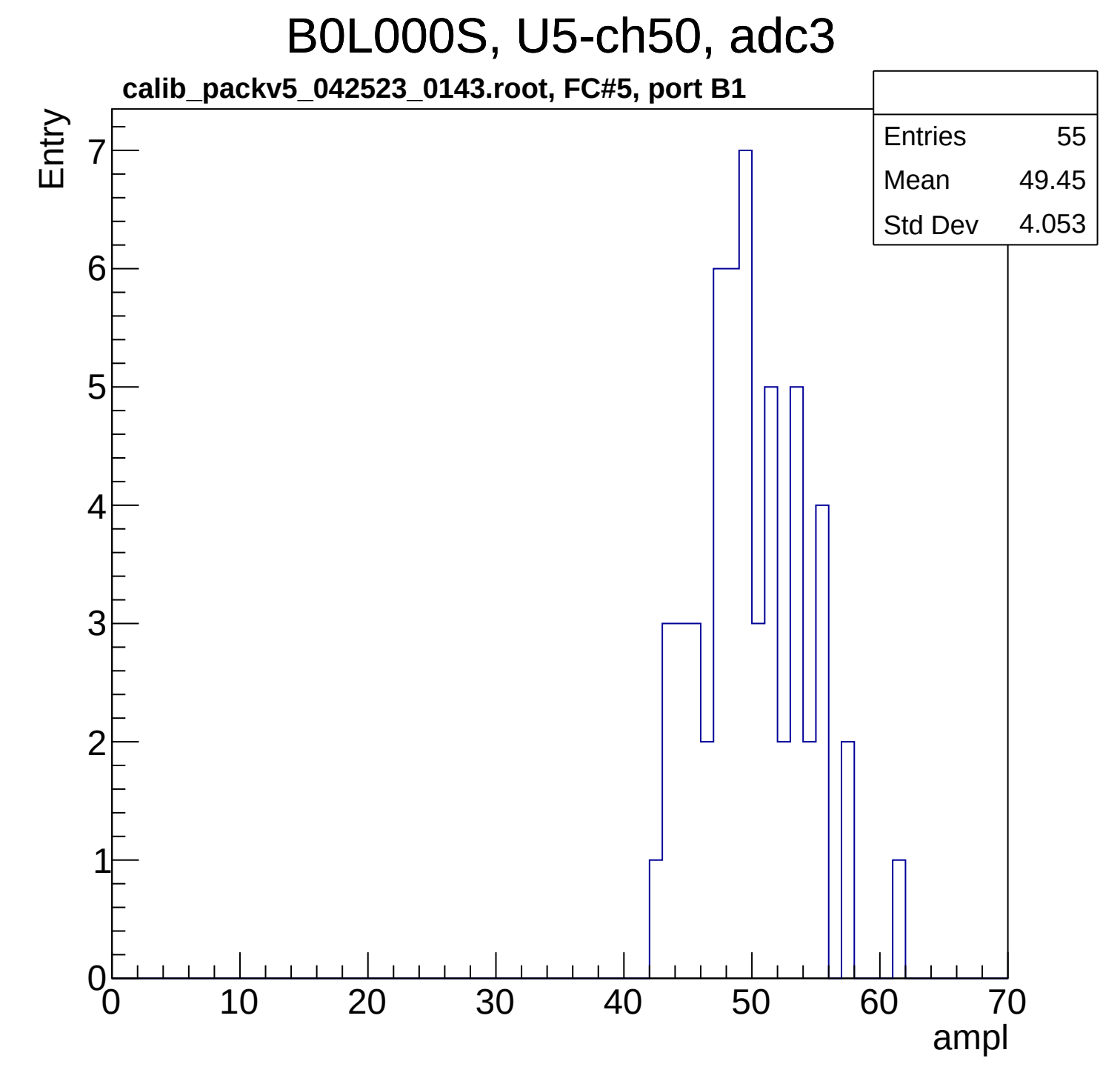
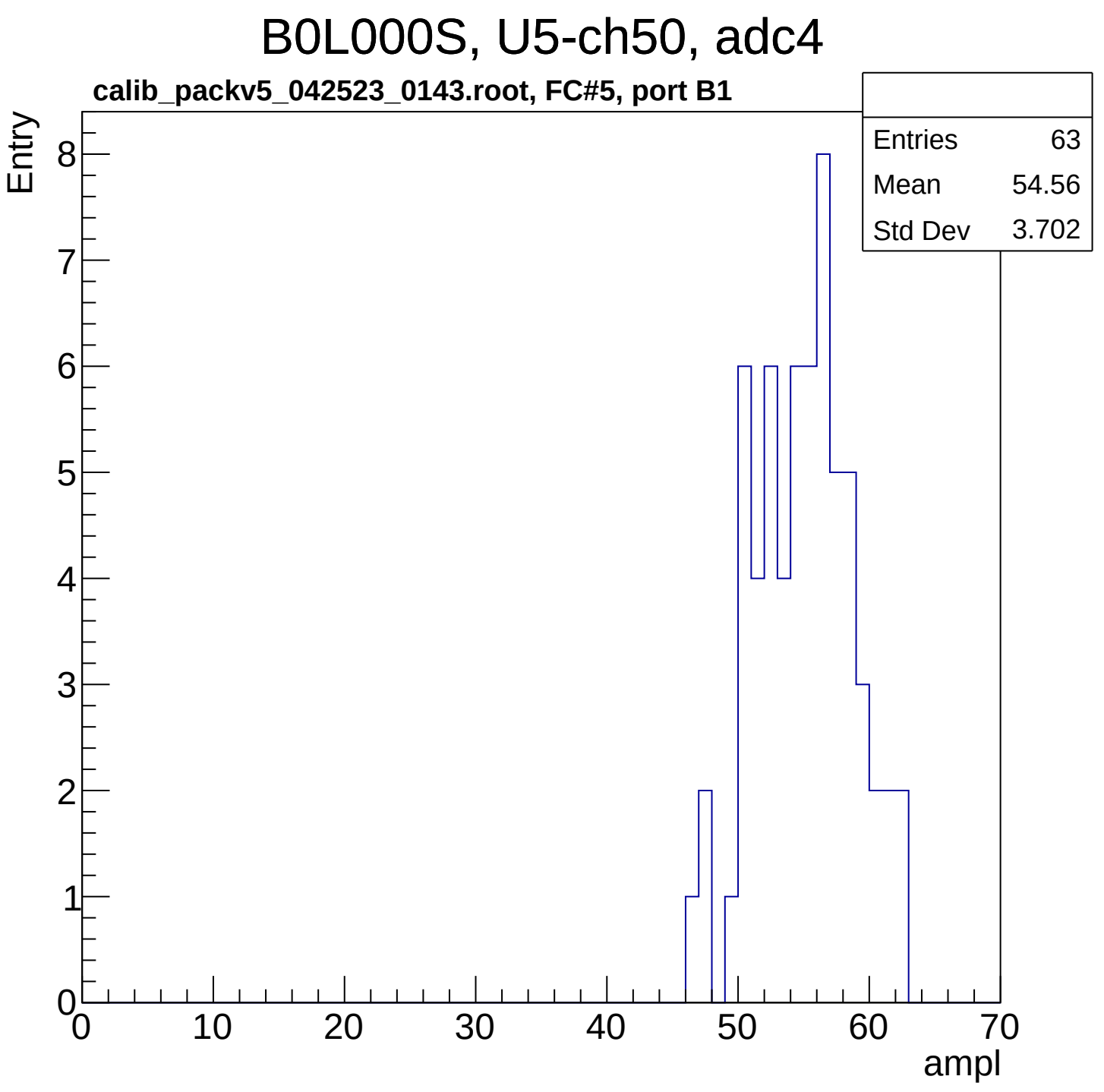
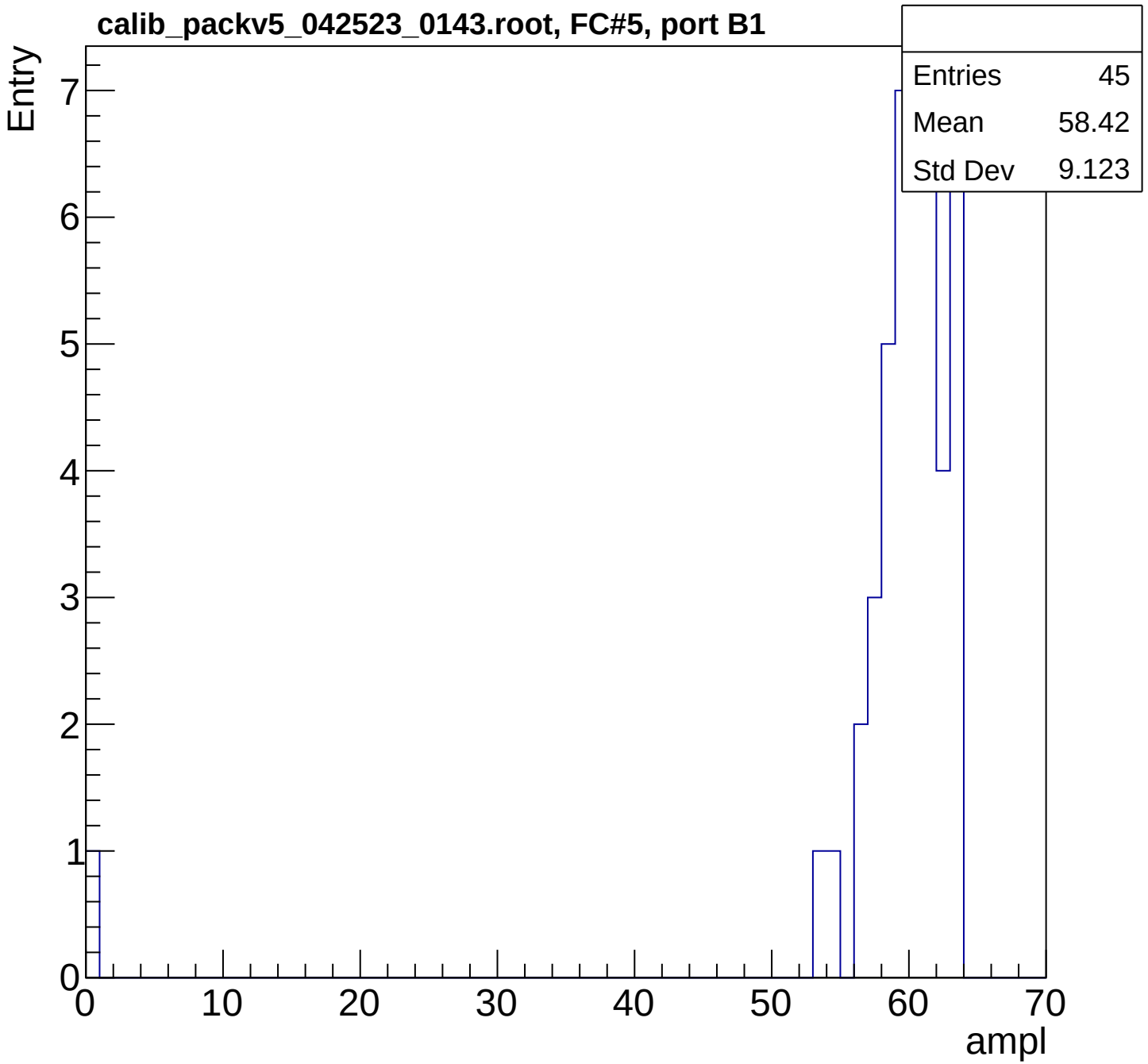
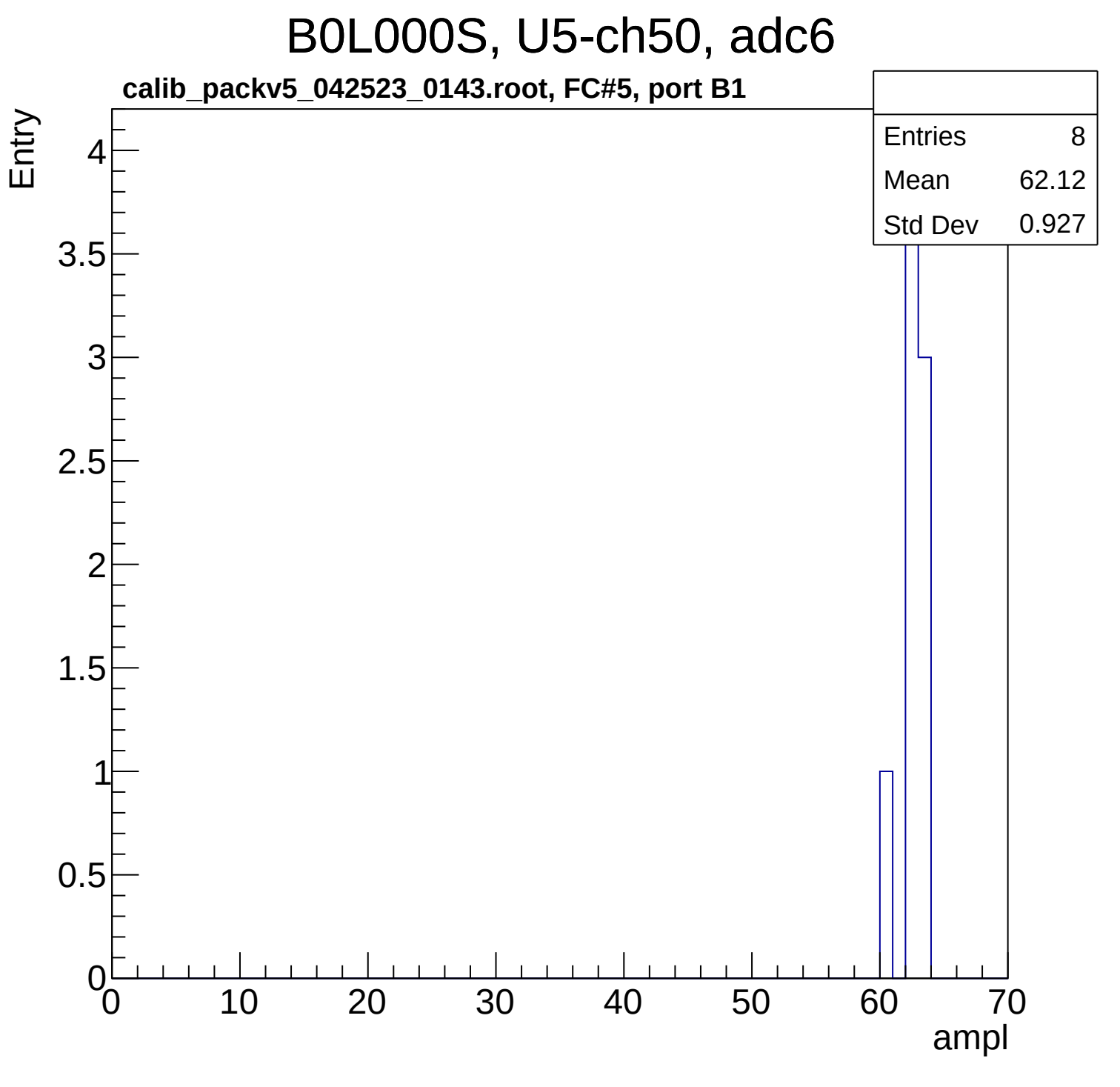

B0L000S, U5-ch50, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 49.45
Std Dev 4.053
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch50, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 54.56
Std Dev 3.702
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch50, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.42
Std Dev 9.123
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch50, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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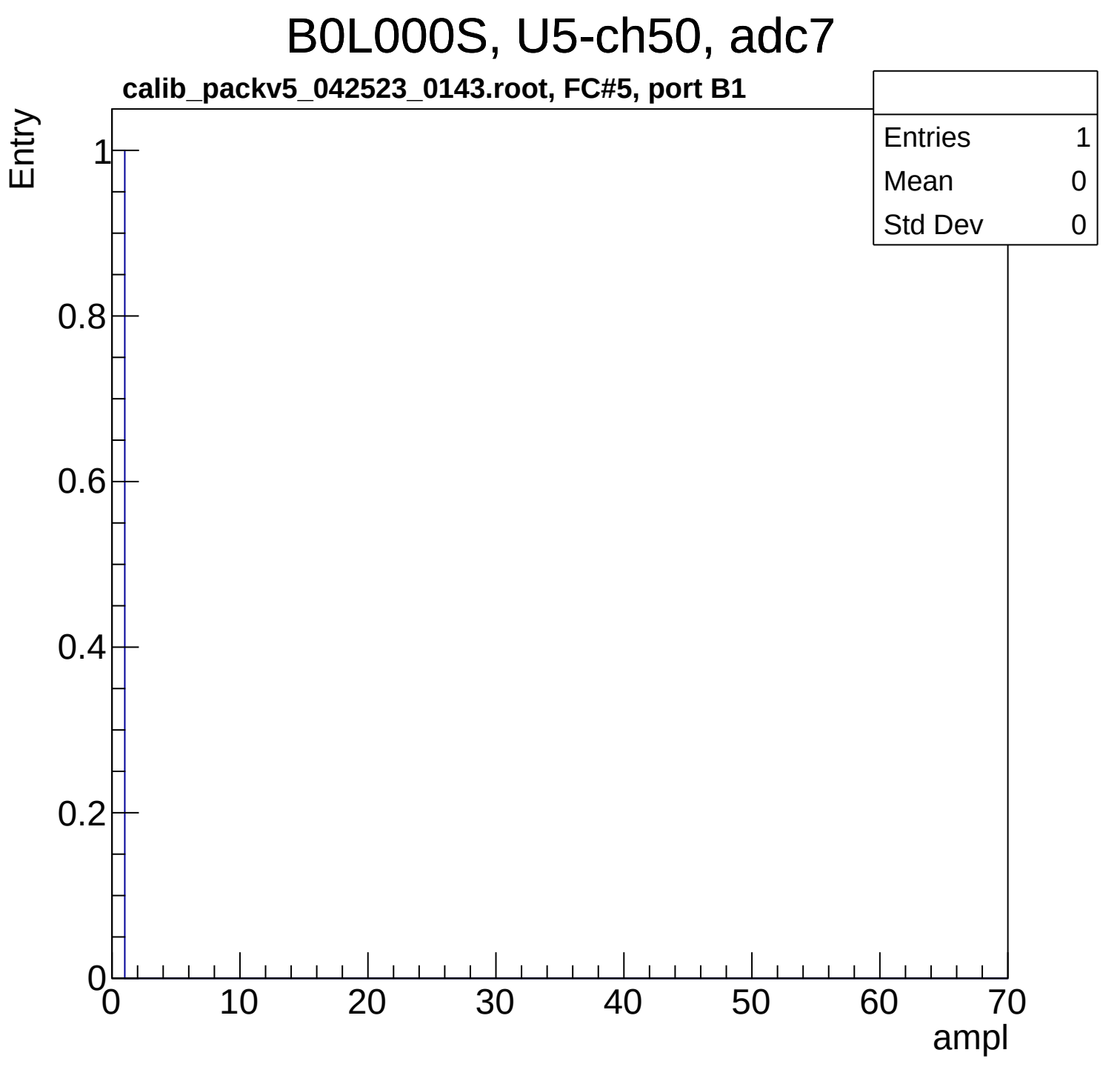
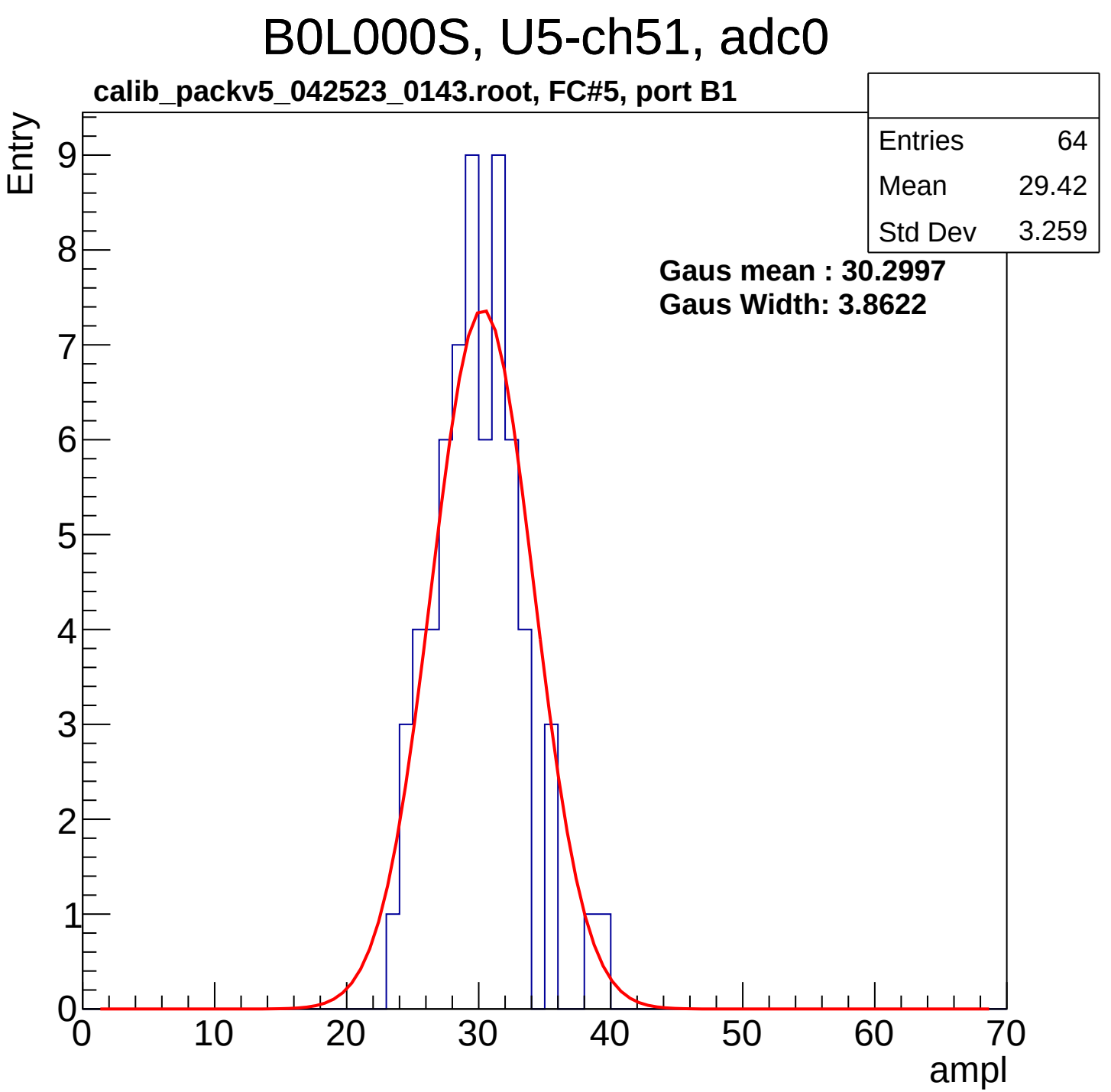
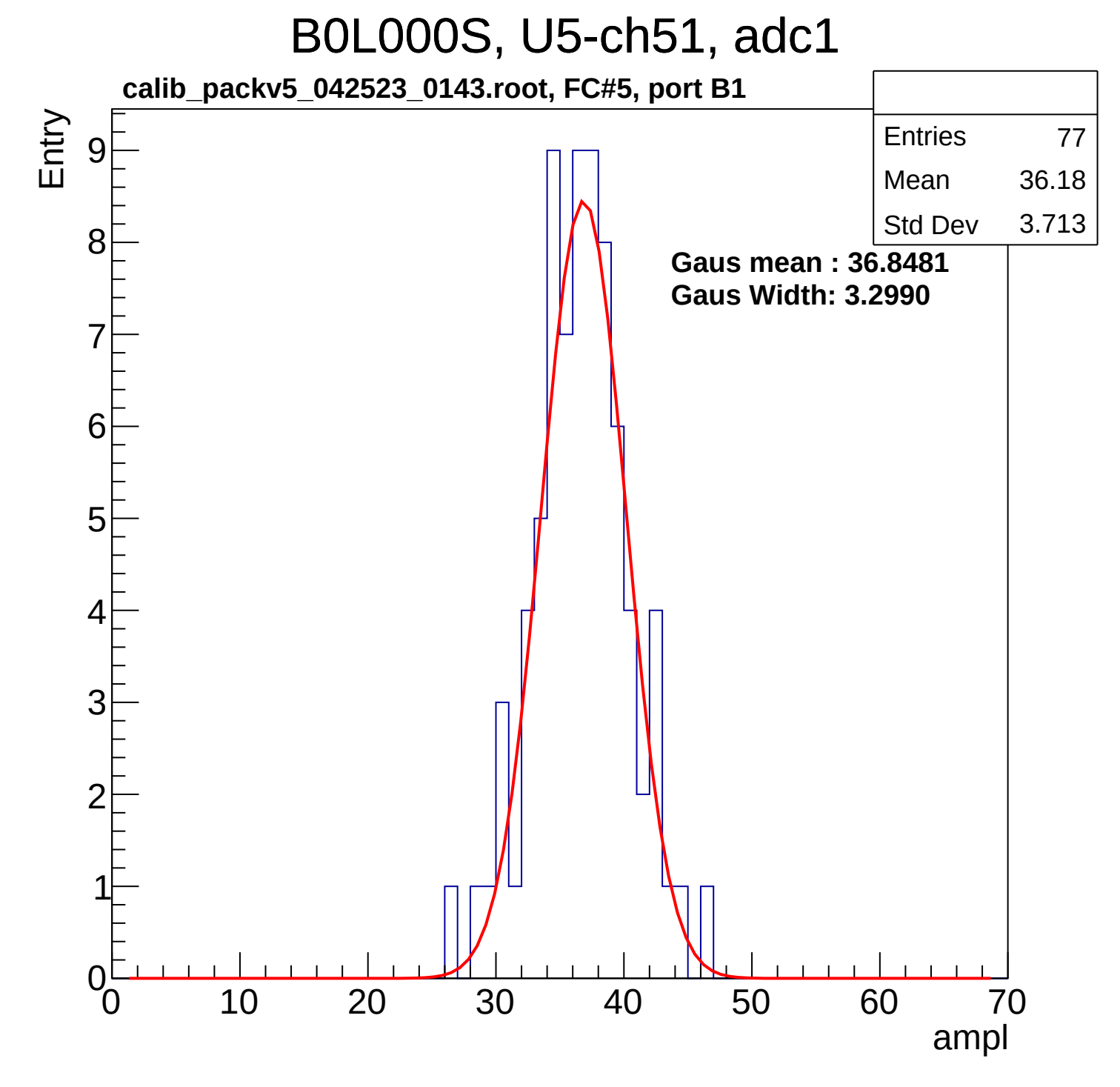
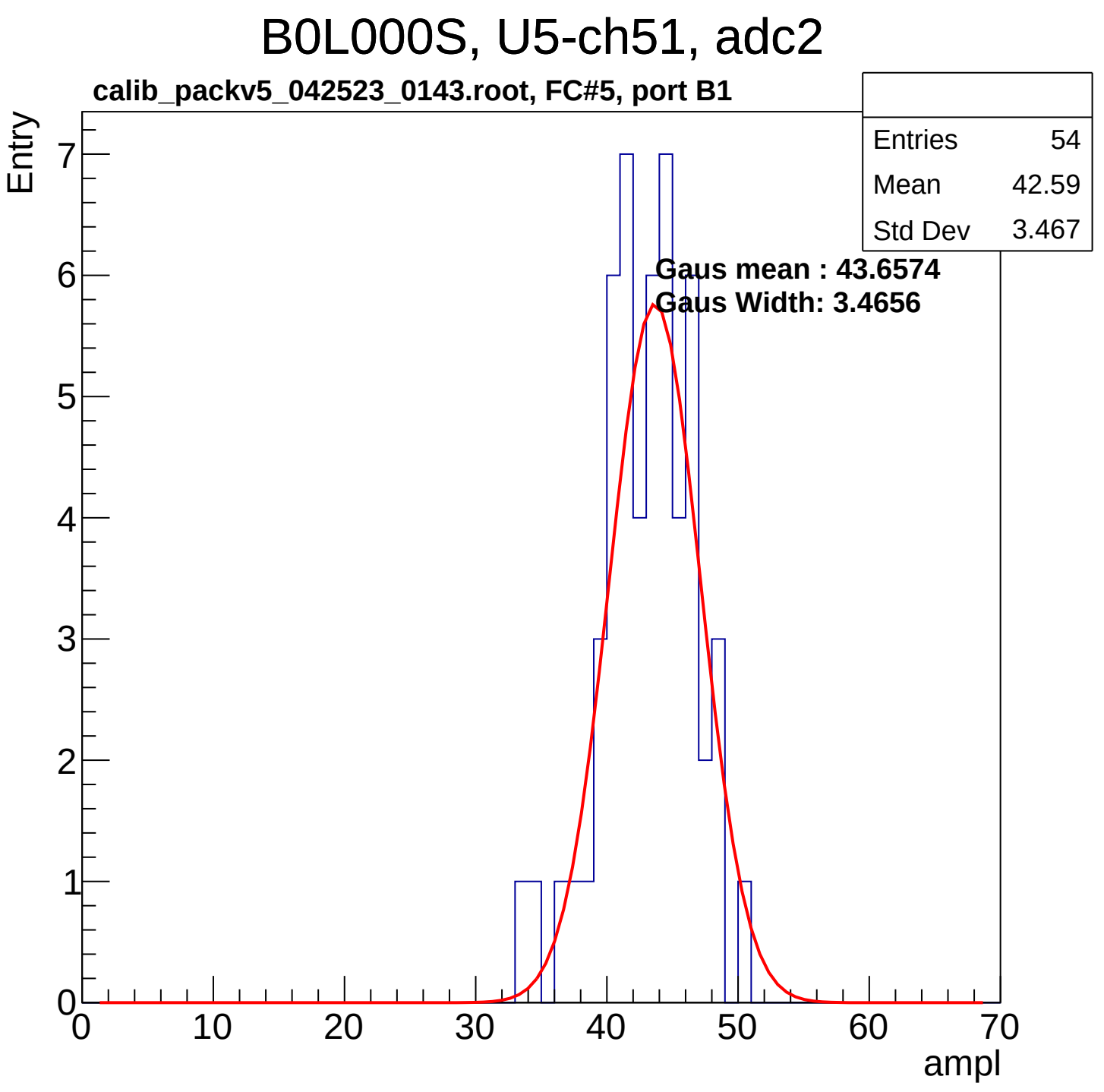
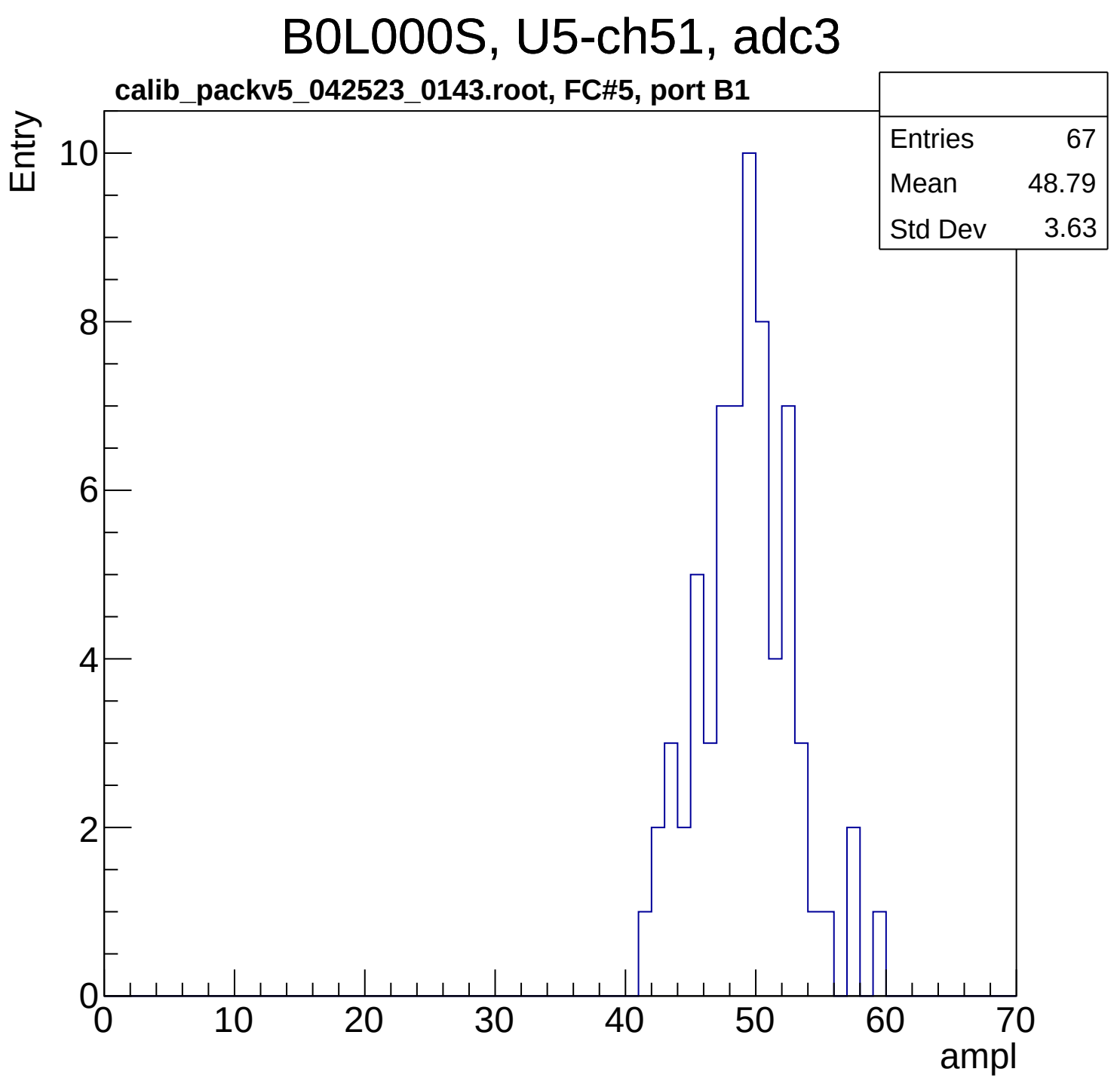
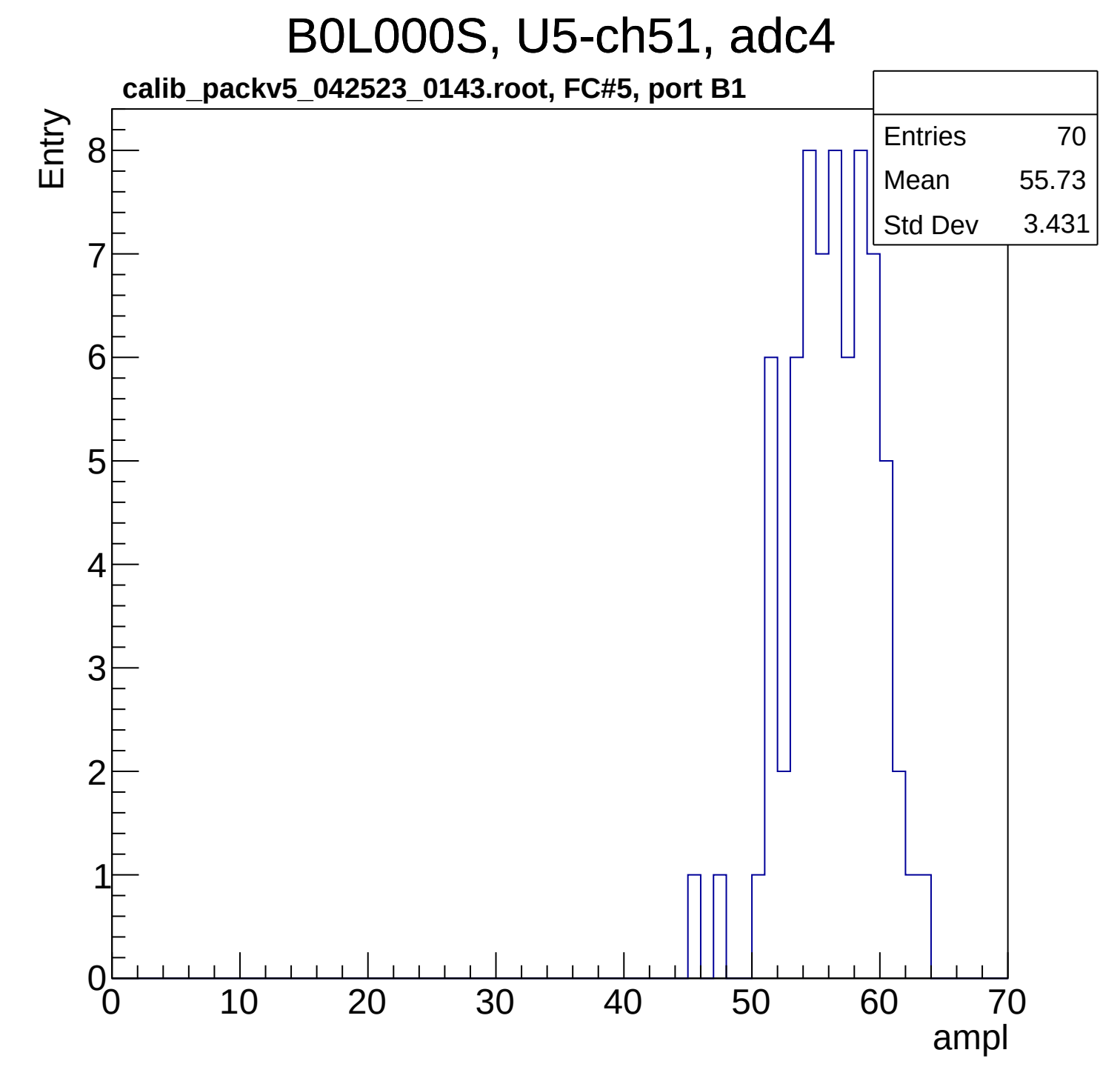

B0L000S, U5-ch50, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

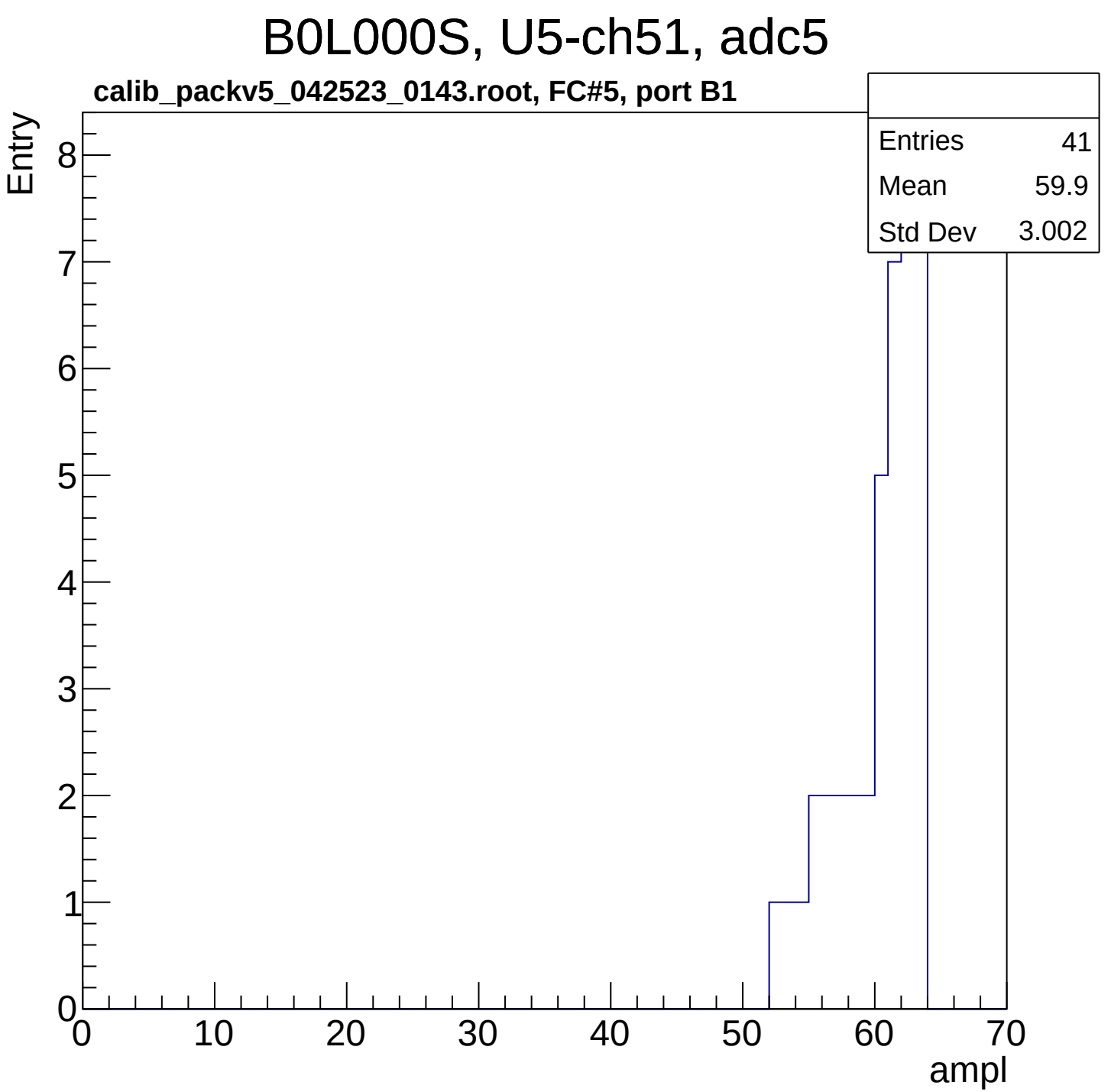

B0L000S, U5-ch51, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 29.42
Std Dev 3.259
Gaus mean : 30.2997
Gaus Width: 3.8622
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

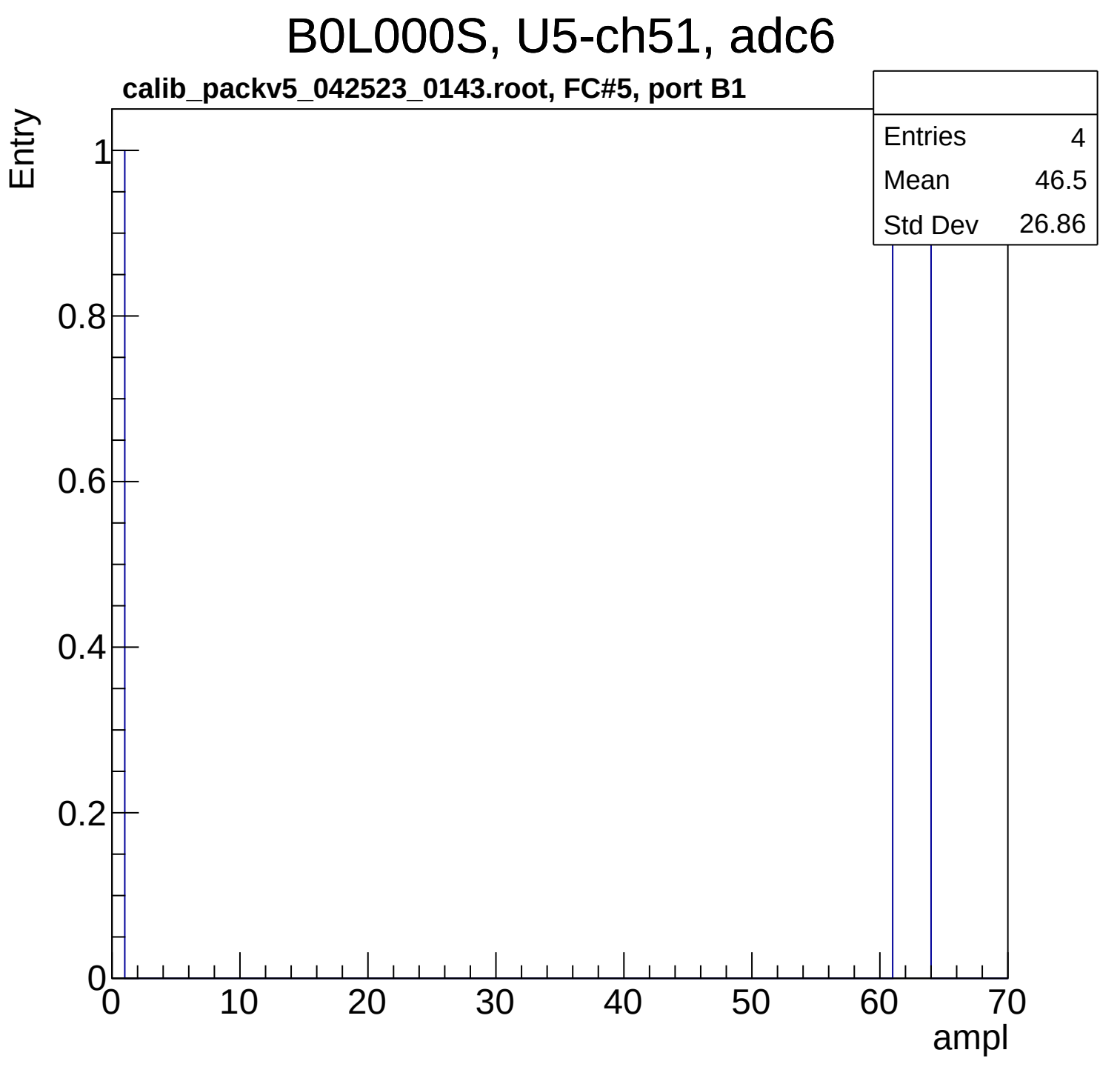

B0L000S, U5-ch51, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 36.18
Std Dev 3.713
Gaus mean : 36.8481
Gaus Width: 3.2990
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

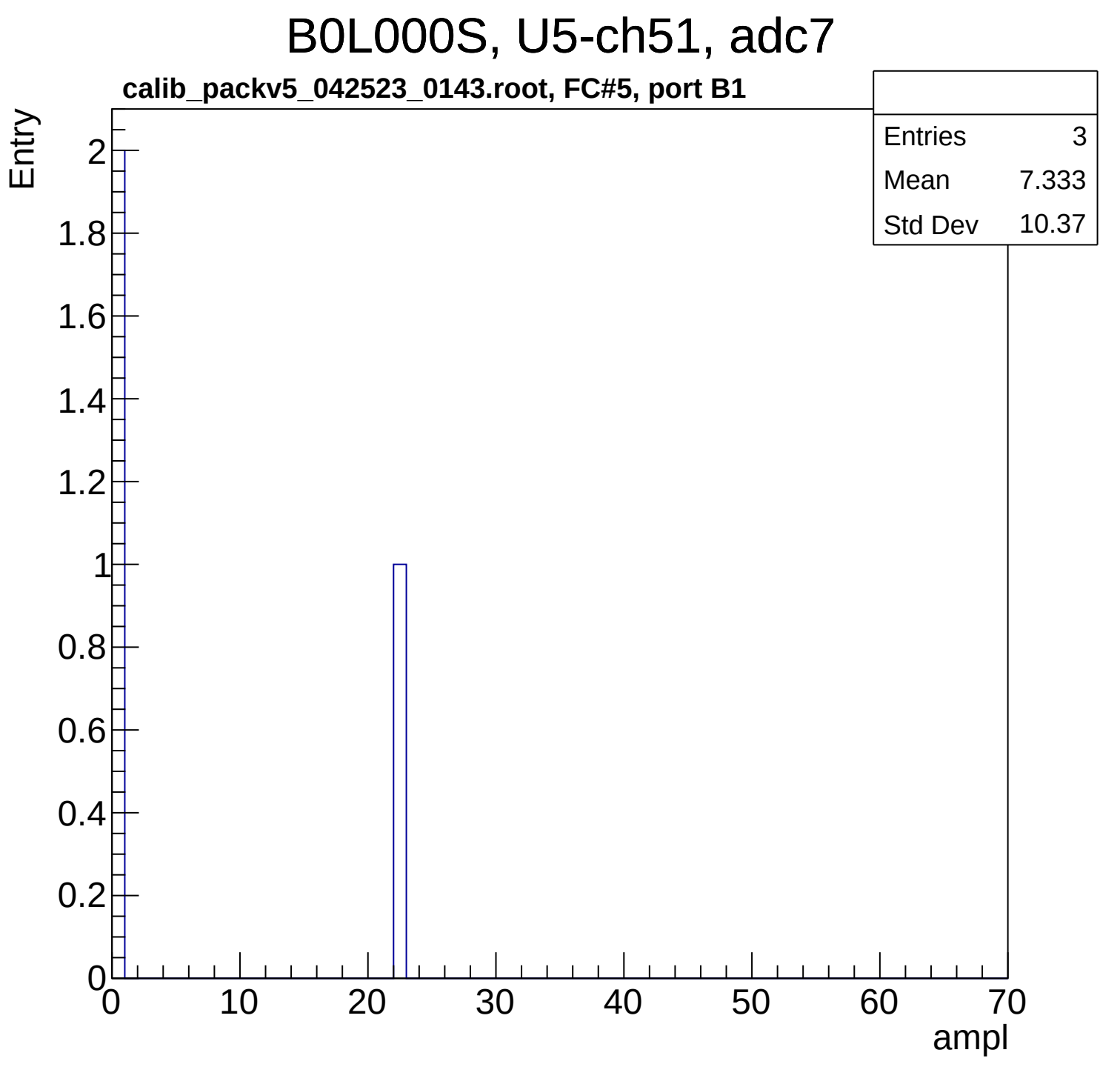

B0L000S, U5-ch51, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 42.59
Std Dev 3.467
Gaus mean : 43.6574
Gaus Width: 3.4656
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

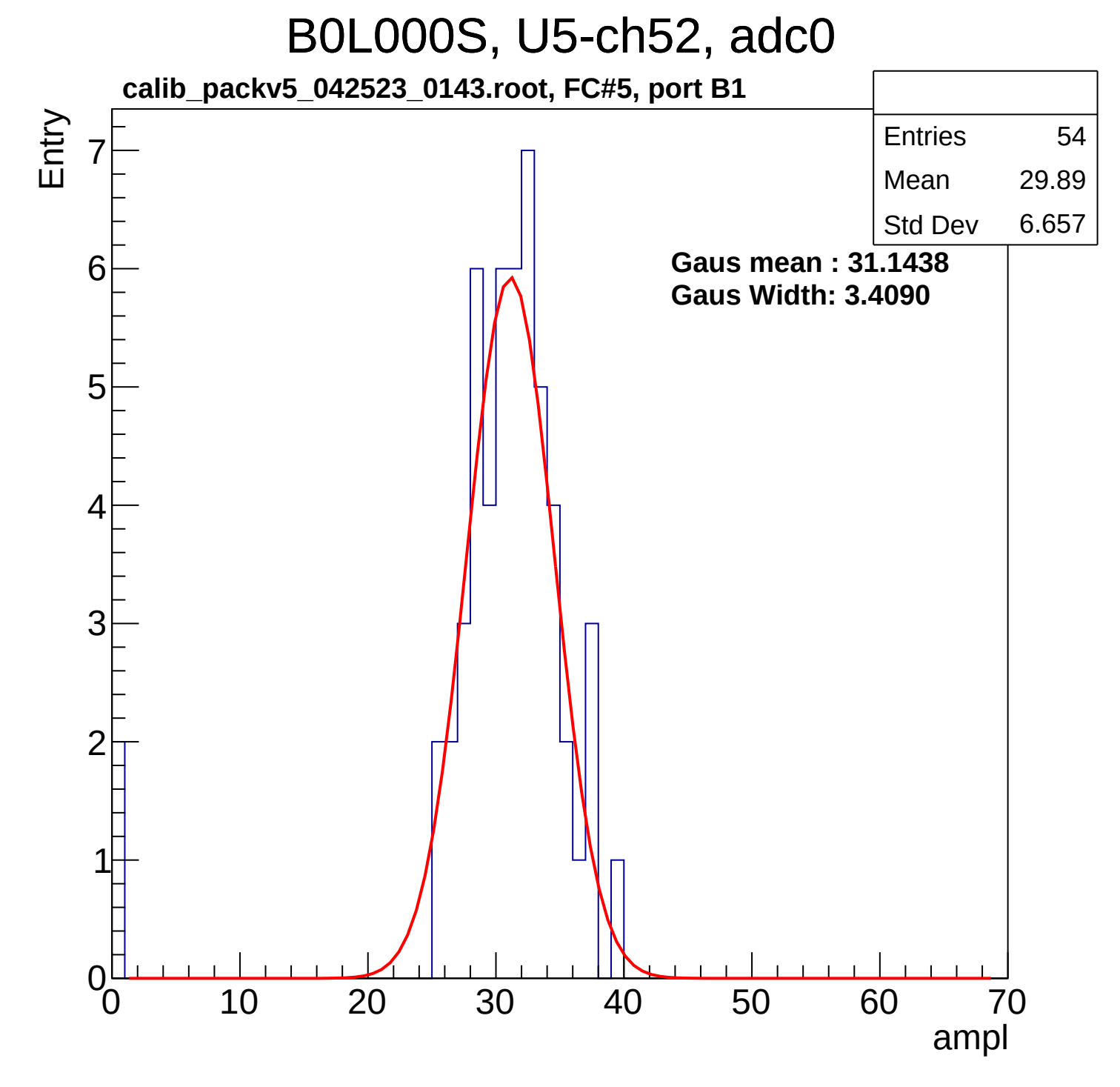

B0L000S, U5-ch51, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 48.79
Std Dev 3.63
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

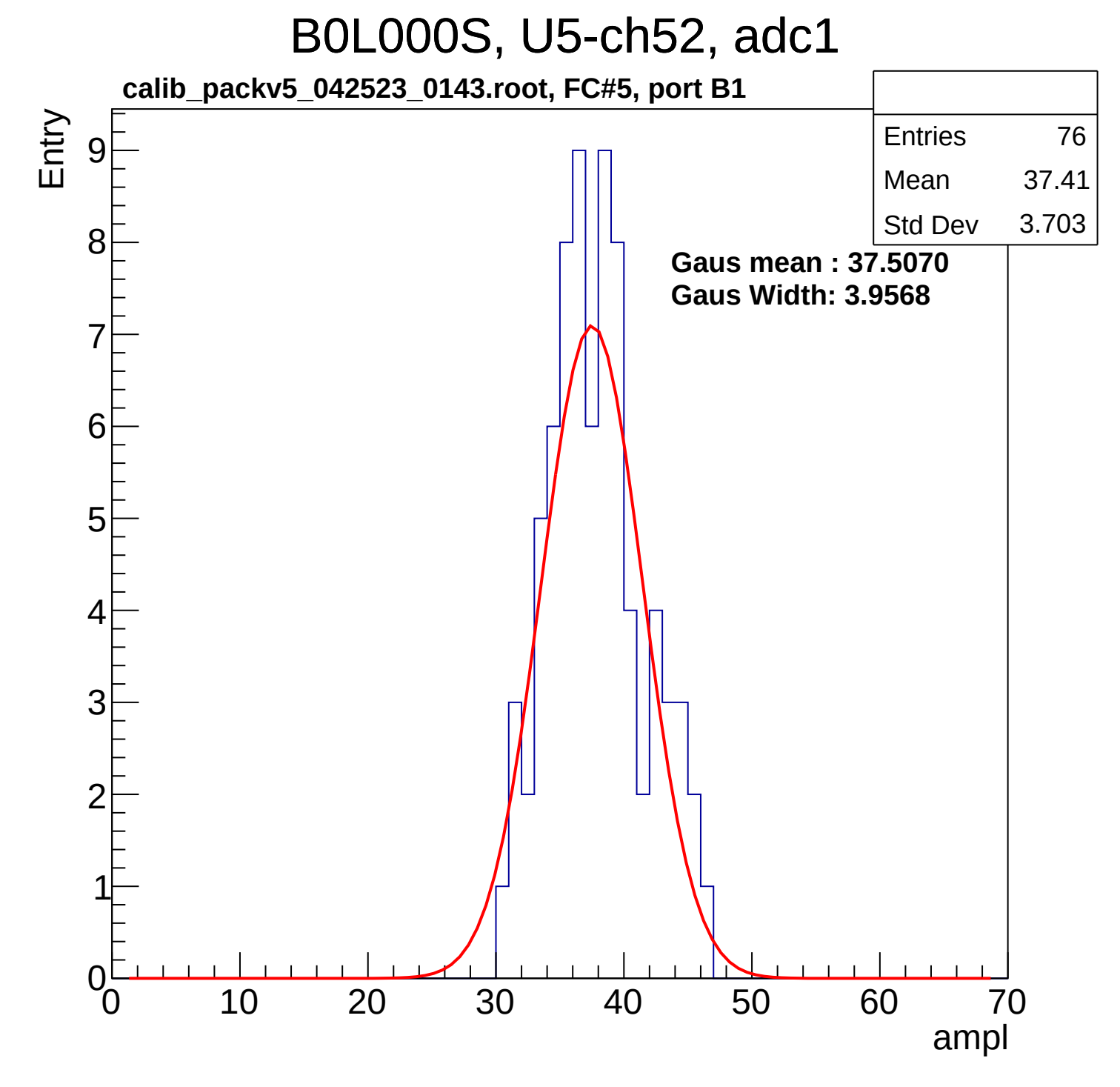

B0L000S, U5-ch51, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 55.73
Std Dev 3.431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

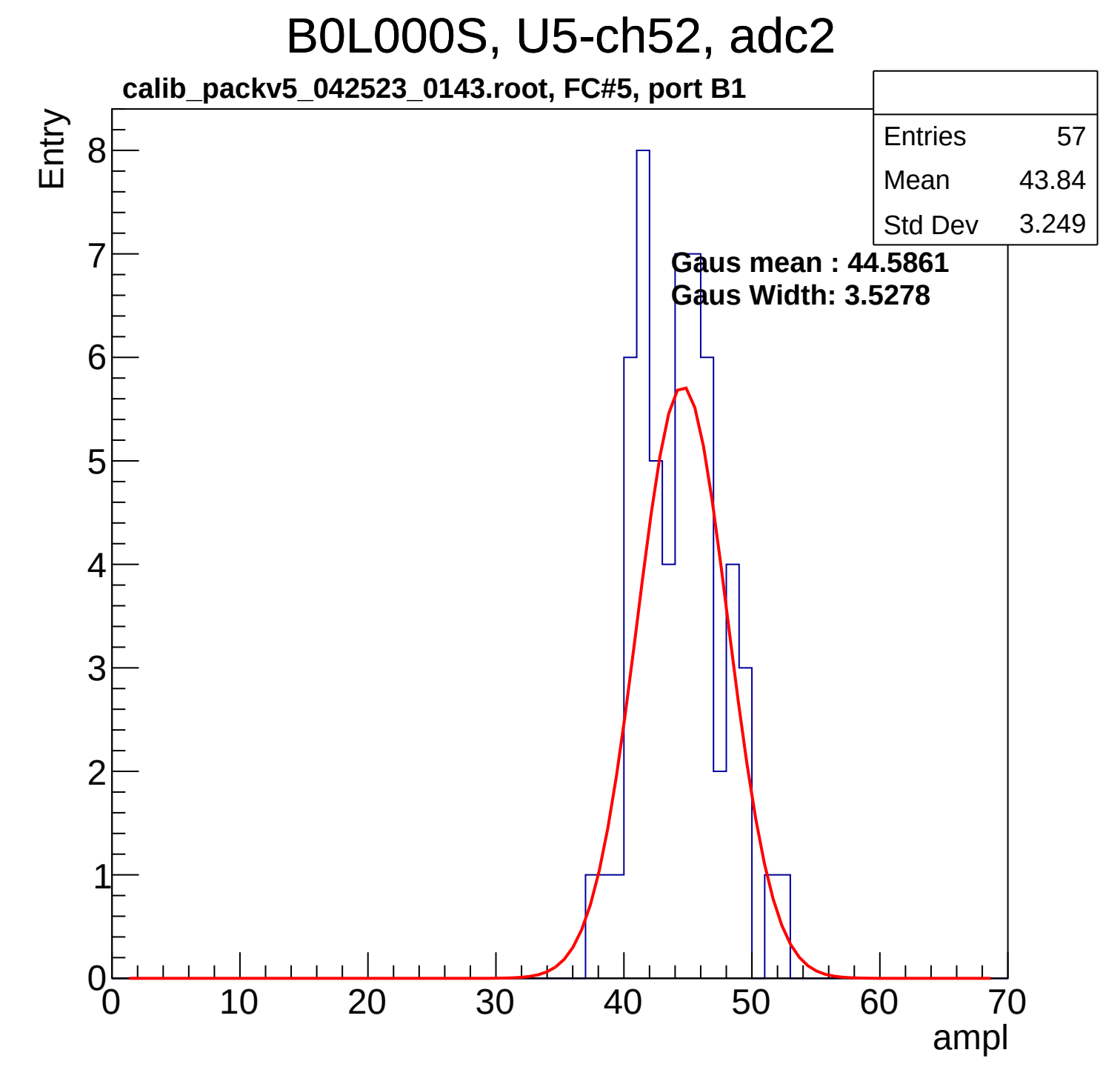

B0L000S, U5-ch51, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 59.9
Std Dev 3.002
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch51, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46.5
Std Dev 26.86
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch51, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 29.89
Std Dev 6.657
Gaus mean : 31.1438
Gaus Width: 3.4090
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.41
Std Dev 3.703
Gaus mean : 37.5070
Gaus Width: 3.9568
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 43.84
Std Dev 3.249
Gaus mean : 44.5861
Gaus Width: 3.5278
Entry
ampl
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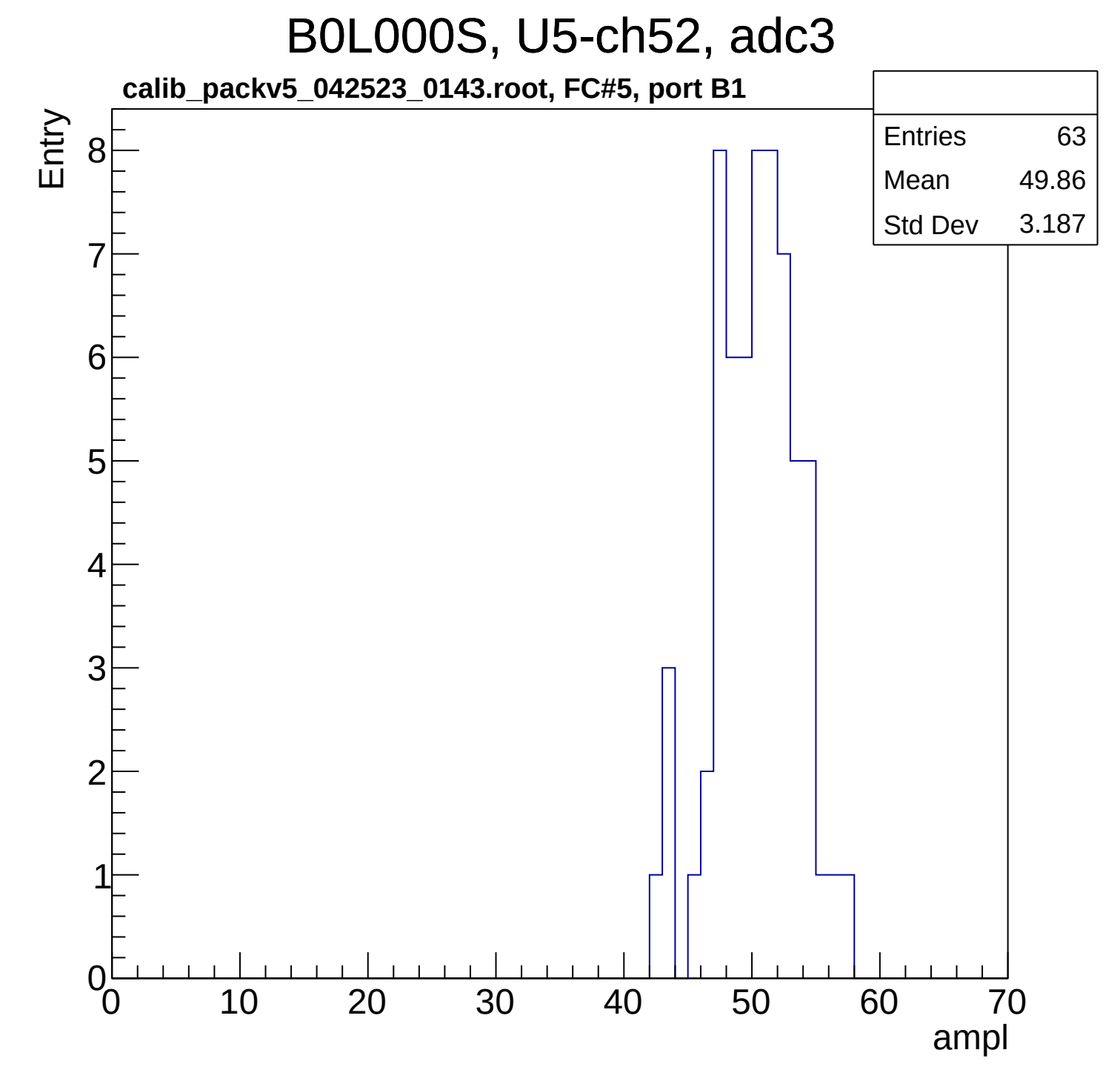
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.86
Std Dev 3.187
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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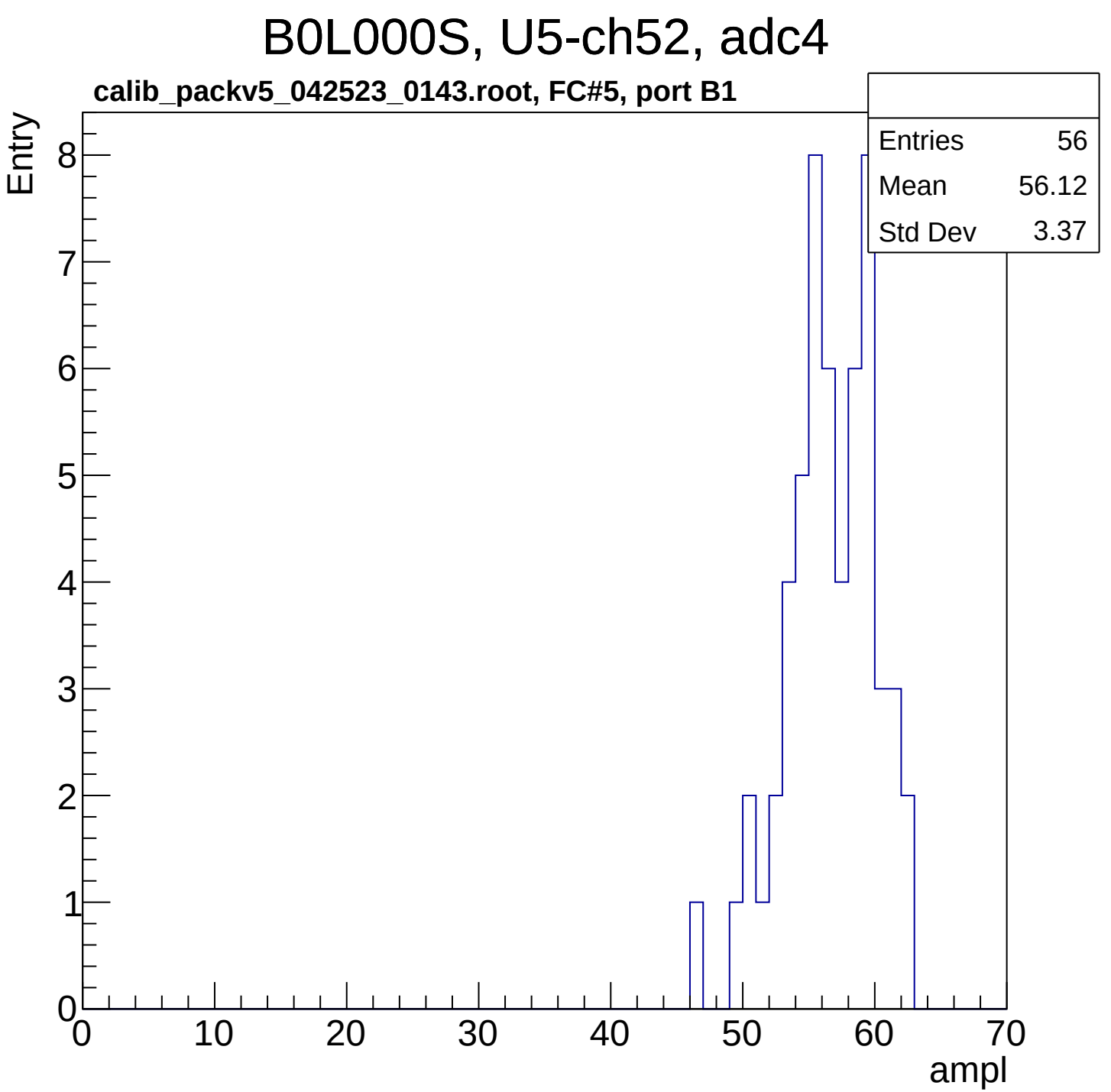
B0L000S, U5-ch52, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.12
Std Dev 3.37
Entry
ampl
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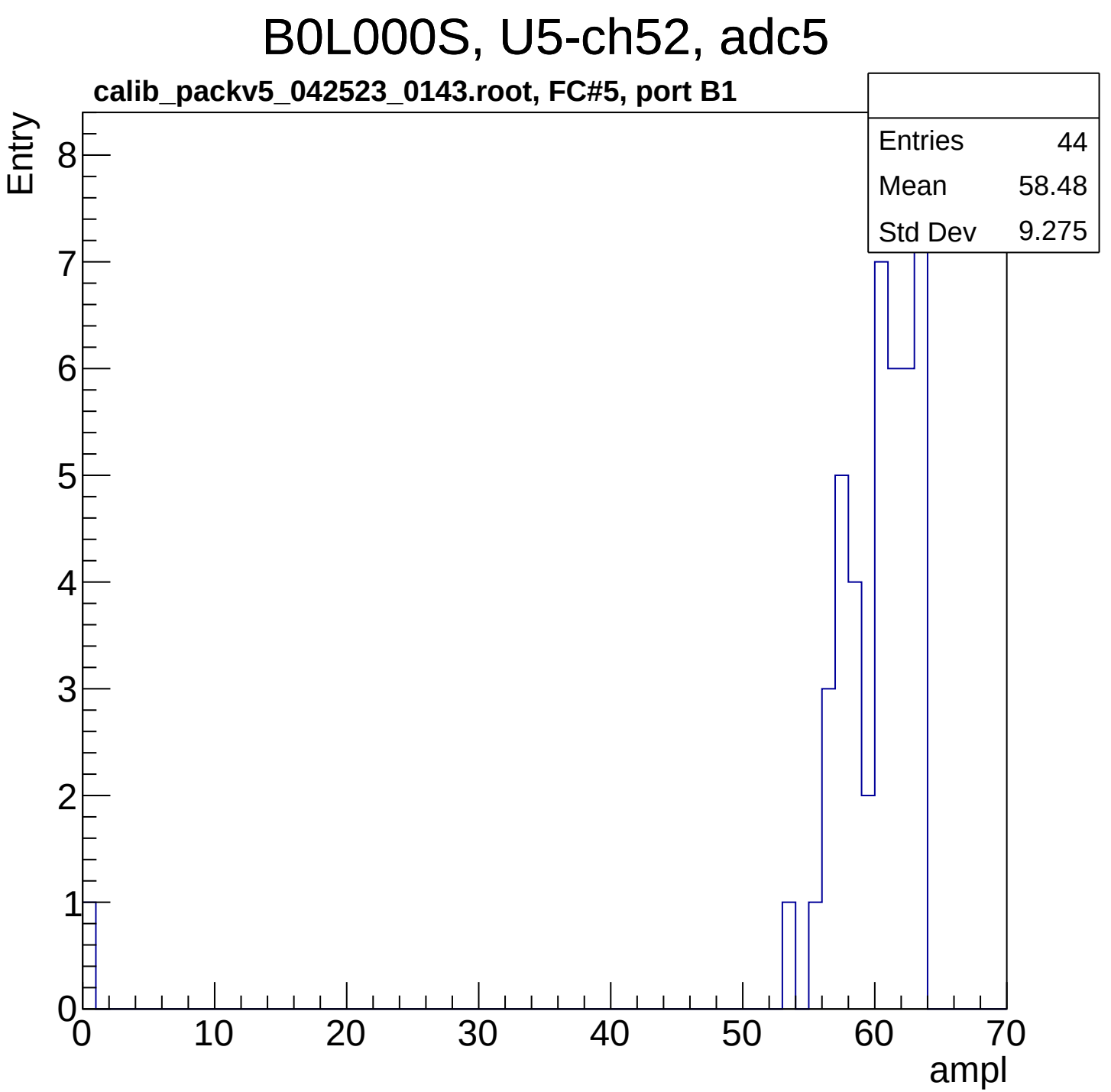
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.48
Std Dev 9.275
Entry
ampl
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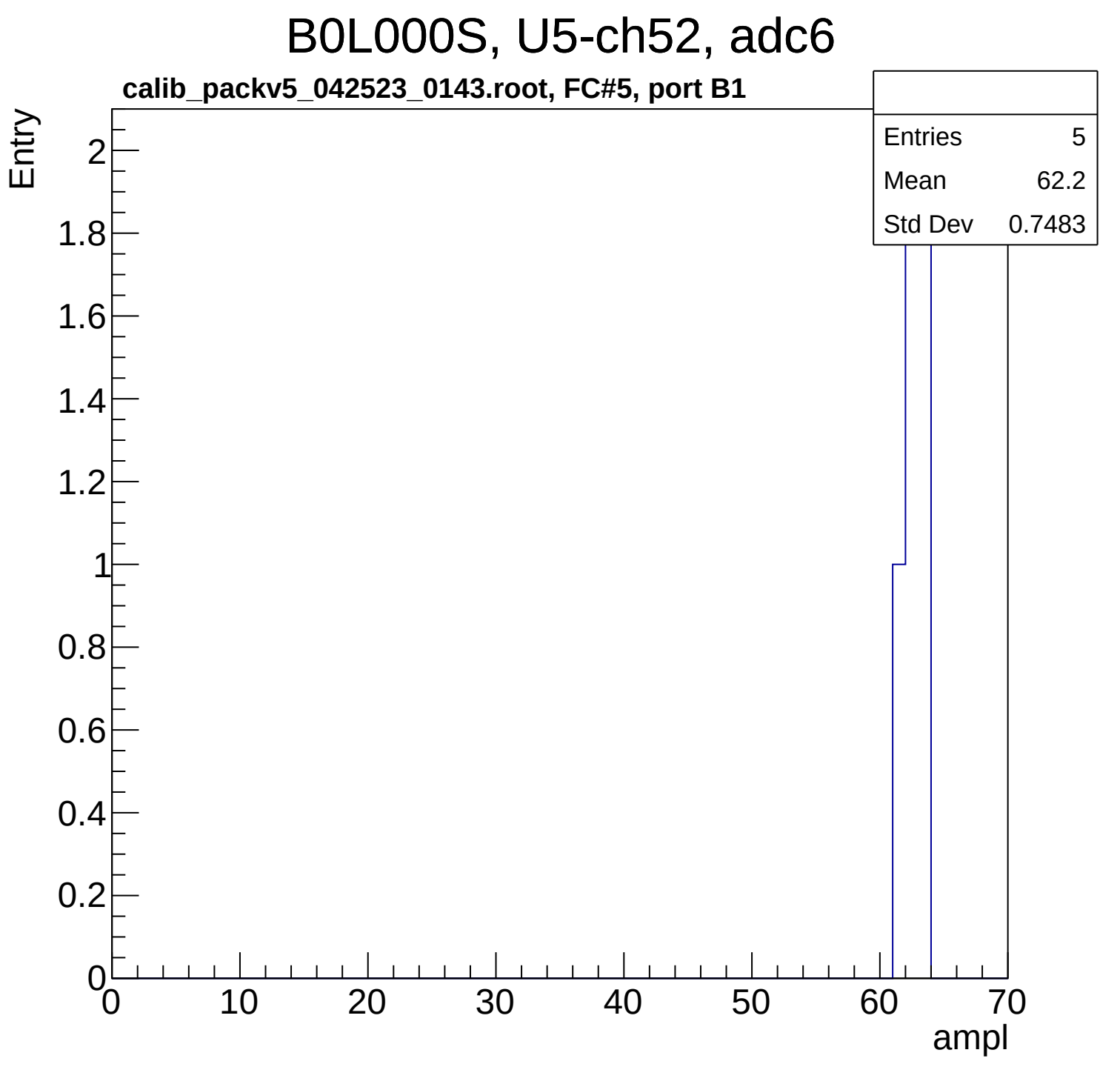
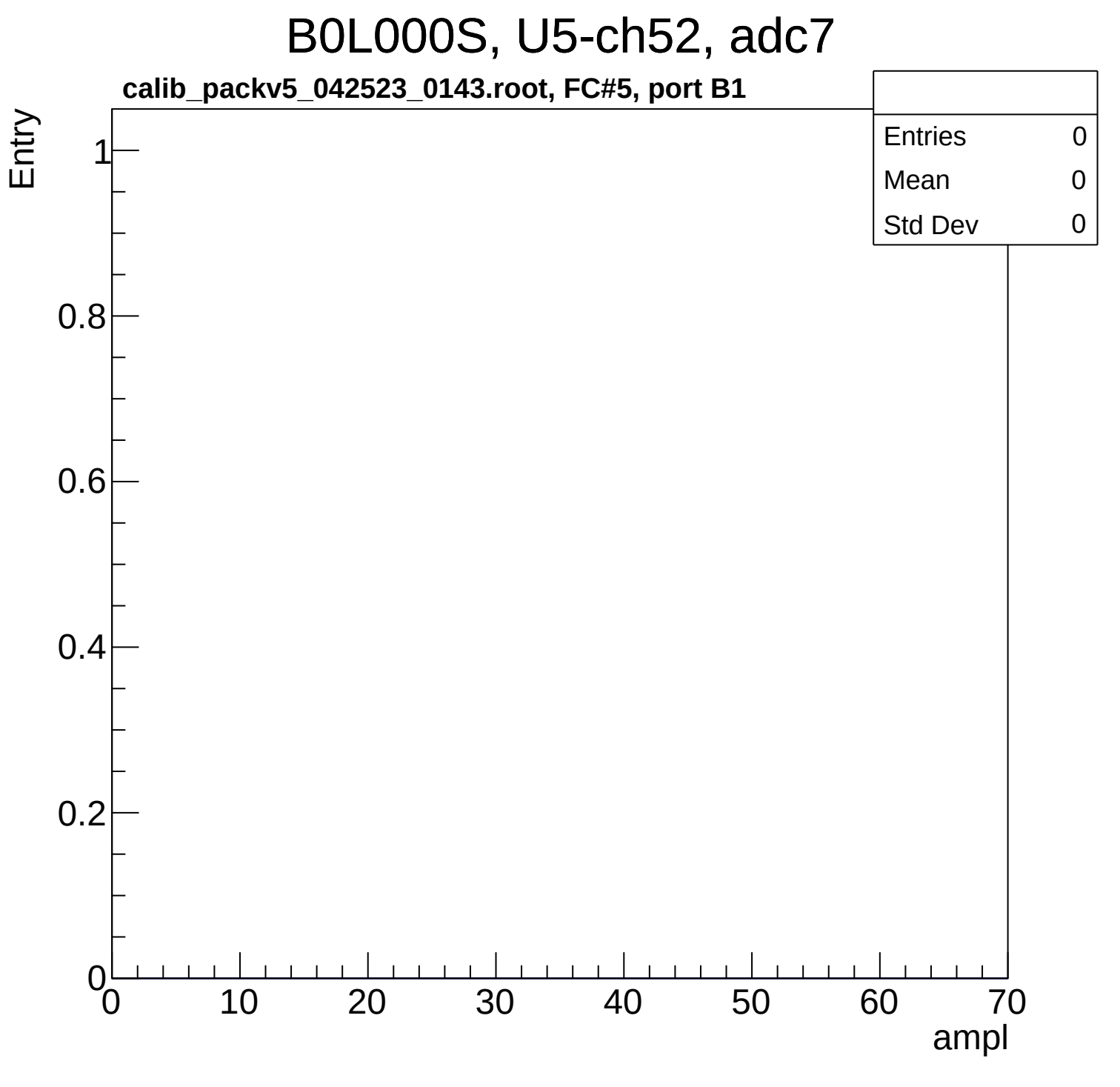
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch52, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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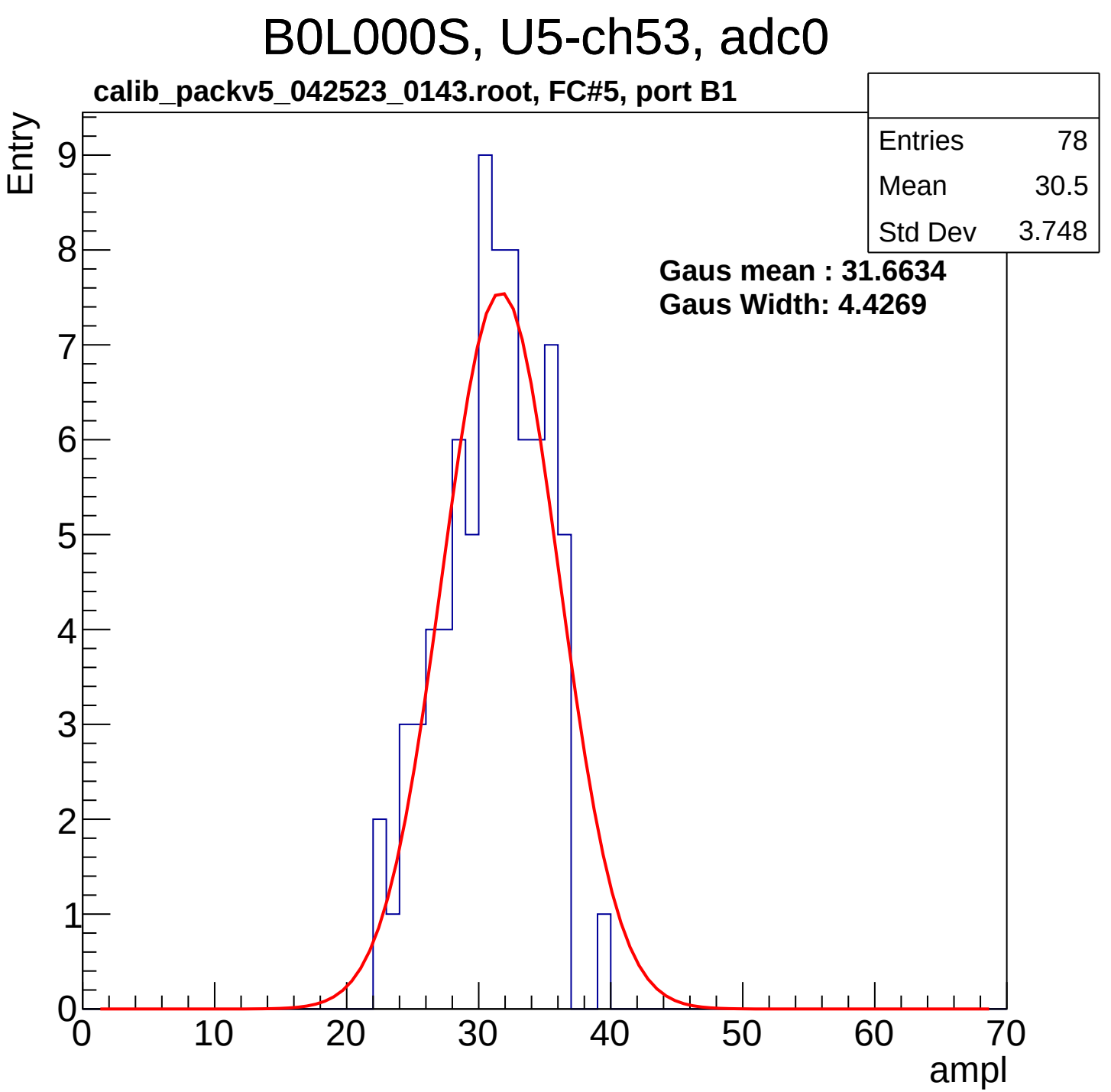
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch53, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 30.5
Std Dev 3.748
Gaus mean : 31.6634
Gaus Width: 4.4269
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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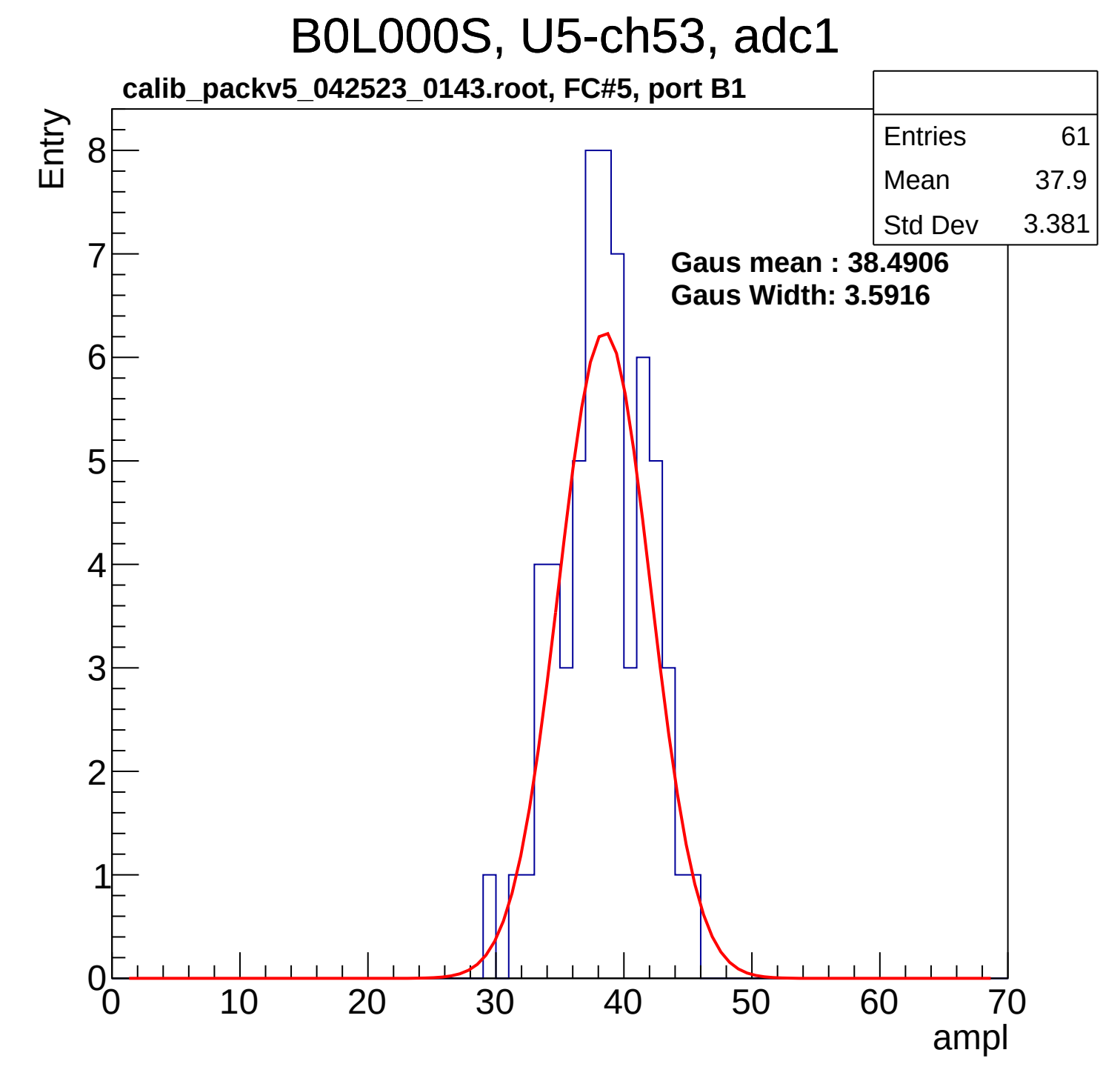
B0L000S, U5-ch53, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 37.9
Std Dev 3.381
Gaus mean : 38.4906
Gaus Width: 3.5916
Entry
ampl
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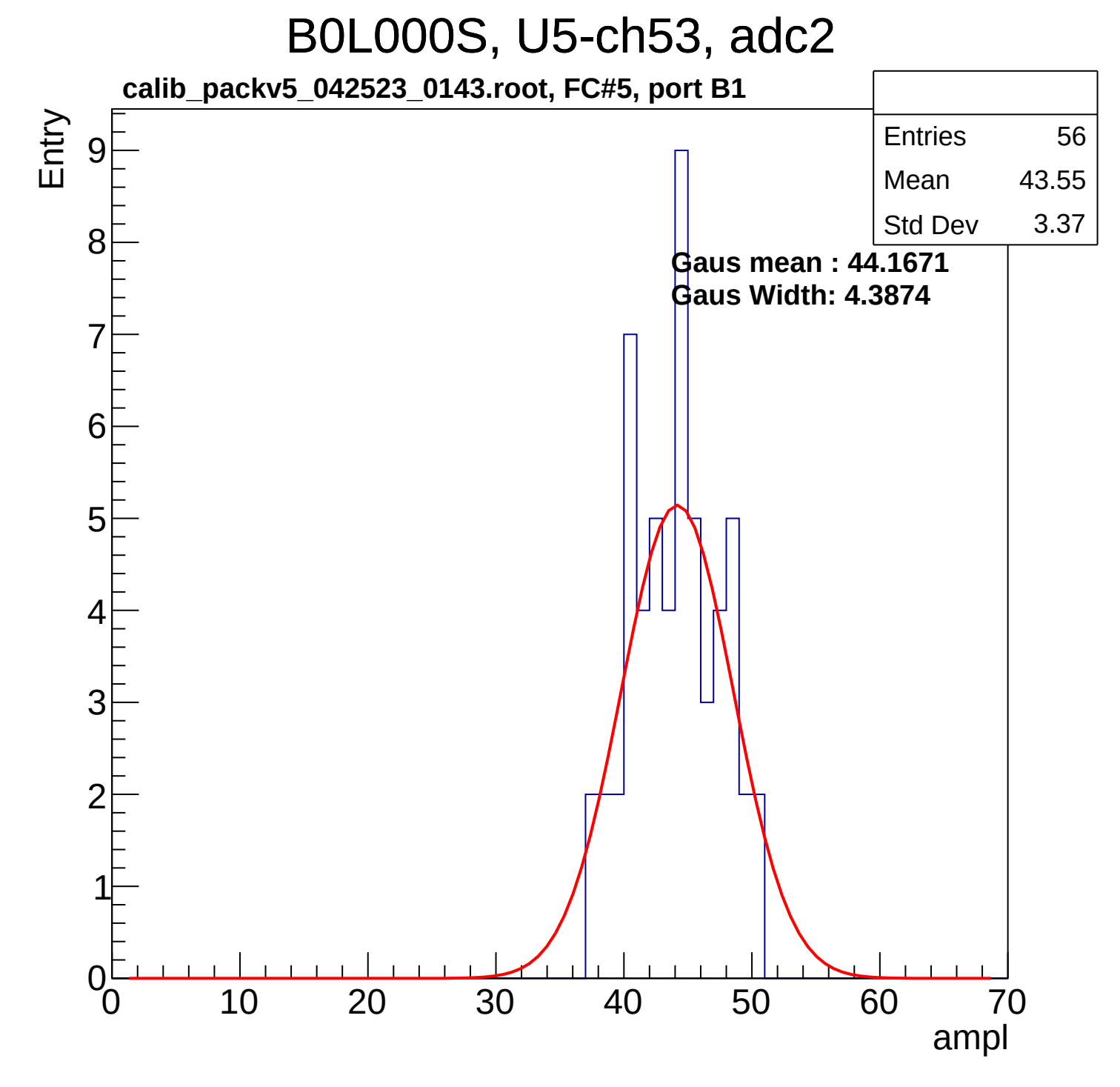
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch53, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.55
Std Dev 3.37
Gaus mean : 44.1671
Gaus Width: 4.3874
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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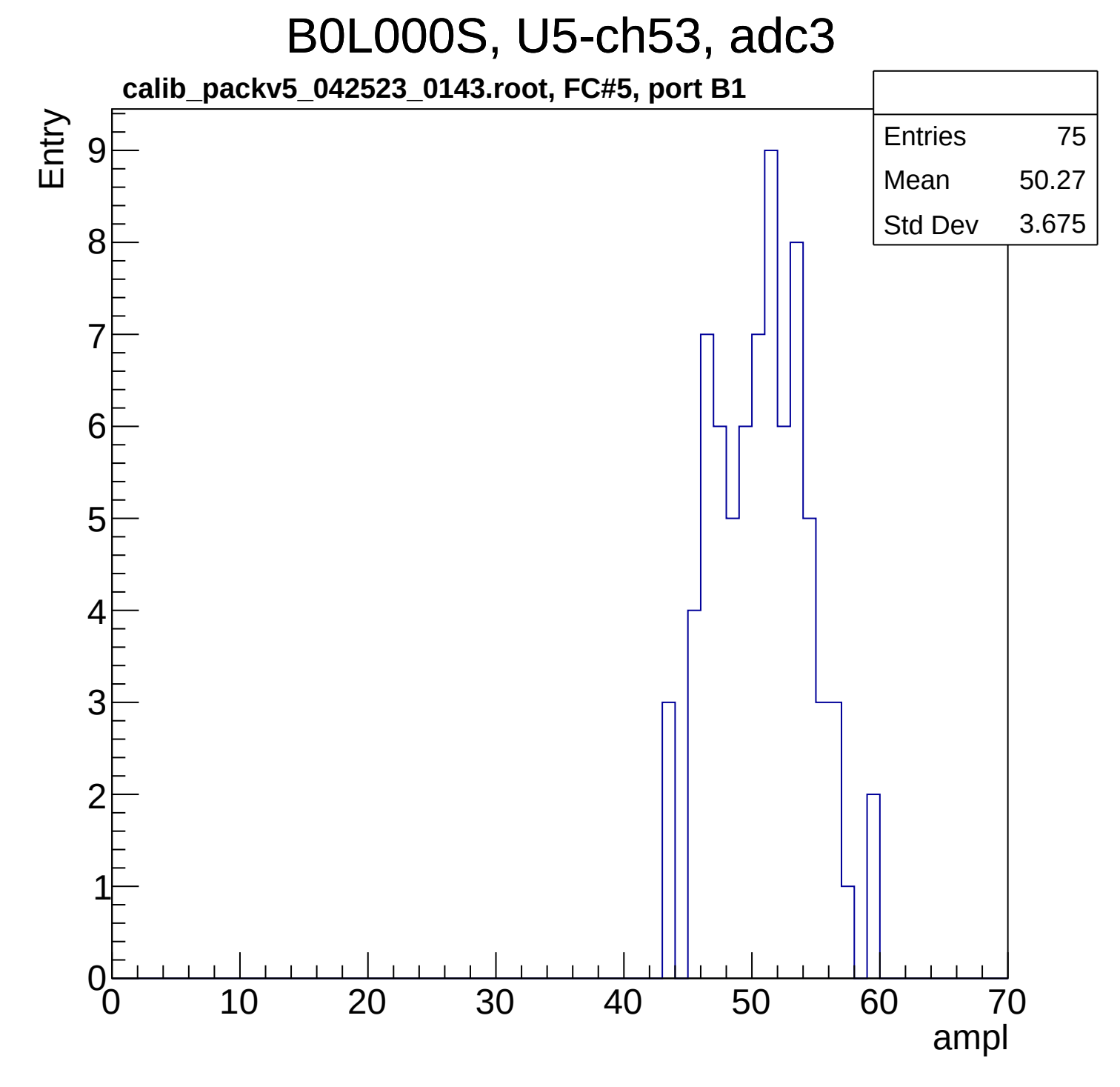
B0L000S, U5-ch53, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.27
Std Dev 3.675
Entry
ampl
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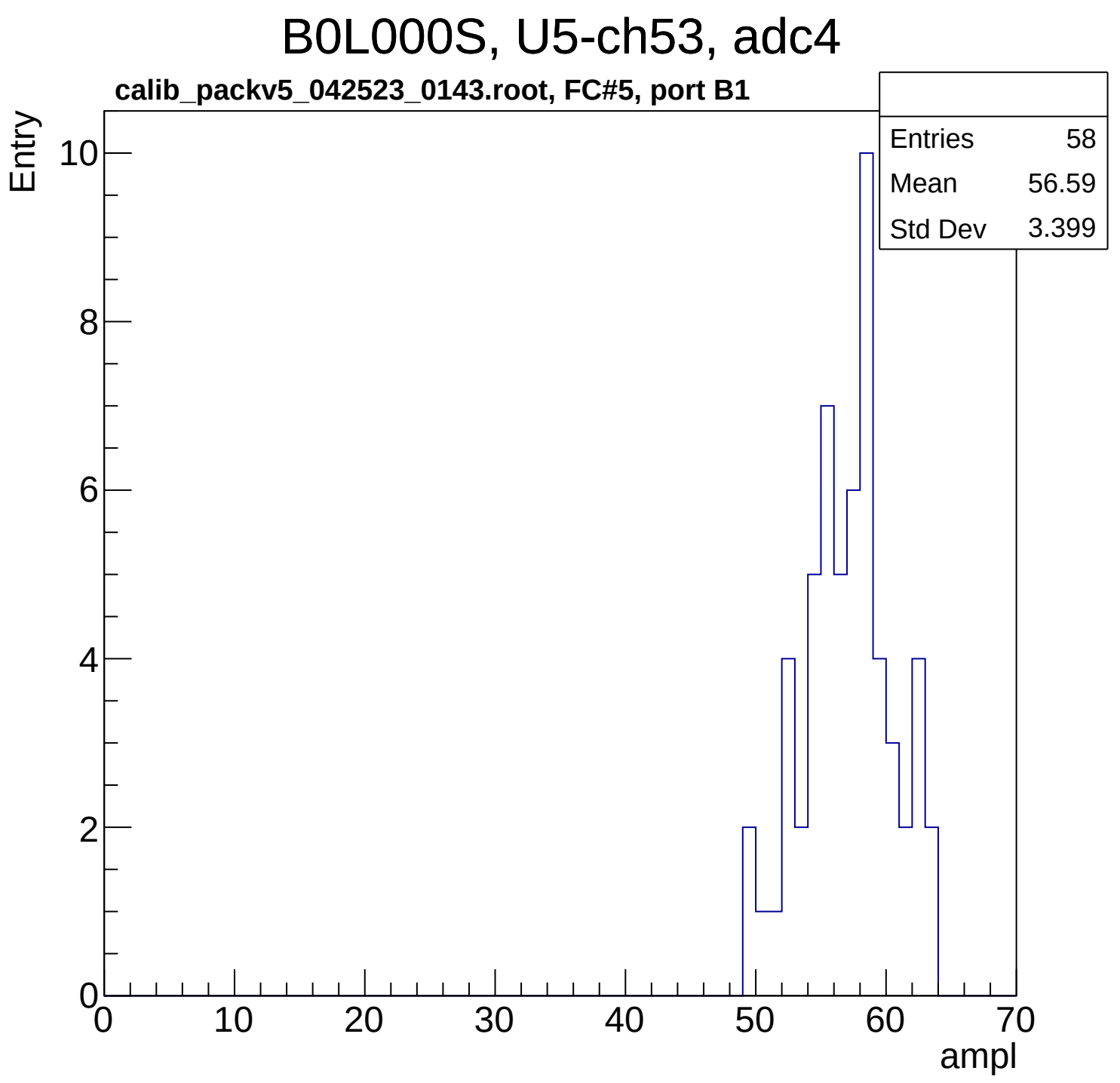
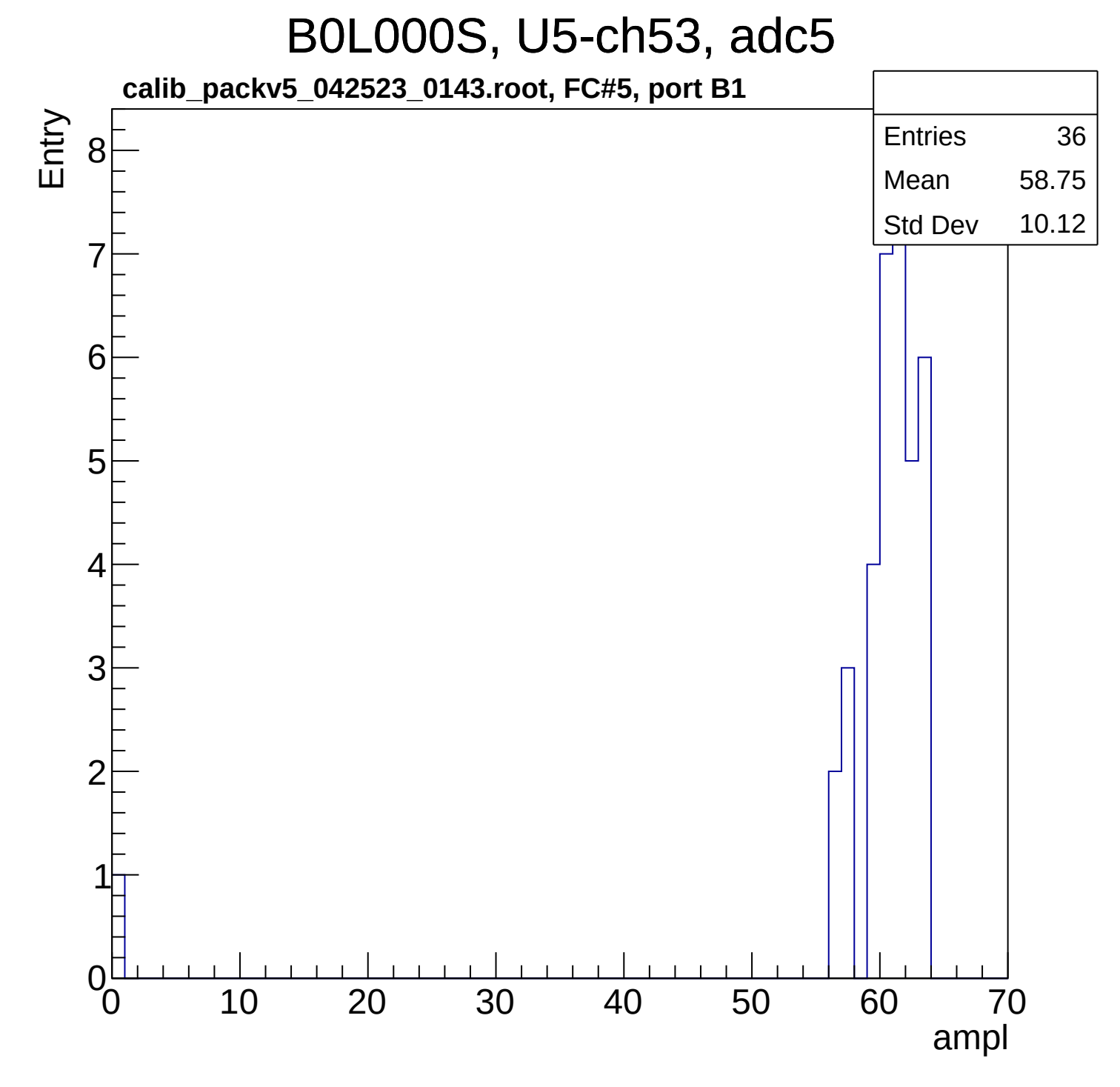
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch53, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.59
Std Dev 3.399
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch53, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 58.75
Std Dev 10.12
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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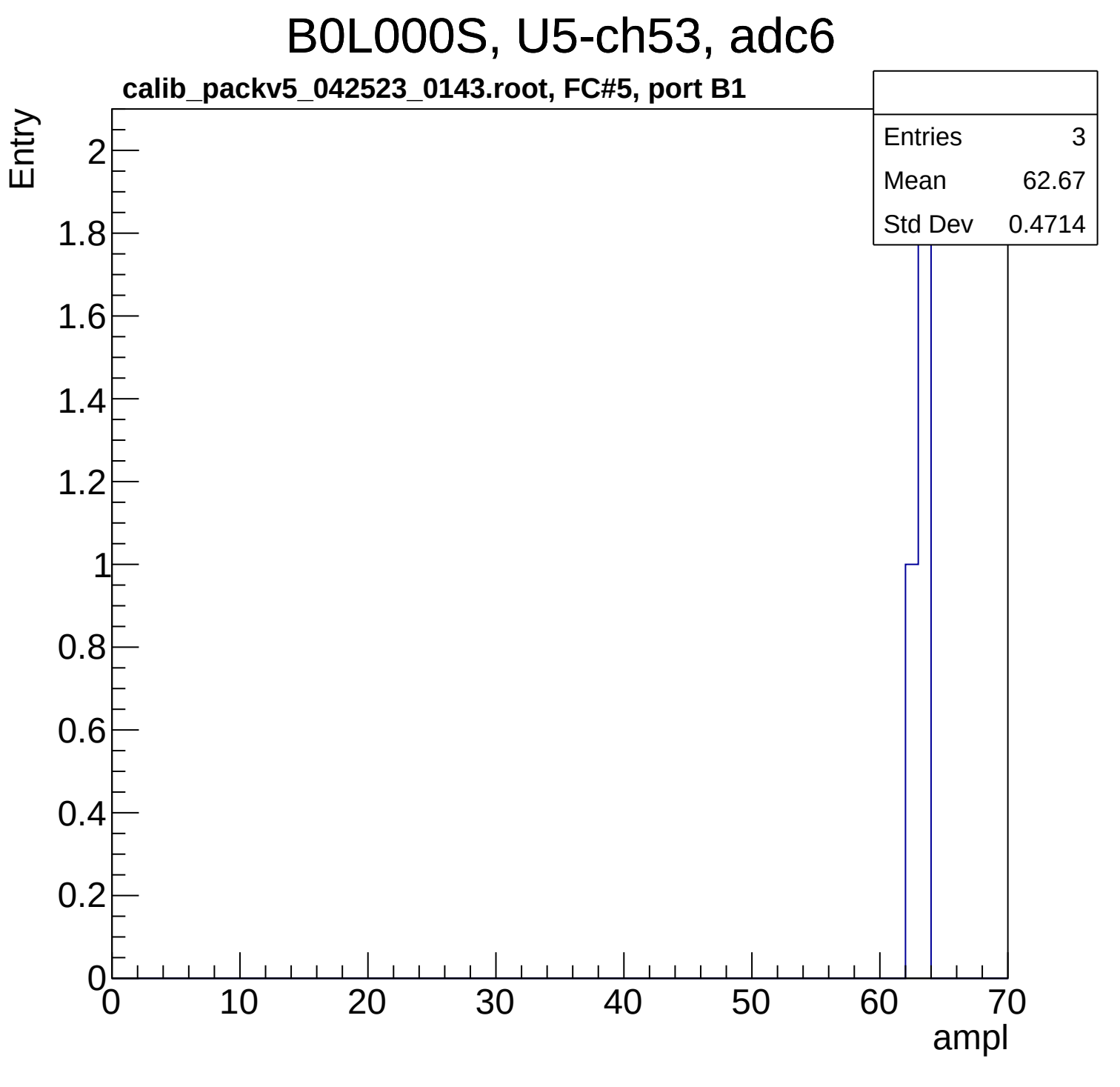
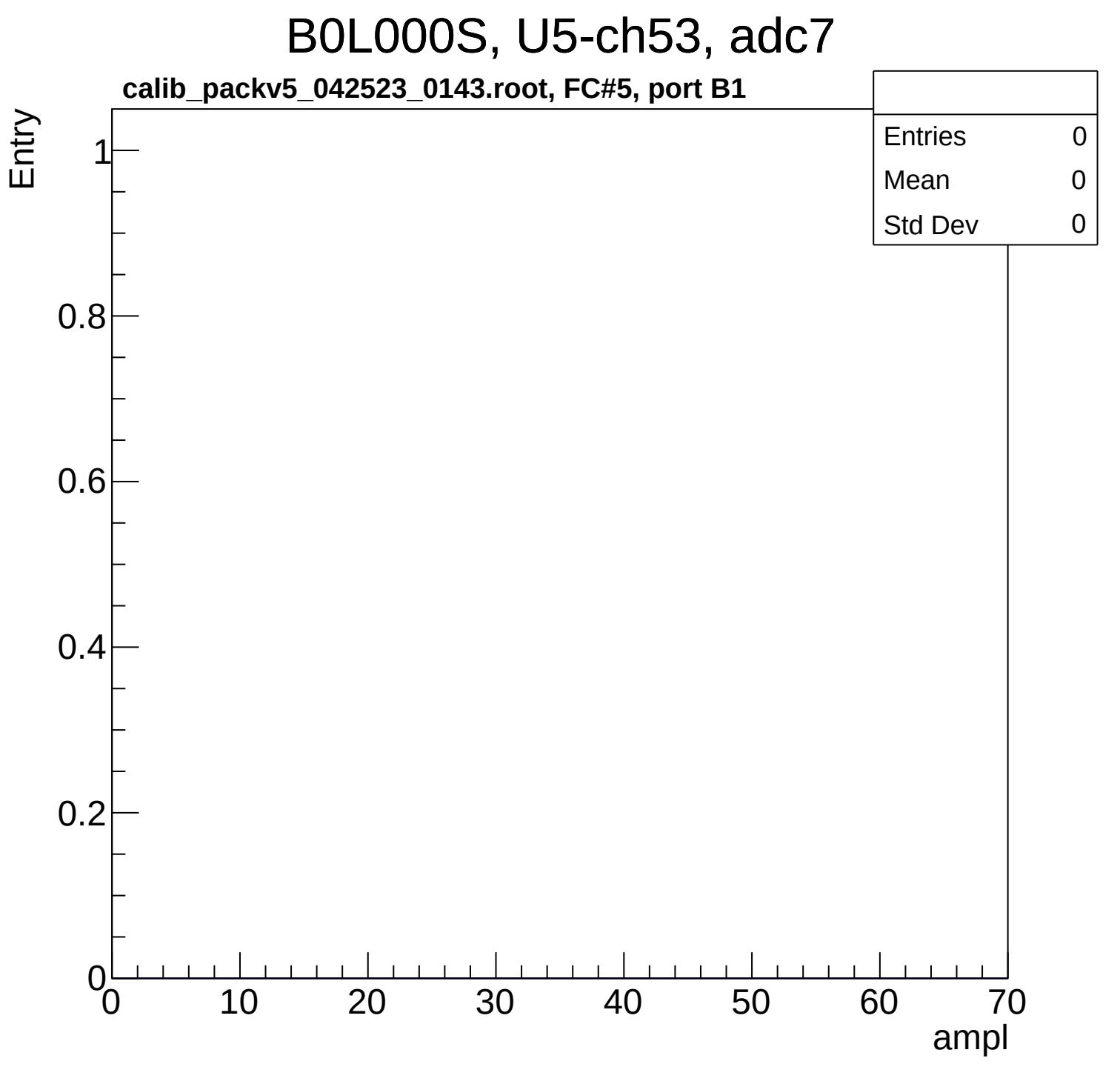
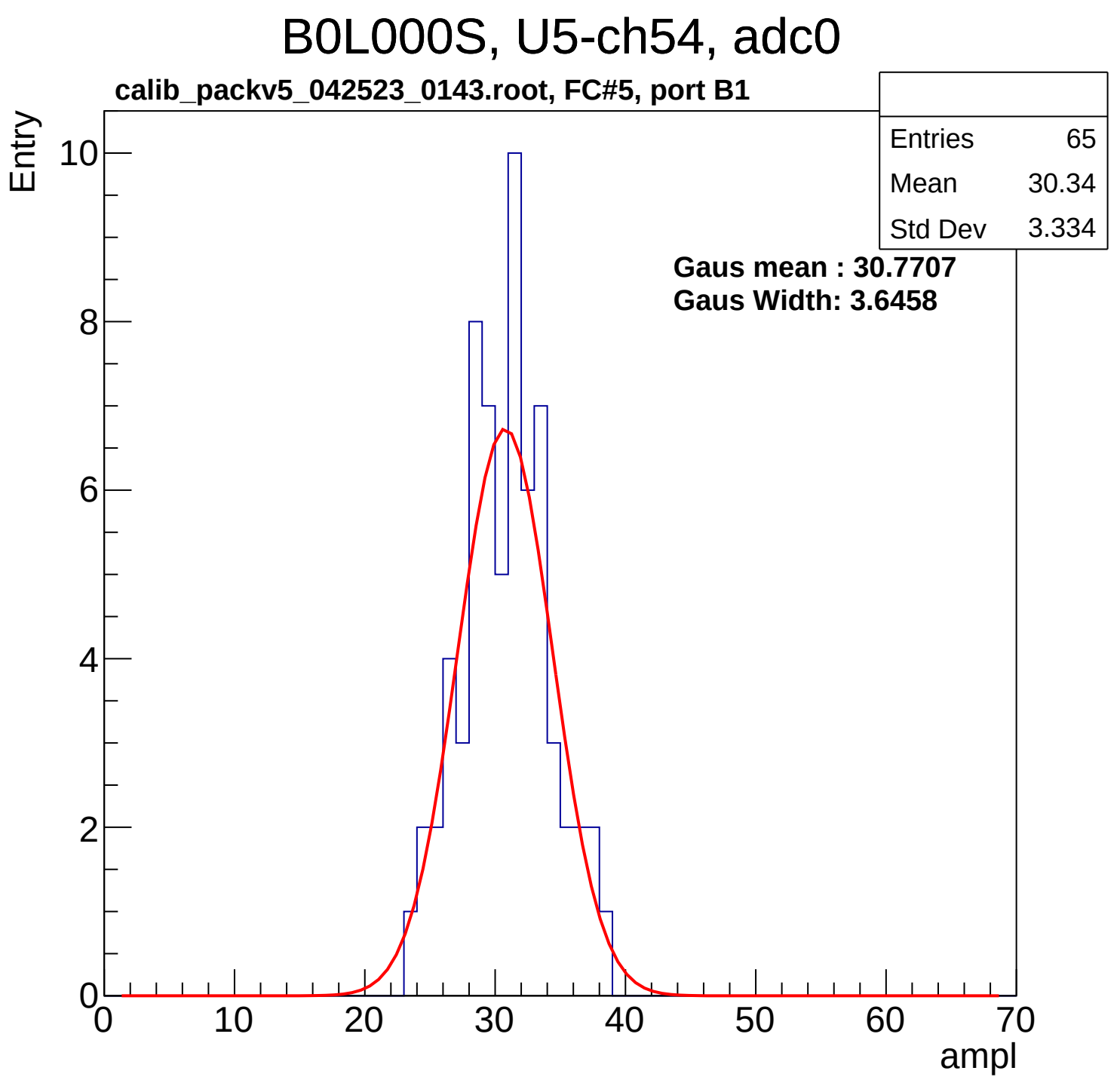

B0L000S, U5-ch53, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch53, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch54, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.34
Std Dev 3.334
Gaus mean : 30.7707
Gaus Width: 3.6458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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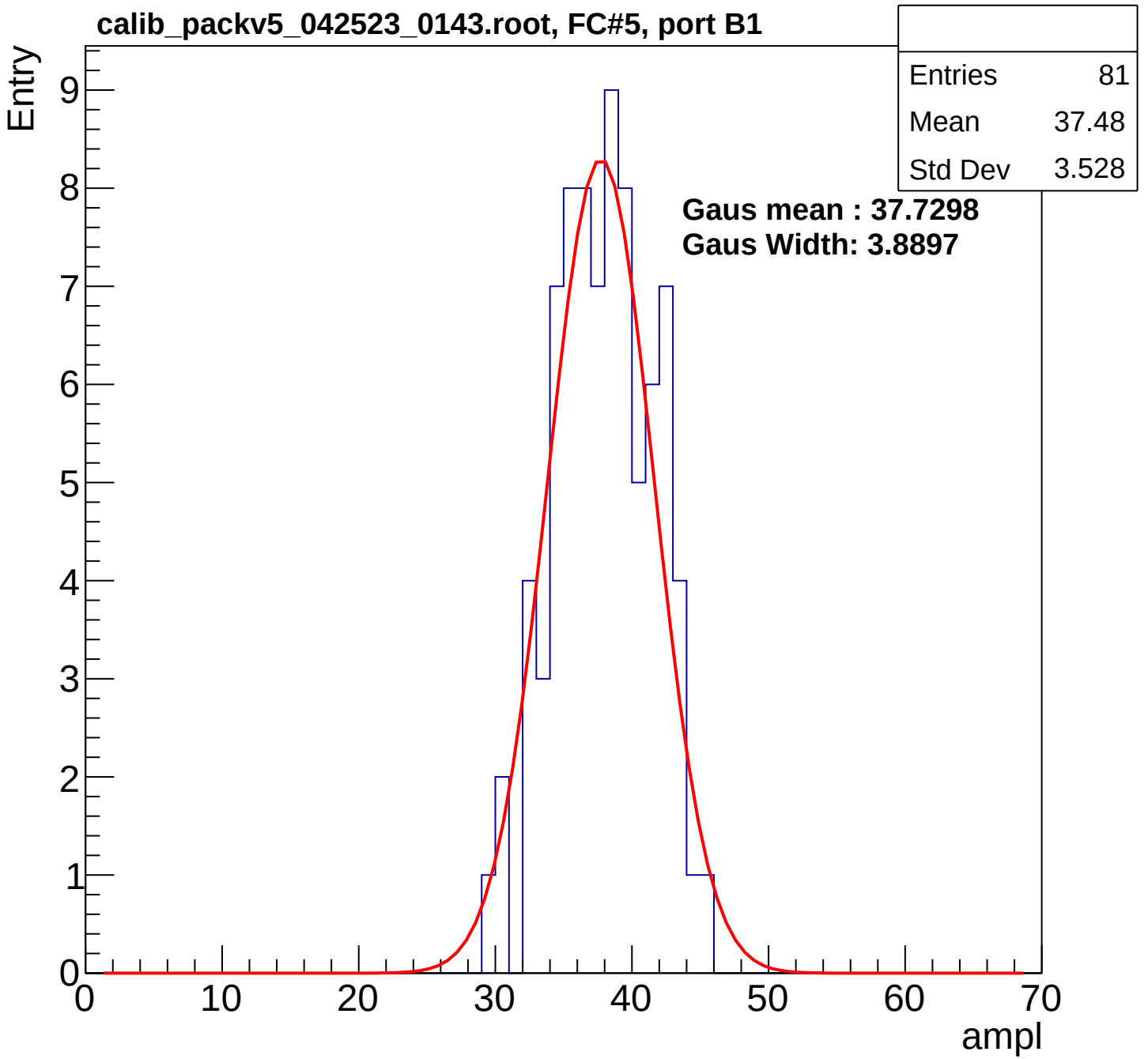
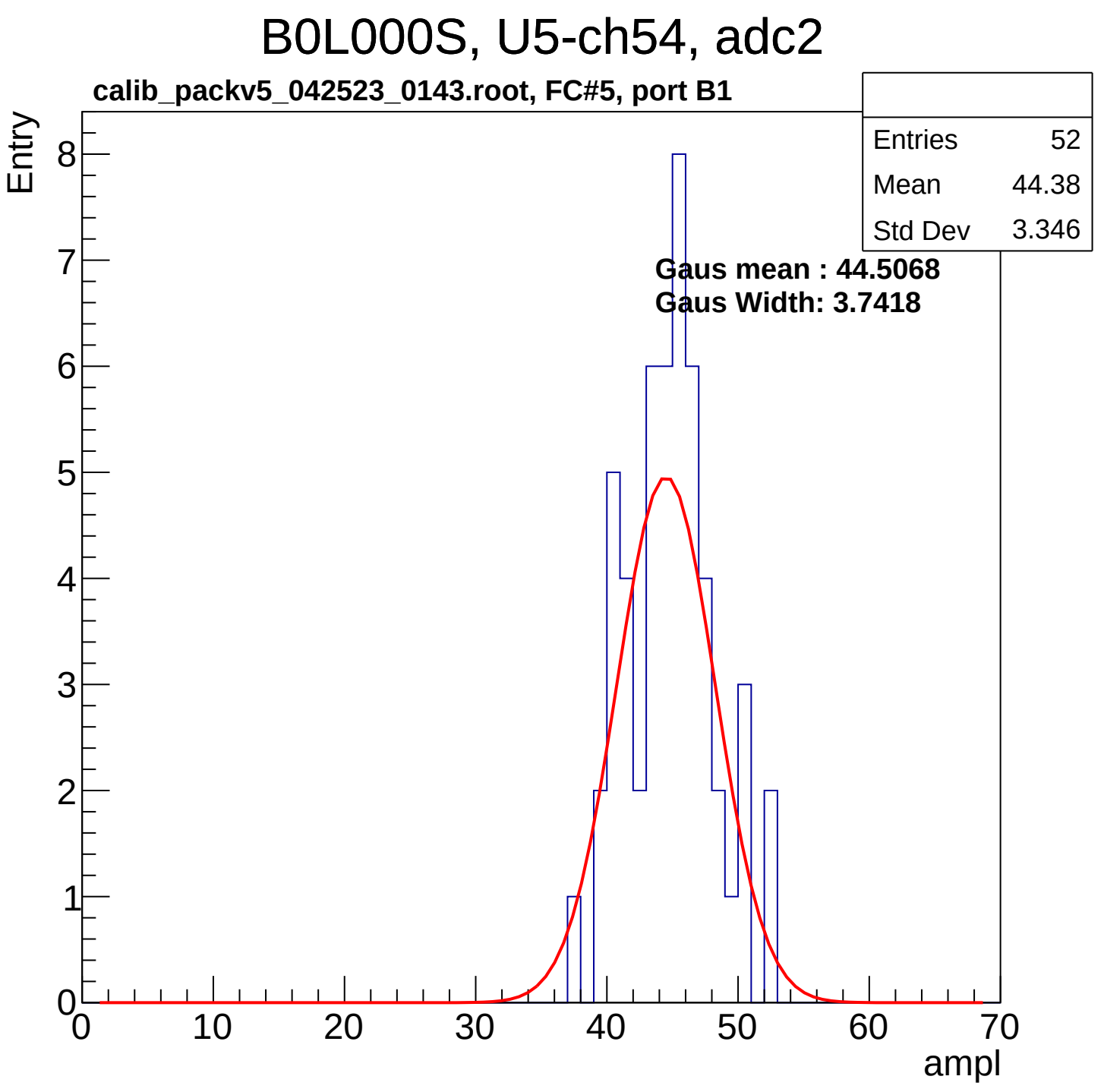

B0L000S, U5-ch54, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 37.48
Std Dev 3.528
Gaus mean : 37.7298
Gaus Width: 3.8897
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U5-ch54, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 44.38
Std Dev 3.346
Gaus mean : 44.5068
Gaus Width: 3.7418
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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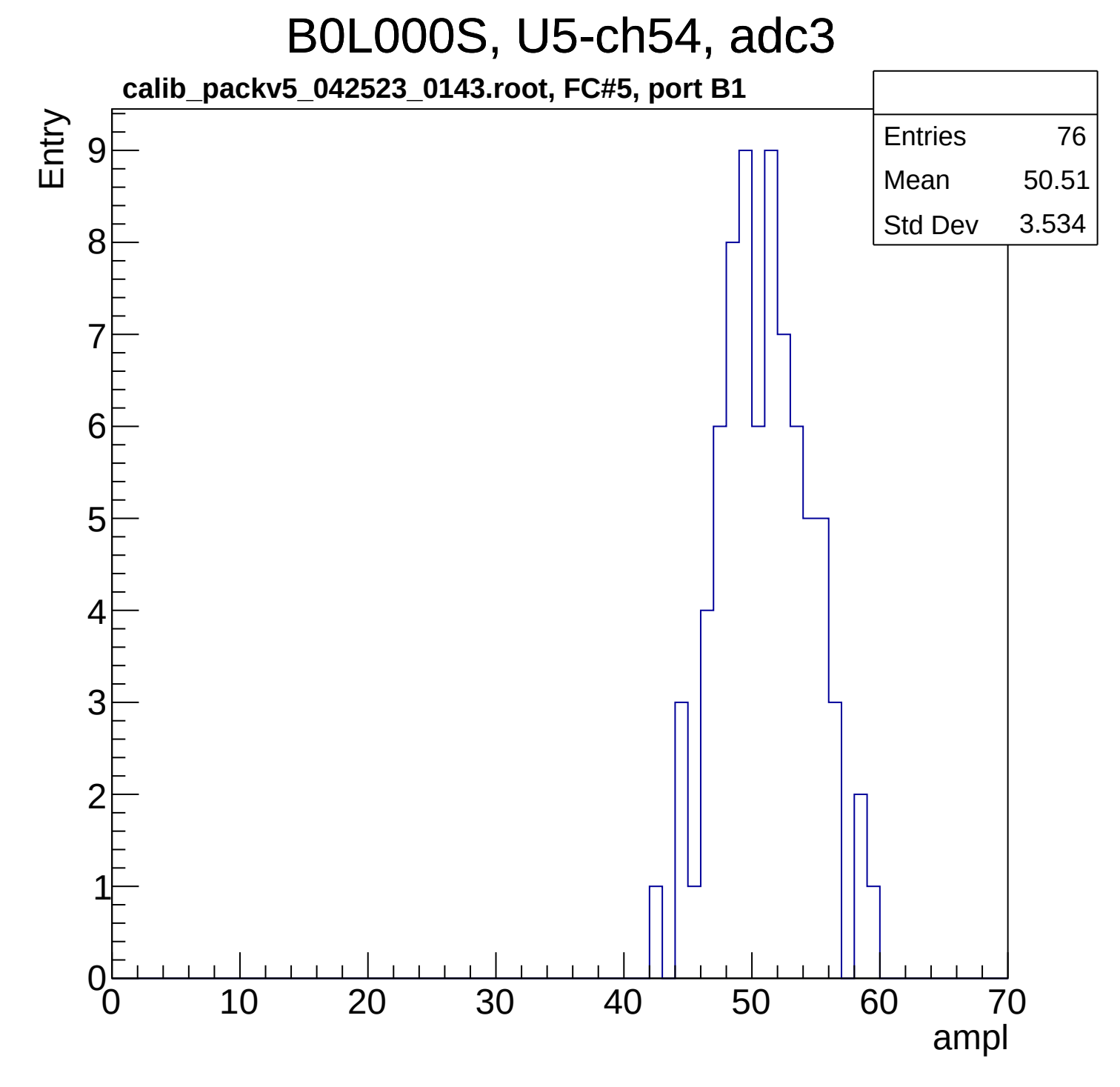
B0L000S, U5-ch54, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.51
Std Dev 3.534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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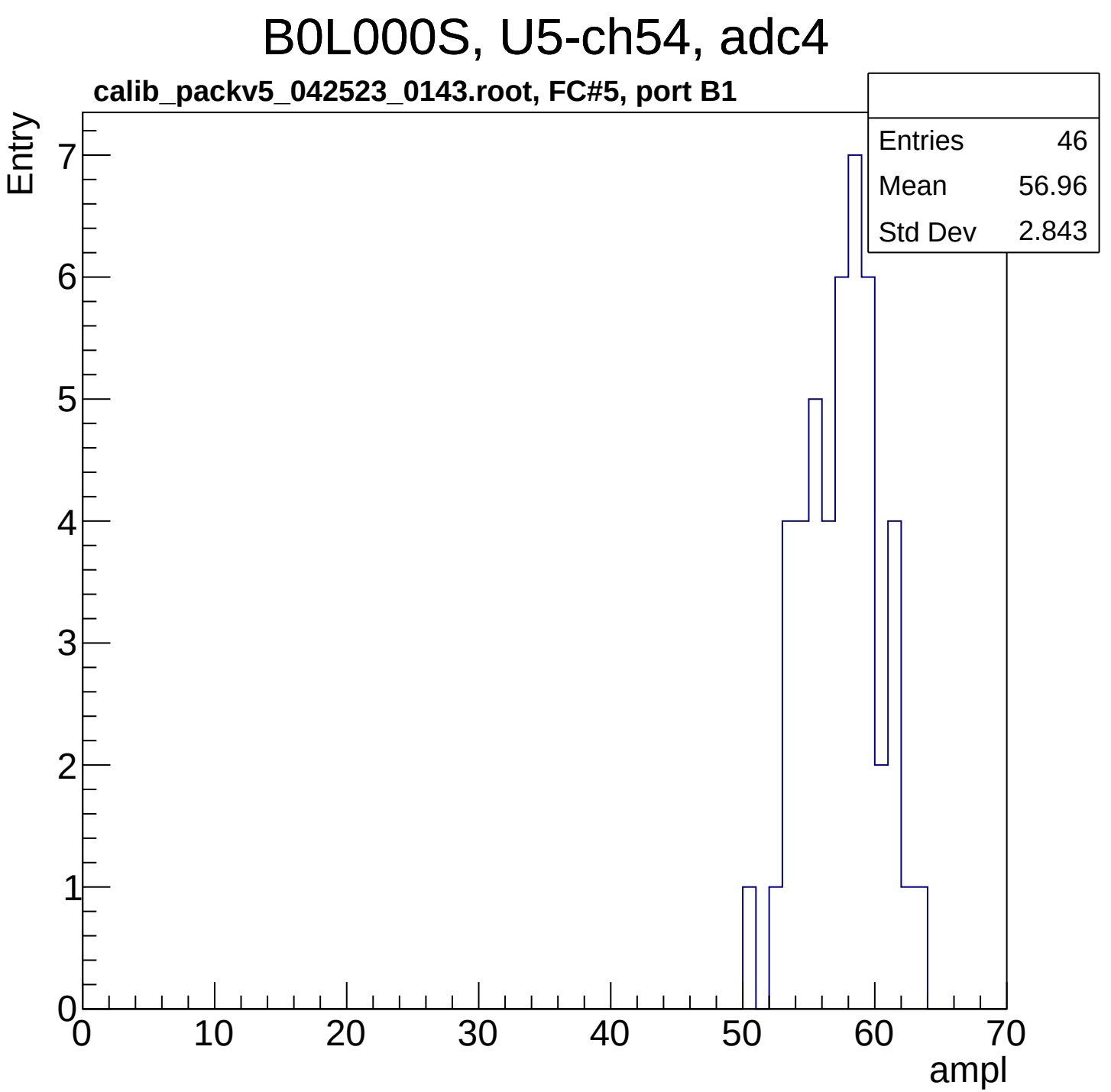
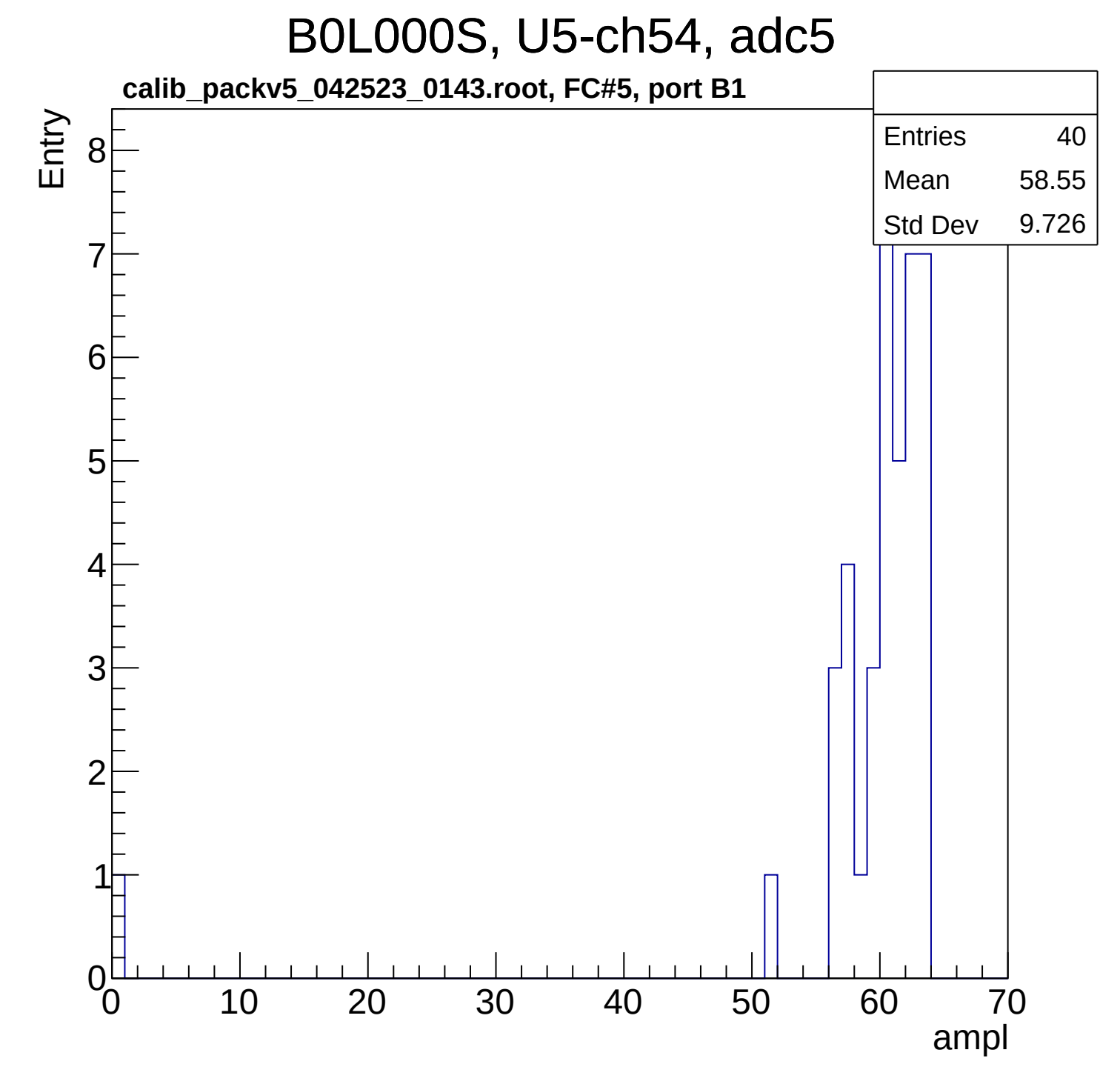

B0L000S, U5-ch54, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 56.96
Std Dev 2.843
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch54, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.55
Std Dev 9.726
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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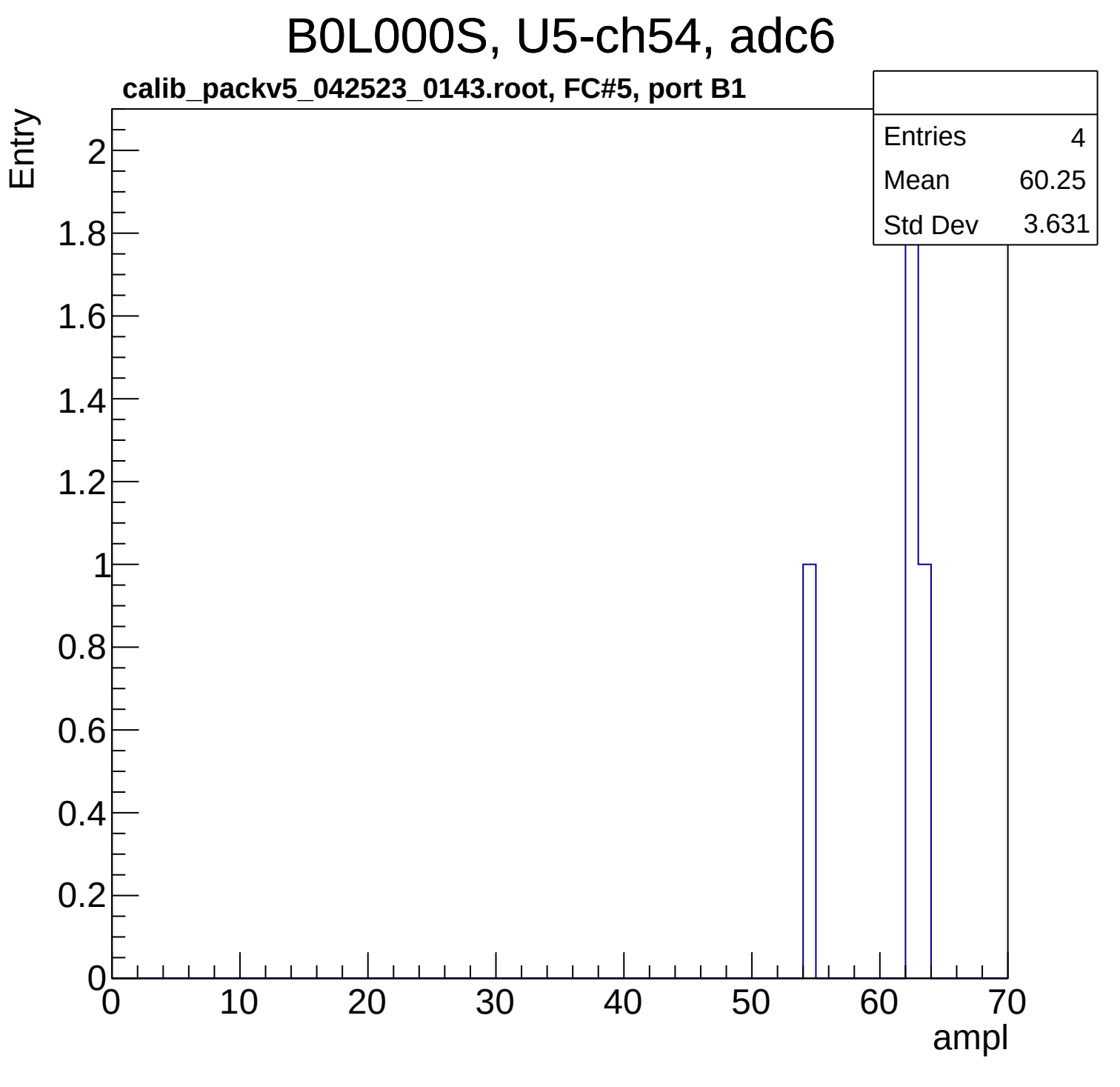
B0L000S, U5-ch54, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.25
Std Dev 3.631
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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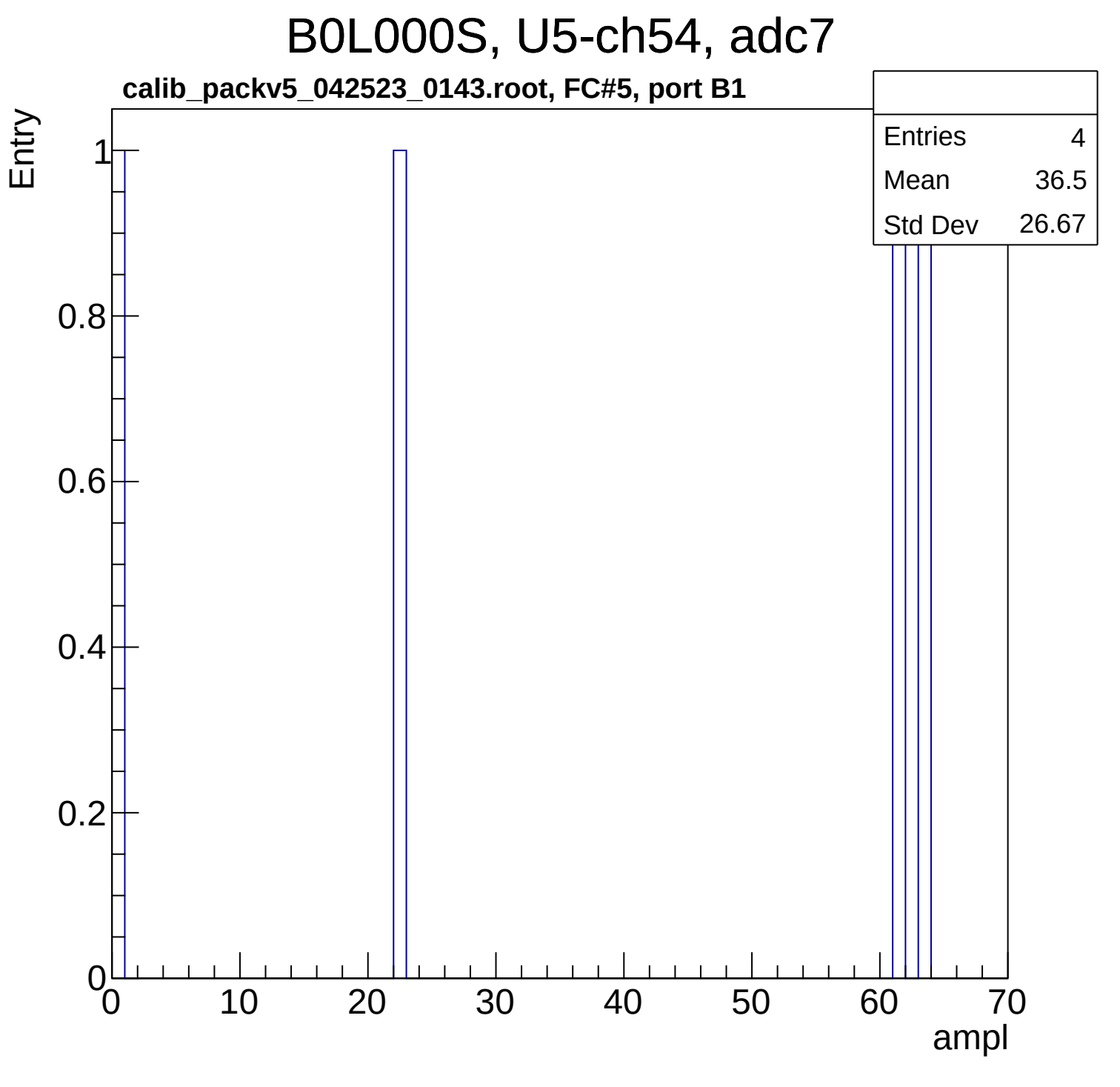
B0L000S, U5-ch54, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 36.5
Std Dev 26.67
Entry
ampl
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0.4
0.6
0.8
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0
10
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40
50
60
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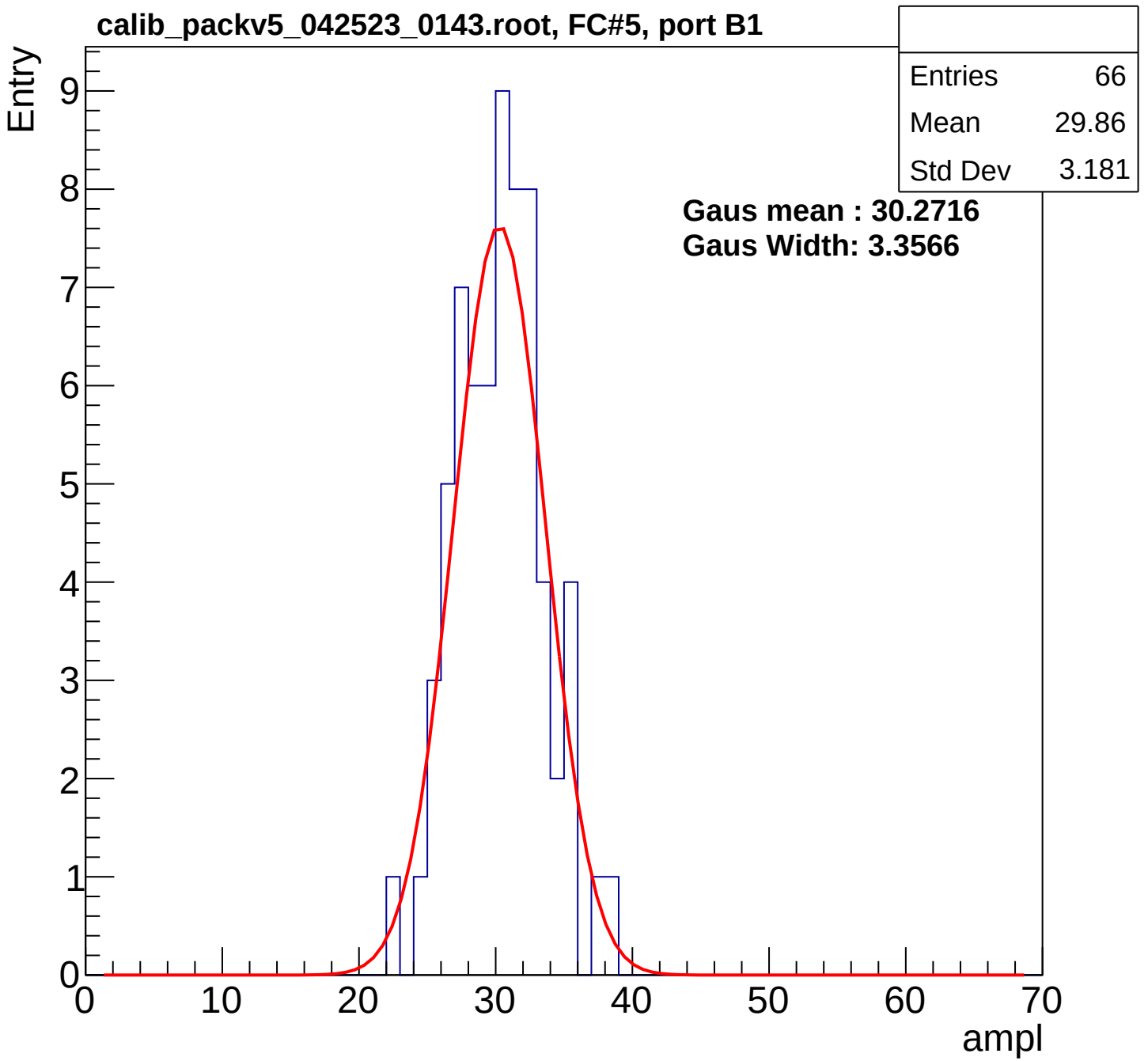
B0L000S, U5-ch55, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.86
Std Dev 3.181
Gaus mean : 30.2716
Gaus Width: 3.3566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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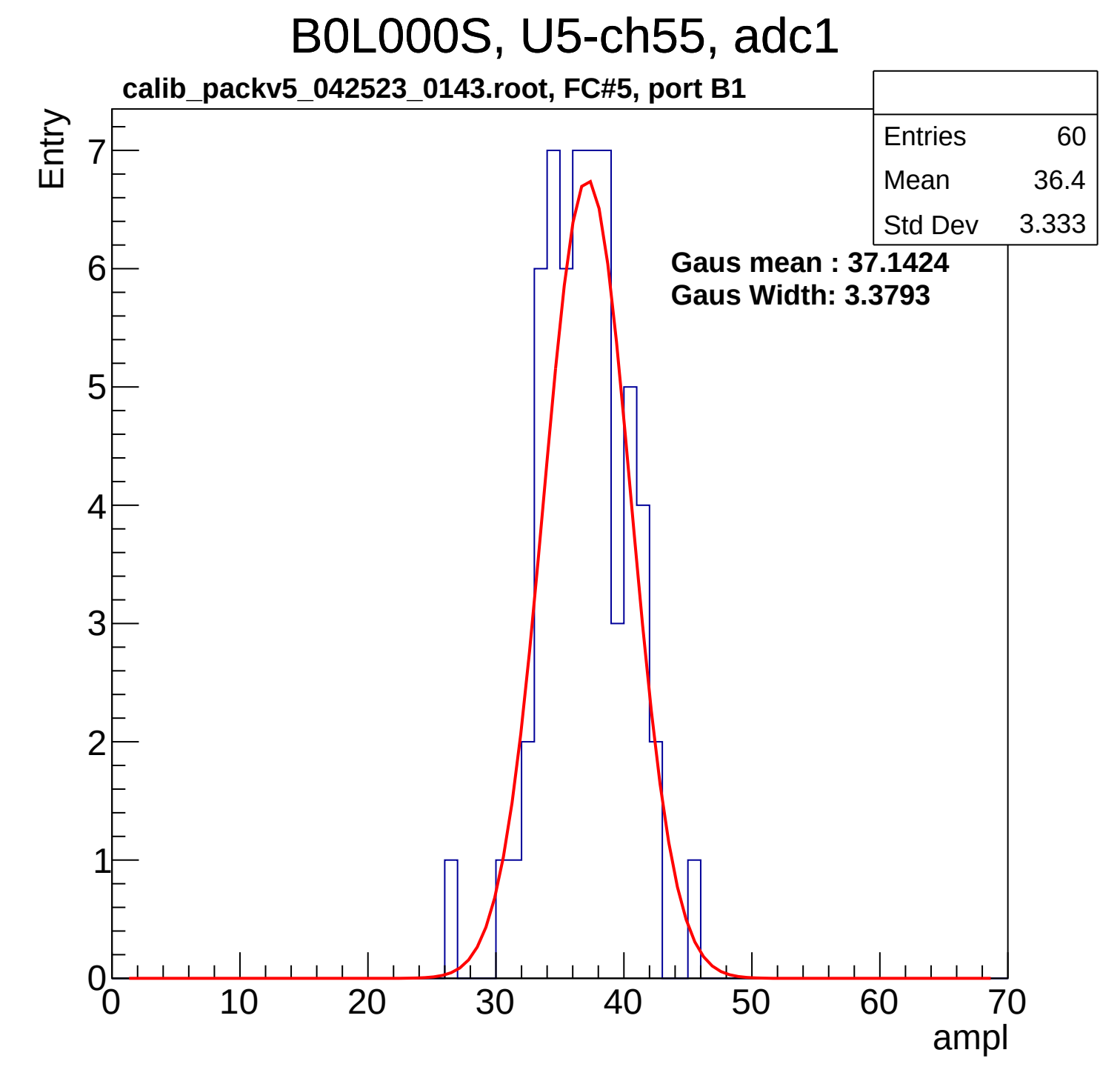
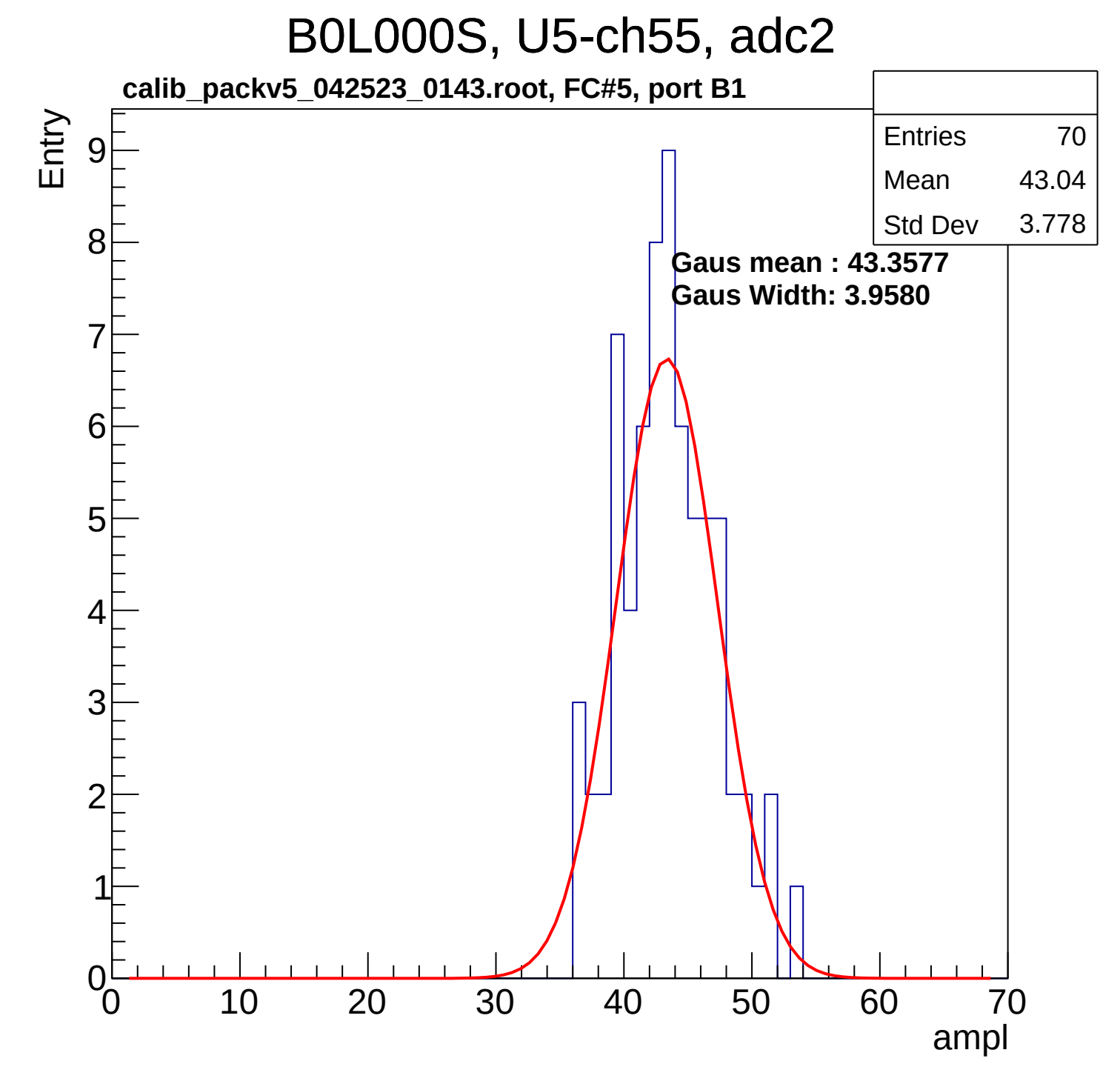

B0L000S, U5-ch55, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 36.4
Std Dev 3.333
Gaus mean : 37.1424
Gaus Width: 3.3793
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch55, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 43.04
Std Dev 3.778
Gaus mean : 43.3577
Gaus Width: 3.9580
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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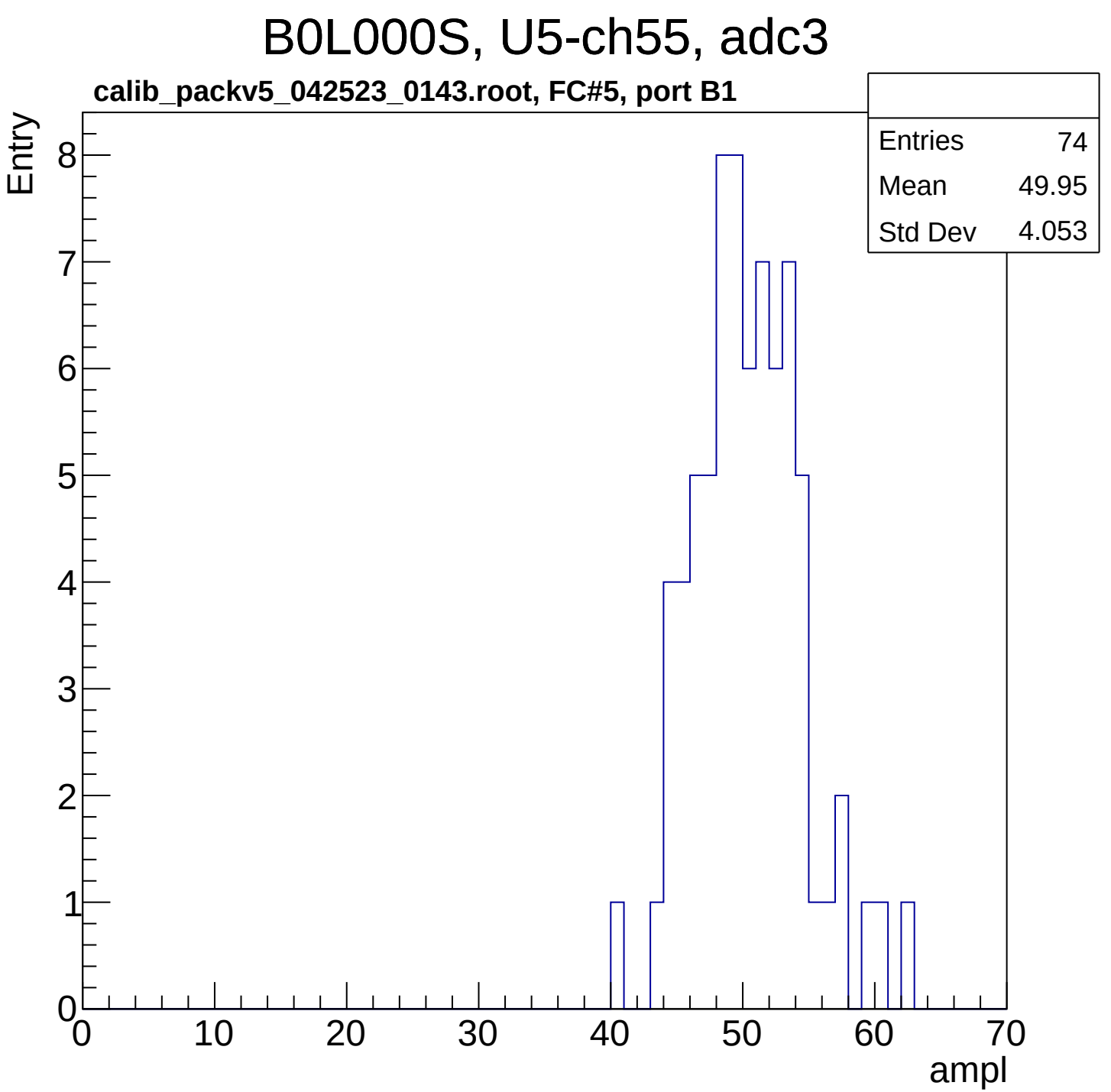
B0L000S, U5-ch55, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.95
Std Dev 4.053
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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50
60
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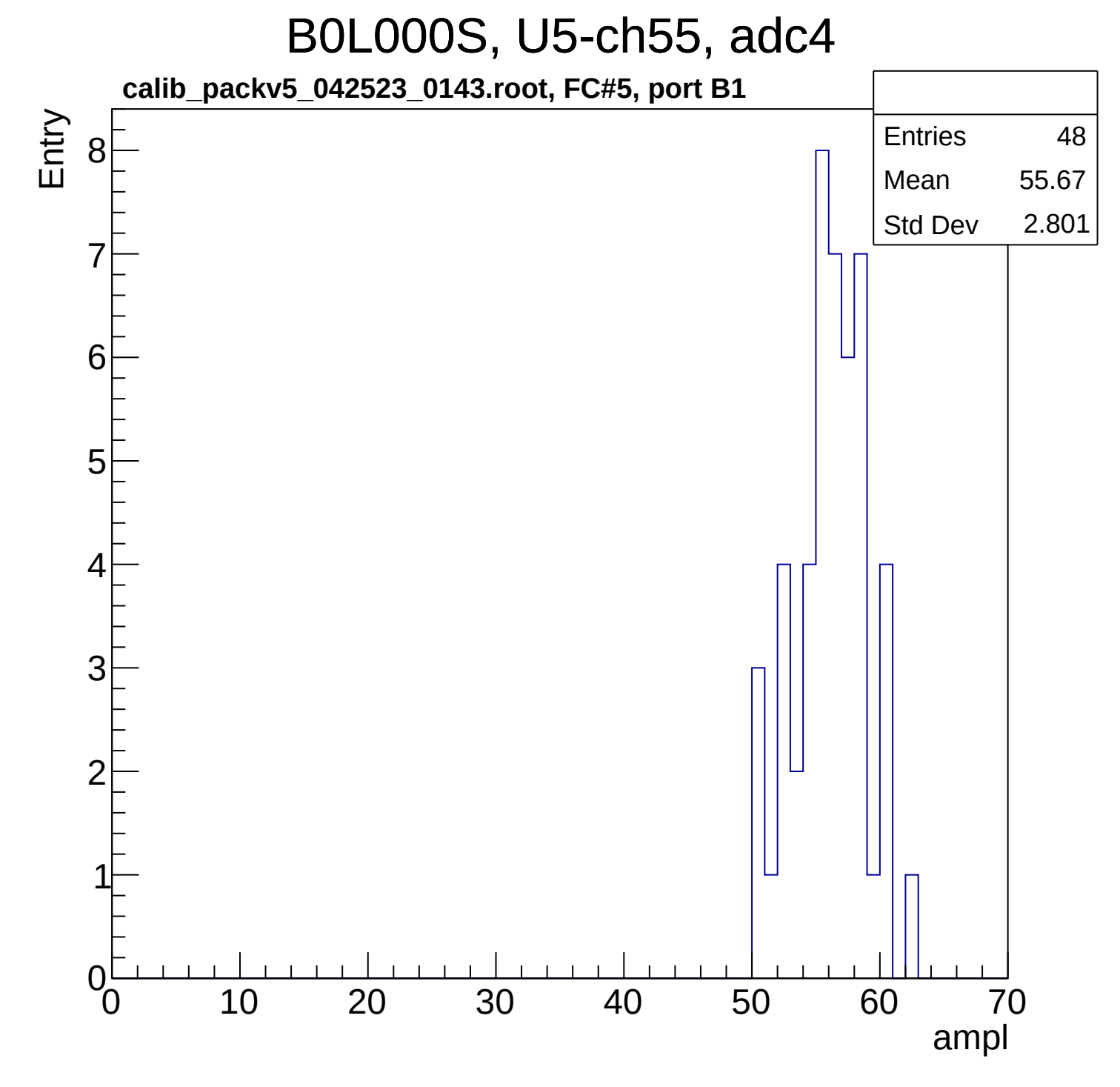
B0L000S, U5-ch55, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 55.67
Std Dev 2.801
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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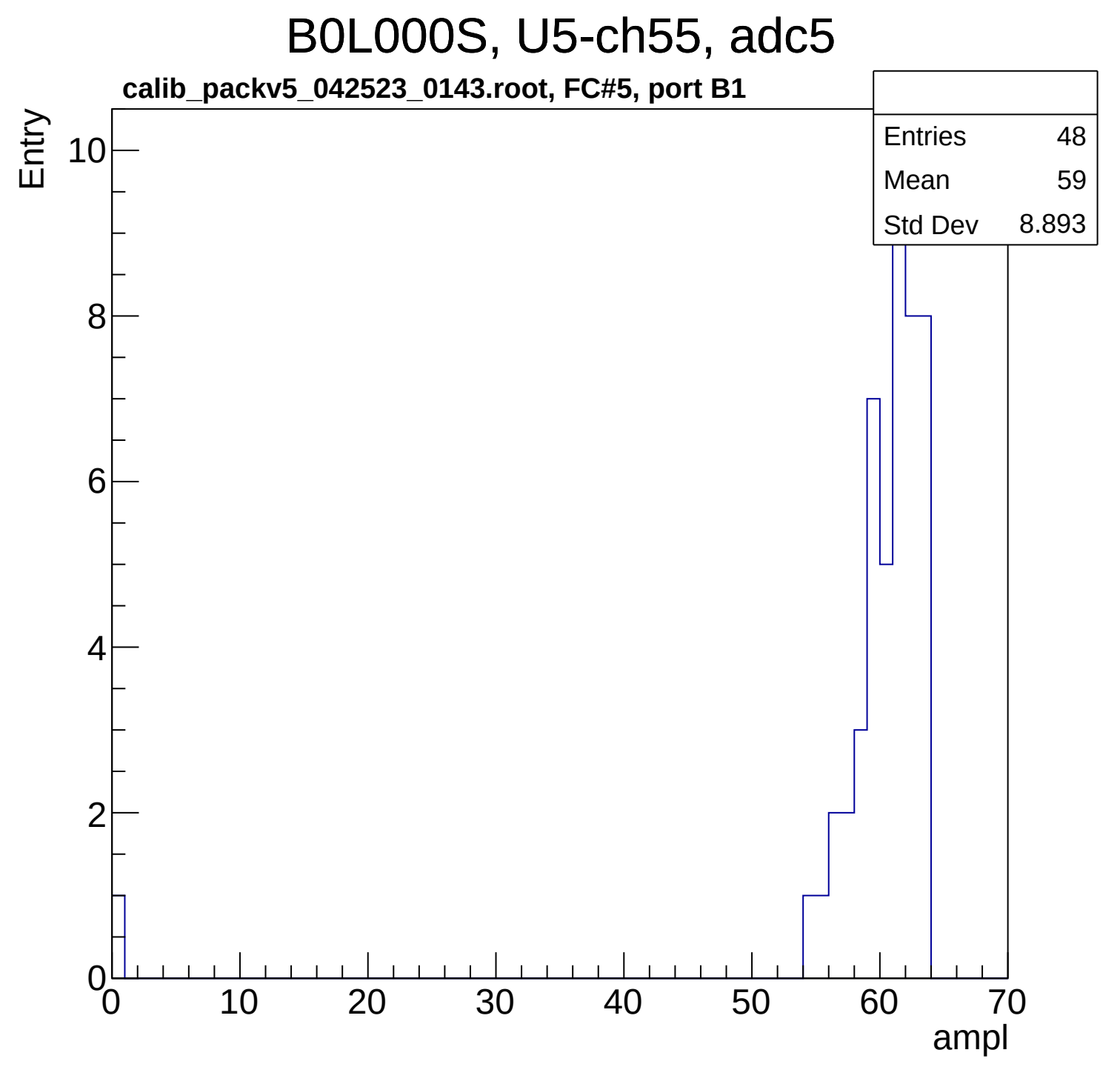
B0L000S, U5-ch55, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59
Std Dev 8.893
Entry
ampl
0
2
4
6
8
10
0
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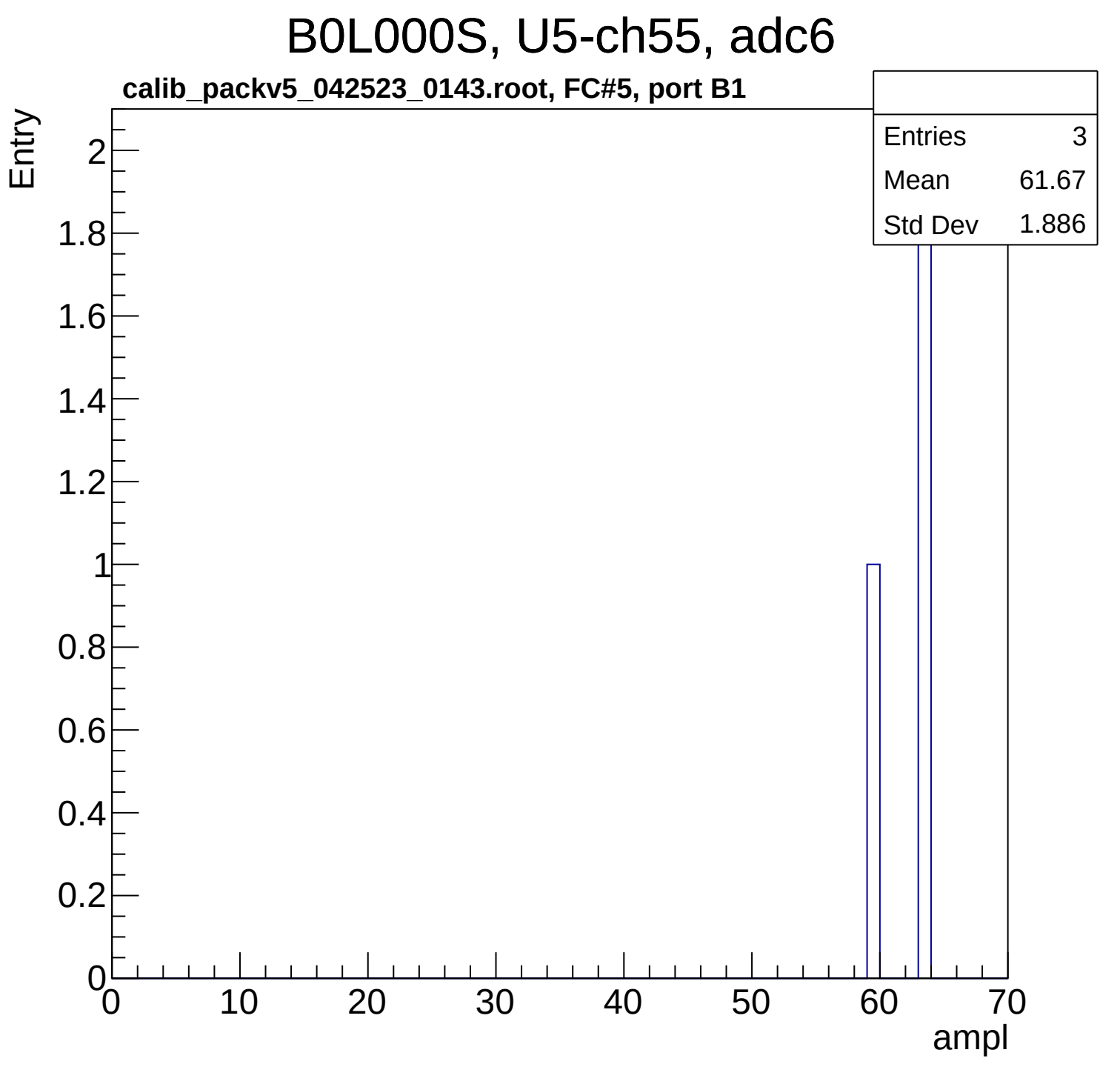
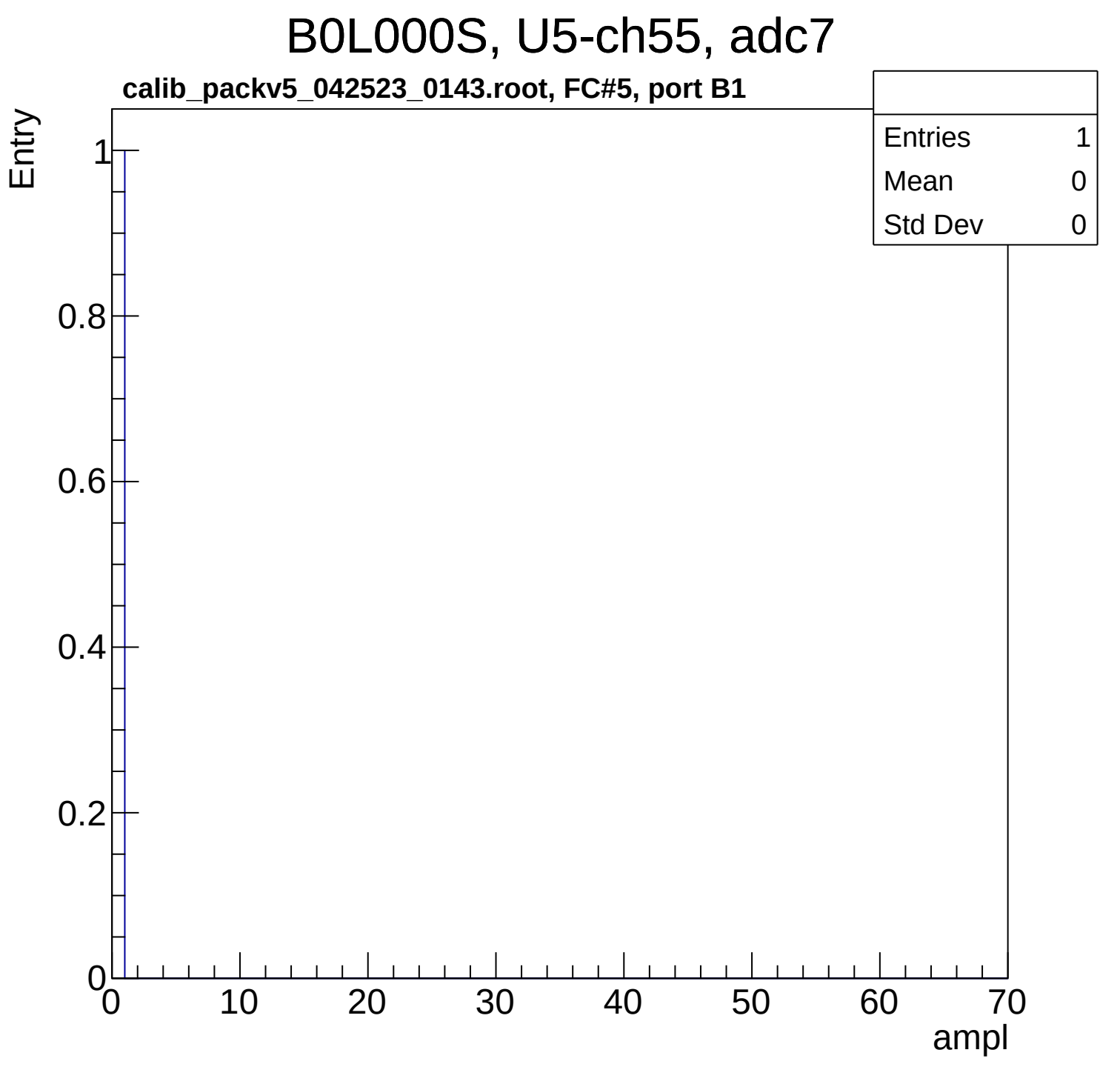
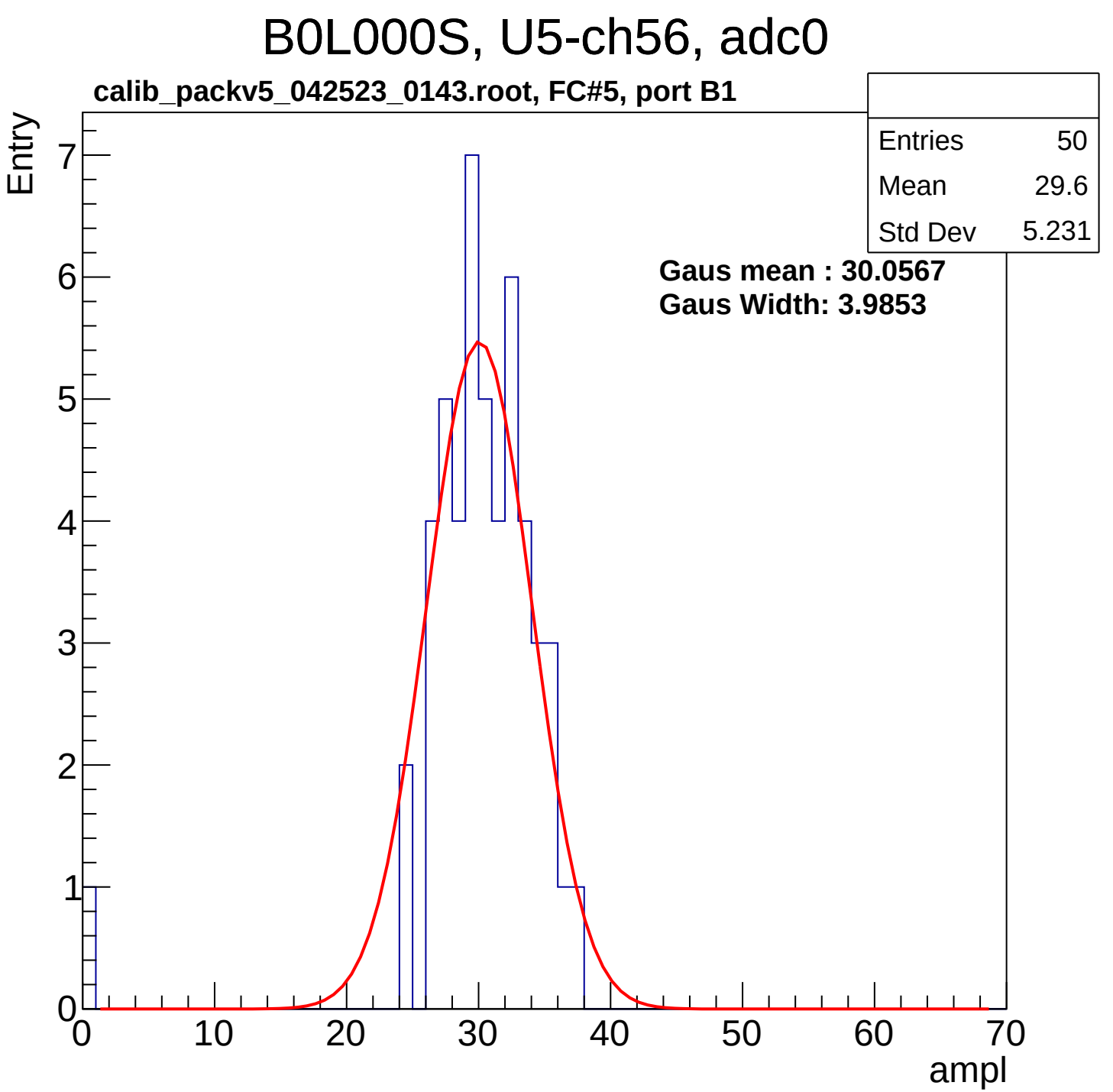
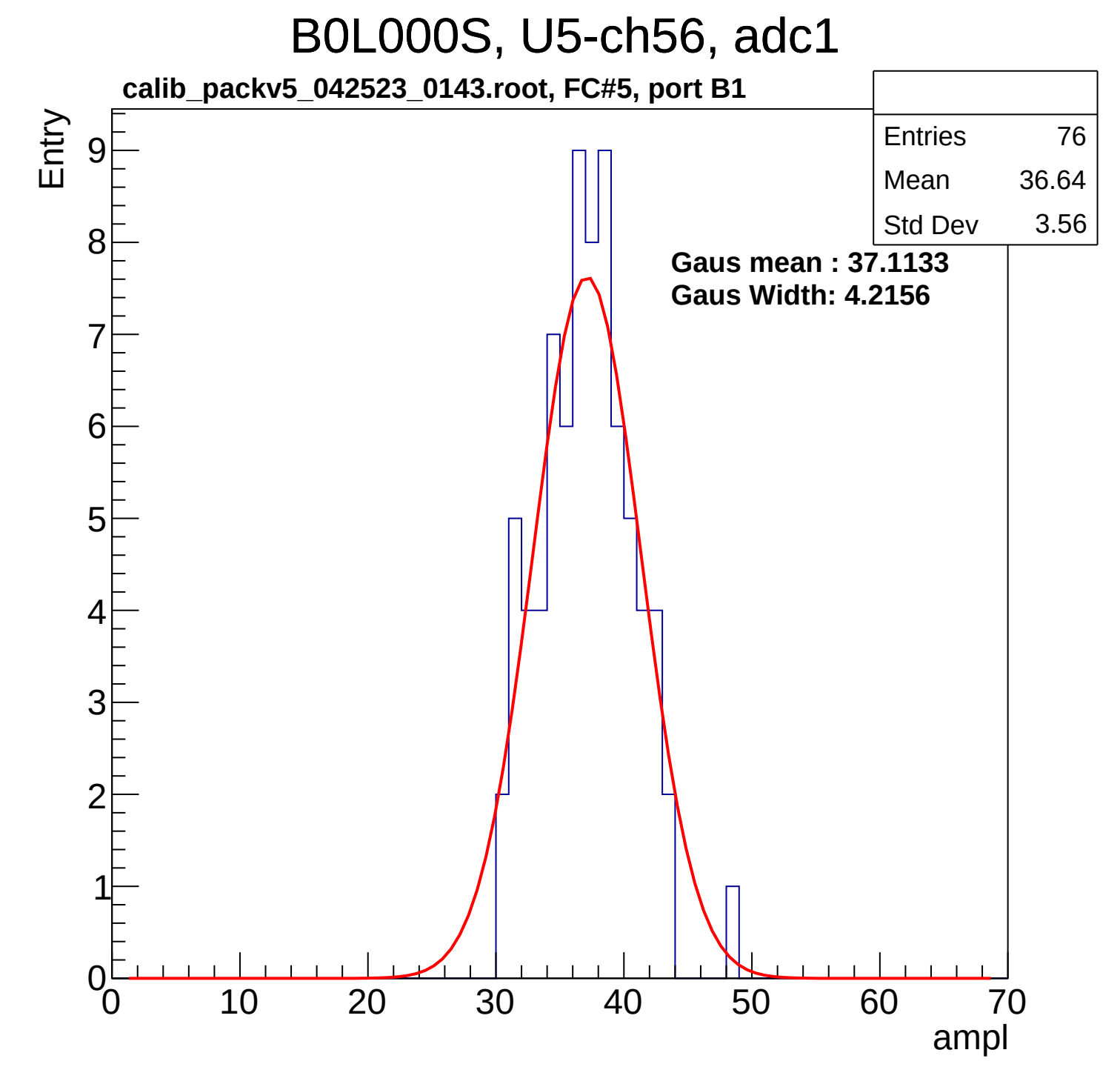
20
30
40
50
60
70


B0L000S, U5-ch55, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch55, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch56, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 29.6
Std Dev 5.231
Gaus mean : 30.0567
Gaus Width: 3.9853
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch56, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.64
Std Dev 3.56
Gaus mean : 37.1133
Gaus Width: 4.2156
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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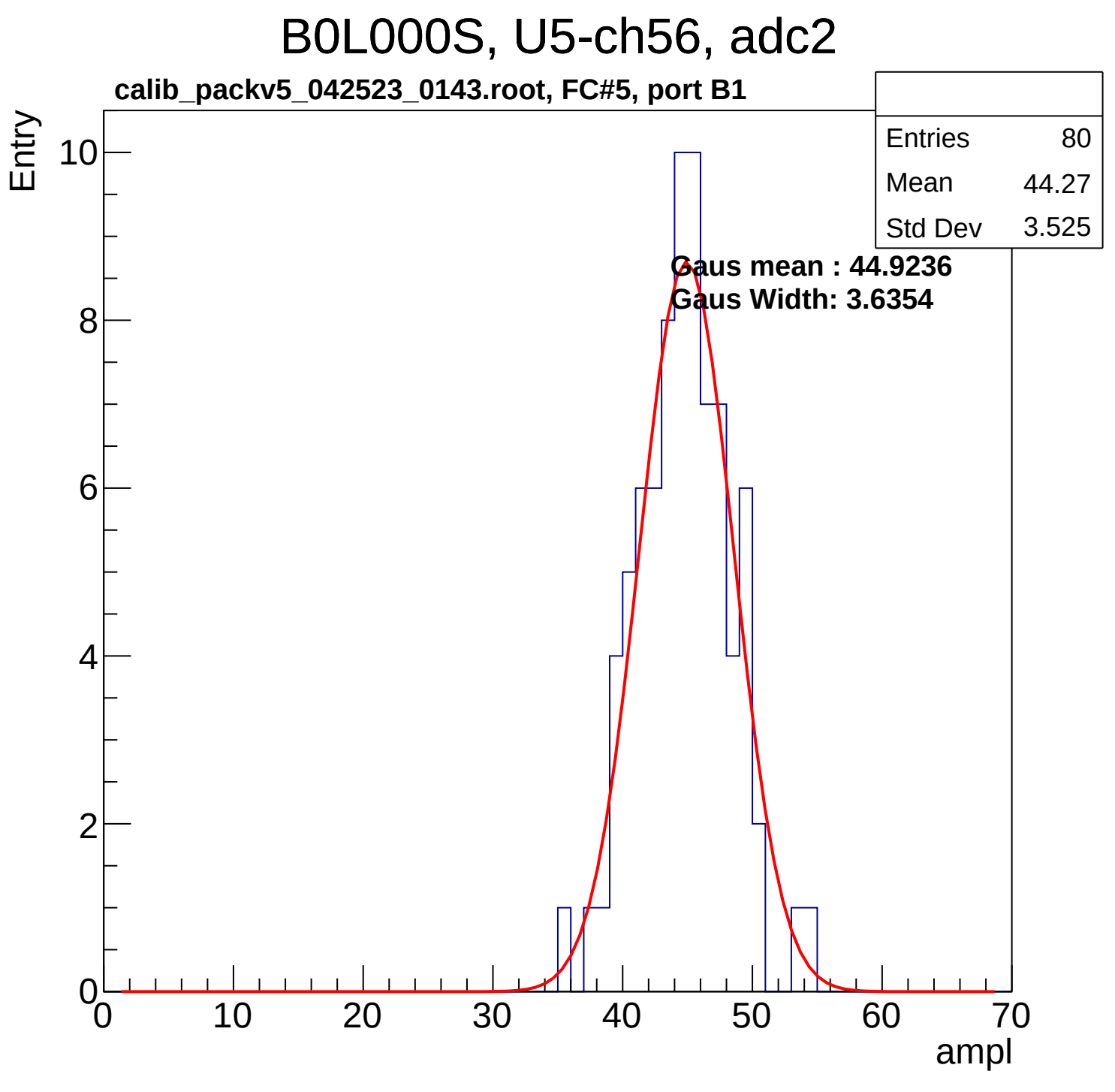
B0L000S, U5-ch56, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 44.27
Std Dev 3.525
Gaus mean : 44.9236
Gaus Width: 3.6354
Entry
ampl
0
2
4
6
8
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50
60
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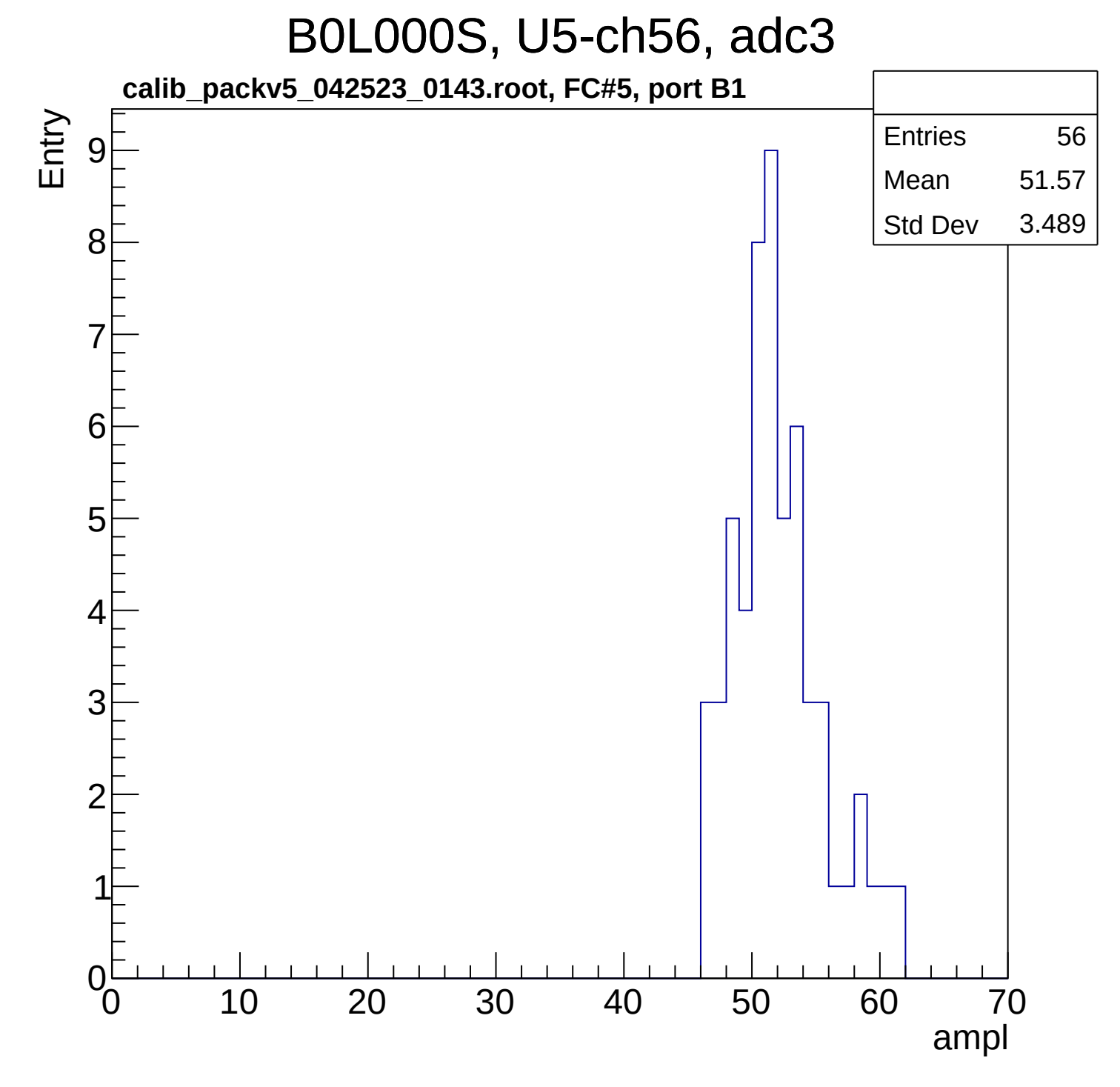
B0L000S, U5-ch56, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 51.57
Std Dev 3.489
Entry
ampl
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4
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6
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60
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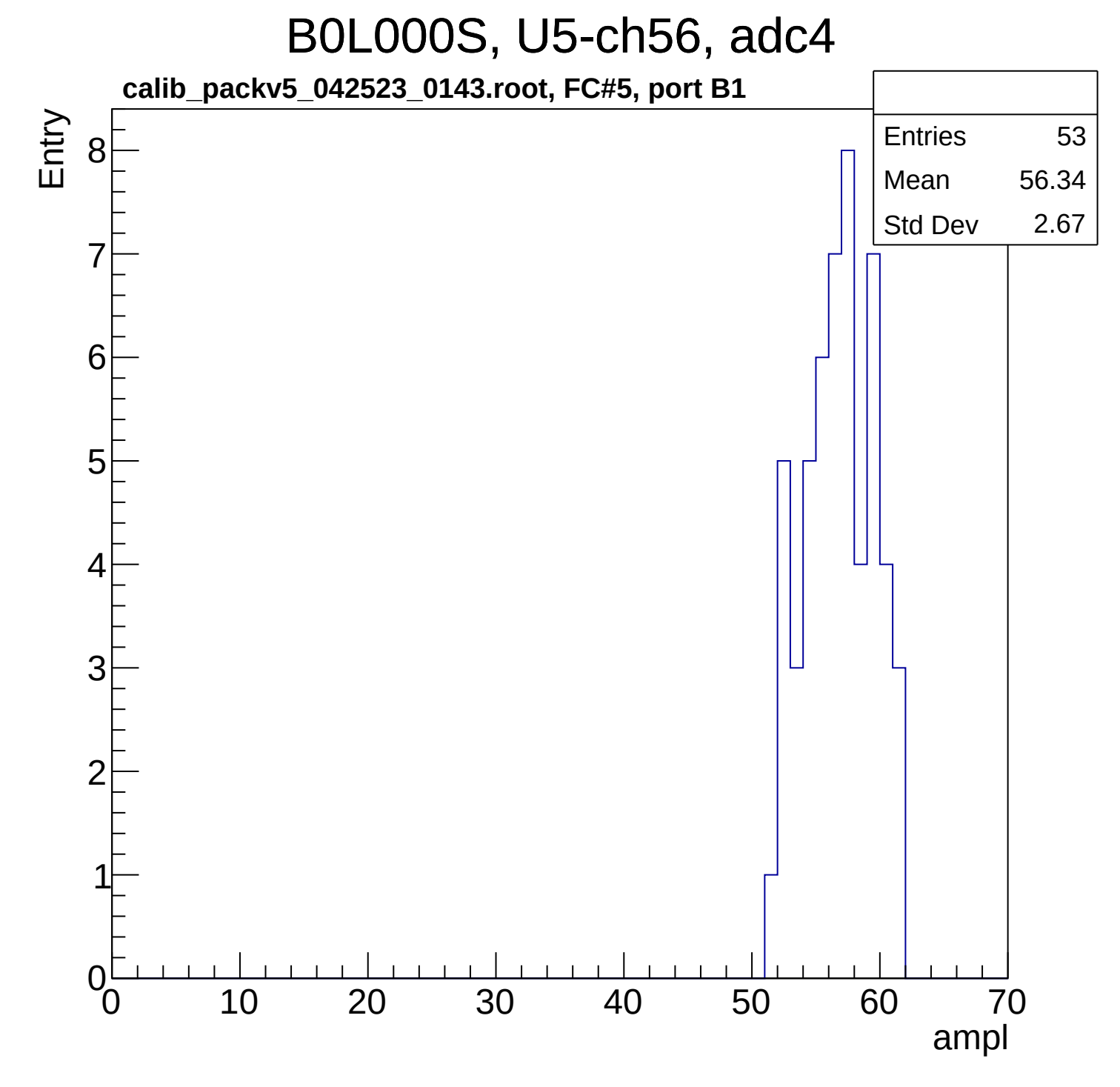
B0L000S, U5-ch56, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.34
Std Dev 2.67
Entry
ampl
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2
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4
5
6
7
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0
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20
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60
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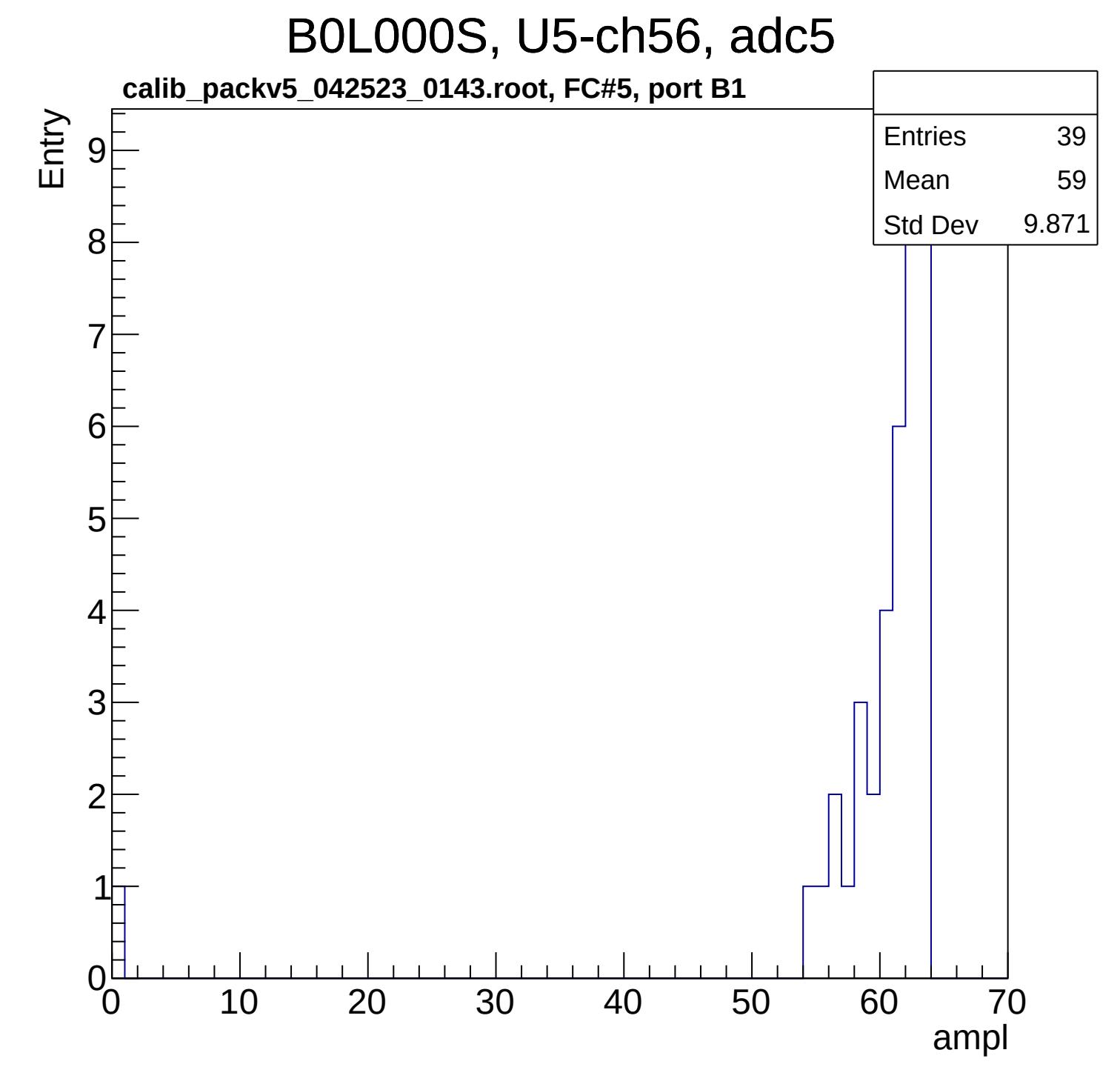
B0L000S, U5-ch56, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 59
Std Dev 9.871
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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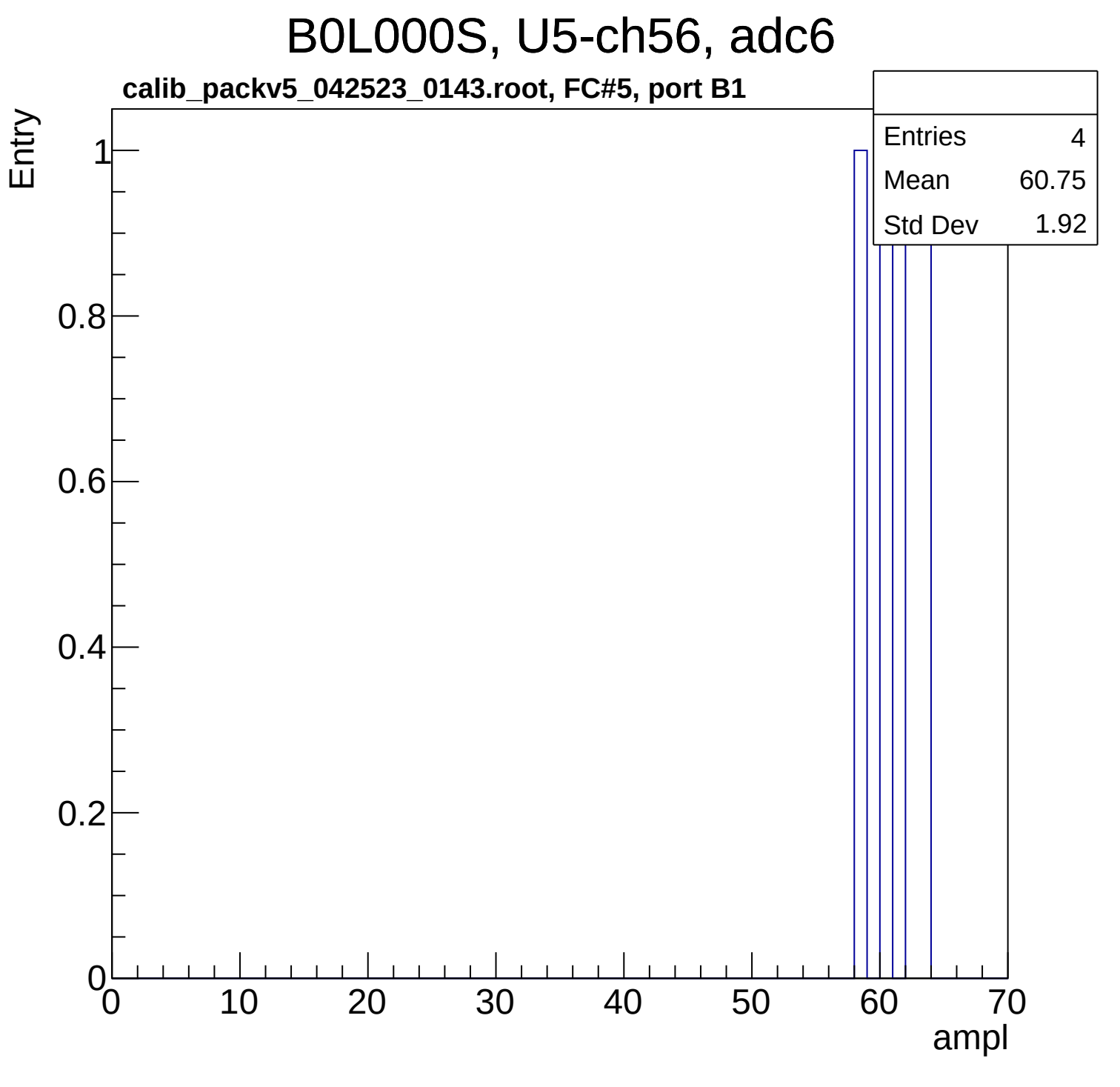
B0L000S, U5-ch56, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.75
Std Dev 1.92
Entry
ampl
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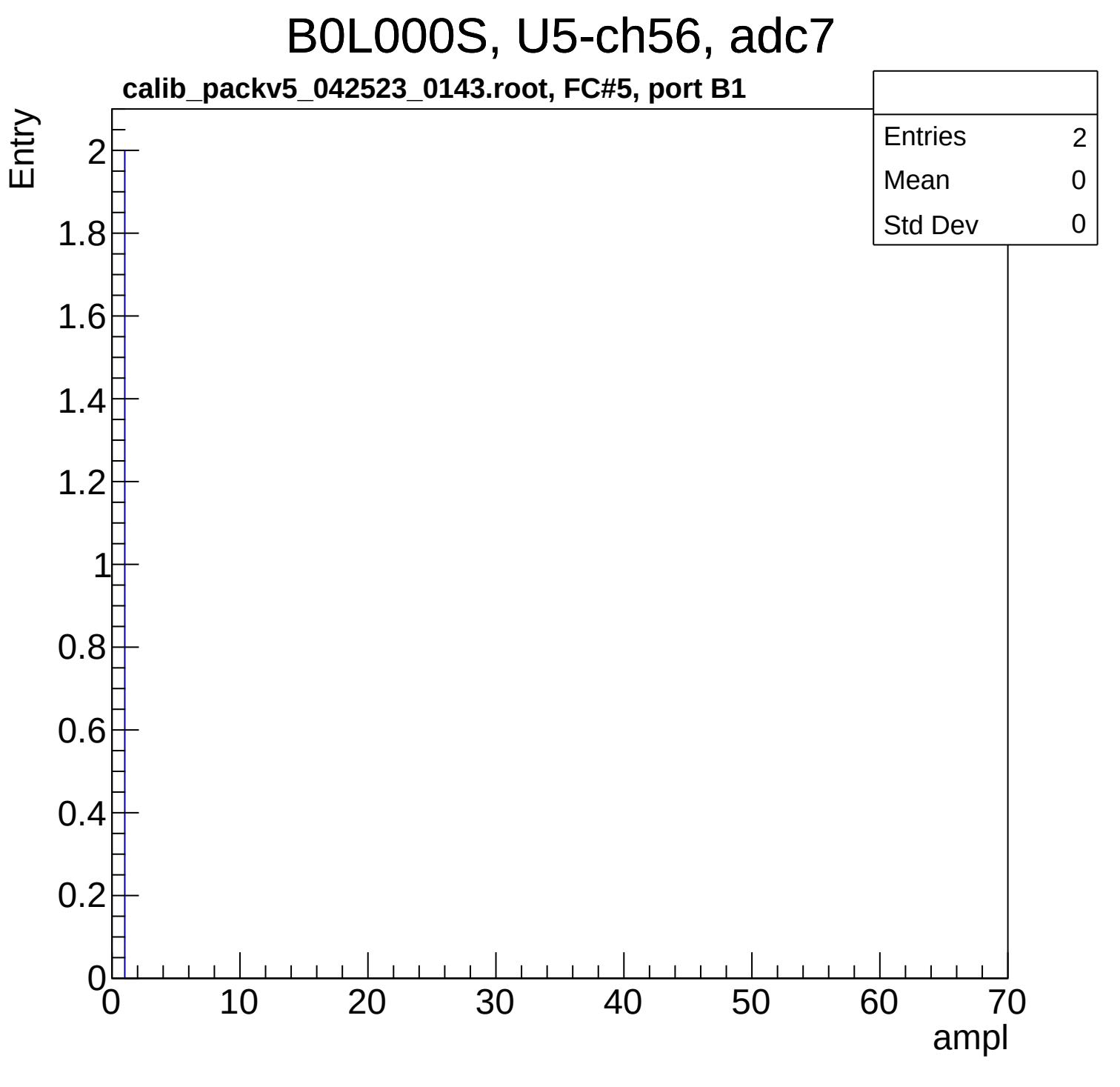
40
50
60
70


B0L000S, U5-ch56, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
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0.6
0.4
0.2
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10
20
30
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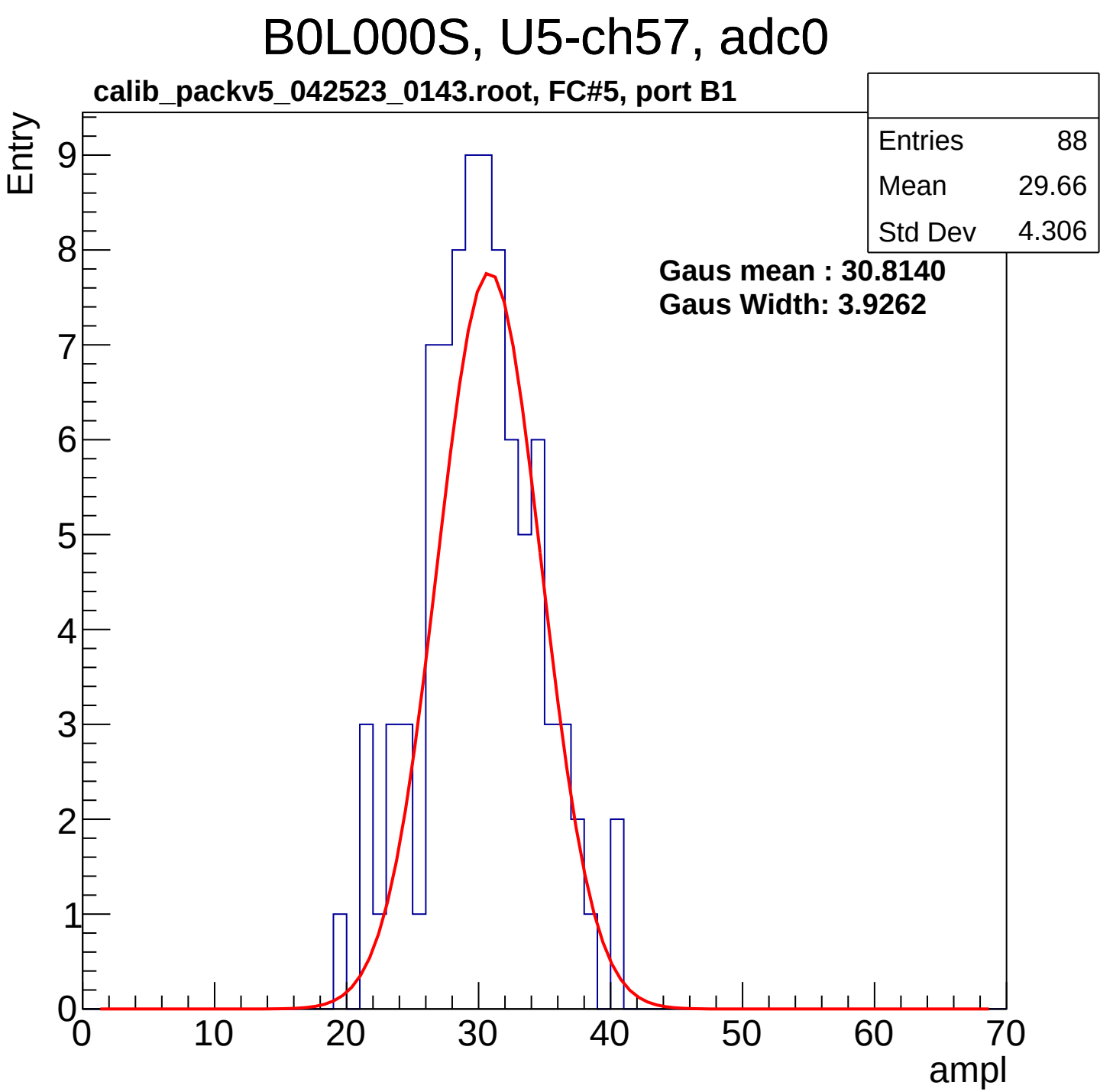
50
60
70
ampl


B0L000S, U5-ch57, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 88
Mean 29.66
Std Dev 4.306
Gaus mean : 30.8140
Gaus Width: 3.9262
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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50
60
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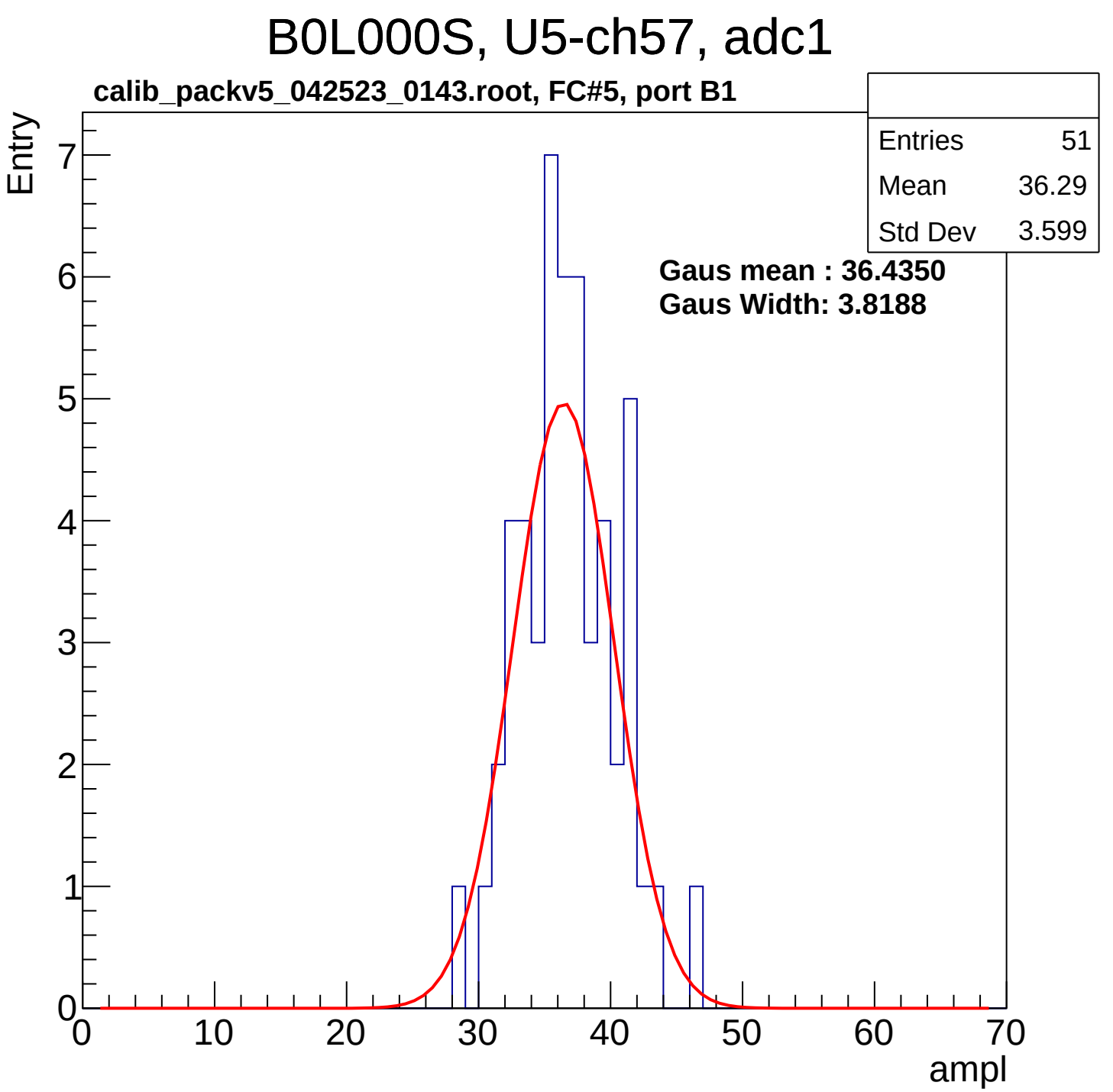
B0L000S, U5-ch57, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 36.29
Std Dev 3.599
Gaus mean : 36.4350
Gaus Width: 3.8188
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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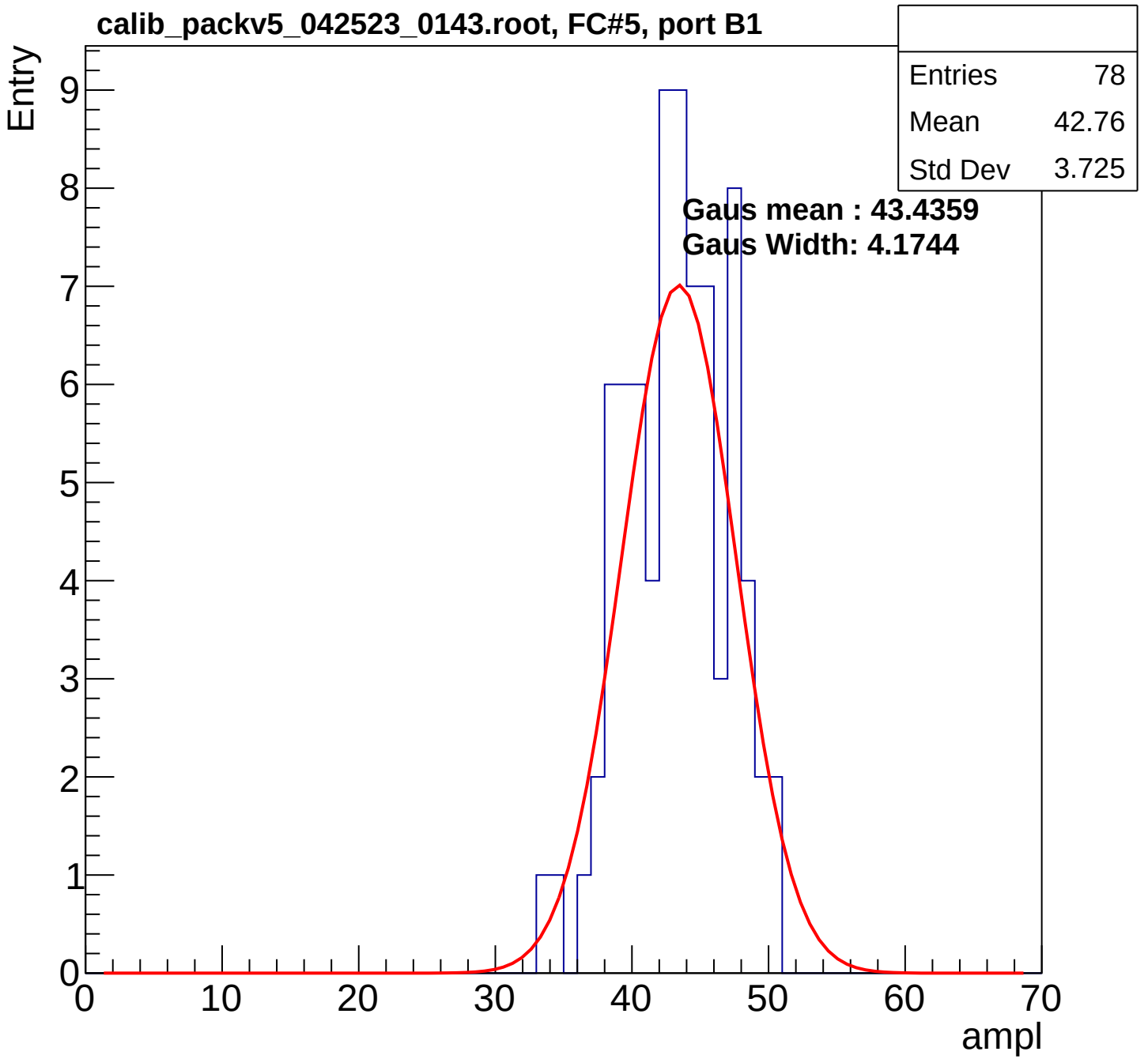
60
70


B0L000S, U5-ch57, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42.76
Std Dev 3.725
Gaus mean : 43.4359
Gaus Width: 4.1744
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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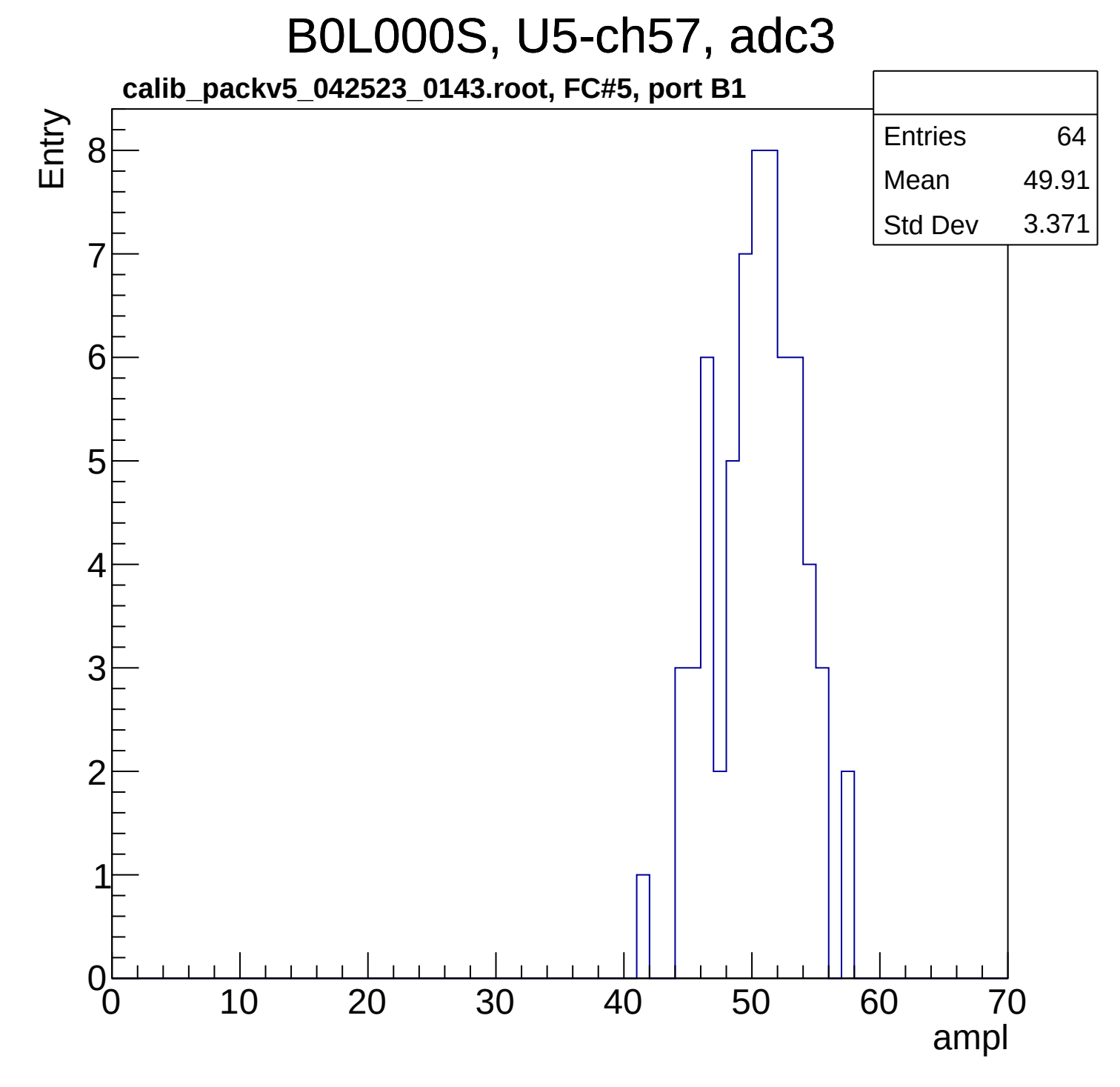
B0L000S, U5-ch57, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.91
Std Dev 3.371
Entry
ampl
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4
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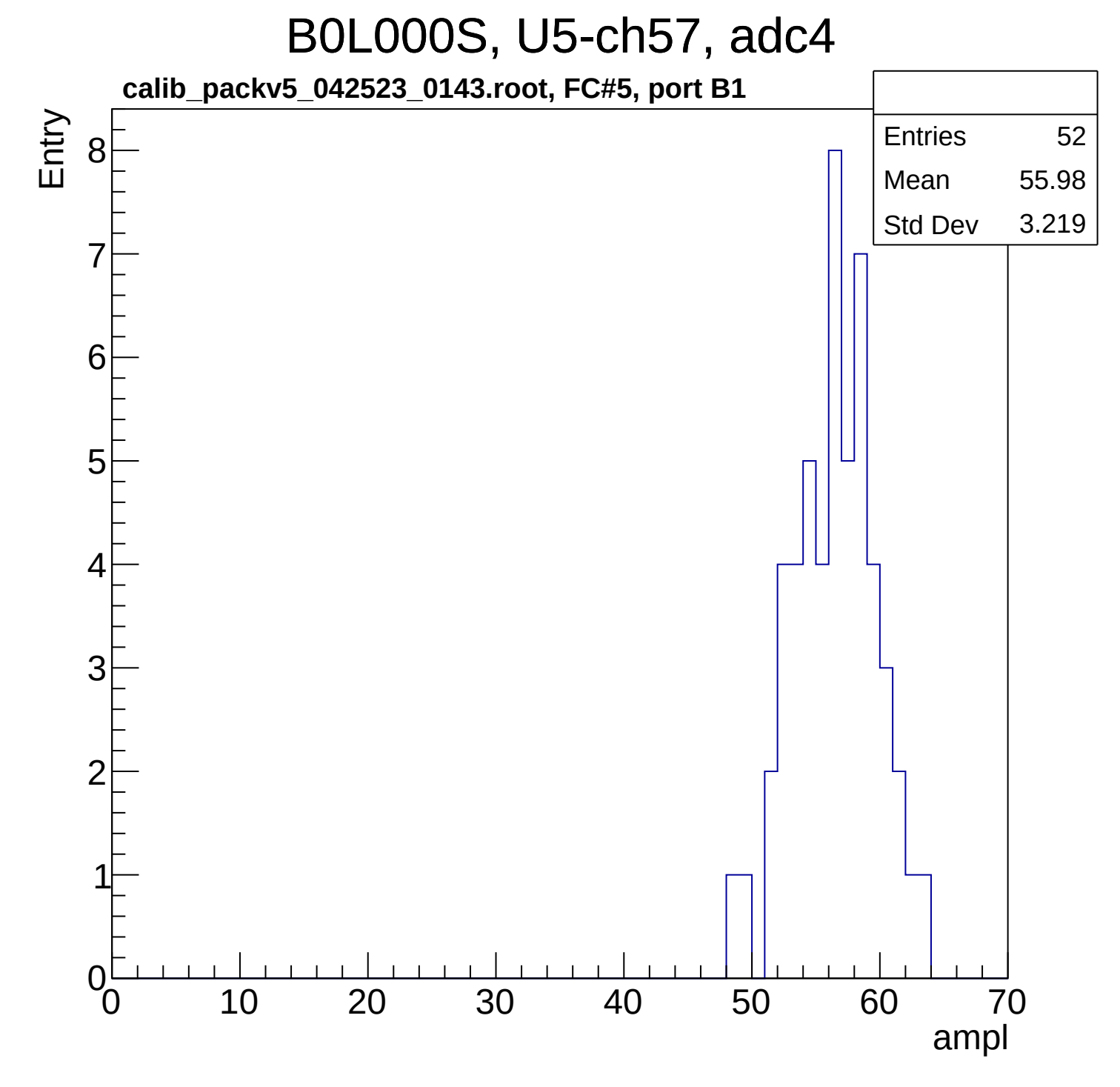
B0L000S, U5-ch57, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.98
Std Dev 3.219
Entry
ampl
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2
3
4
5
6
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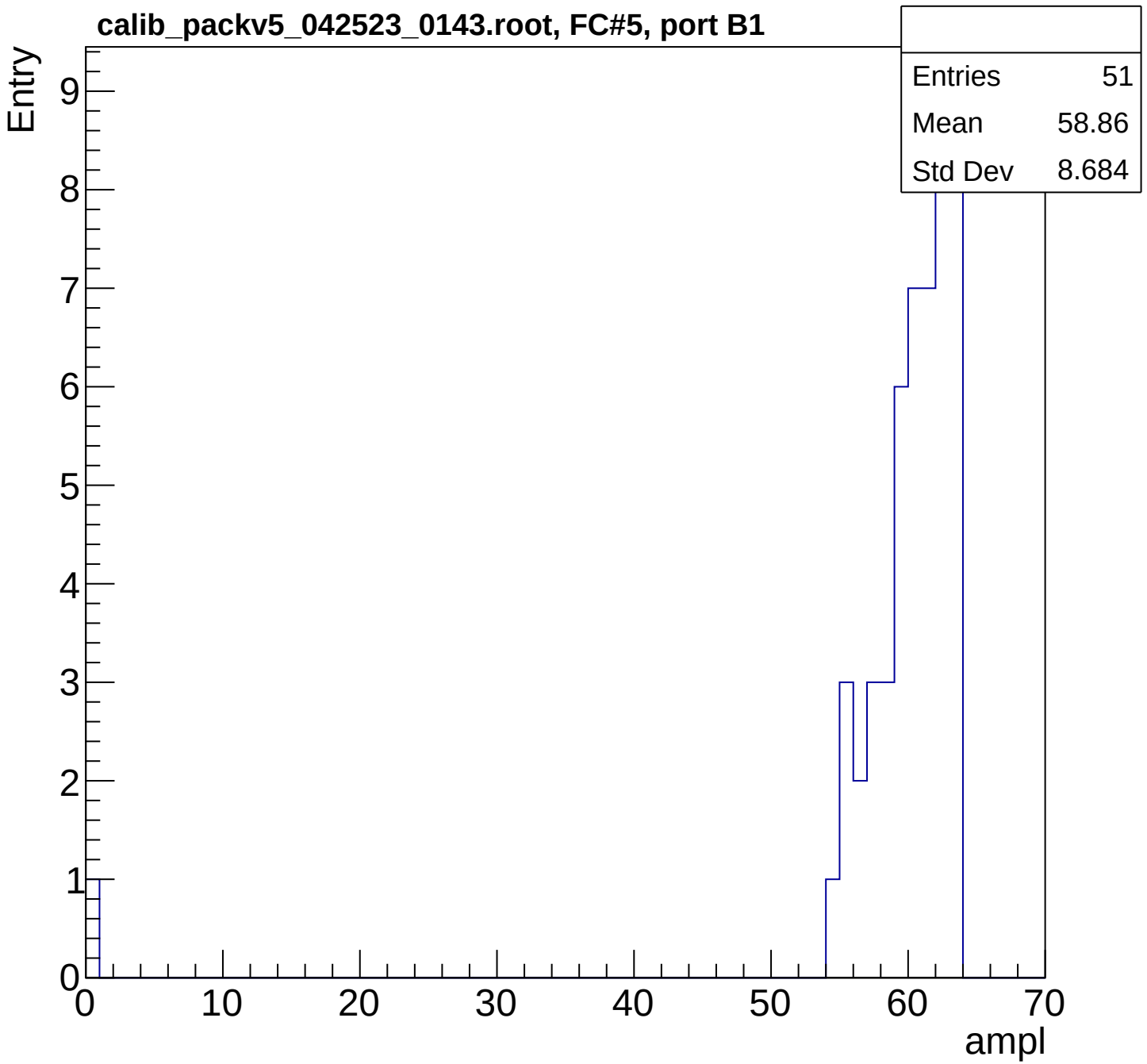
B0L000S, U5-ch57, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 58.86
Std Dev 8.684
Entry
ampl
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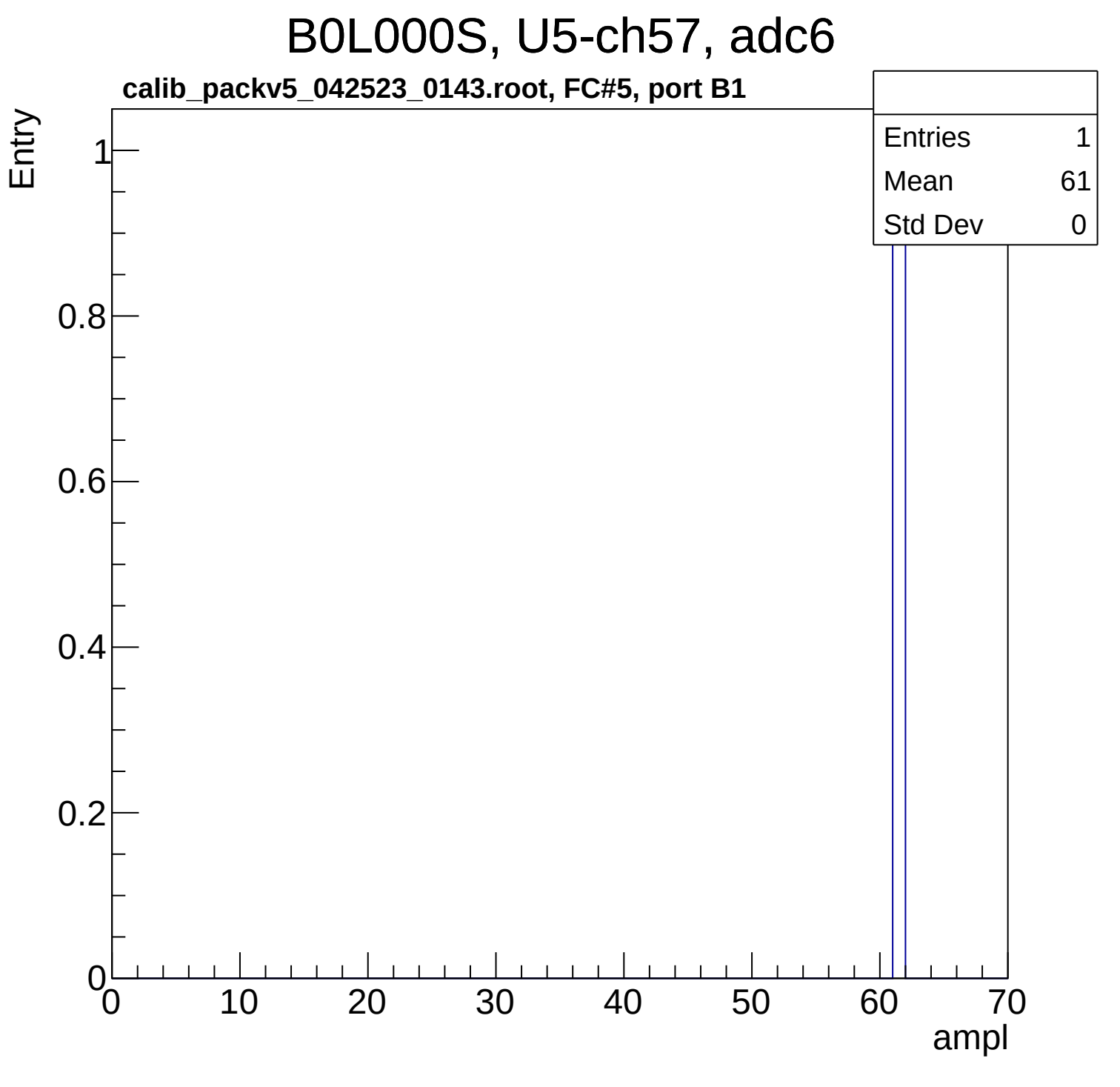
B0L000S, U5-ch57, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
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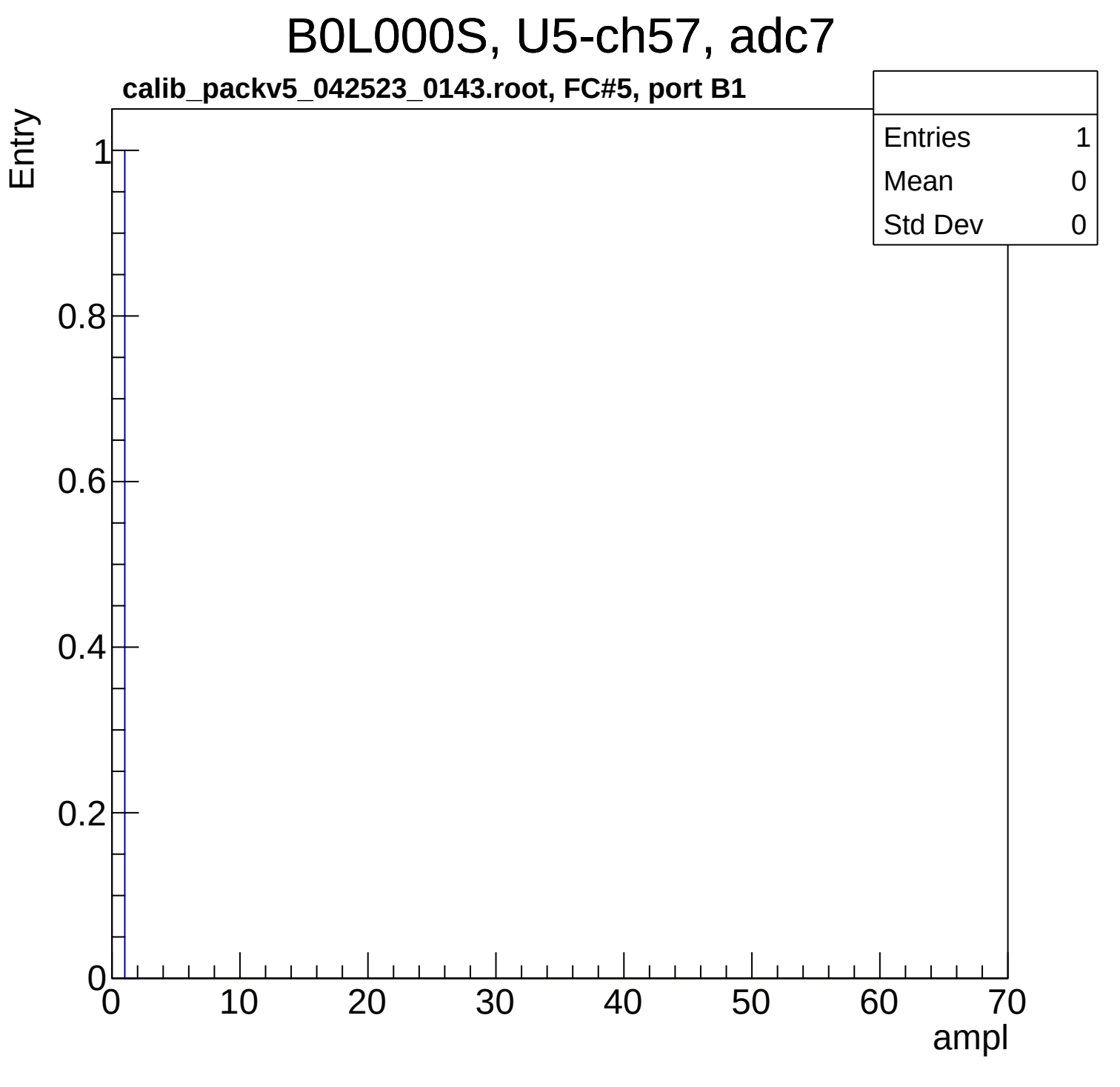
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch57, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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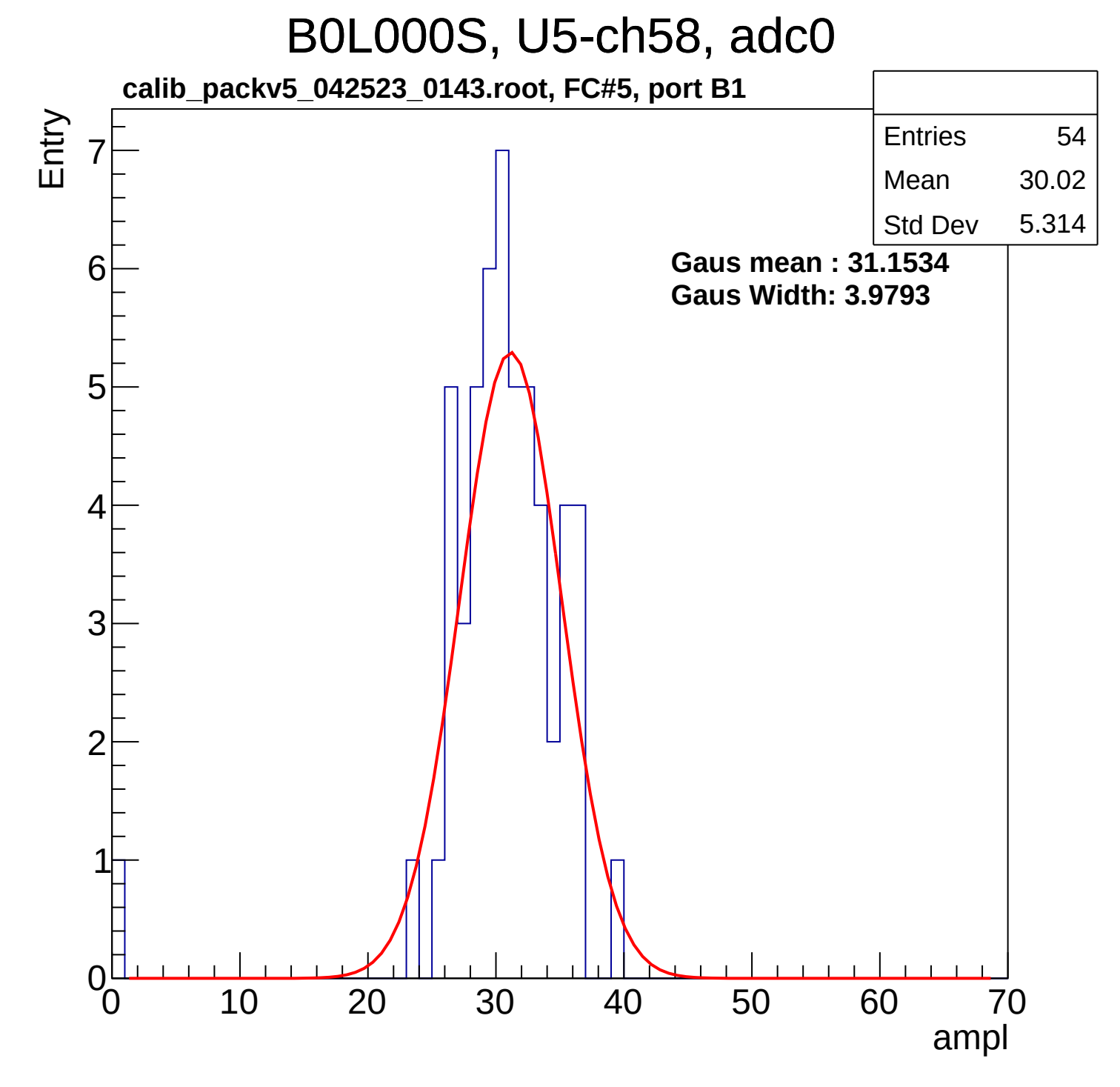
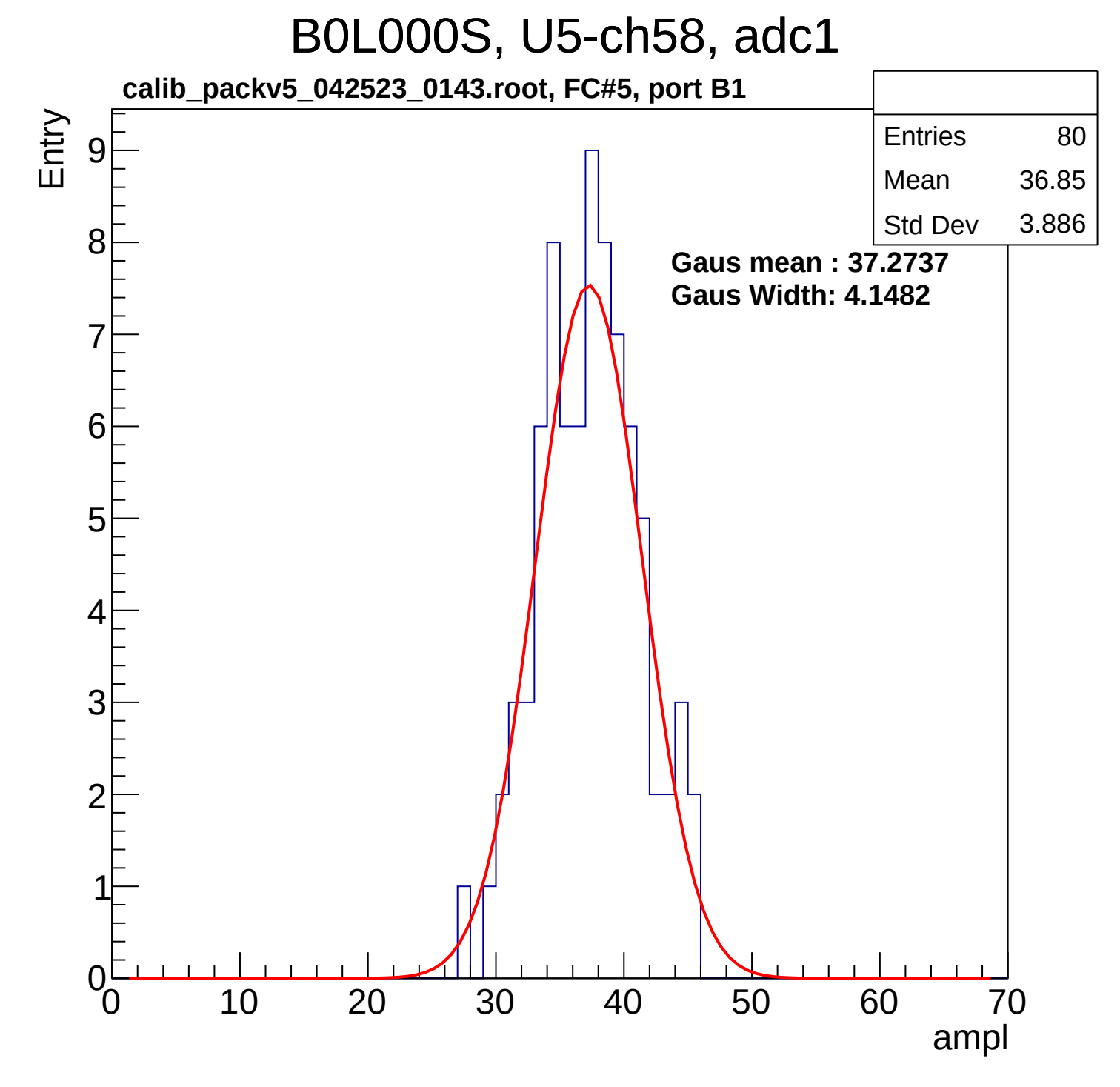
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch58, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 30.02
Std Dev 5.314
Gaus mean : 31.1534
Gaus Width: 3.9793
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch58, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 36.85
Std Dev 3.886
Gaus mean : 37.2737
Gaus Width: 4.1482
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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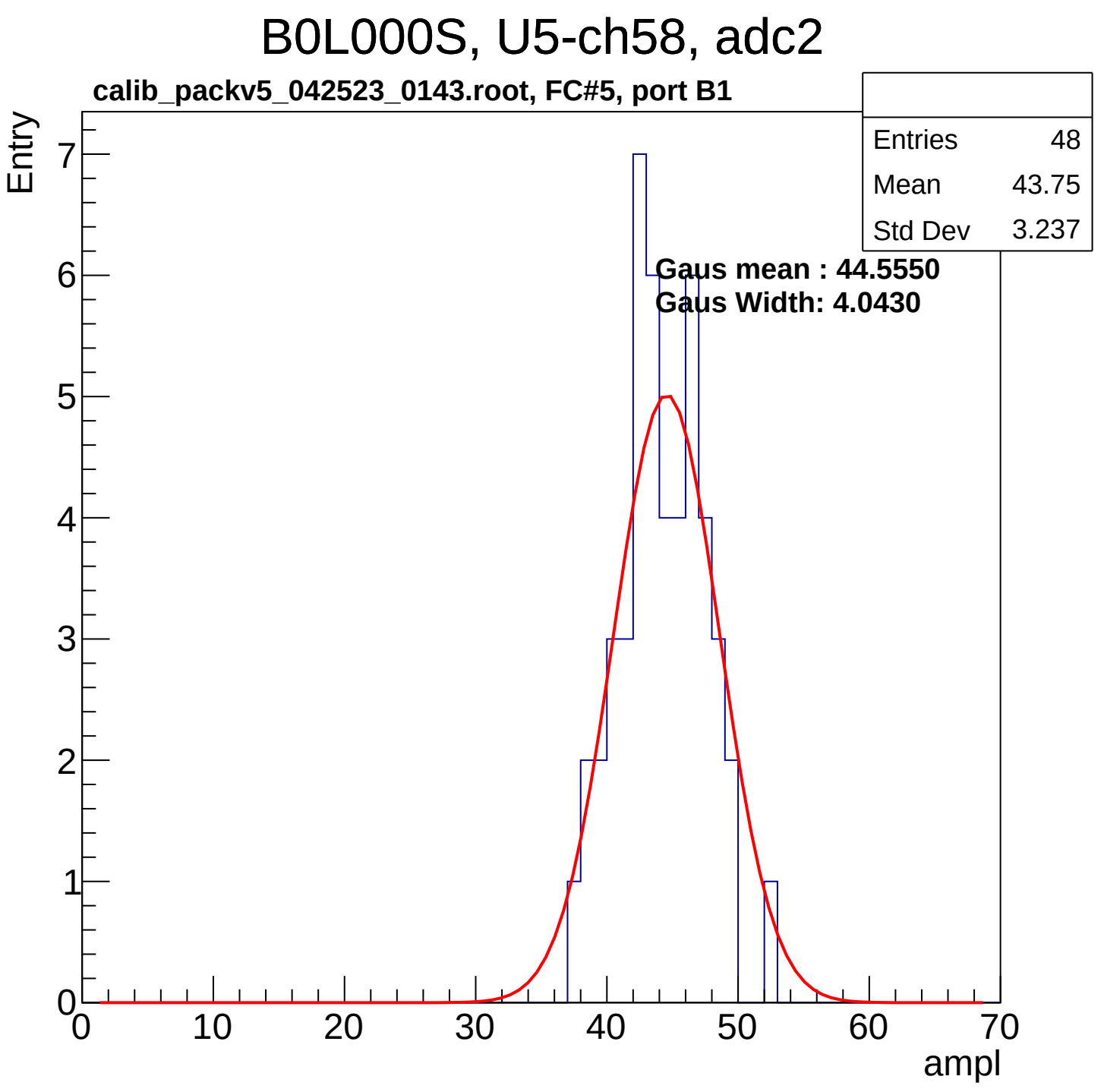
B0L000S, U5-ch58, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 43.75
Std Dev 3.237
Gaus mean : 44.5550
Gaus Width: 4.0430
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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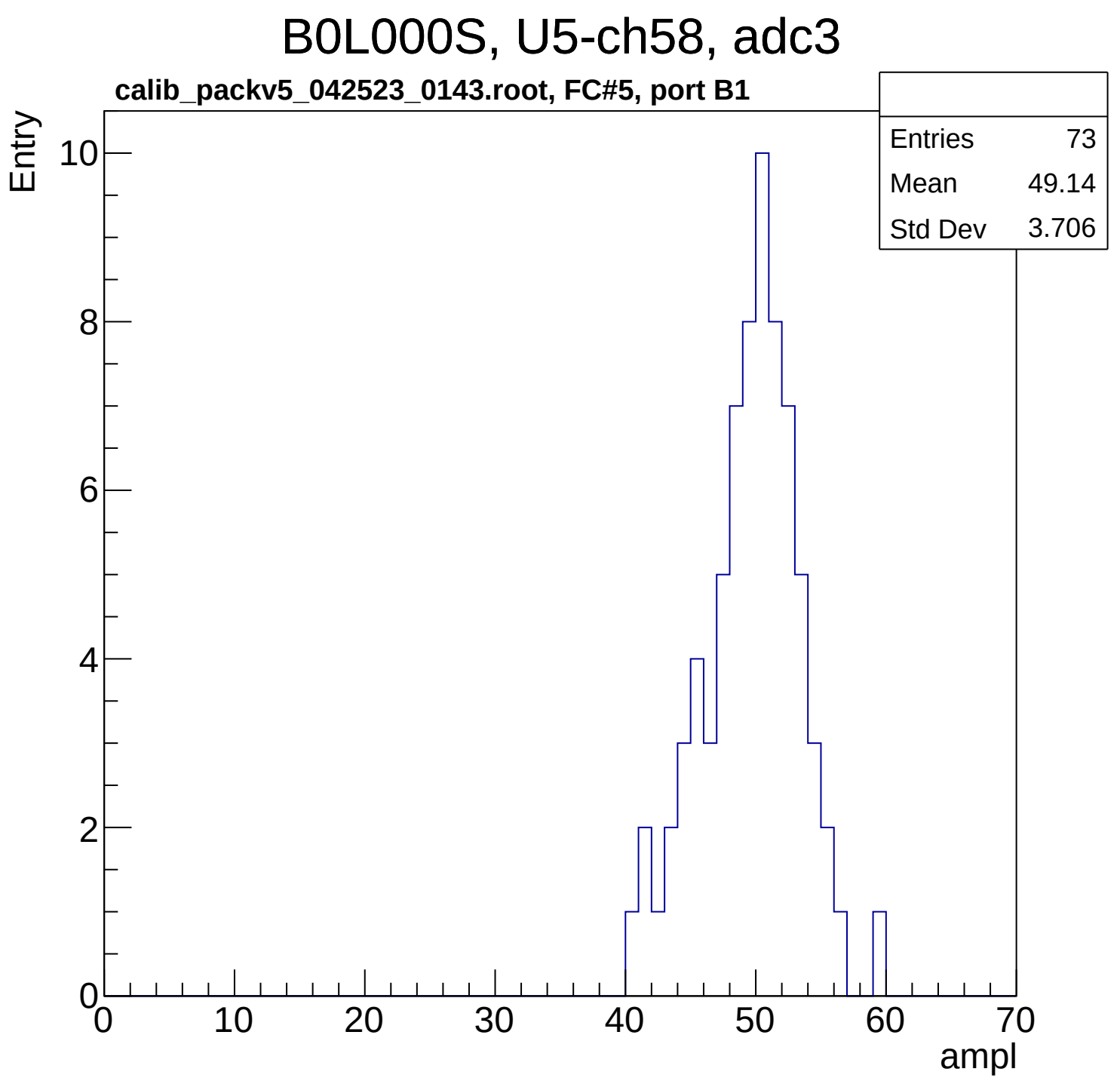
B0L000S, U5-ch58, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.14
Std Dev 3.706
Entry
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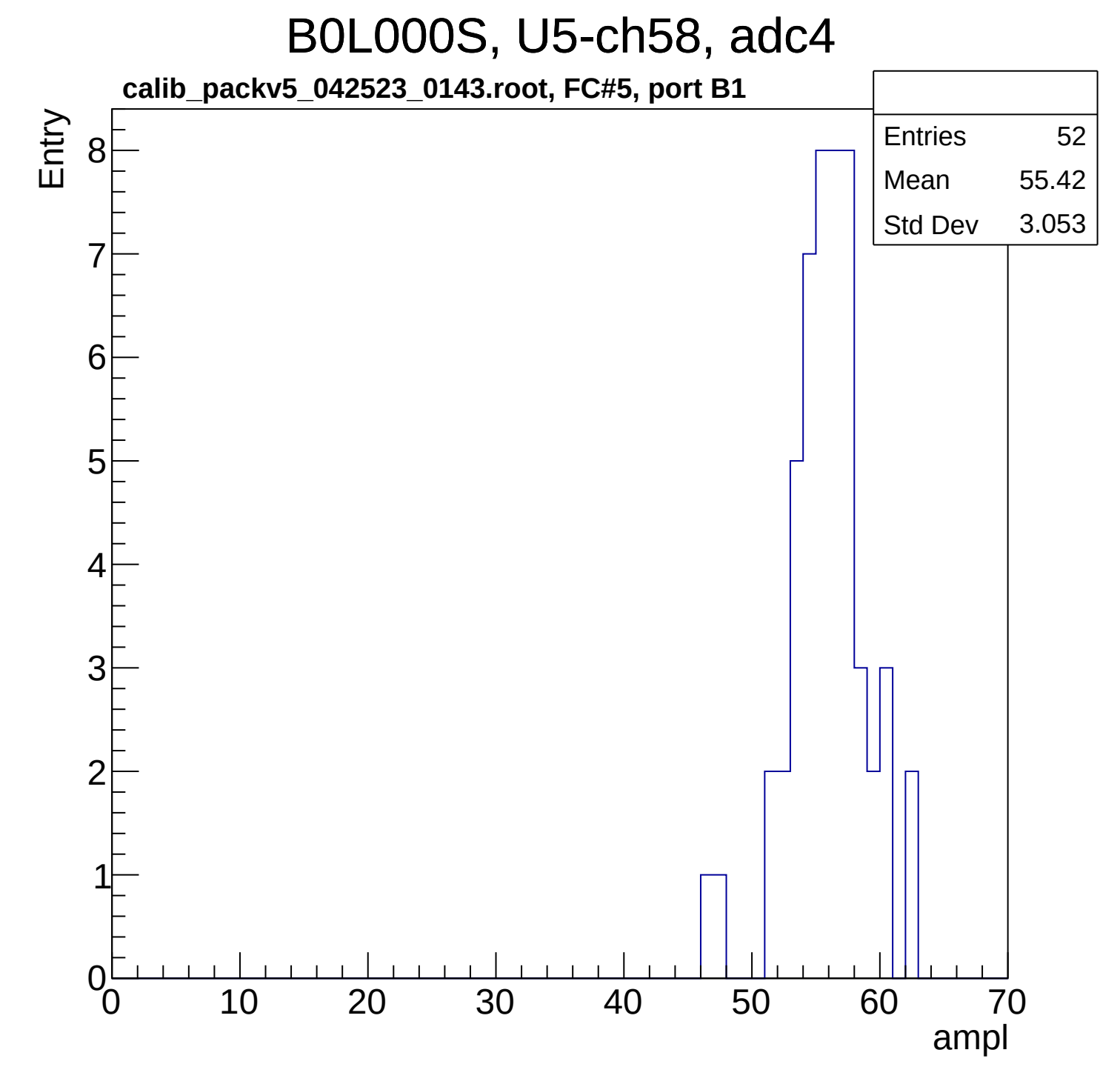
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch58, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.42
Std Dev 3.053
Entry
ampl
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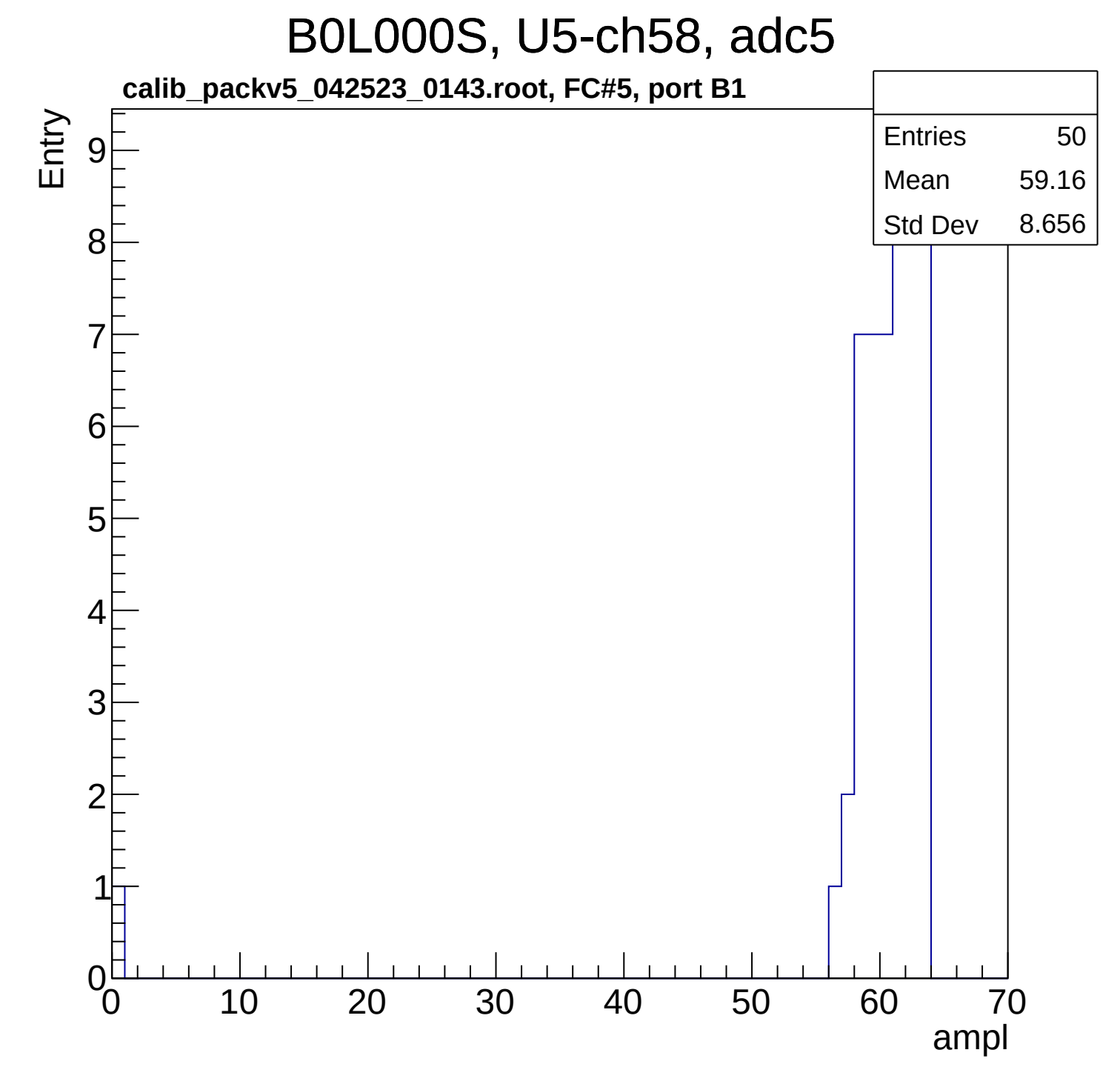
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch58, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 59.16
Std Dev 8.656
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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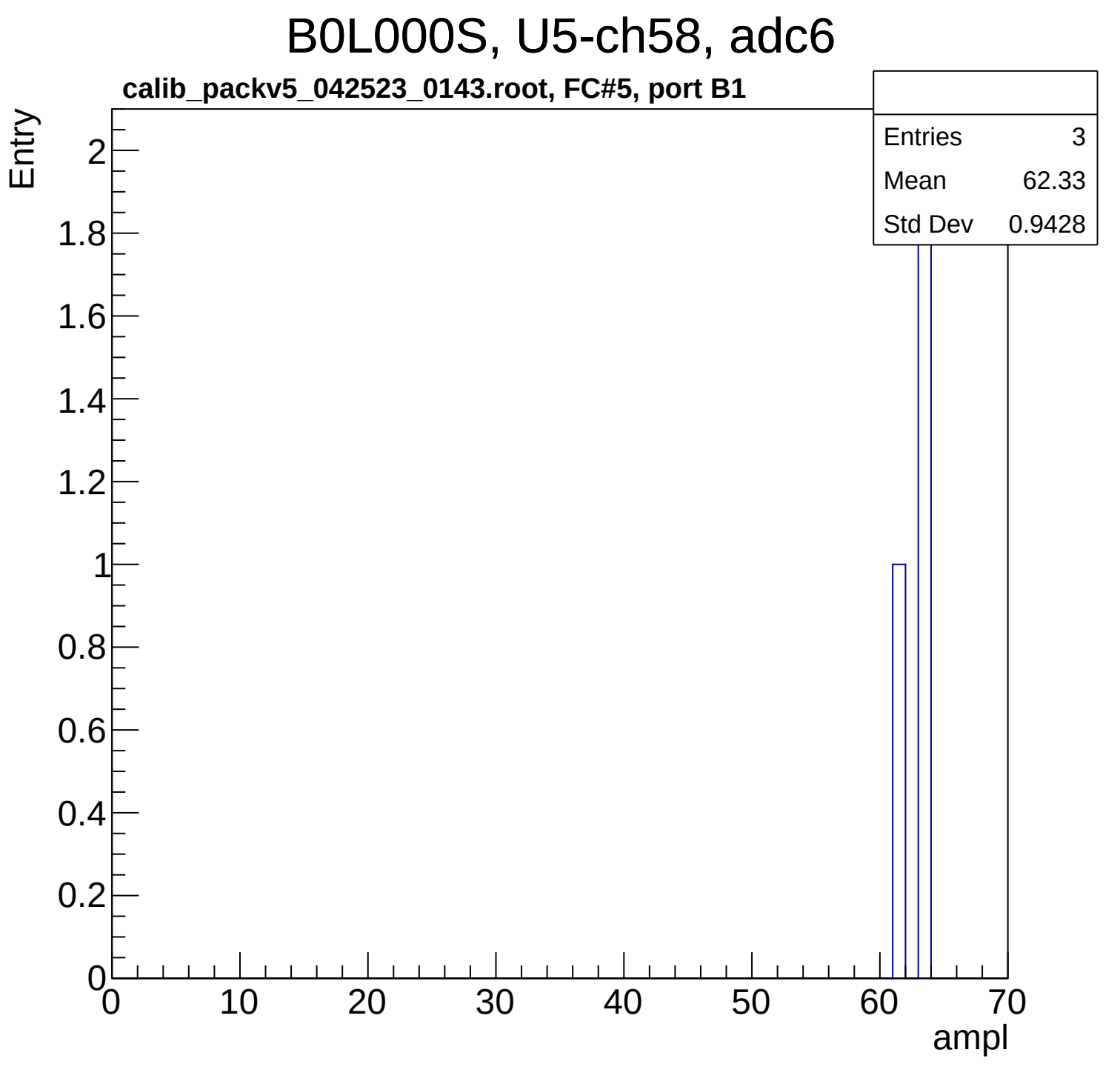
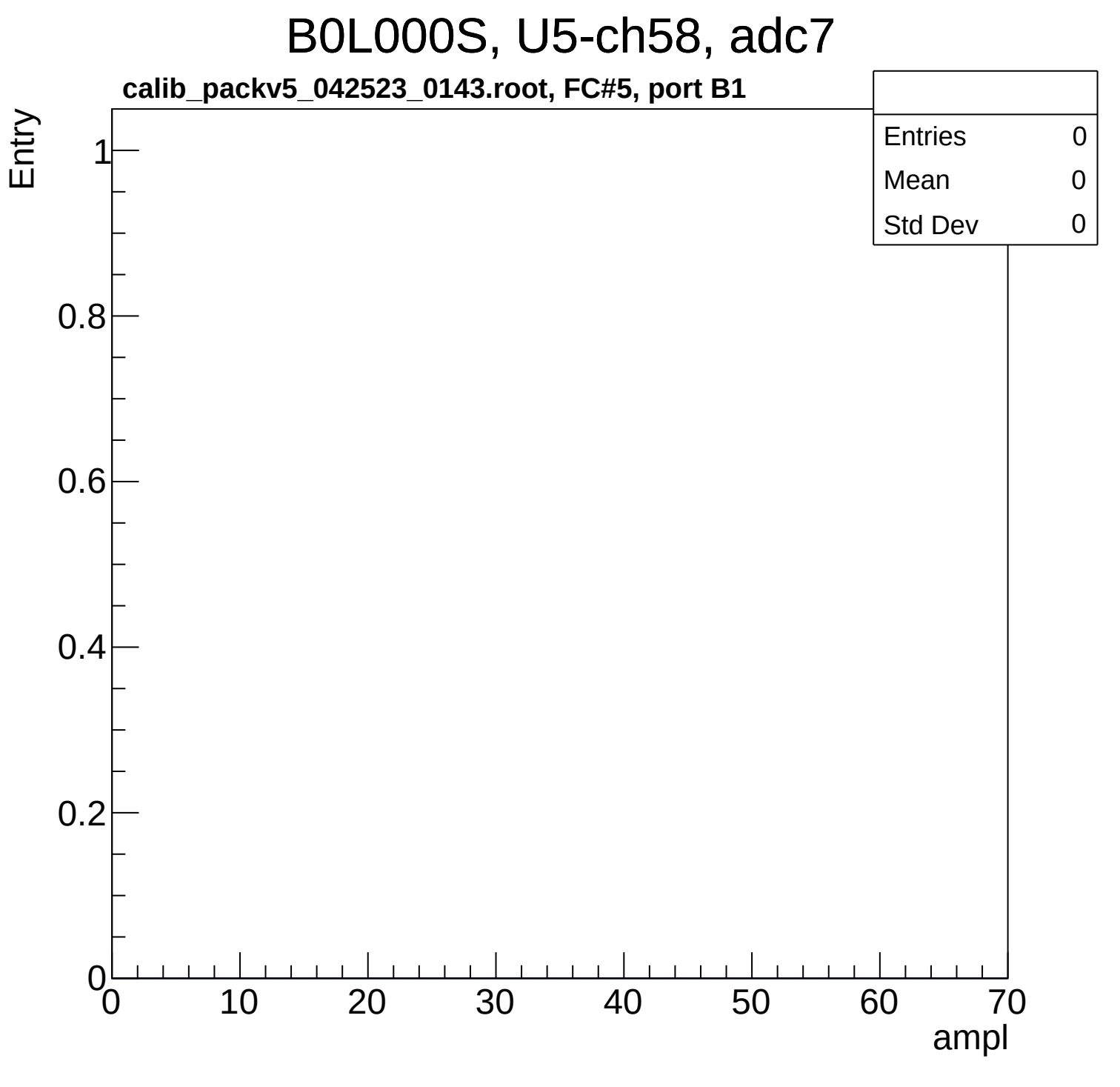
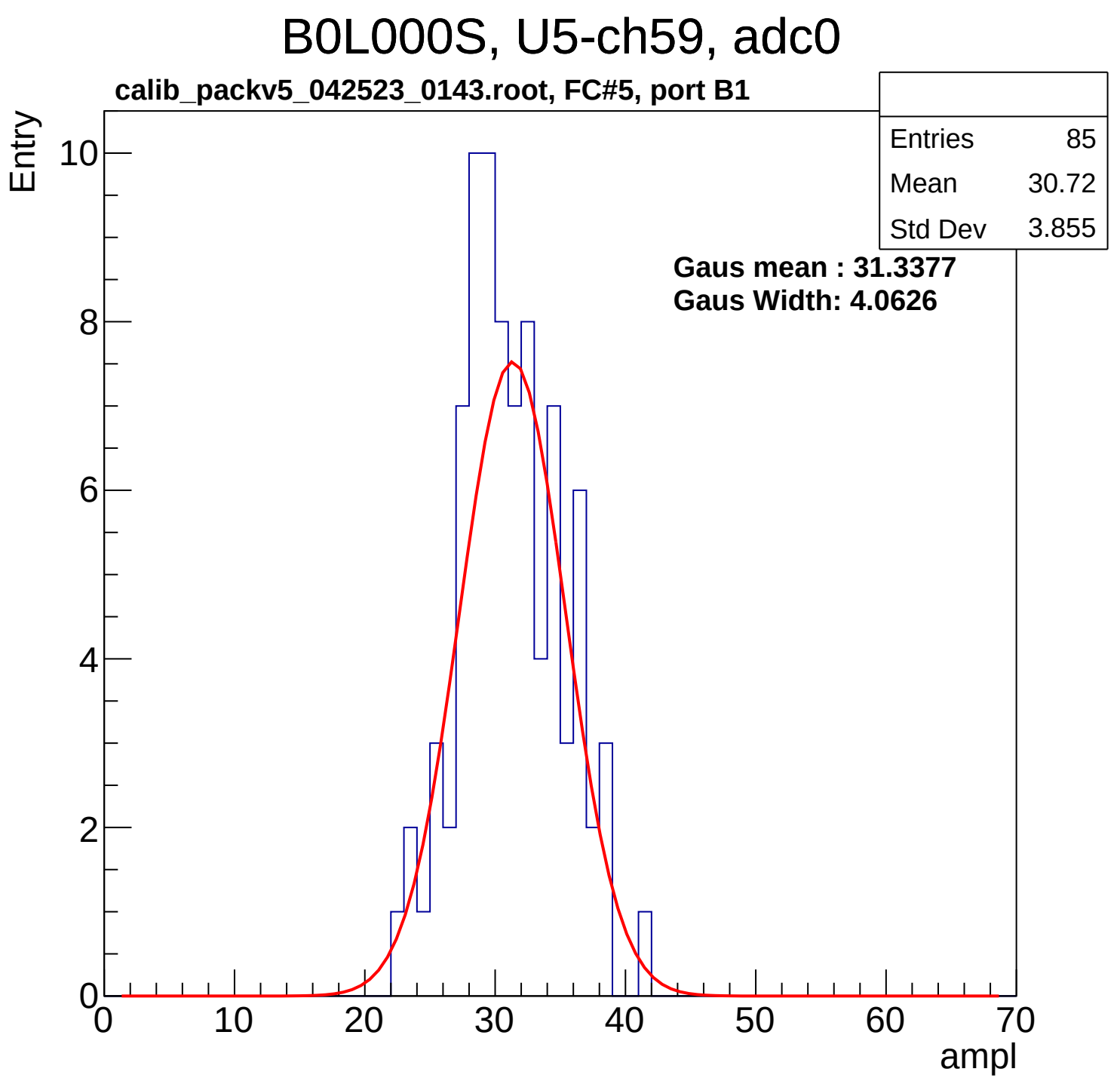
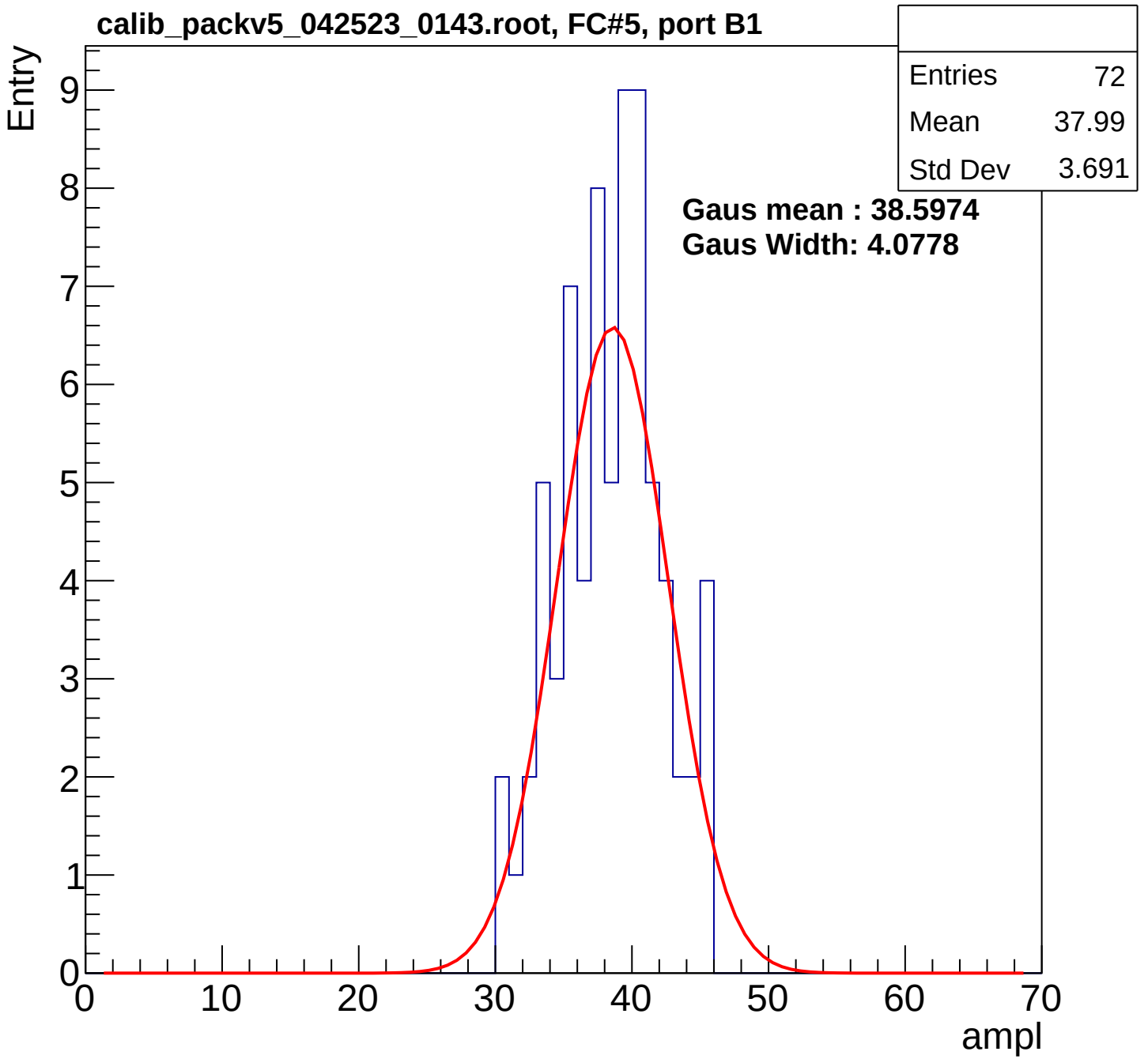
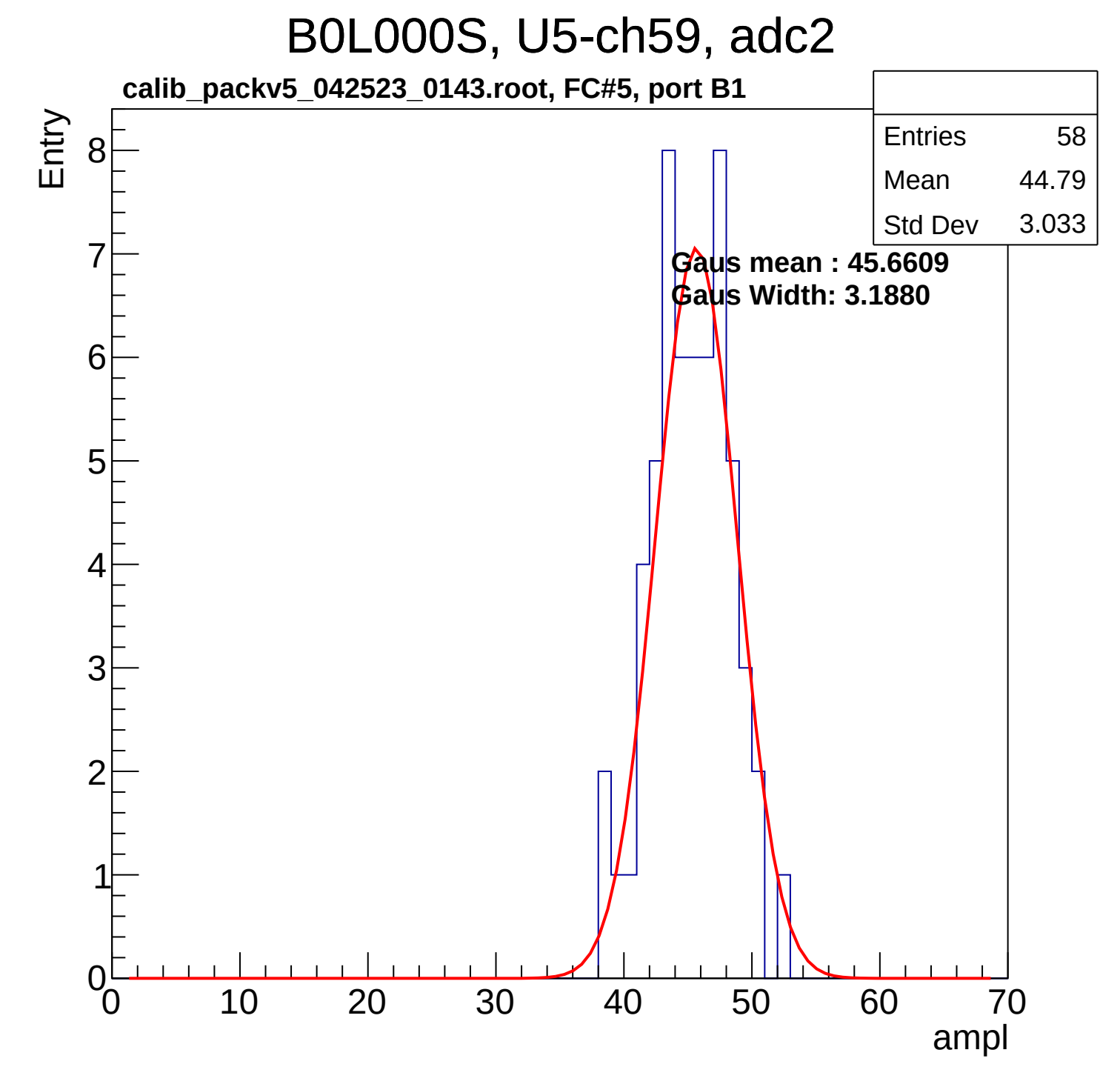

B0L000S, U5-ch58, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch58, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch59, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.72
Std Dev 3.855
Gaus mean : 31.3377
Gaus Width: 4.0626
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch59, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.99
Std Dev 3.691
Gaus mean : 38.5974
Gaus Width: 4.0778
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U5-ch59, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 44.79
Std Dev 3.033
Gaus mean : 45.6609
Gaus Width: 3.1880
Entry
ampl
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1
2
3
4
5
6
7
8
0
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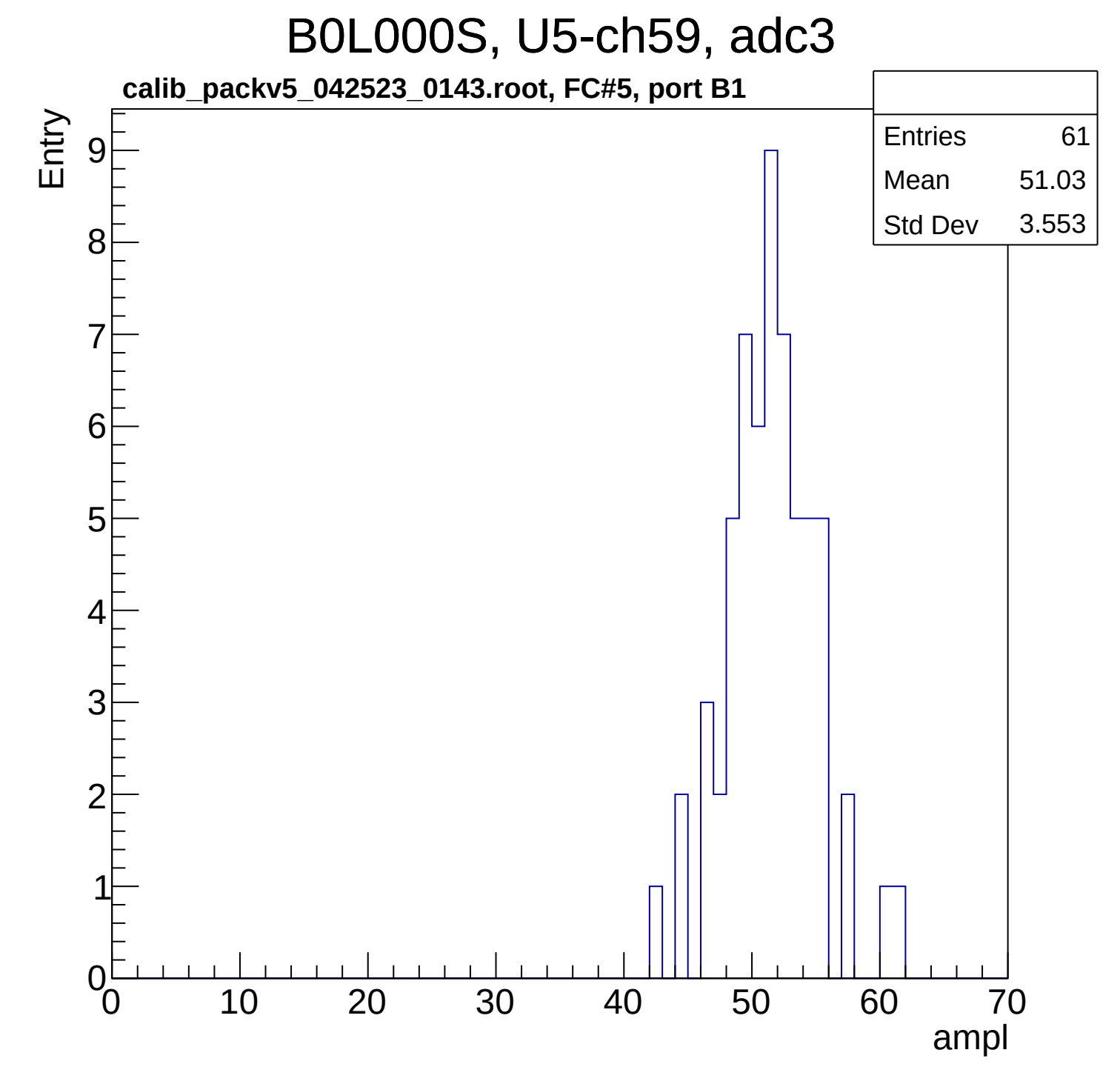
B0L000S, U5-ch59, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 51.03
Std Dev 3.553
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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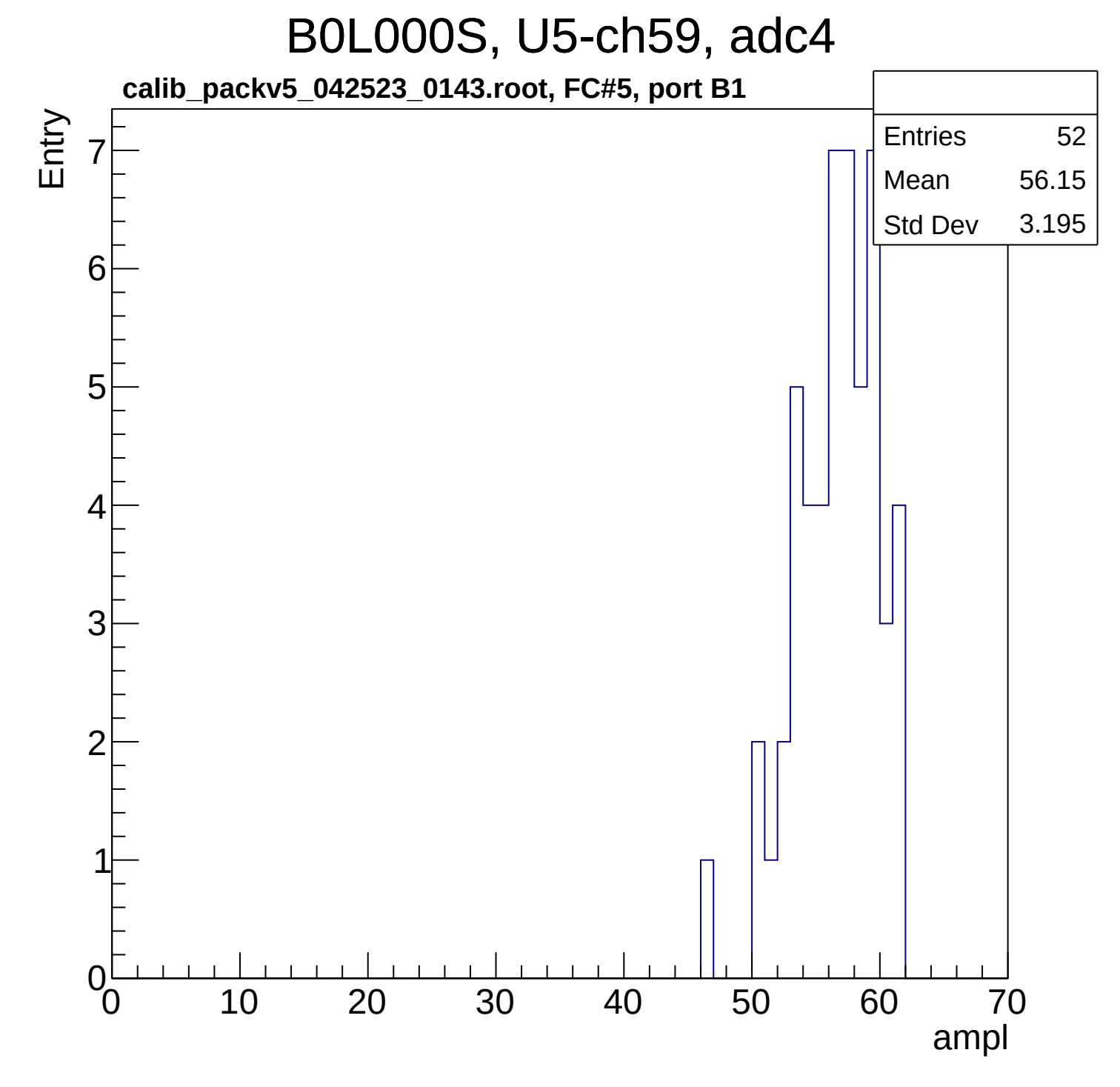
B0L000S, U5-ch59, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.15
Std Dev 3.195
Entry
ampl
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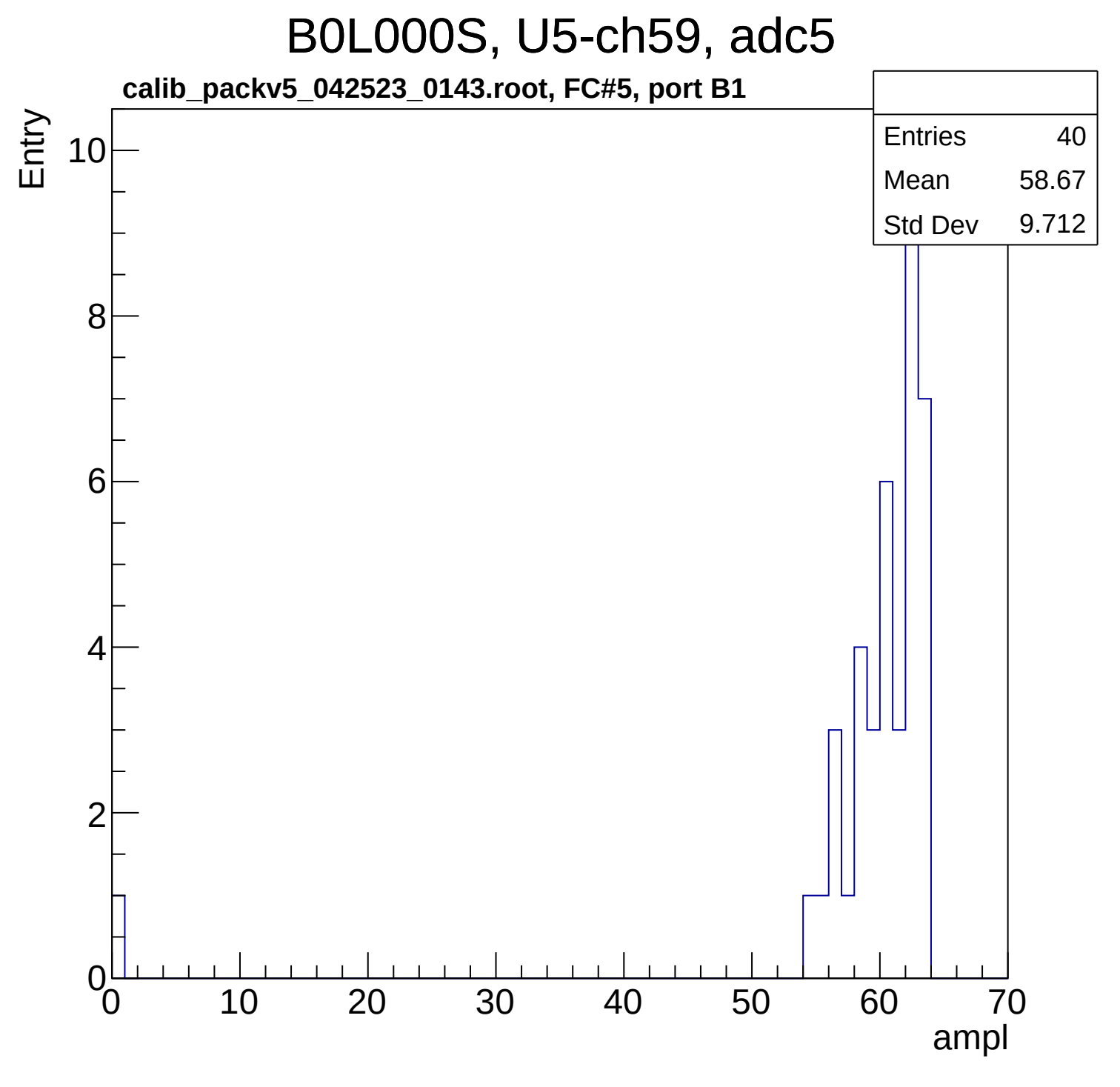
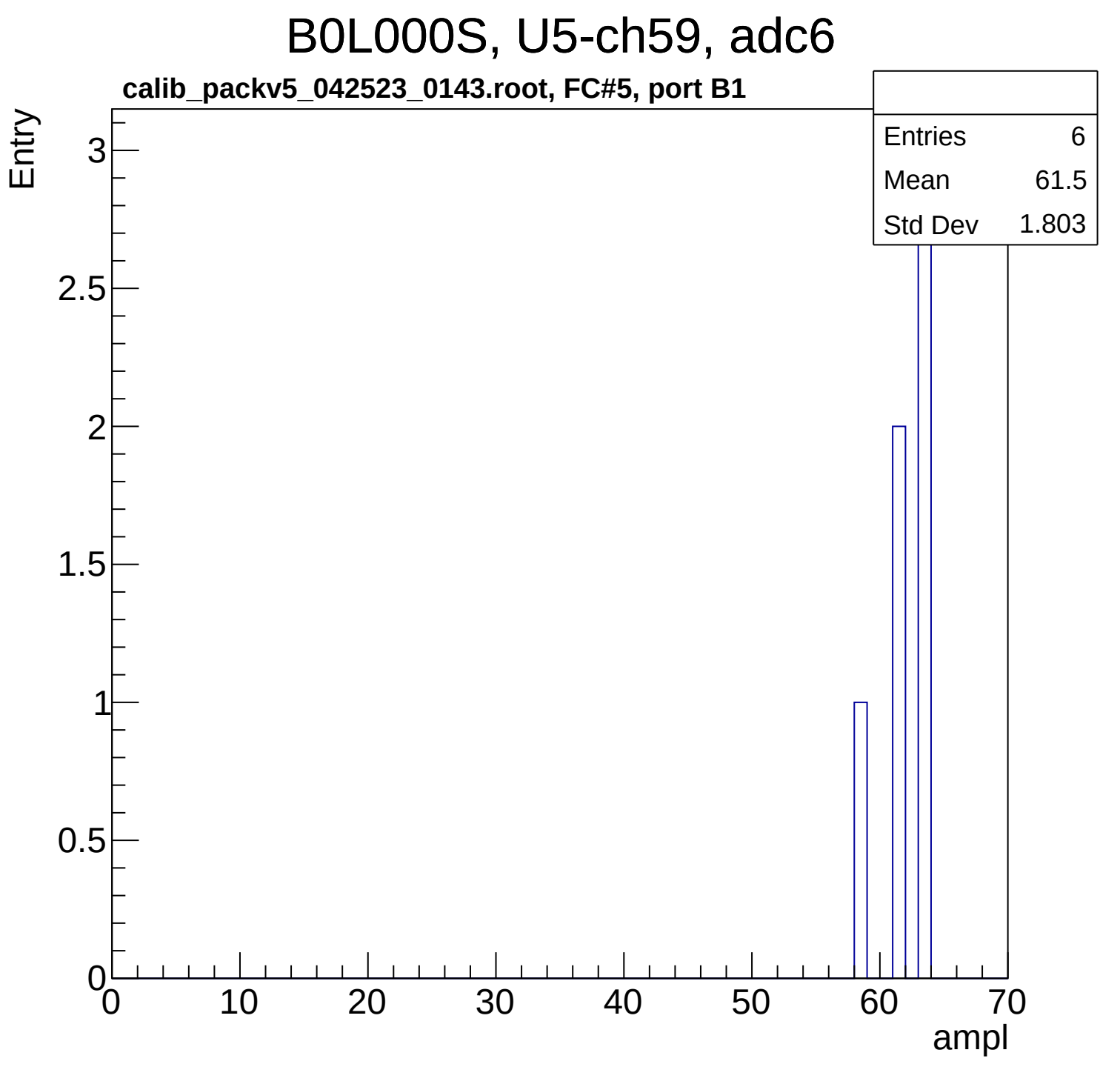
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch59, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.67
Std Dev 9.712
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch59, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.5
Std Dev 1.803
Entry
ampl
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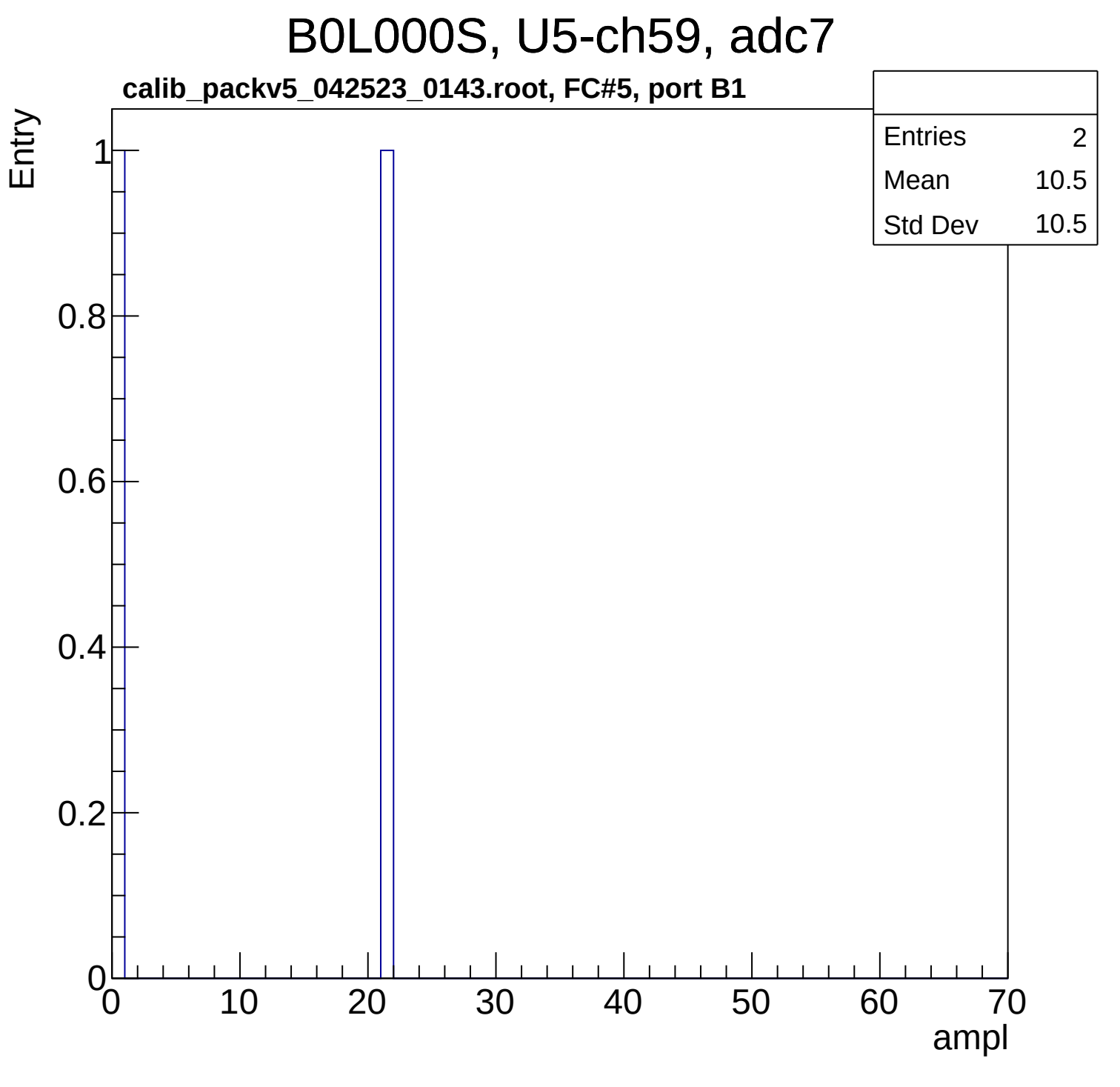
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch59, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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0.6
0.4
0.2
0
0
10
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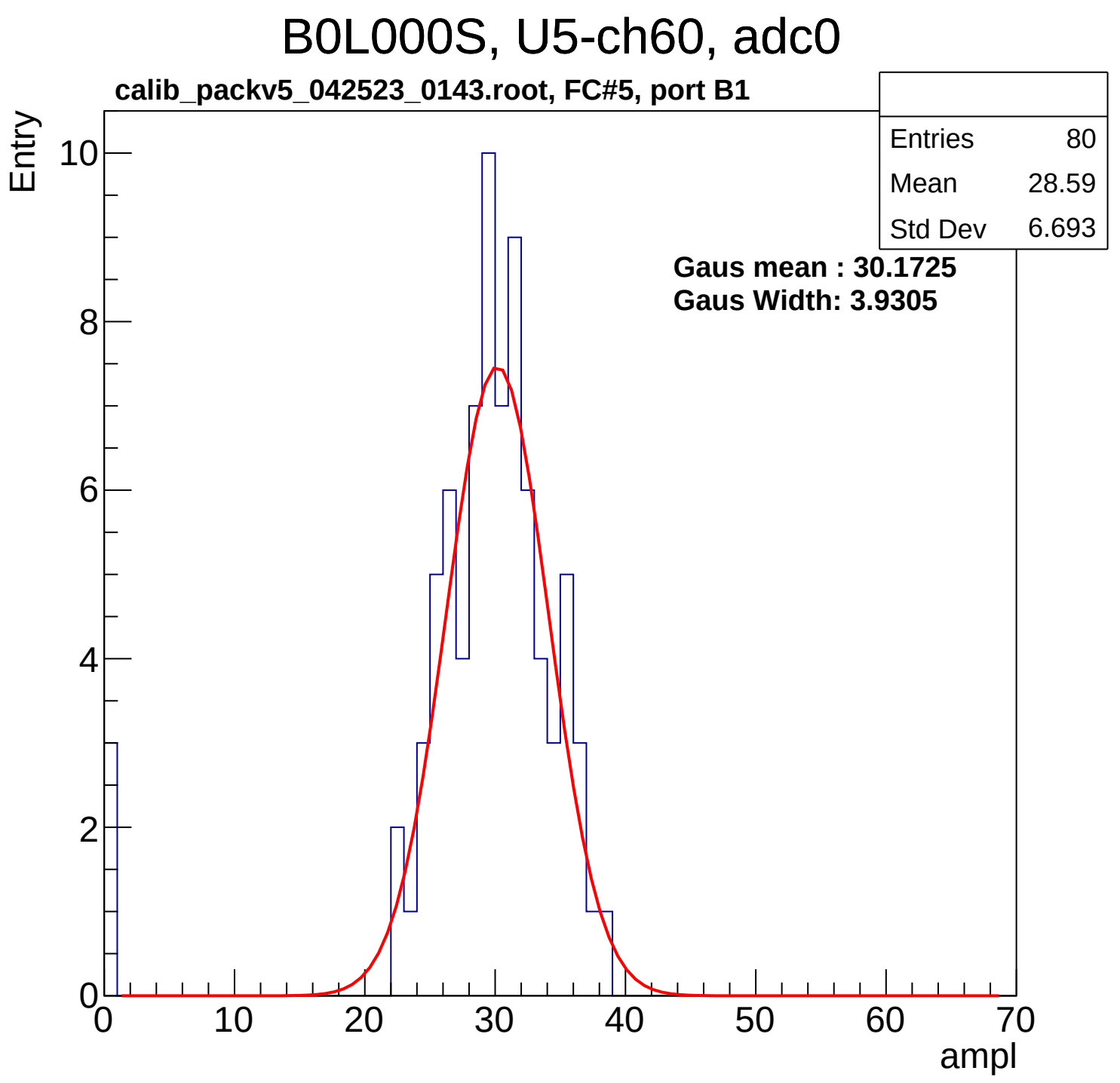
50
60
70
ampl


B0L000S, U5-ch60, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 28.59
Std Dev 6.693
Gaus mean : 30.1725
Gaus Width: 3.9305
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
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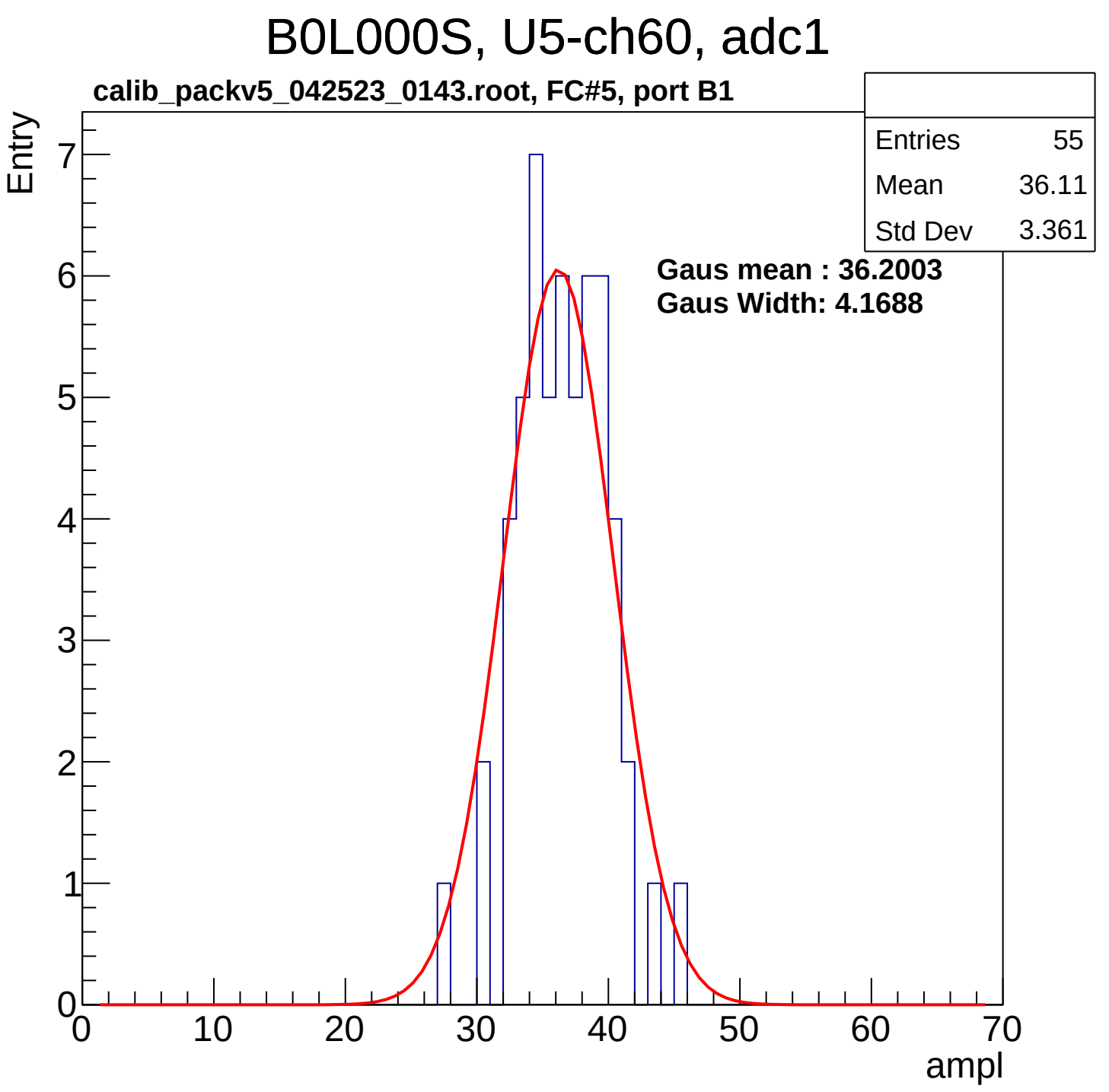
60
70


B0L000S, U5-ch60, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 36.11
Std Dev 3.361
Gaus mean : 36.2003
Gaus Width: 4.1688
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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60
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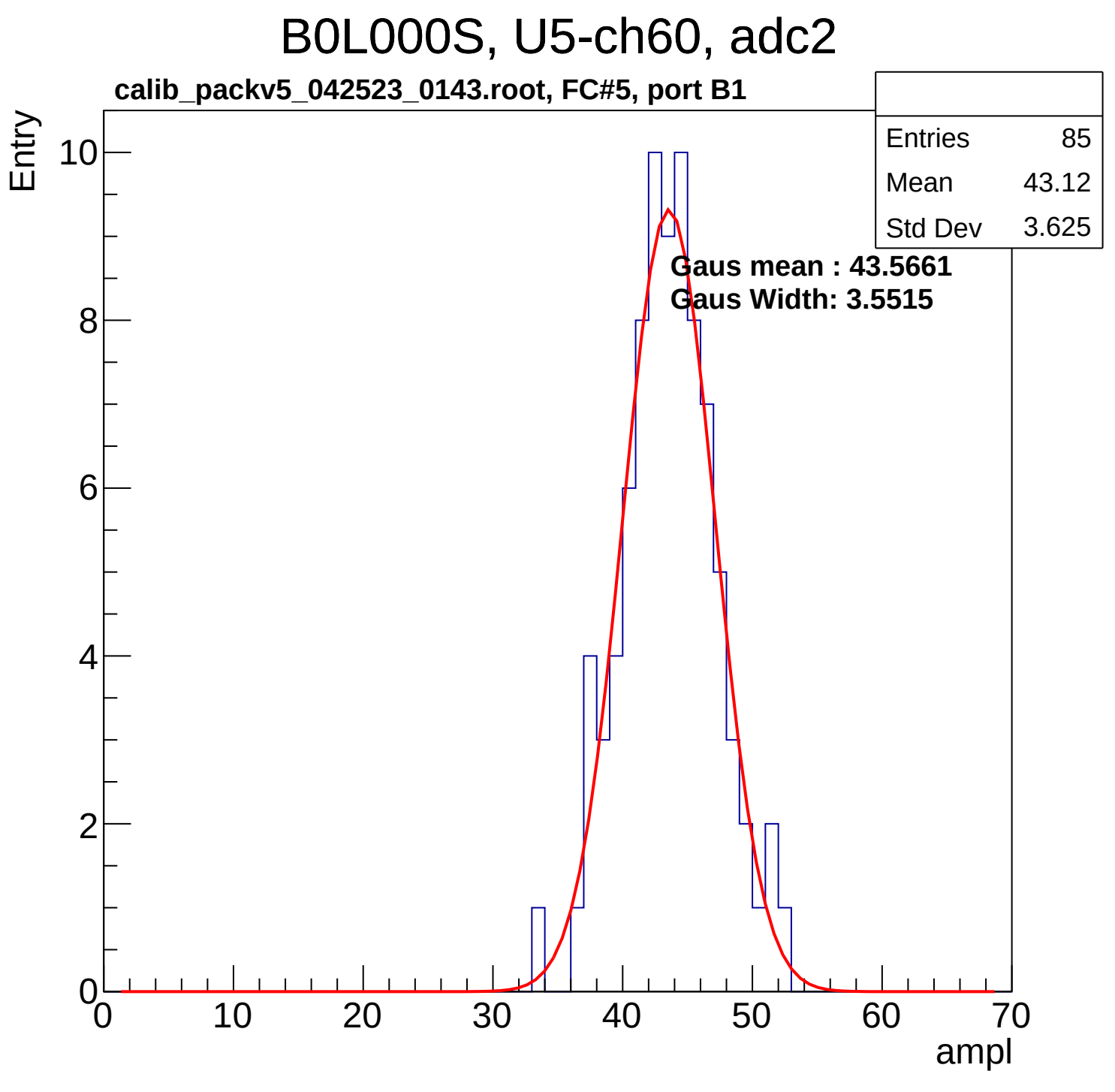
B0L000S, U5-ch60, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 43.12
Std Dev 3.625
Gaus mean : 43.5661
Gaus Width: 3.5515
Entry
ampl
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6
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0
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60
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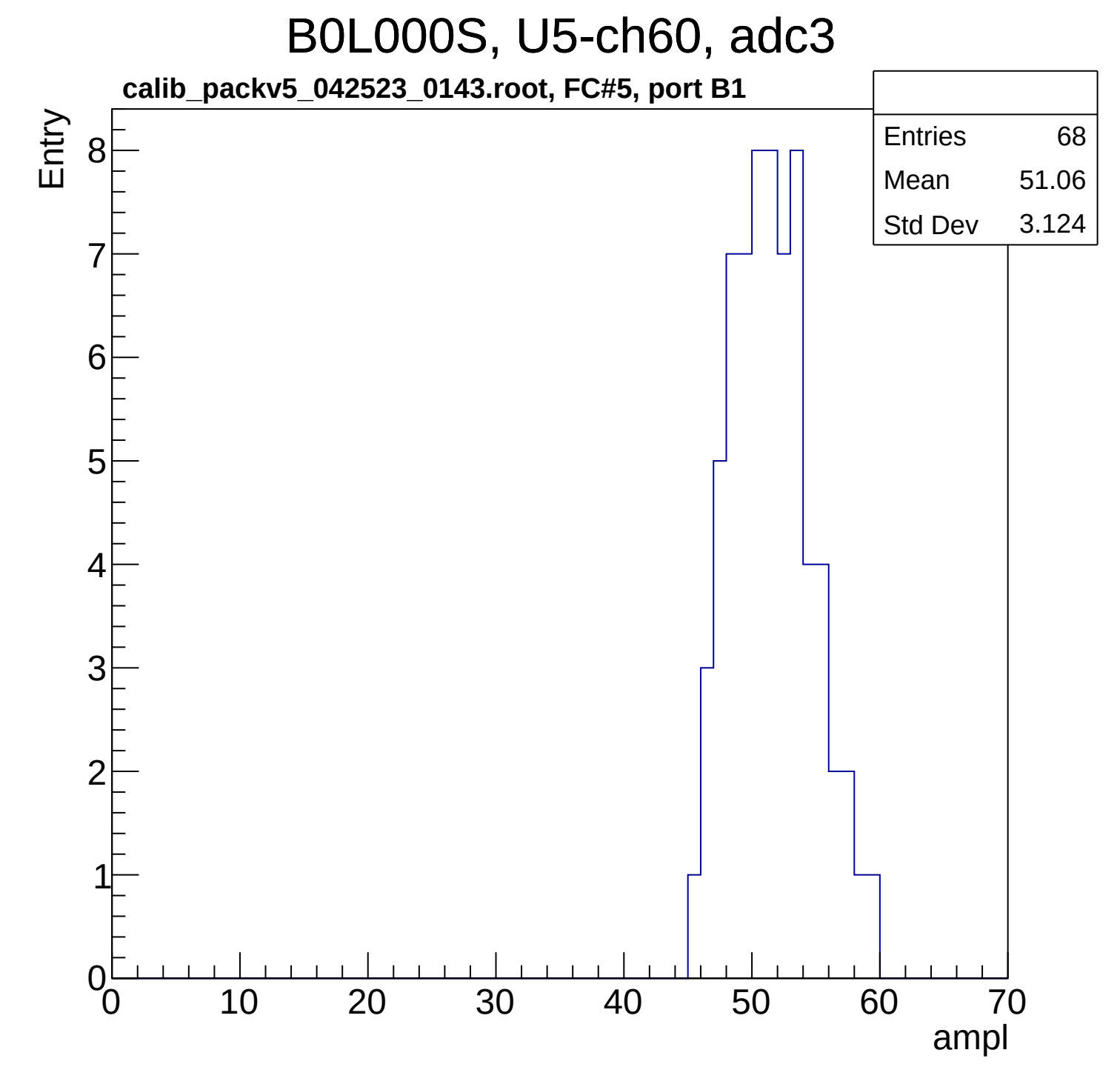
B0L000S, U5-ch60, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 51.06
Std Dev 3.124
Entry
ampl
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5
6
7
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0
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60
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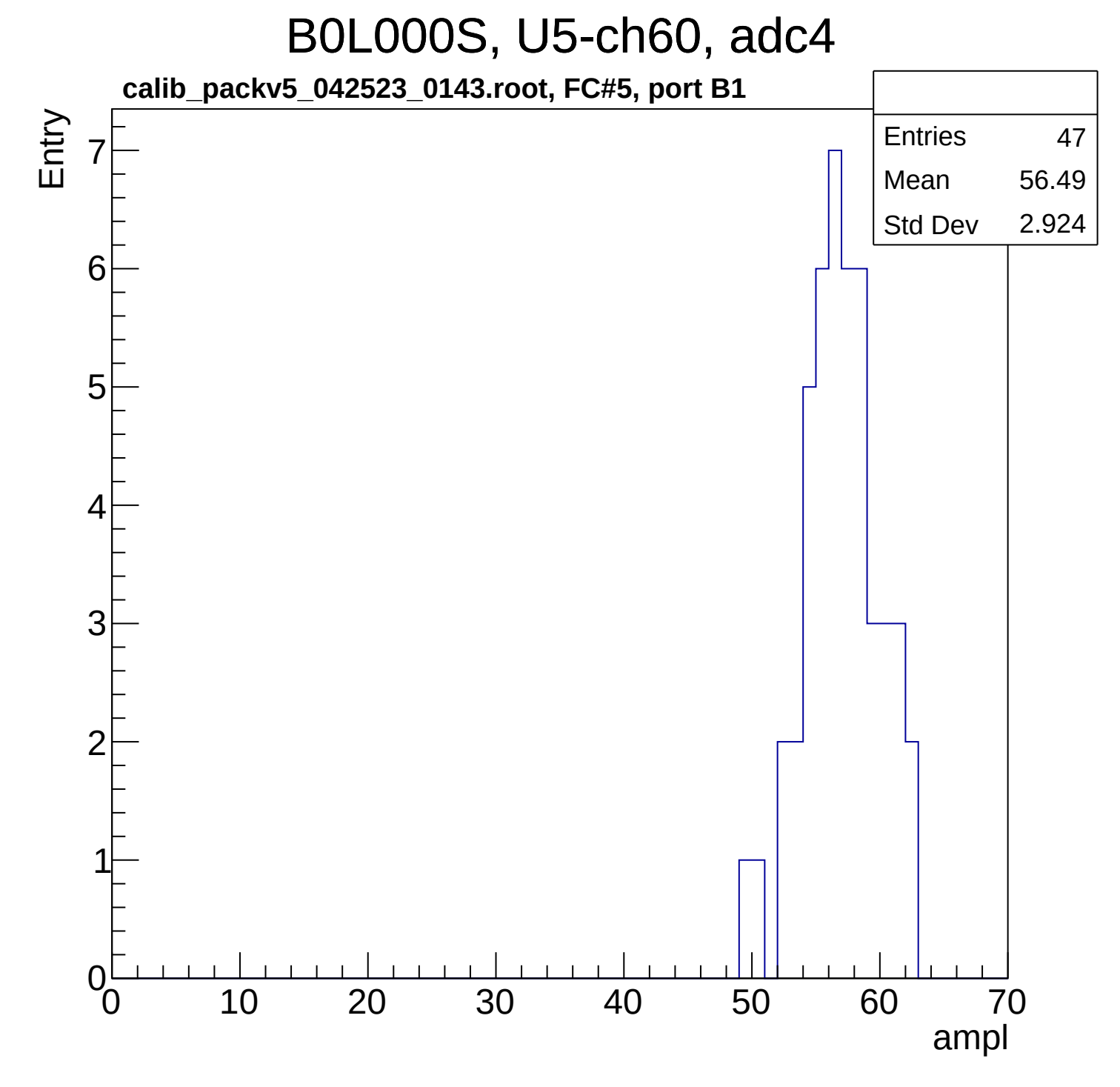
B0L000S, U5-ch60, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 56.49
Std Dev 2.924
Entry
ampl
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0
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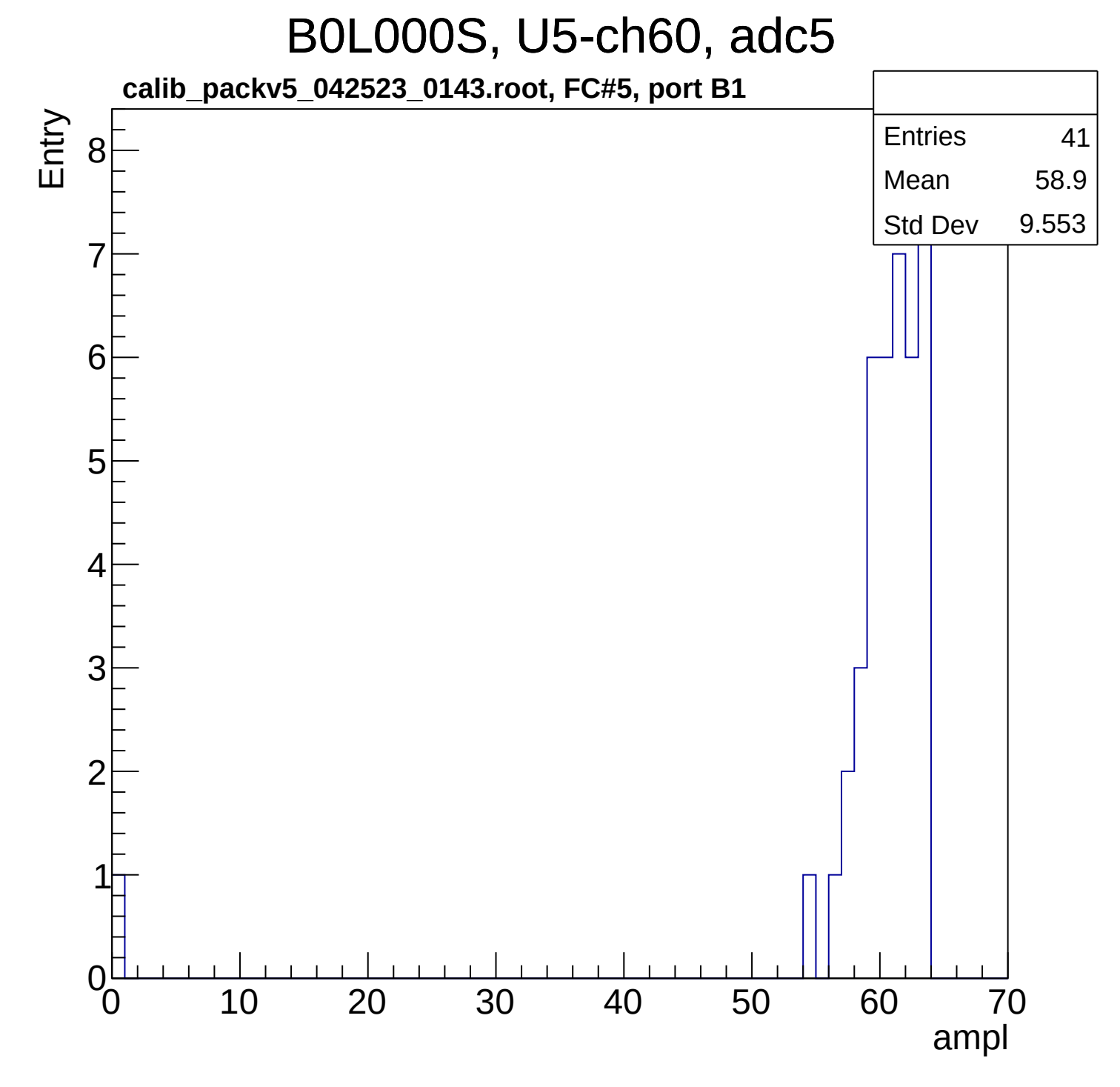
B0L000S, U5-ch60, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.9
Std Dev 9.553
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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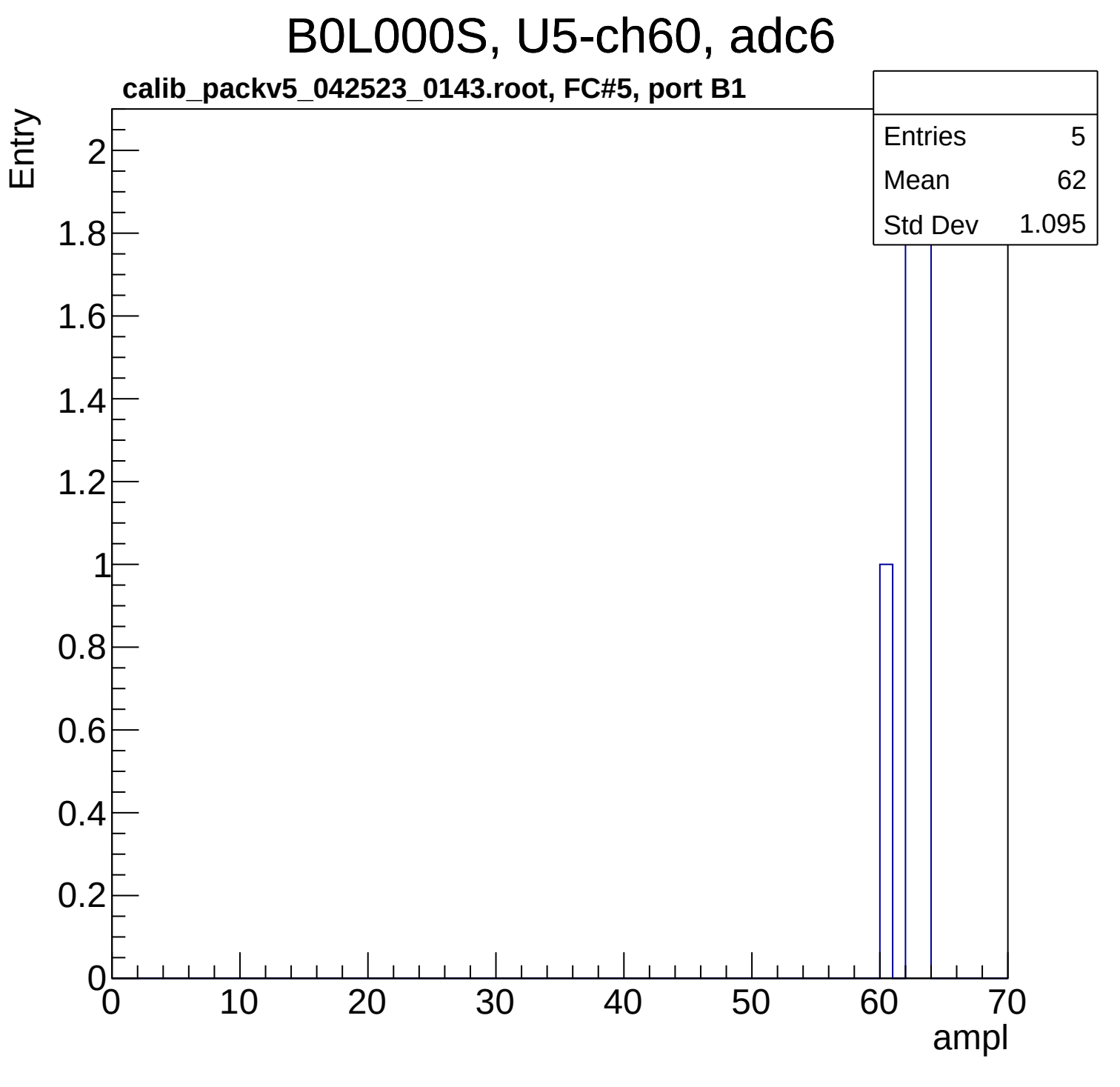
B0L000S, U5-ch60, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
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40
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60
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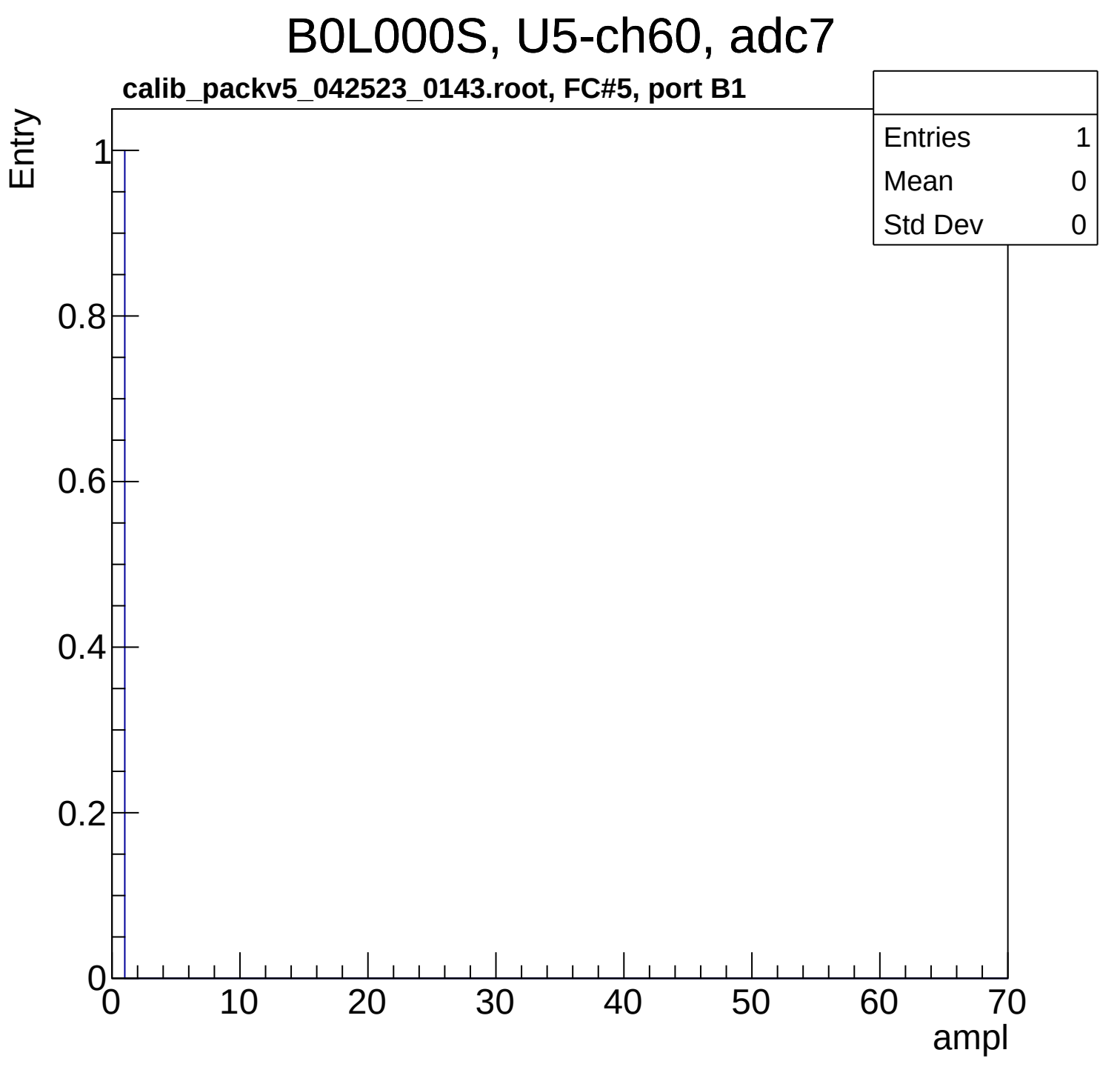
B0L000S, U5-ch60, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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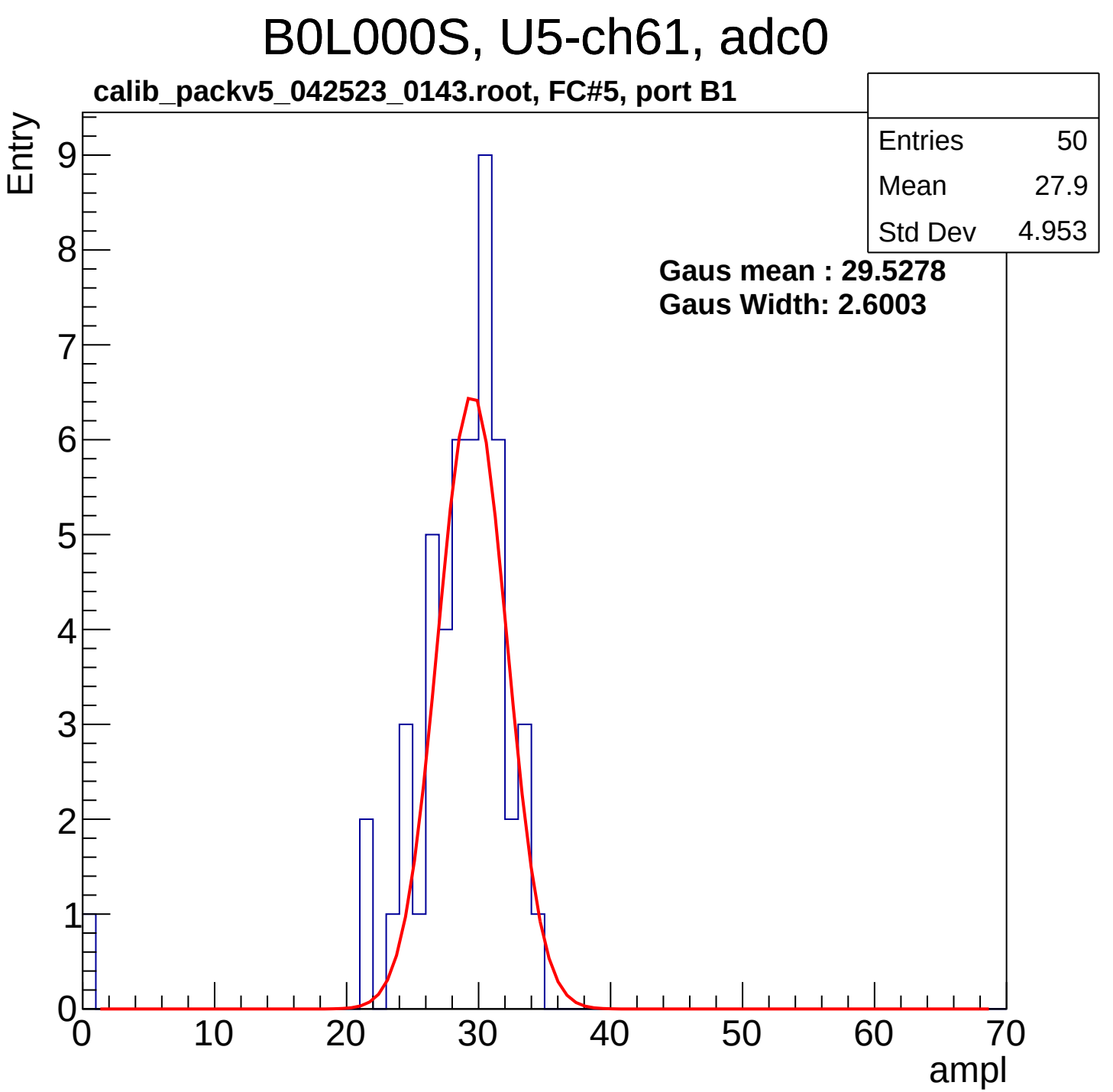
30
40
50
60
70
ampl


B0L000S, U5-ch61, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 27.9
Std Dev 4.953
Gaus mean : 29.5278
Gaus Width: 2.6003
Entry
ampl
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2
3
4
5
6
7
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0
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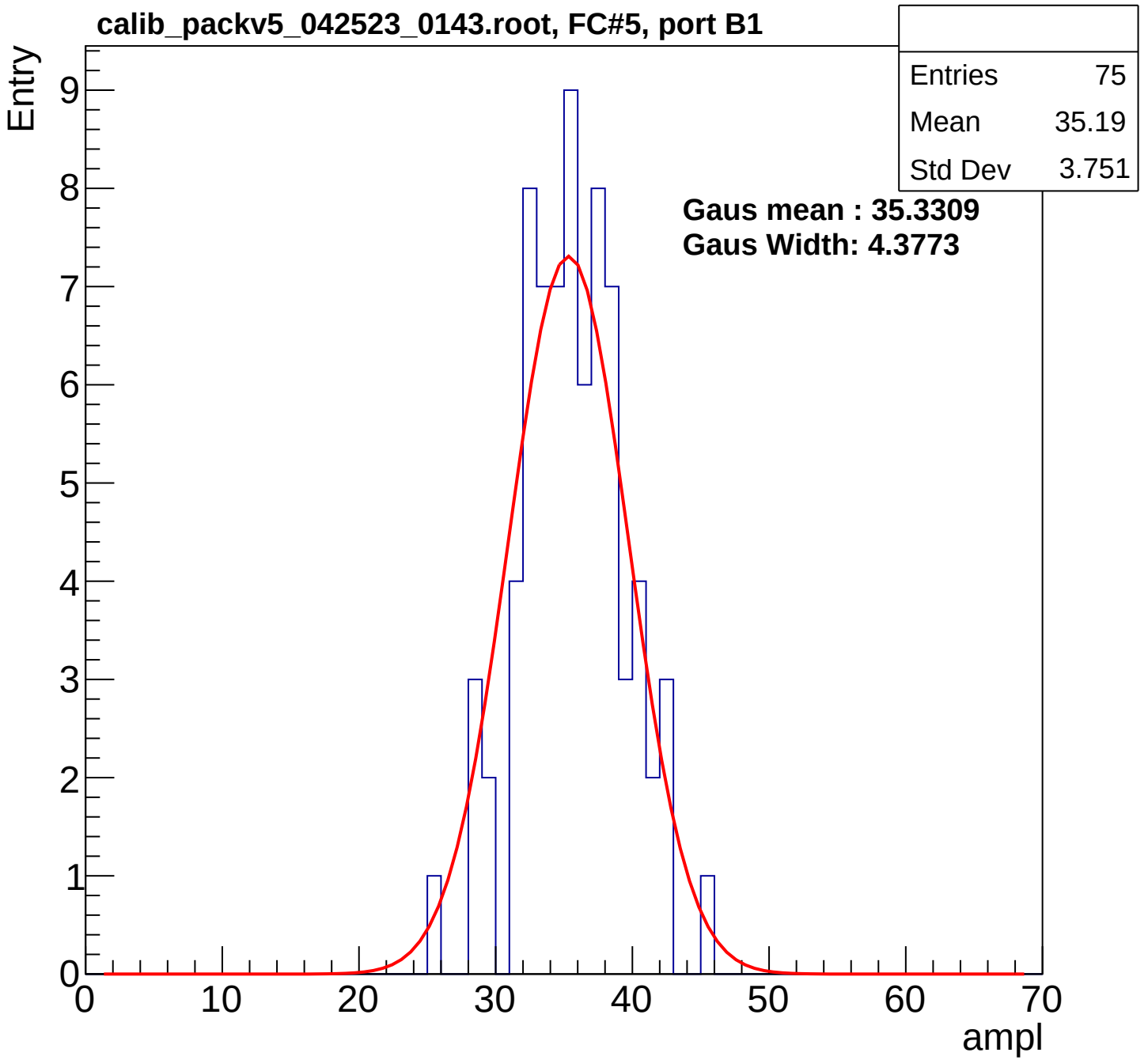
B0L000S, U5-ch61, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.19
Std Dev 3.751
Gaus mean : 35.3309
Gaus Width: 4.3773
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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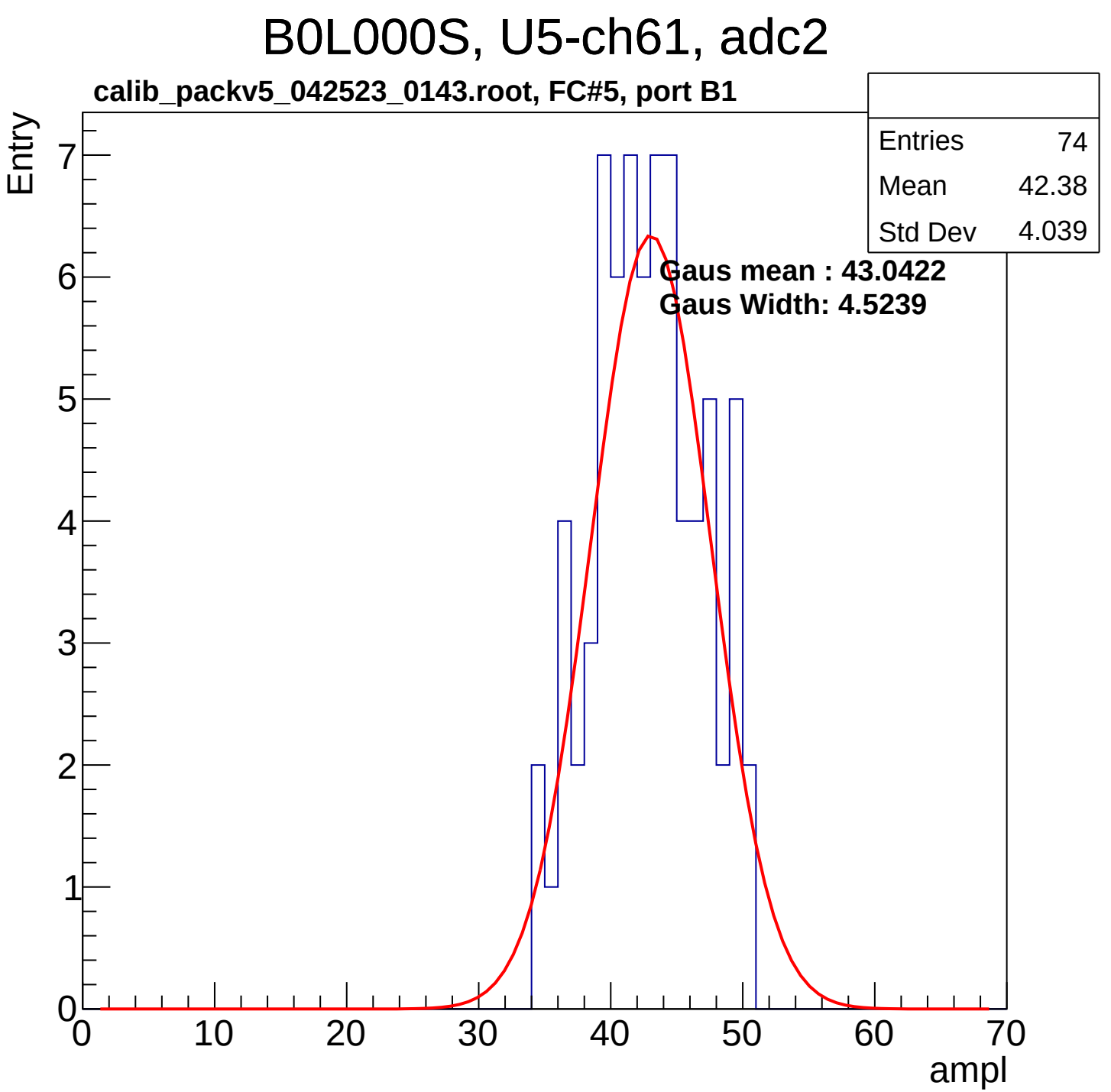
B0L000S, U5-ch61, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 42.38
Std Dev 4.039
Gaus mean : 43.0422
Gaus Width: 4.5239
Entry
ampl
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1
2
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4
5
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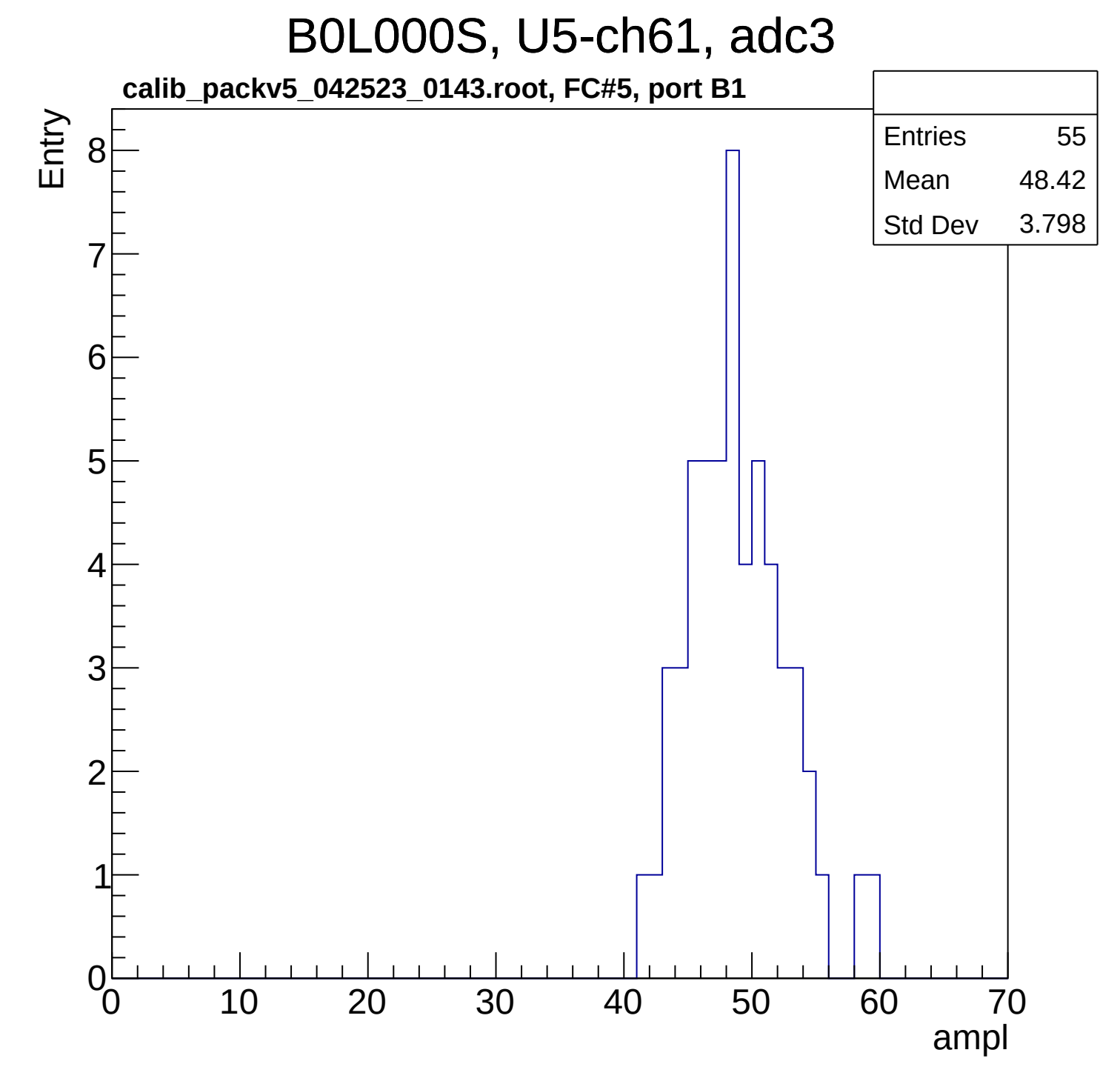
B0L000S, U5-ch61, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 48.42
Std Dev 3.798
Entry
ampl
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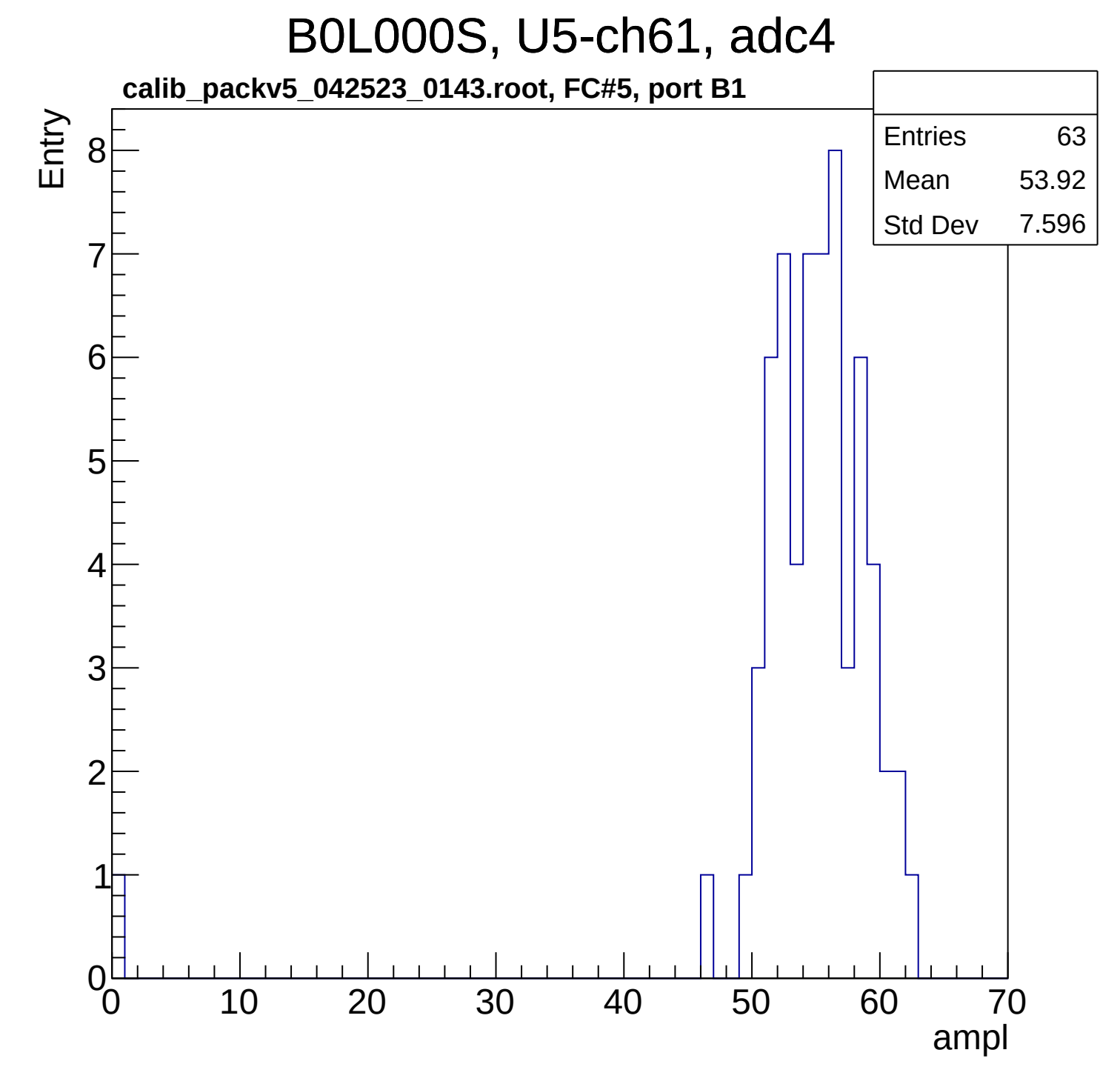
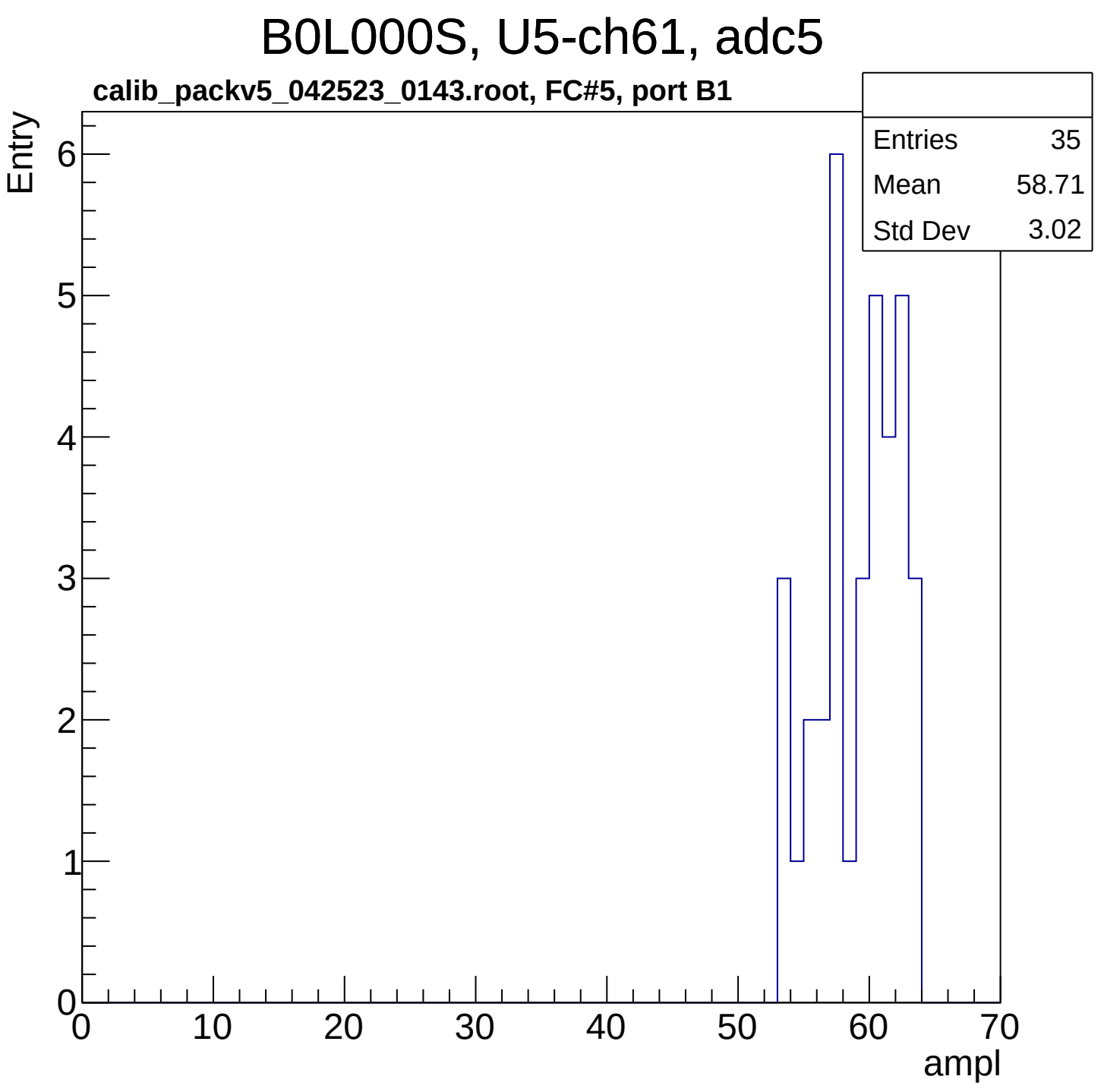
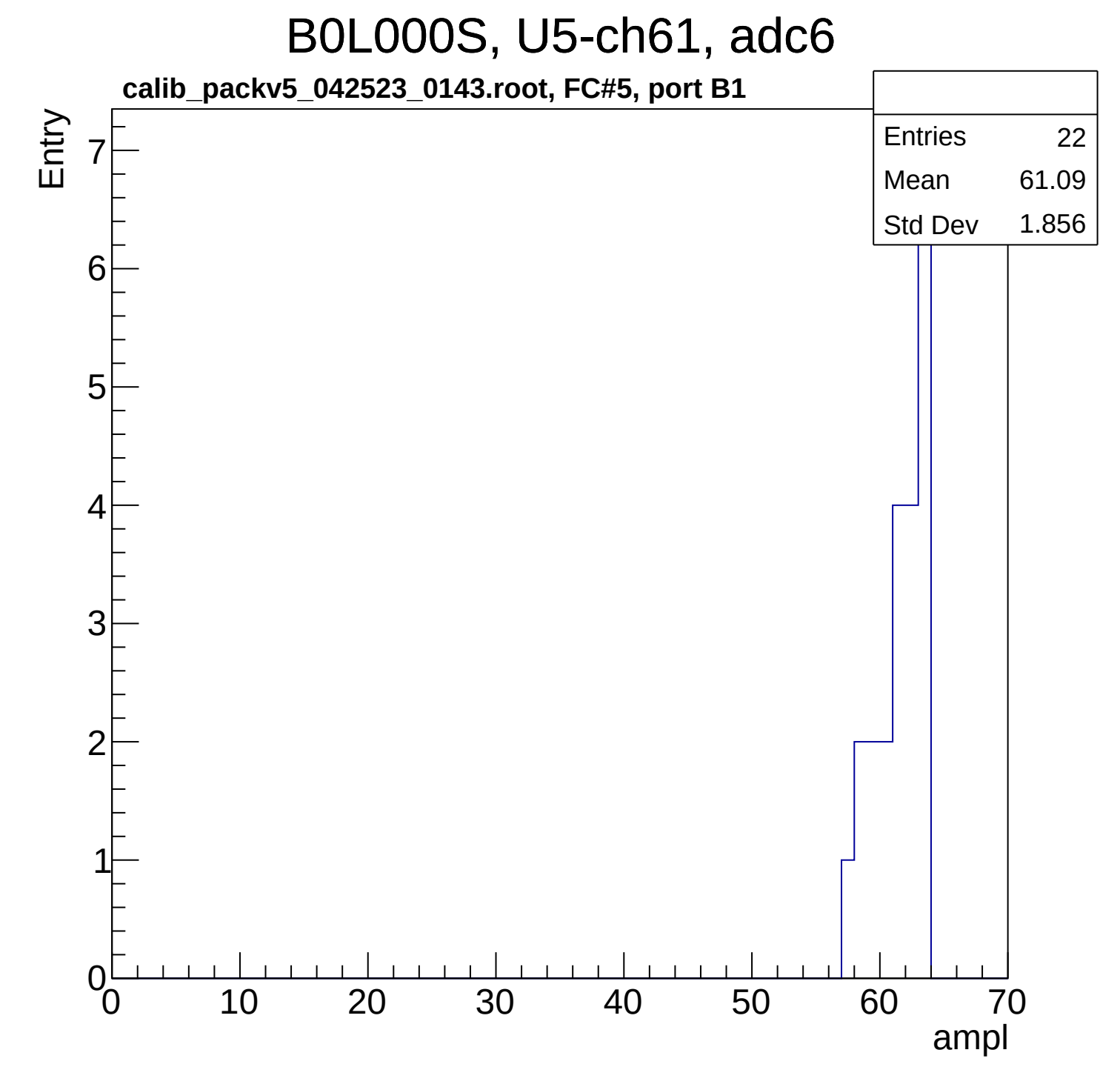
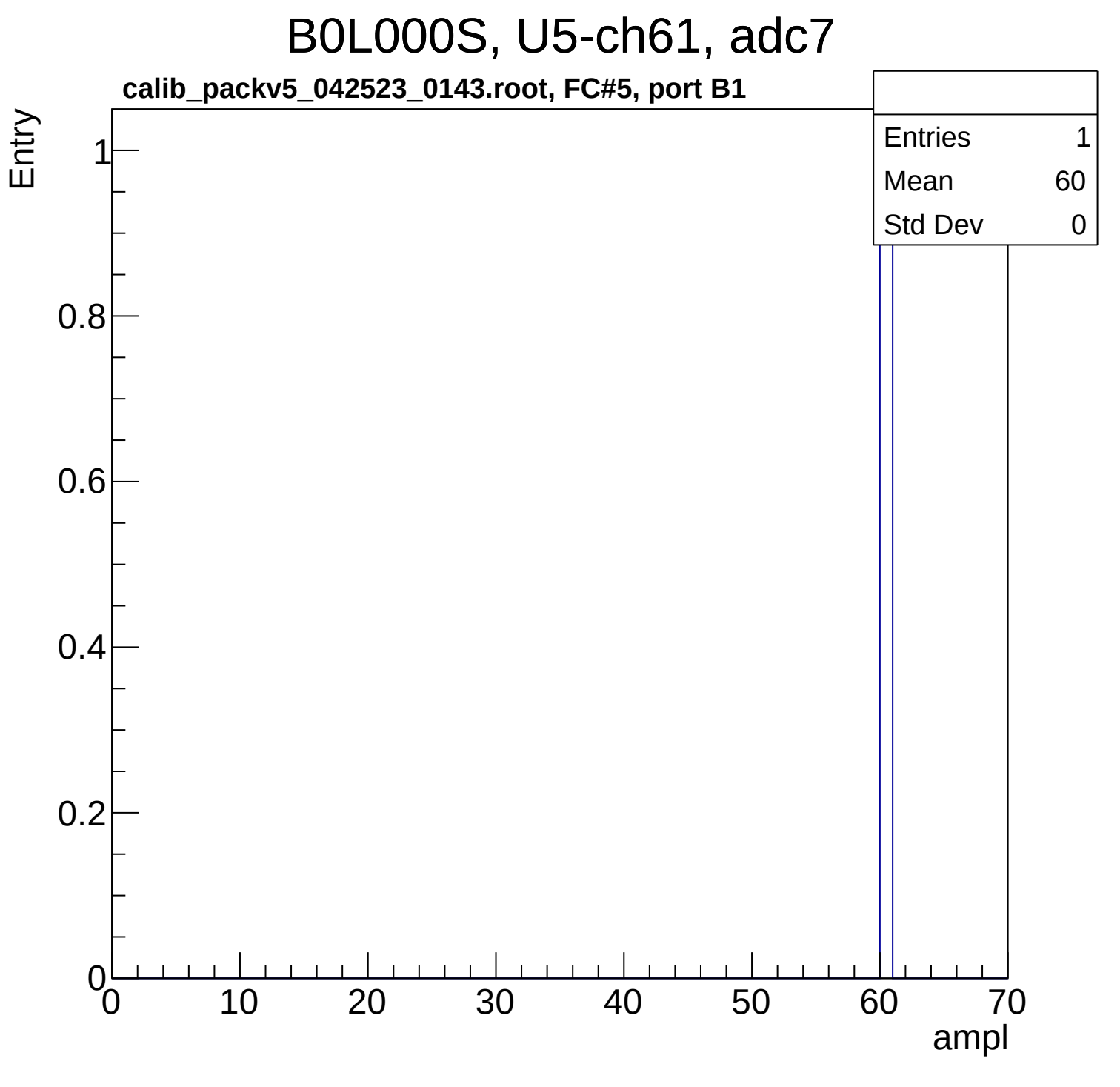
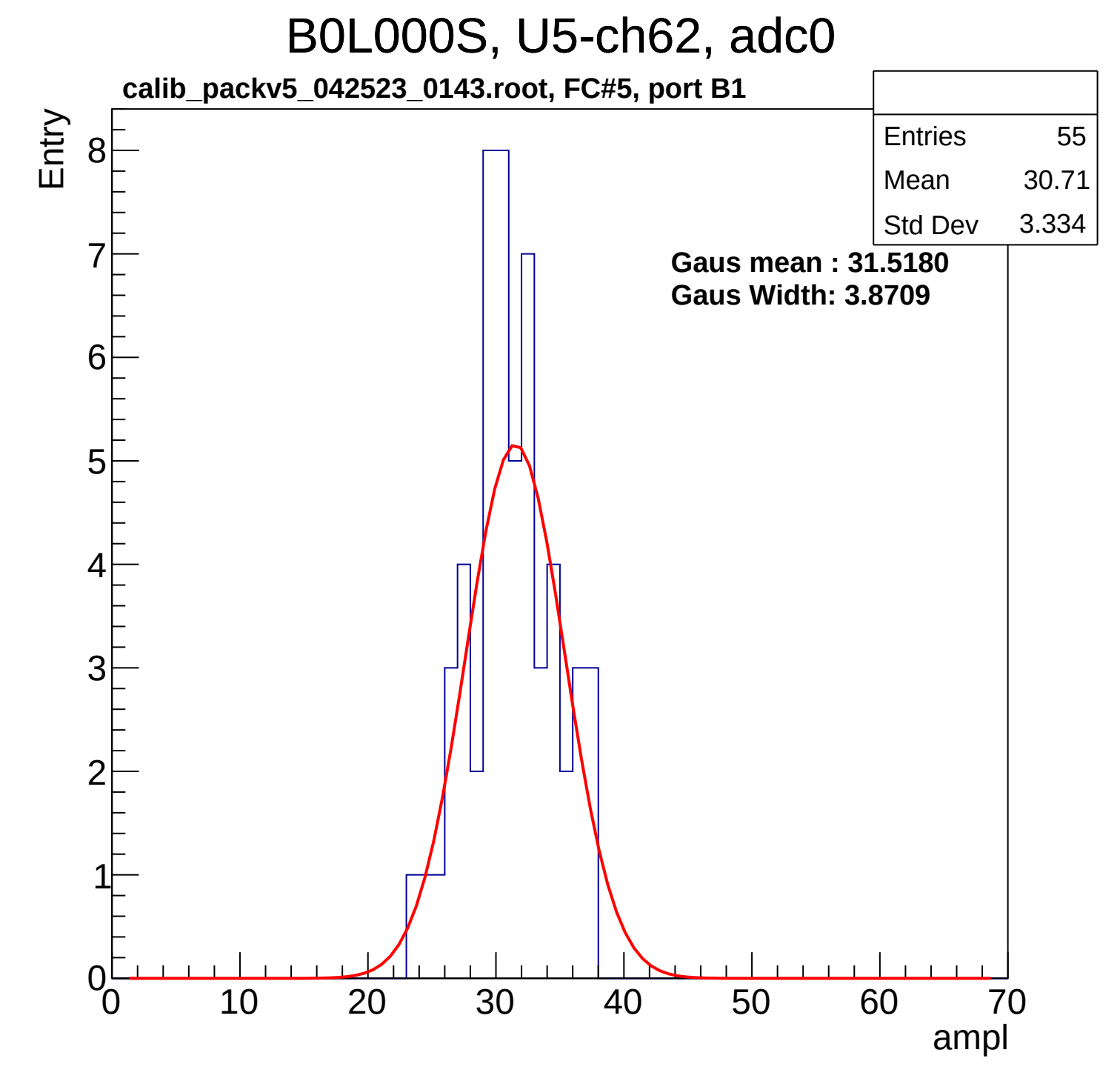
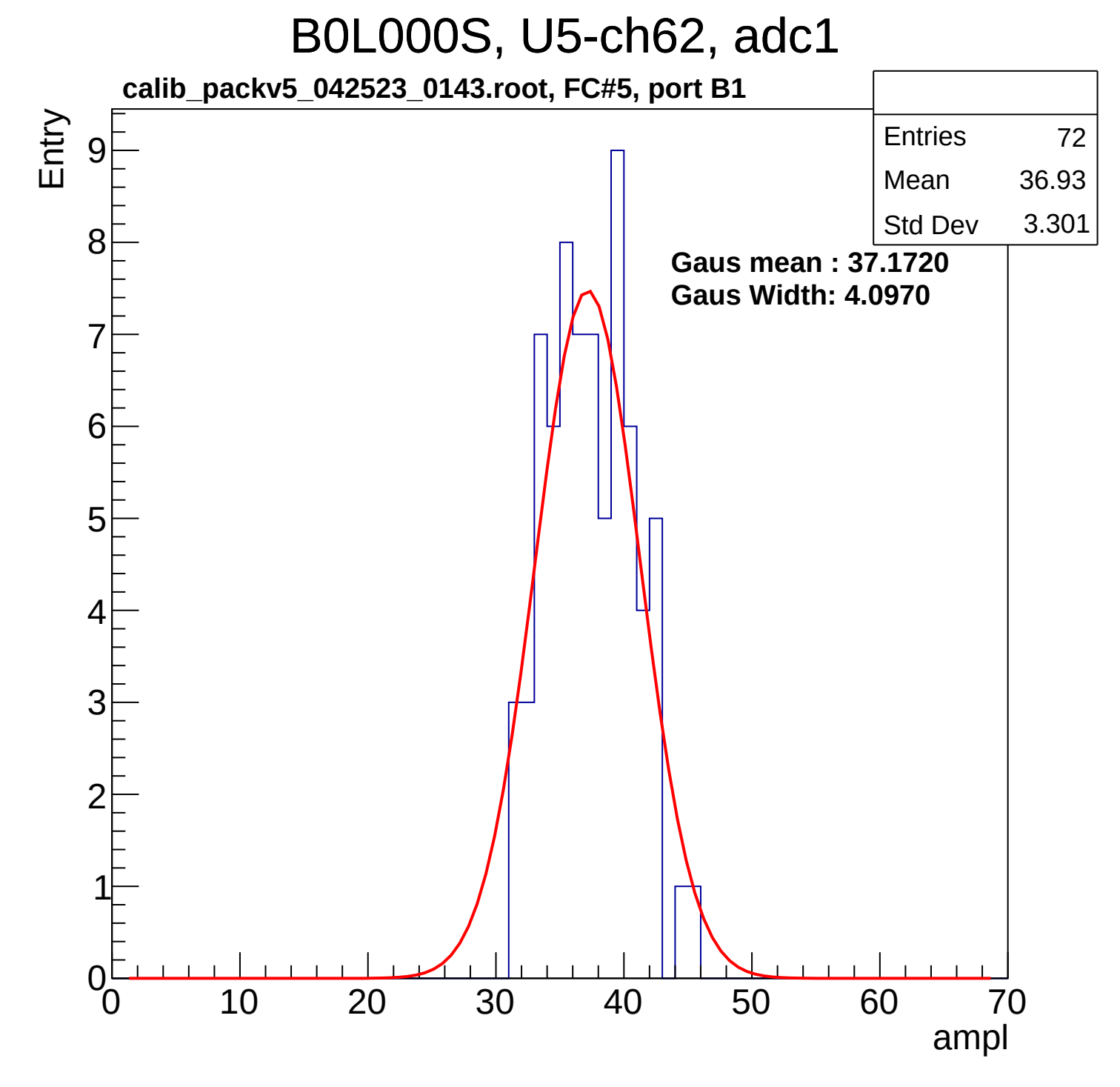

B0L000S, U5-ch61, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 53.92
Std Dev 7.596
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch61, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.71
Std Dev 3.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch61, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 22
Mean 61.09
Std Dev 1.856
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch61, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch62, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 30.71
Std Dev 3.334
Gaus mean : 31.5180
Gaus Width: 3.8709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch62, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 36.93
Std Dev 3.301
Gaus mean : 37.1720
Gaus Width: 4.0970
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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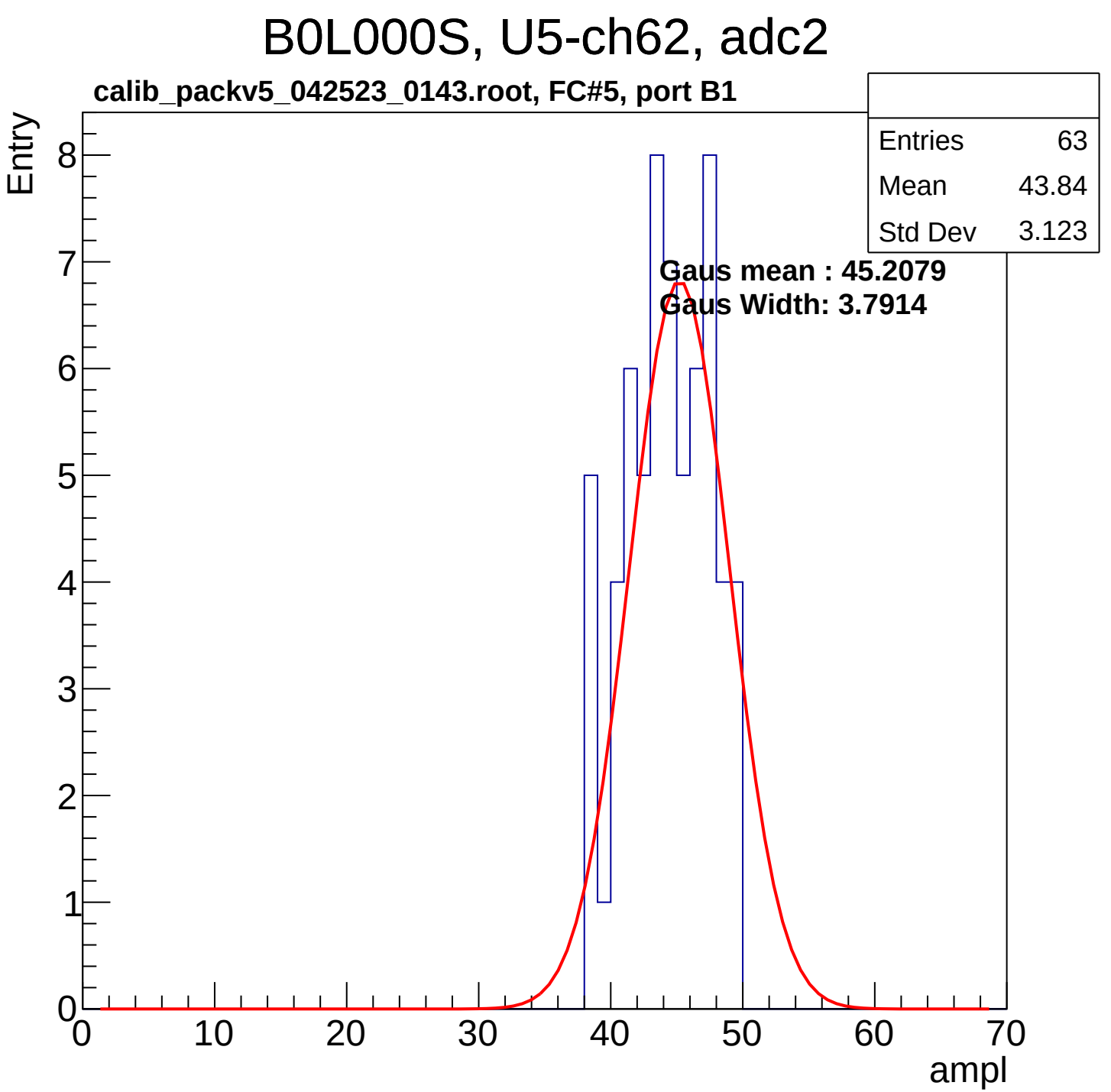
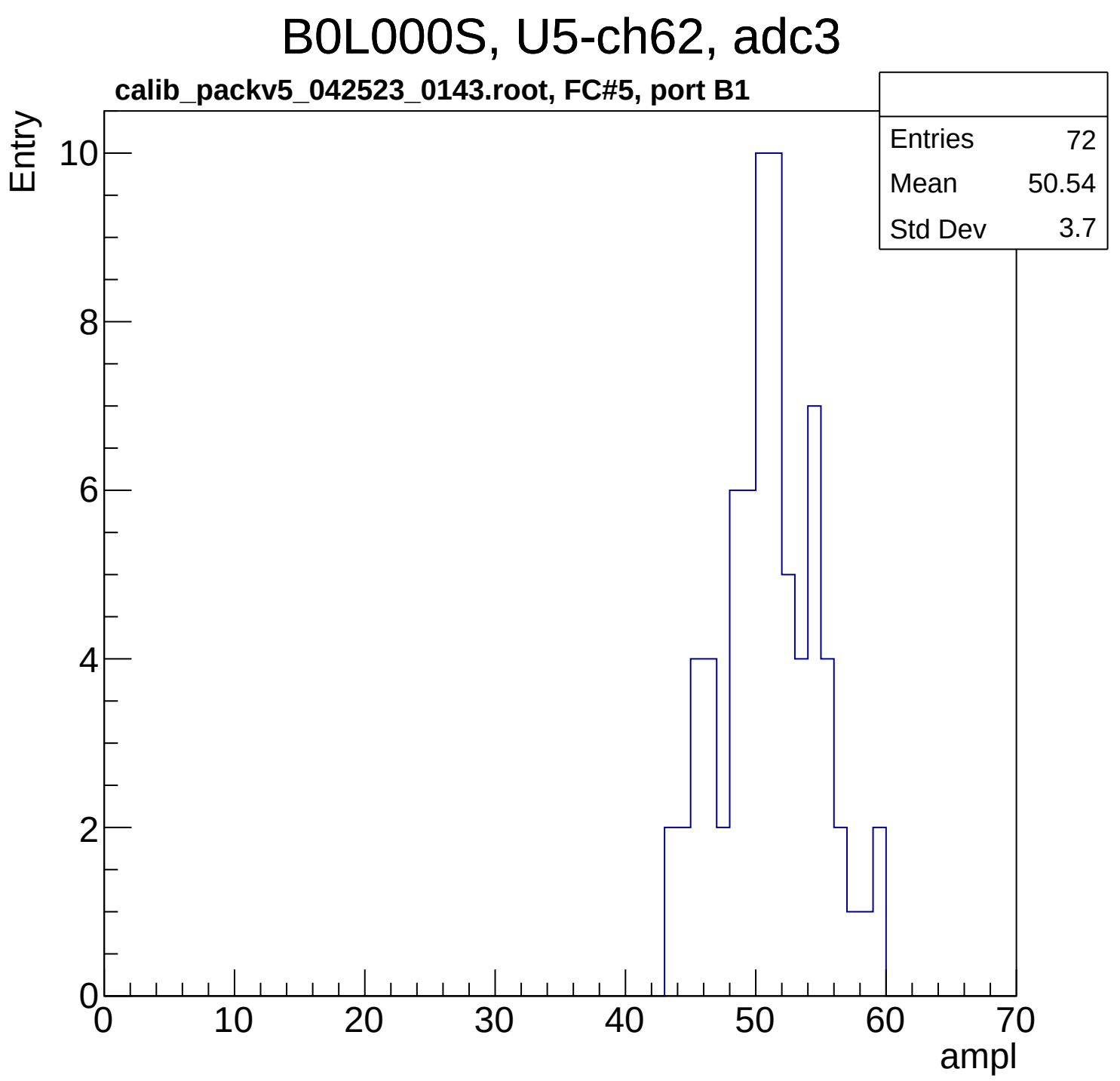
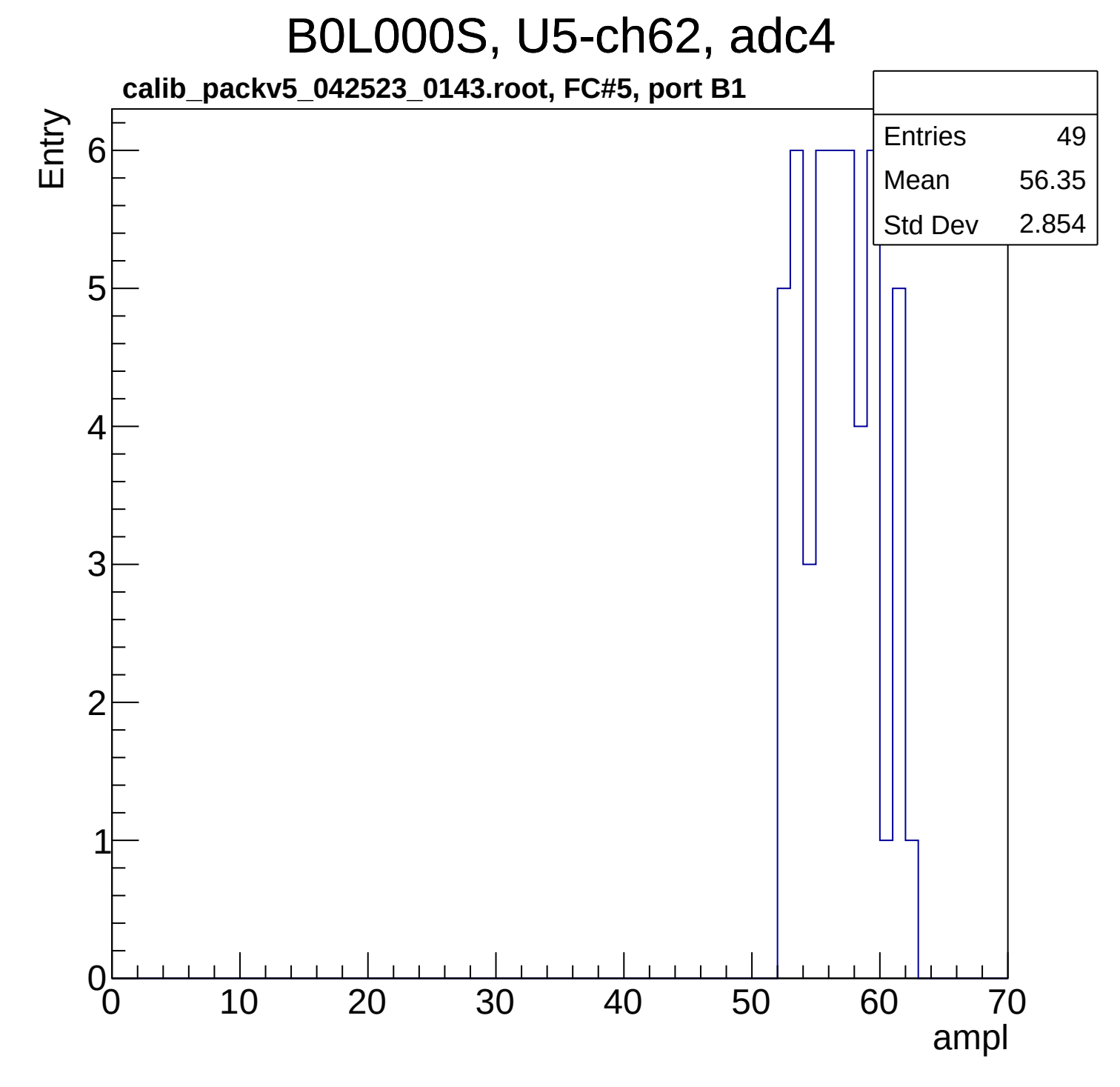
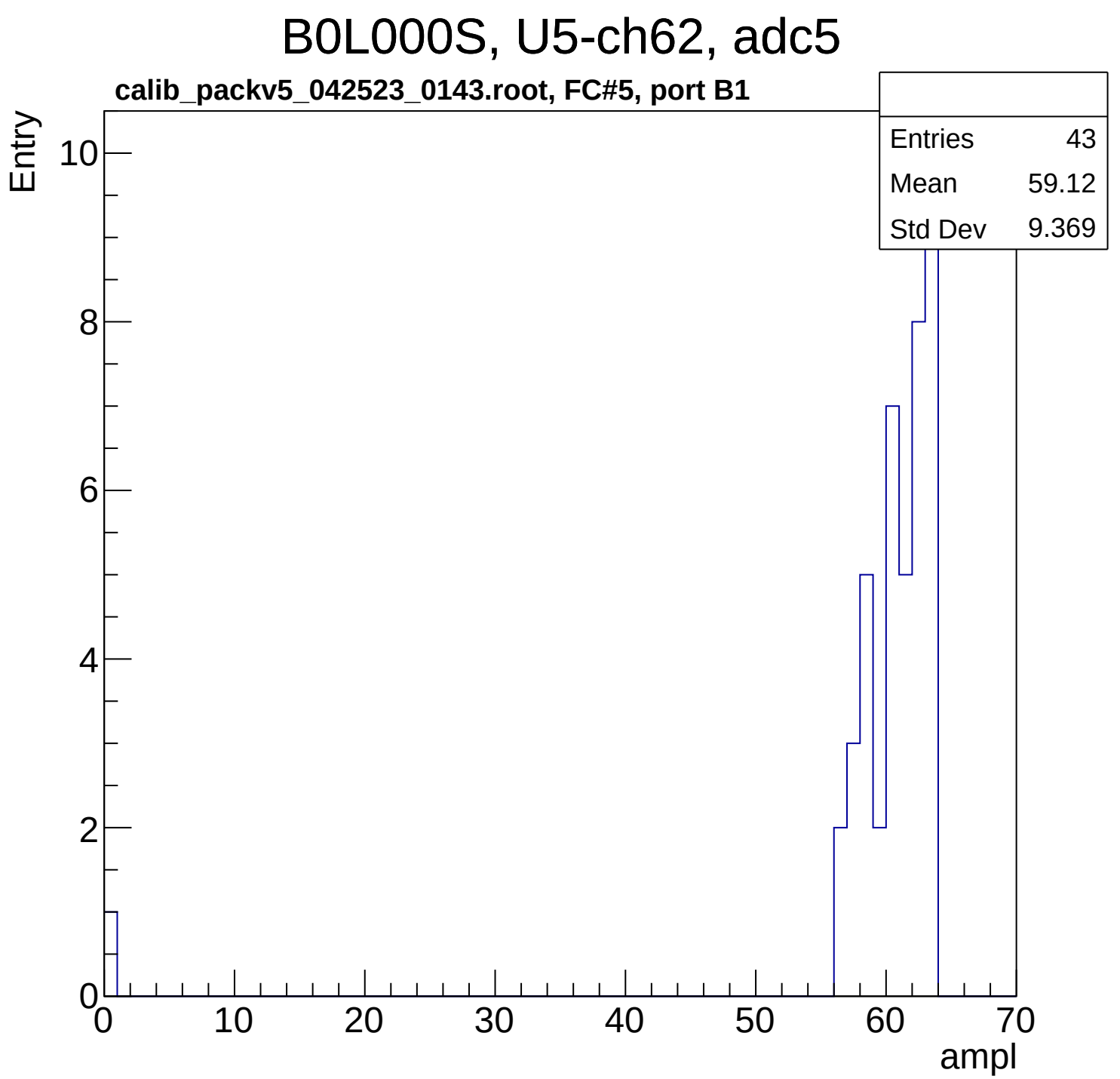
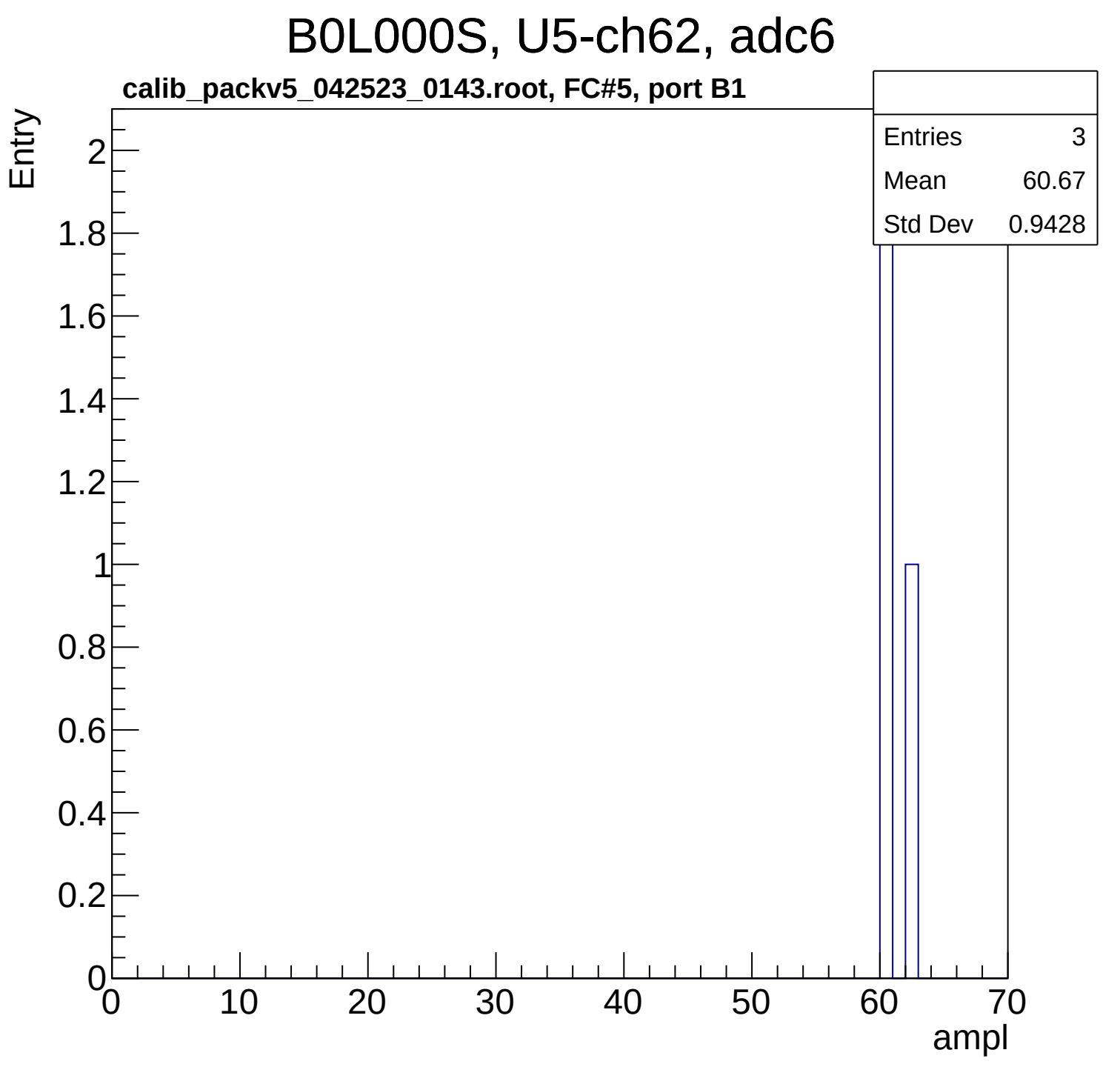
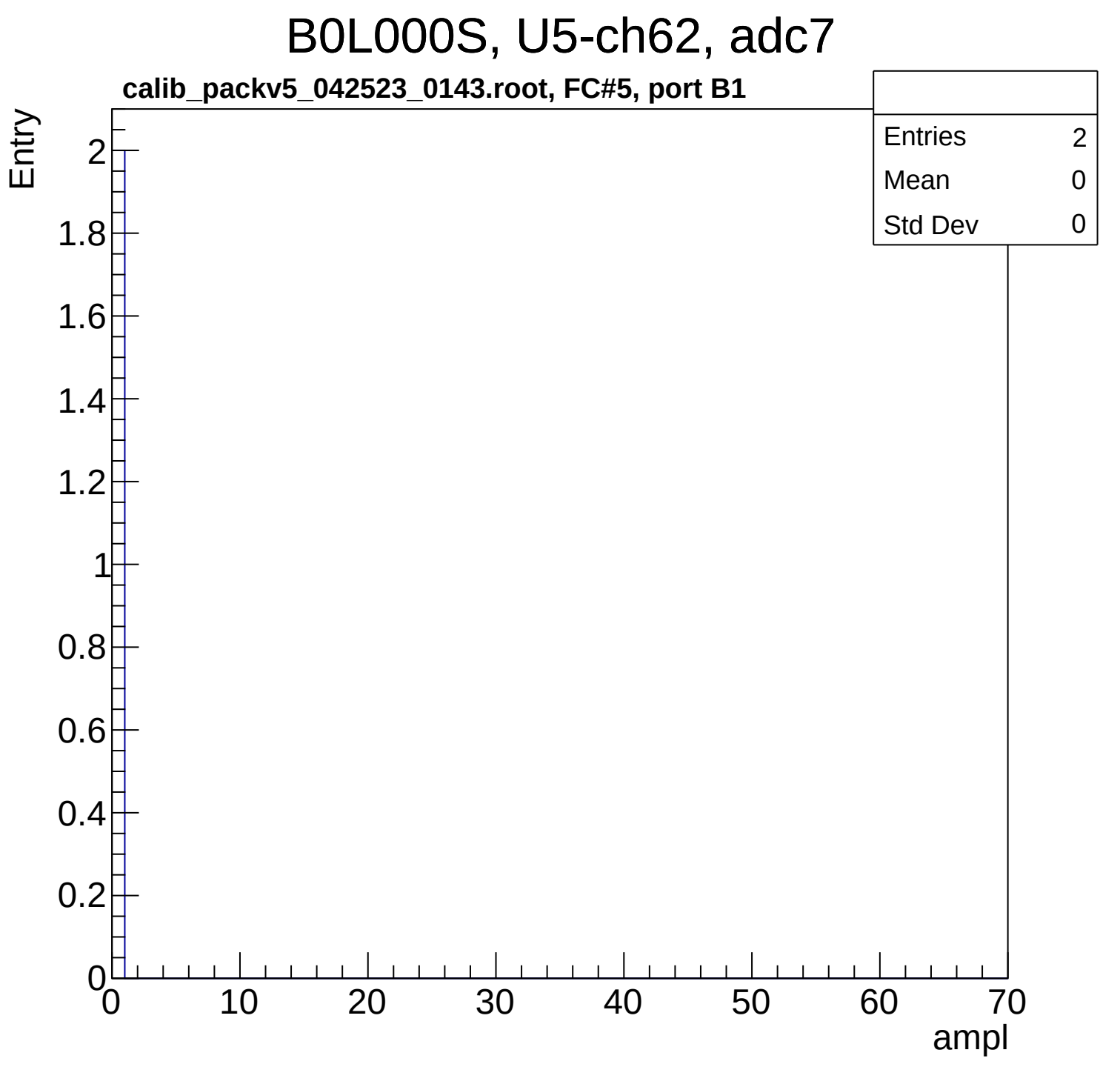

B0L000S, U5-ch62, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 43.84
Std Dev 3.123
Gaus mean : 45.2079
Gaus Width: 3.7914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

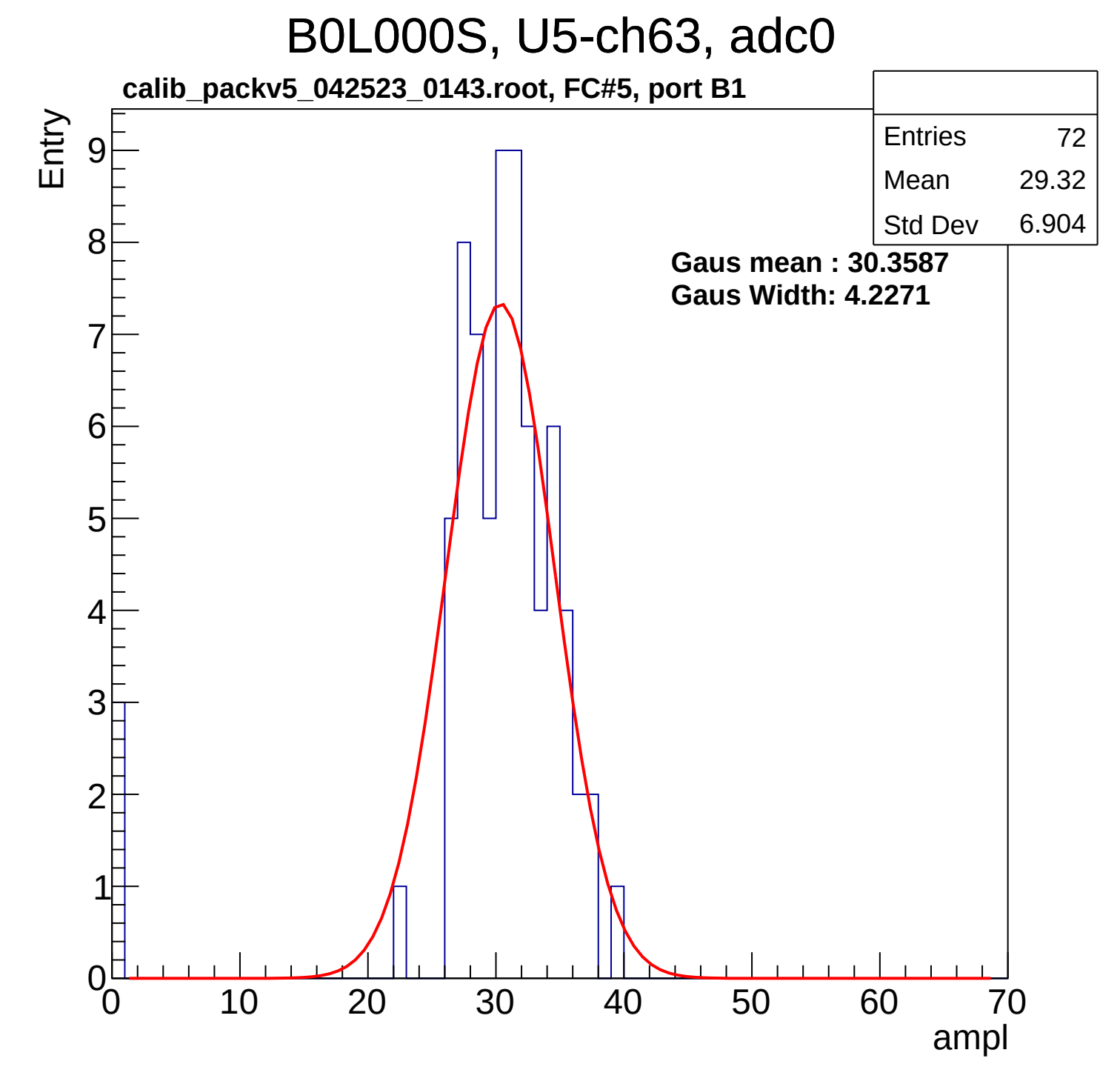

B0L000S, U5-ch62, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 50.54
Std Dev 3.7
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

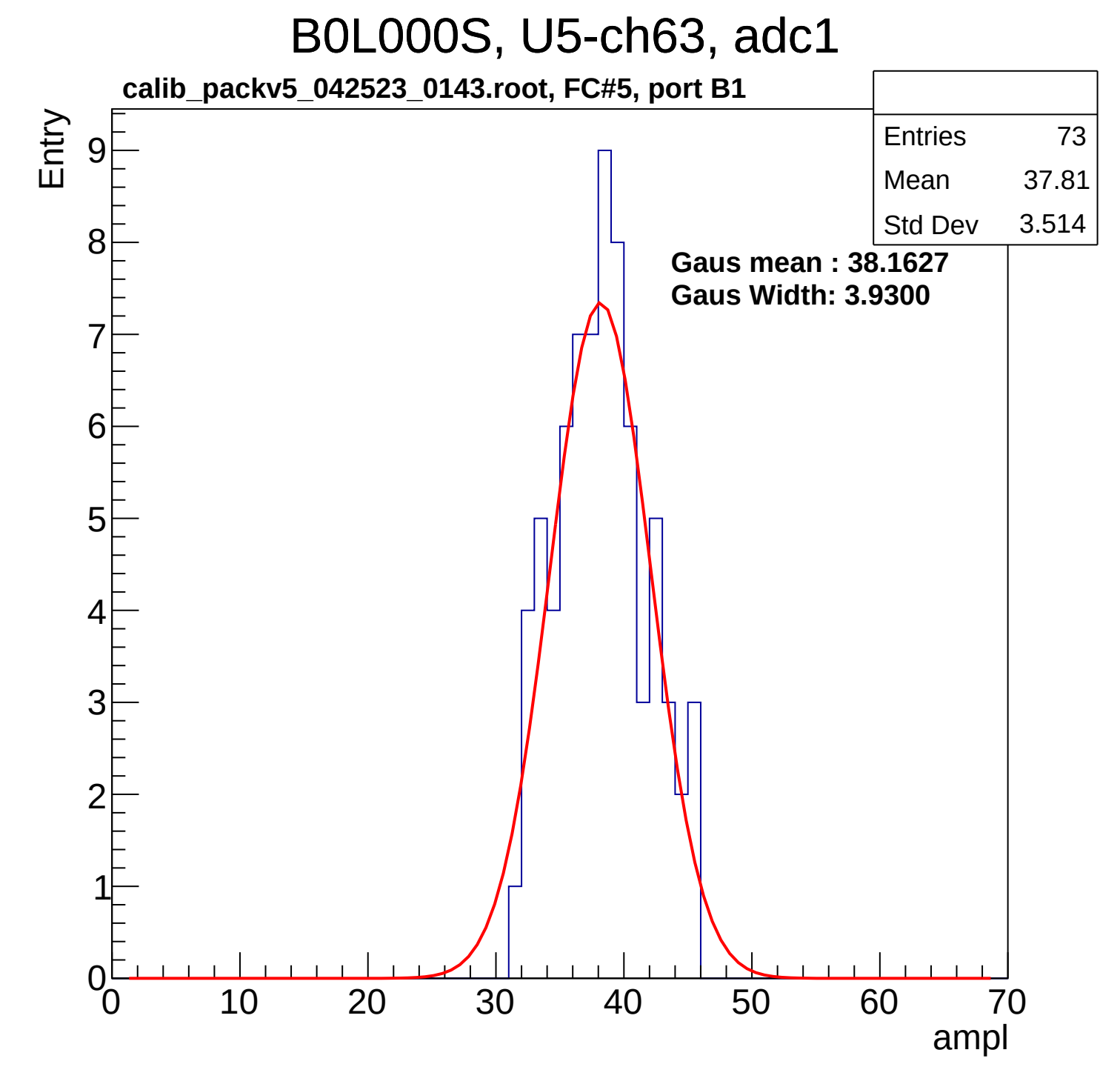

B0L000S, U5-ch62, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 56.35
Std Dev 2.854
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

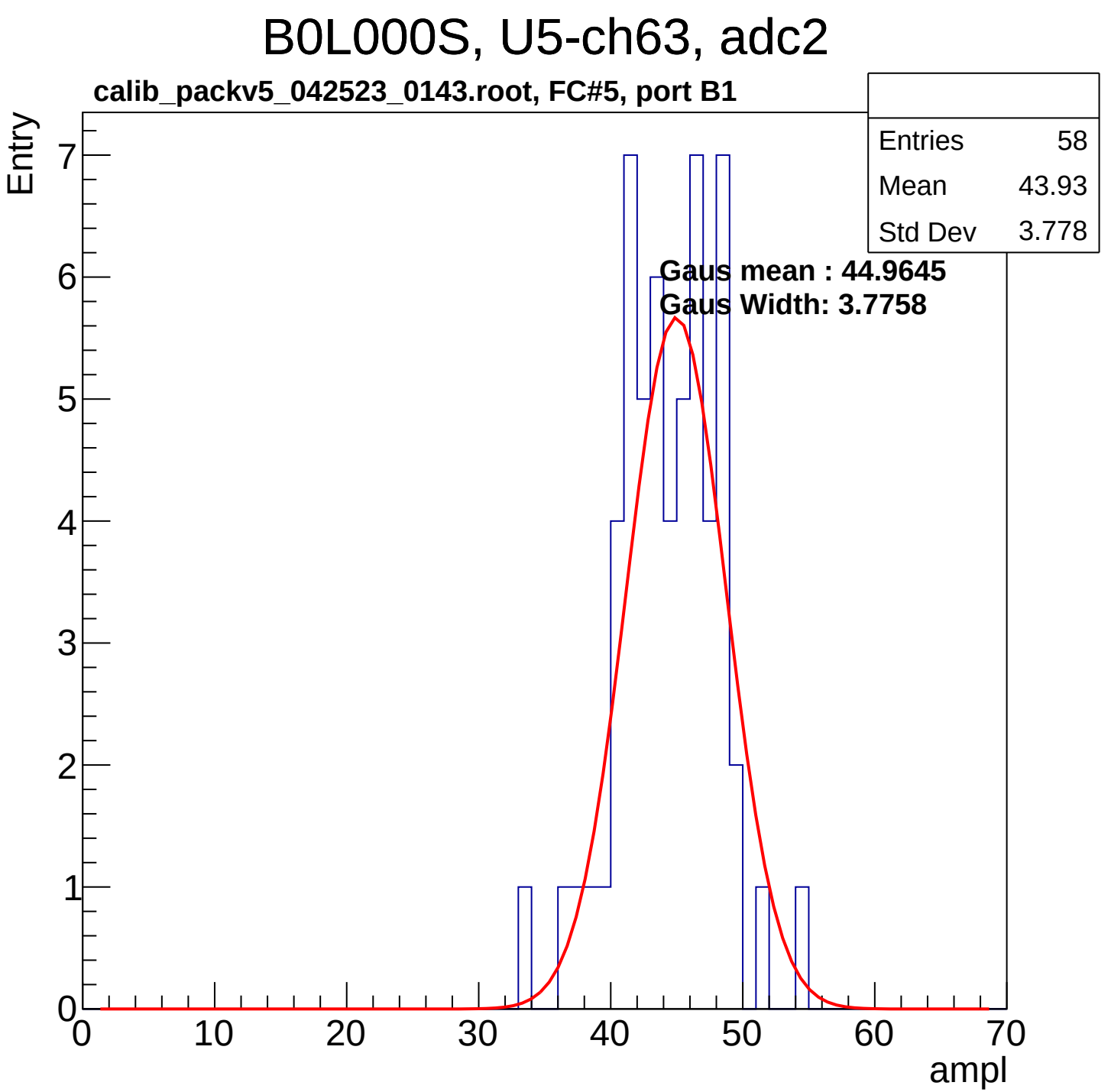

B0L000S, U5-ch62, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.12
Std Dev 9.369
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

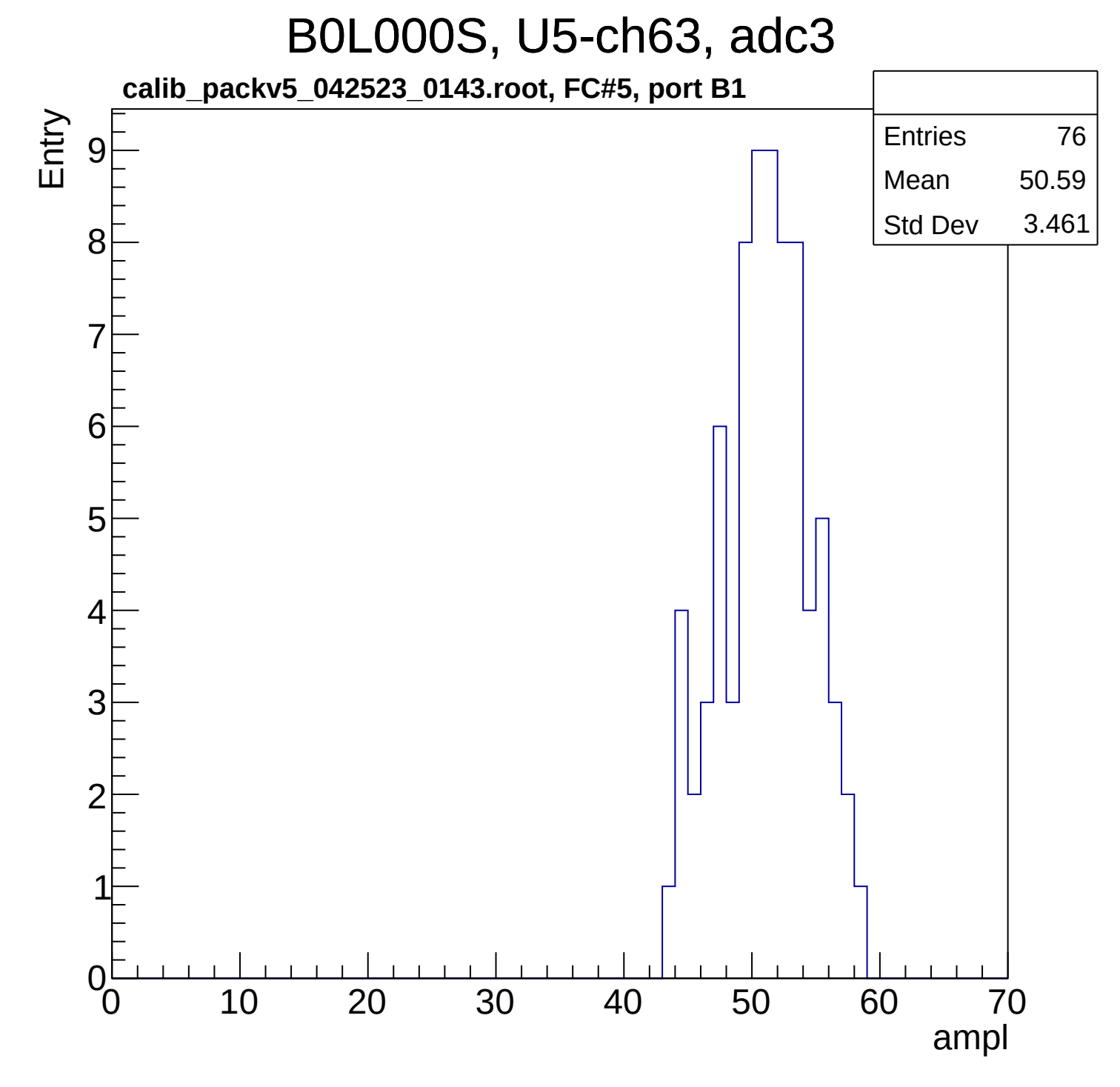

B0L000S, U5-ch62, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch62, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch63, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.32
Std Dev 6.904
Gaus mean : 30.3587
Gaus Width: 4.2271
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch63, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 37.81
Std Dev 3.514
Gaus mean : 38.1627
Gaus Width: 3.9300
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch63, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.93
Std Dev 3.778
Gaus mean : 44.9645
Gaus Width: 3.7758
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch63, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 50.59
Std Dev 3.461
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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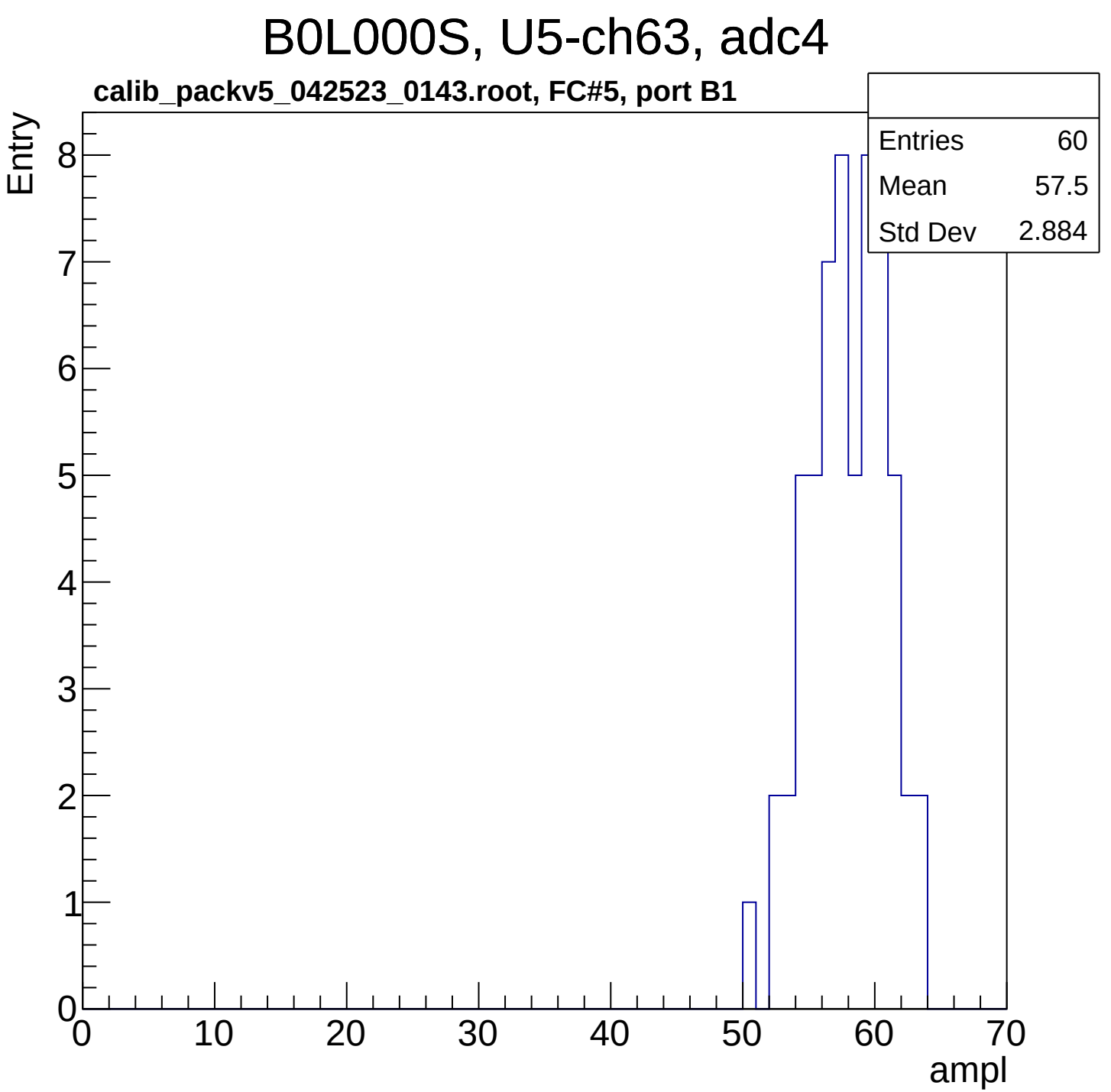
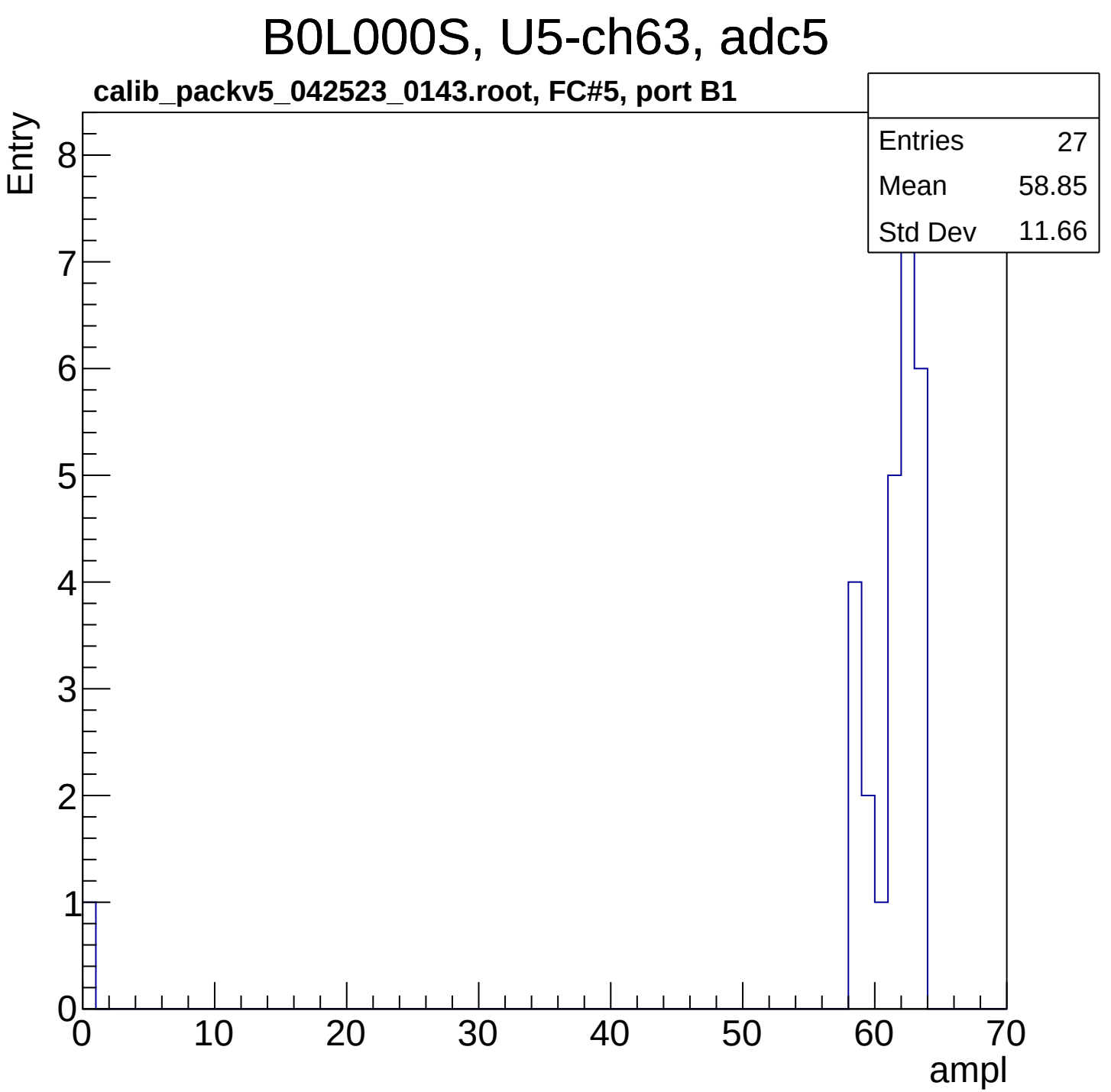
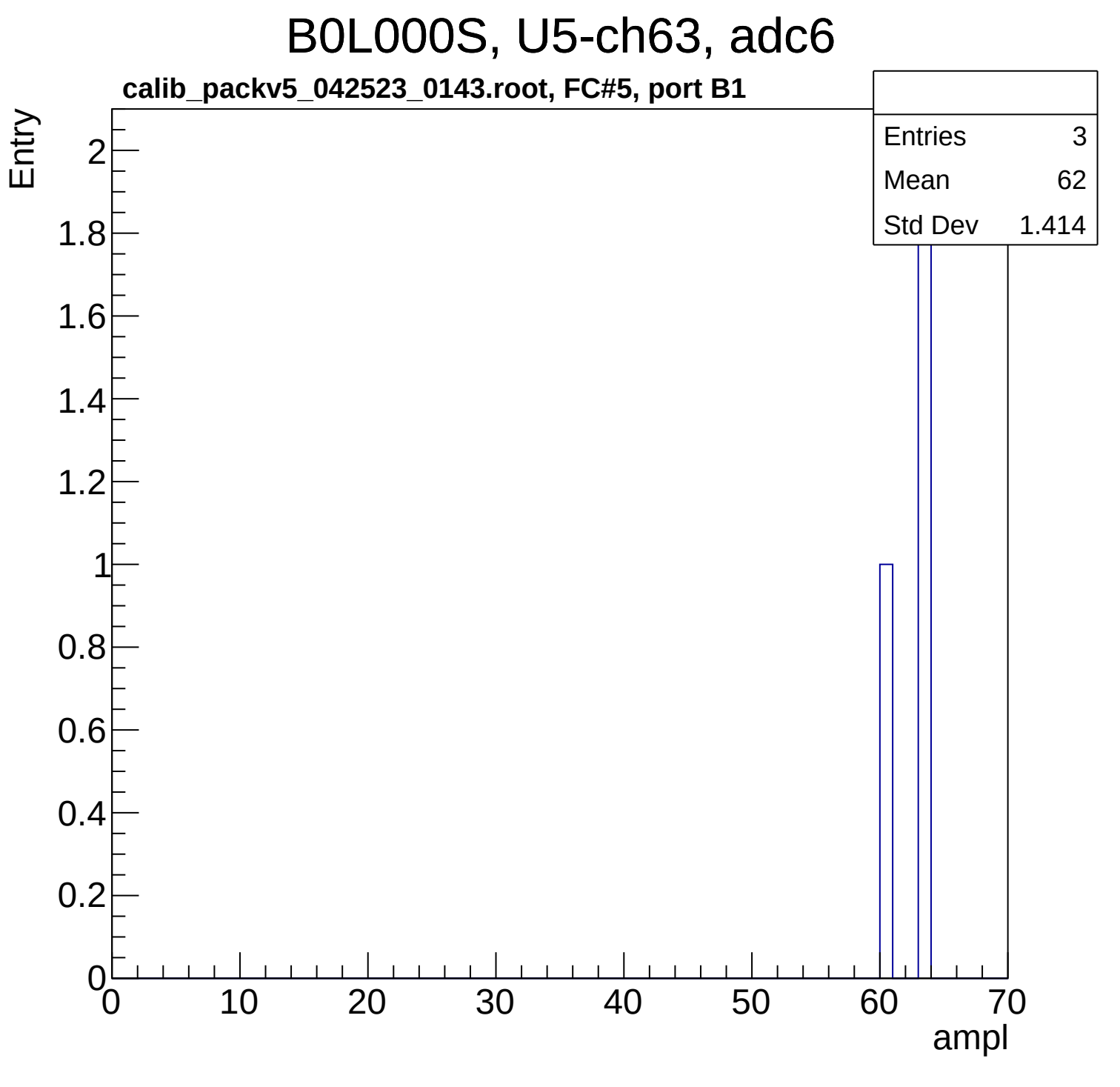
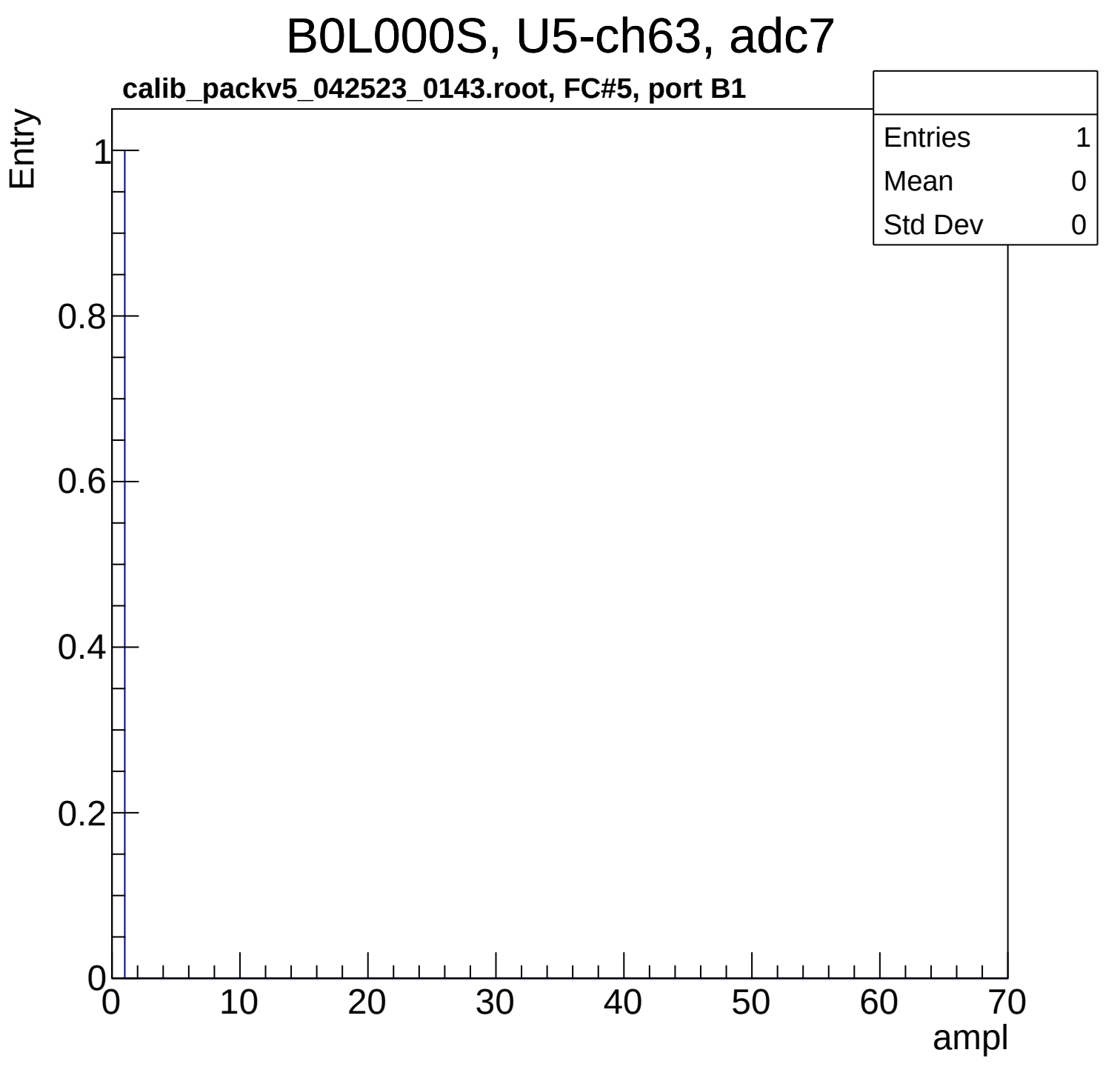
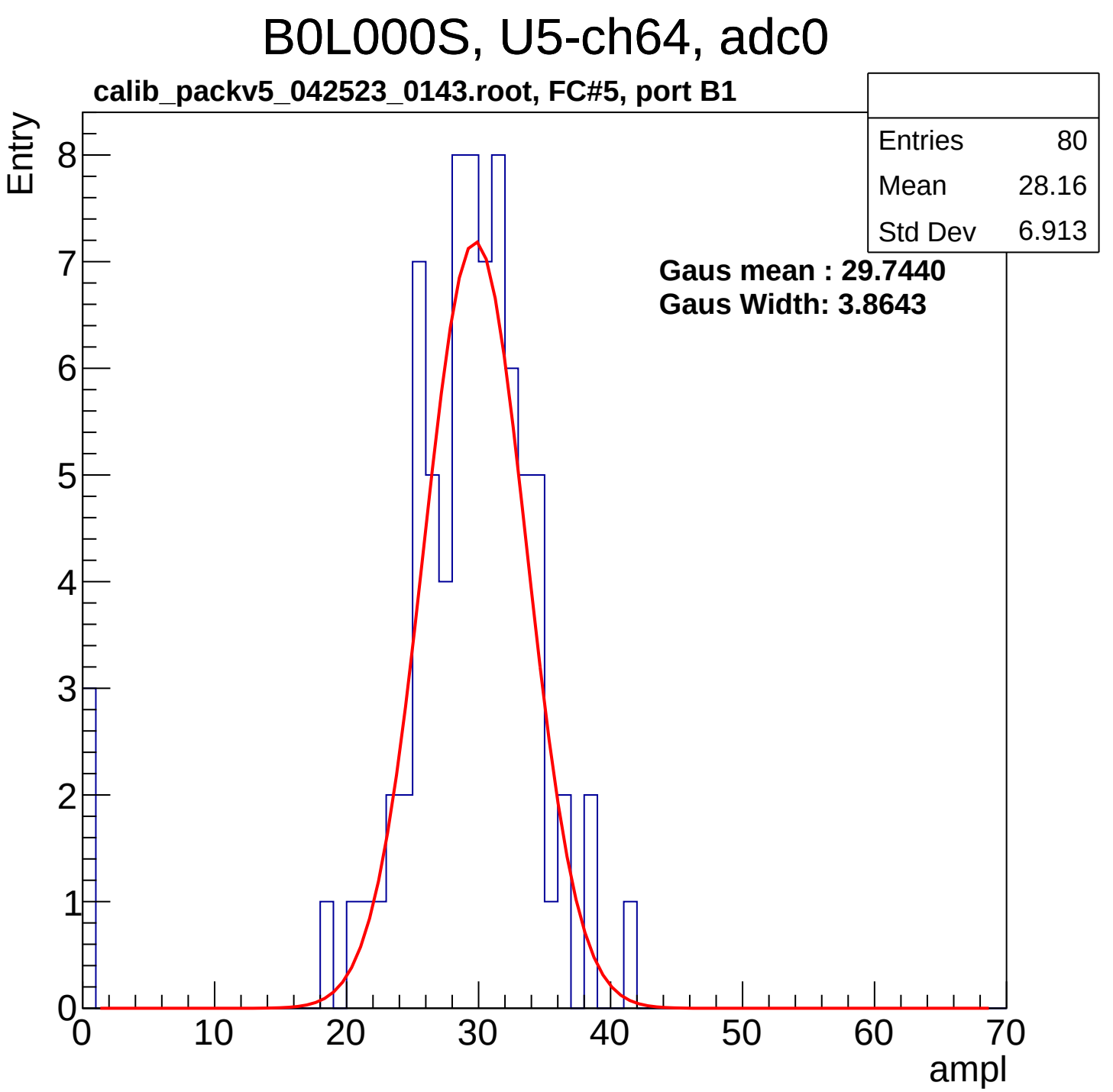
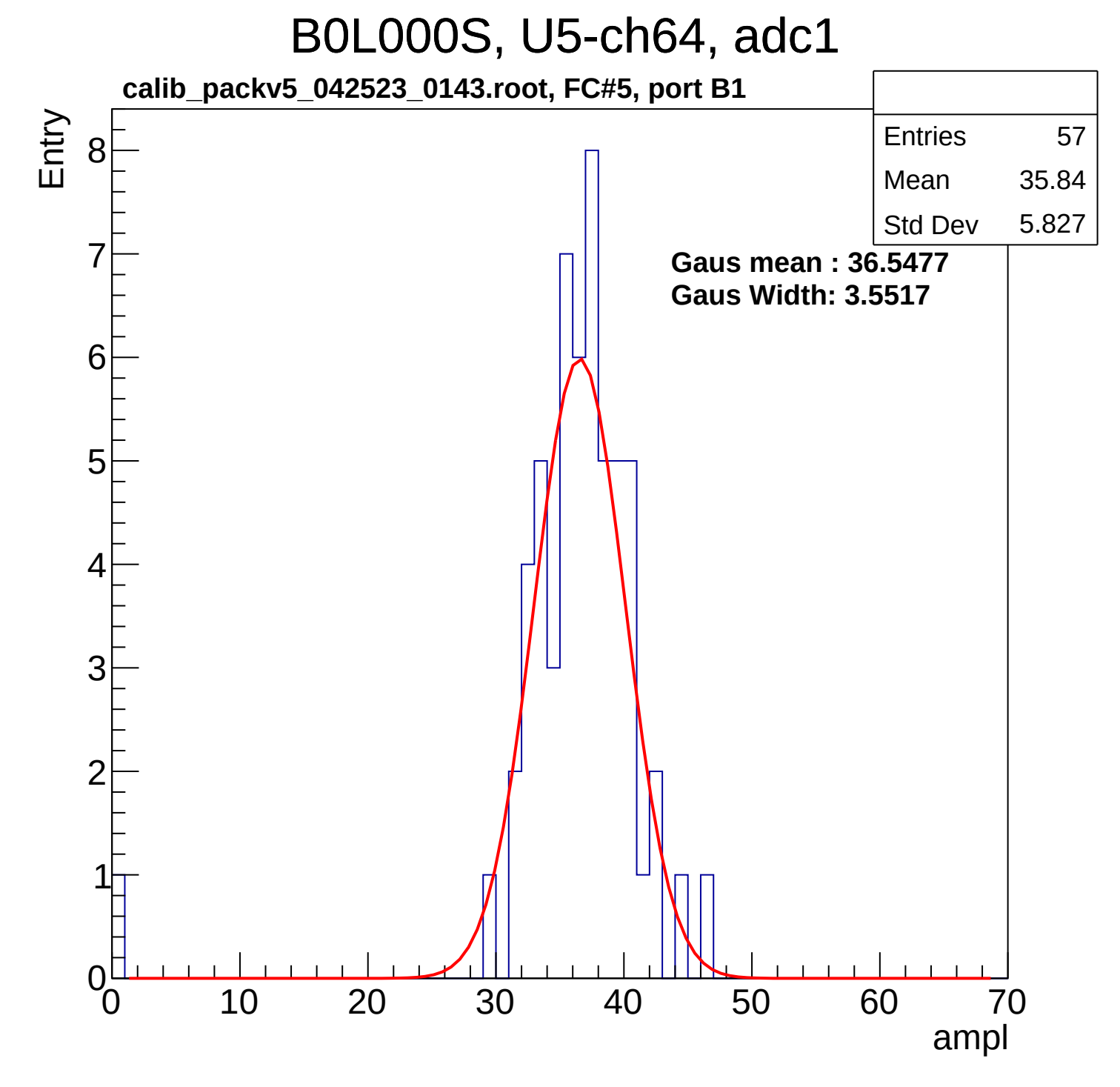

B0L000S, U5-ch63, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.5
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

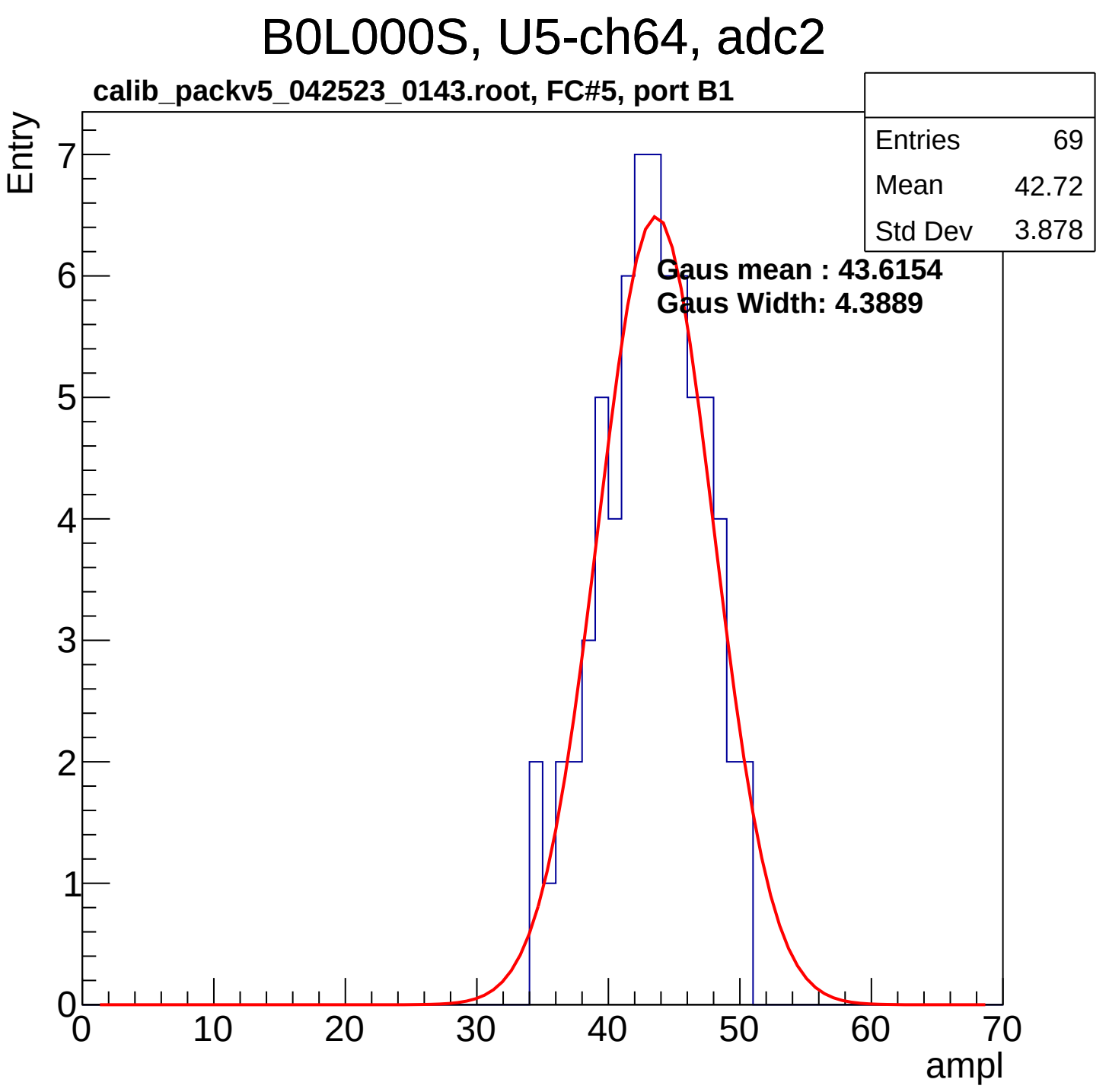

B0L000S, U5-ch63, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.85
Std Dev 11.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch63, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch63, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch64, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 28.16
Std Dev 6.913
Gaus mean : 29.7440
Gaus Width: 3.8643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch64, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 35.84
Std Dev 5.827
Gaus mean : 36.5477
Gaus Width: 3.5517
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch64, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 42.72
Std Dev 3.878
Gaus mean : 43.6154
Gaus Width: 4.3889
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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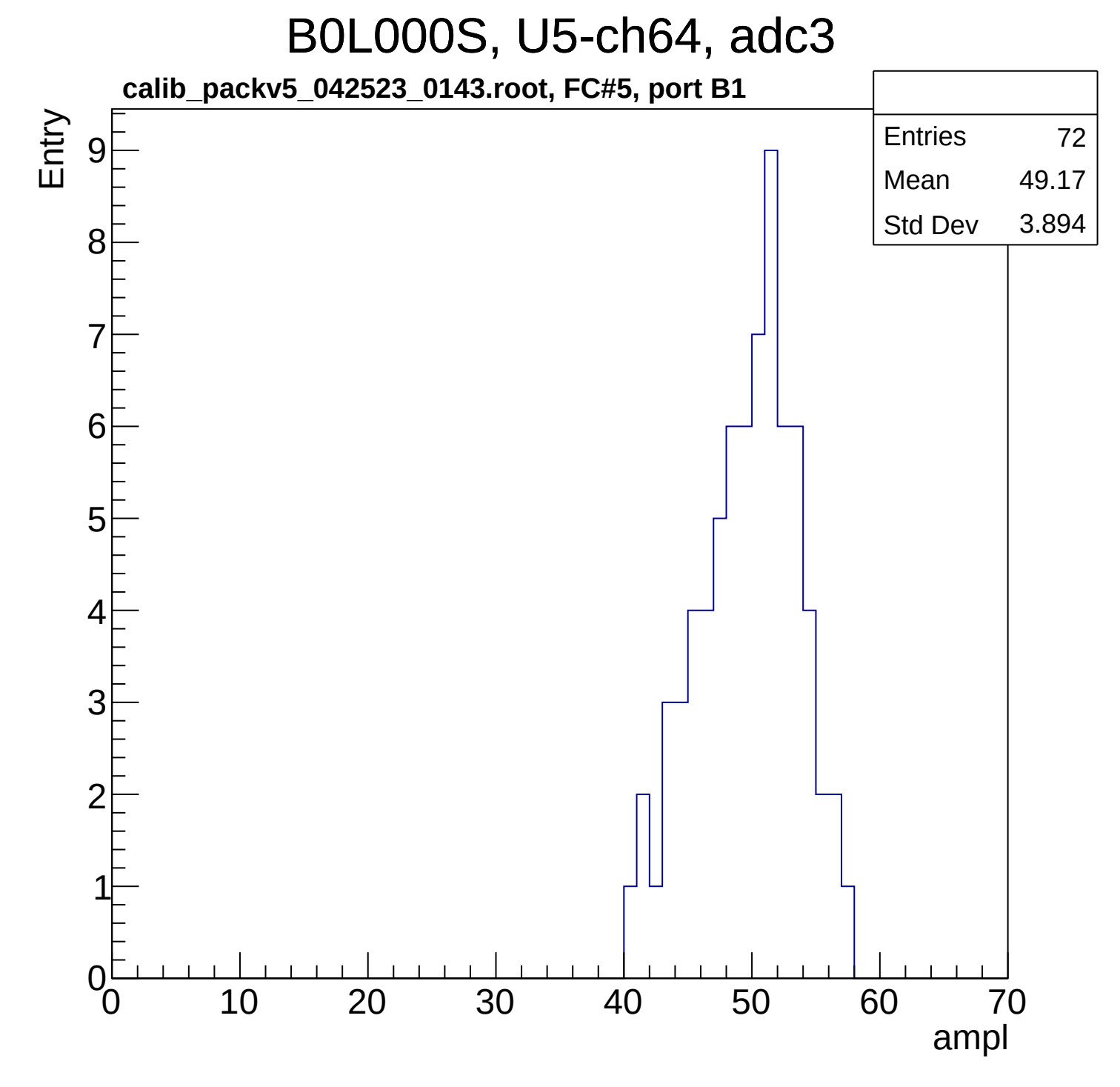
B0L000S, U5-ch64, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.17
Std Dev 3.894
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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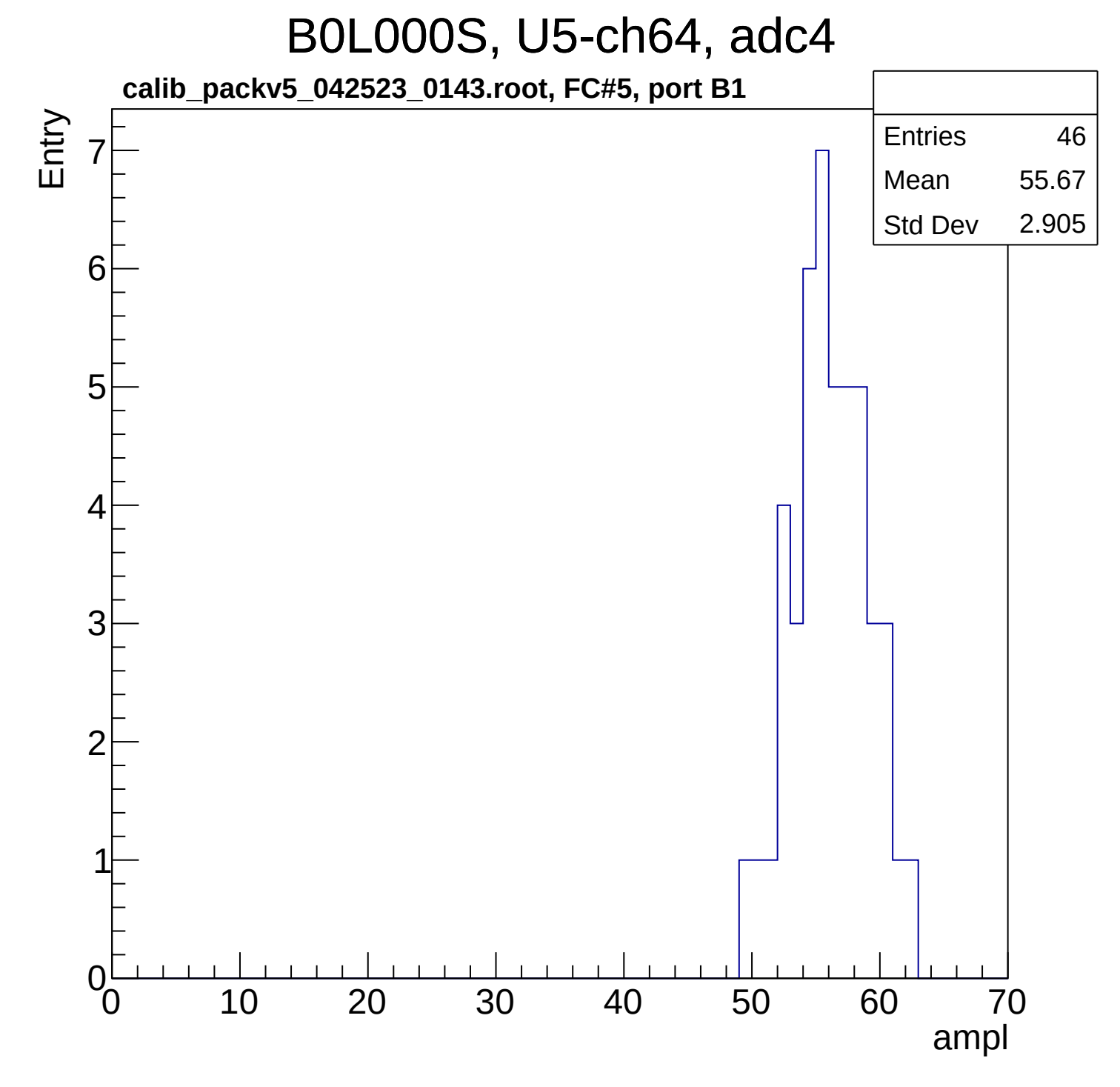
B0L000S, U5-ch64, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 55.67
Std Dev 2.905
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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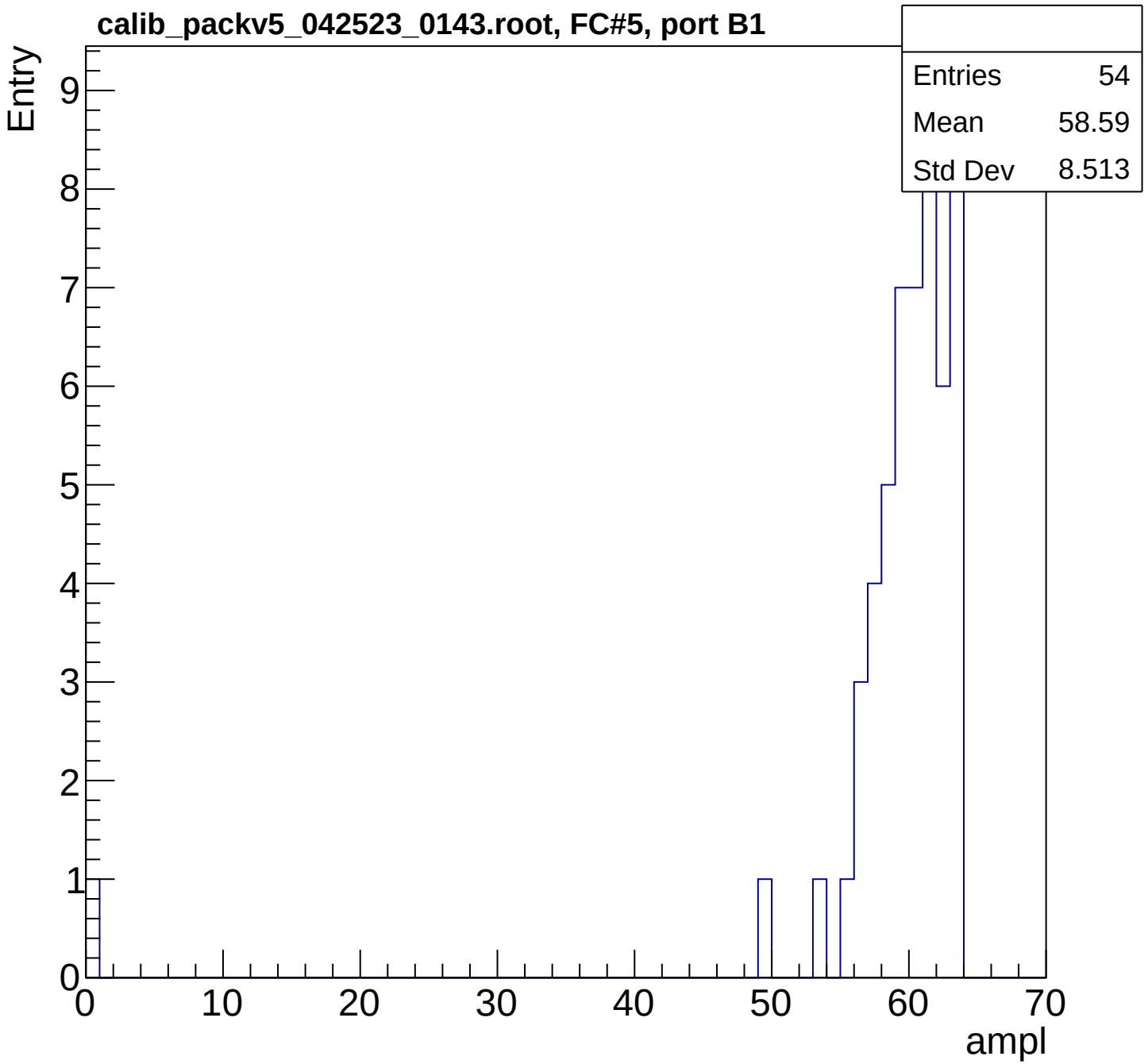
B0L000S, U5-ch64, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.59
Std Dev 8.513
Entry
ampl
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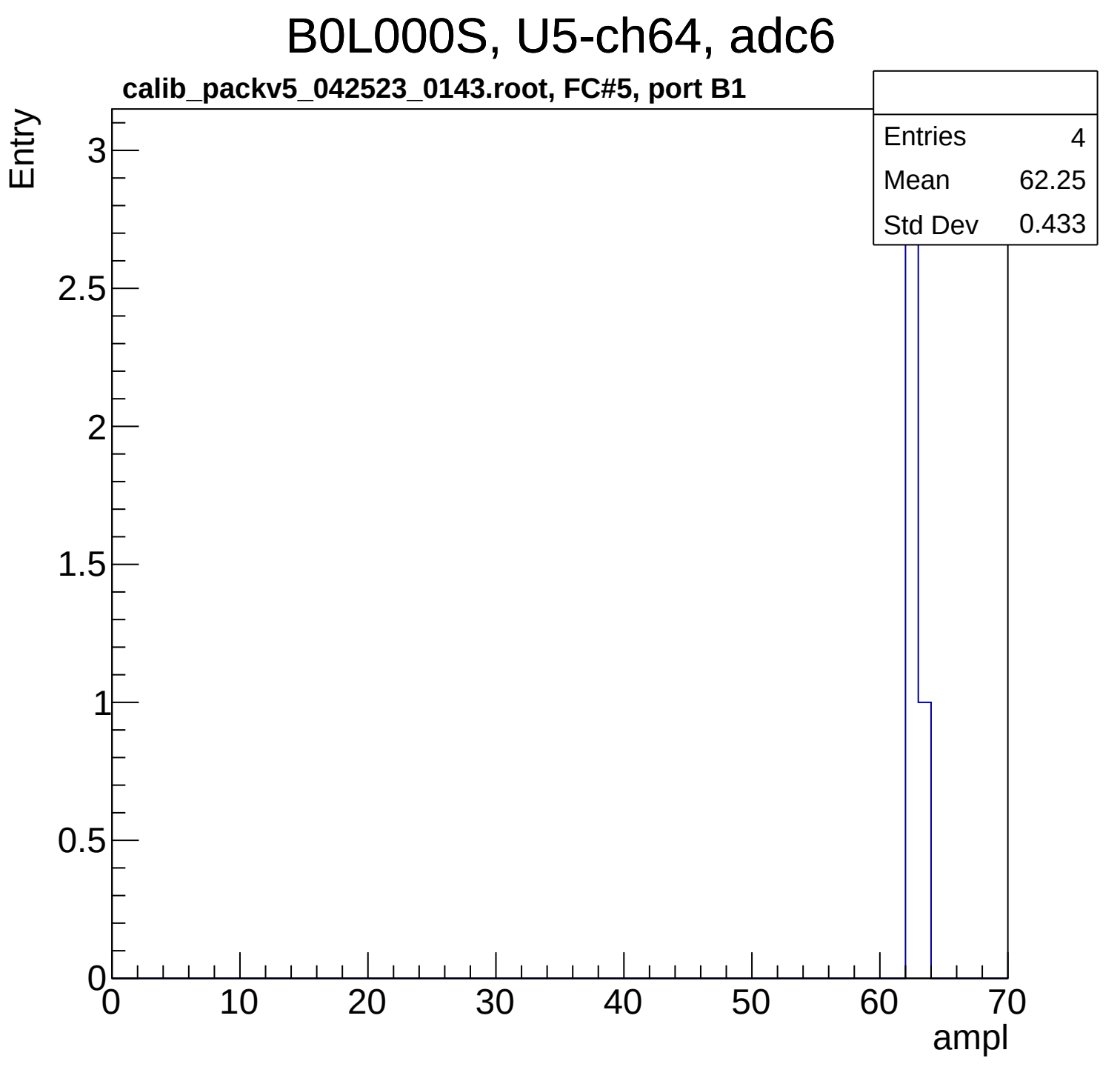
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch64, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
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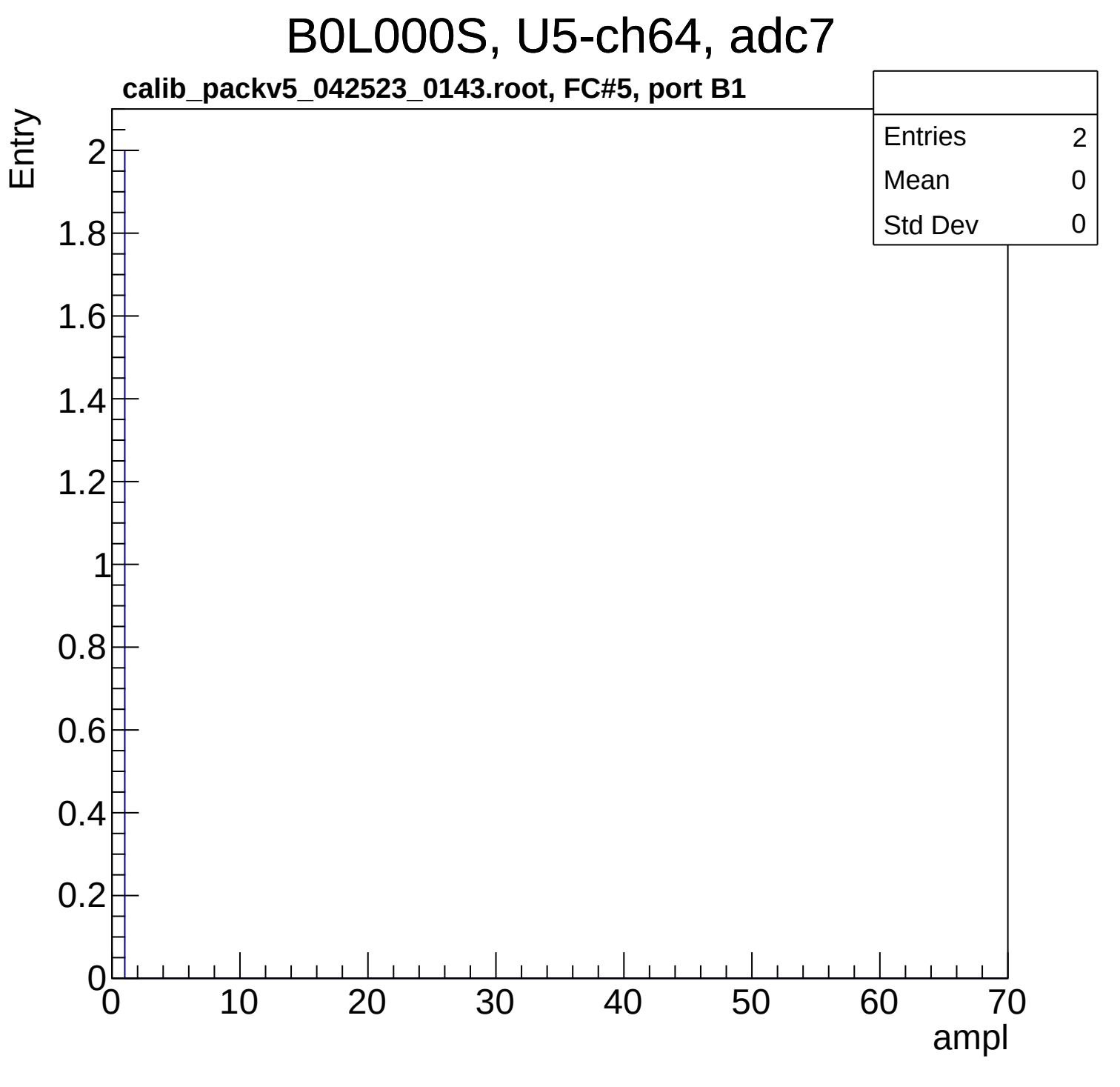
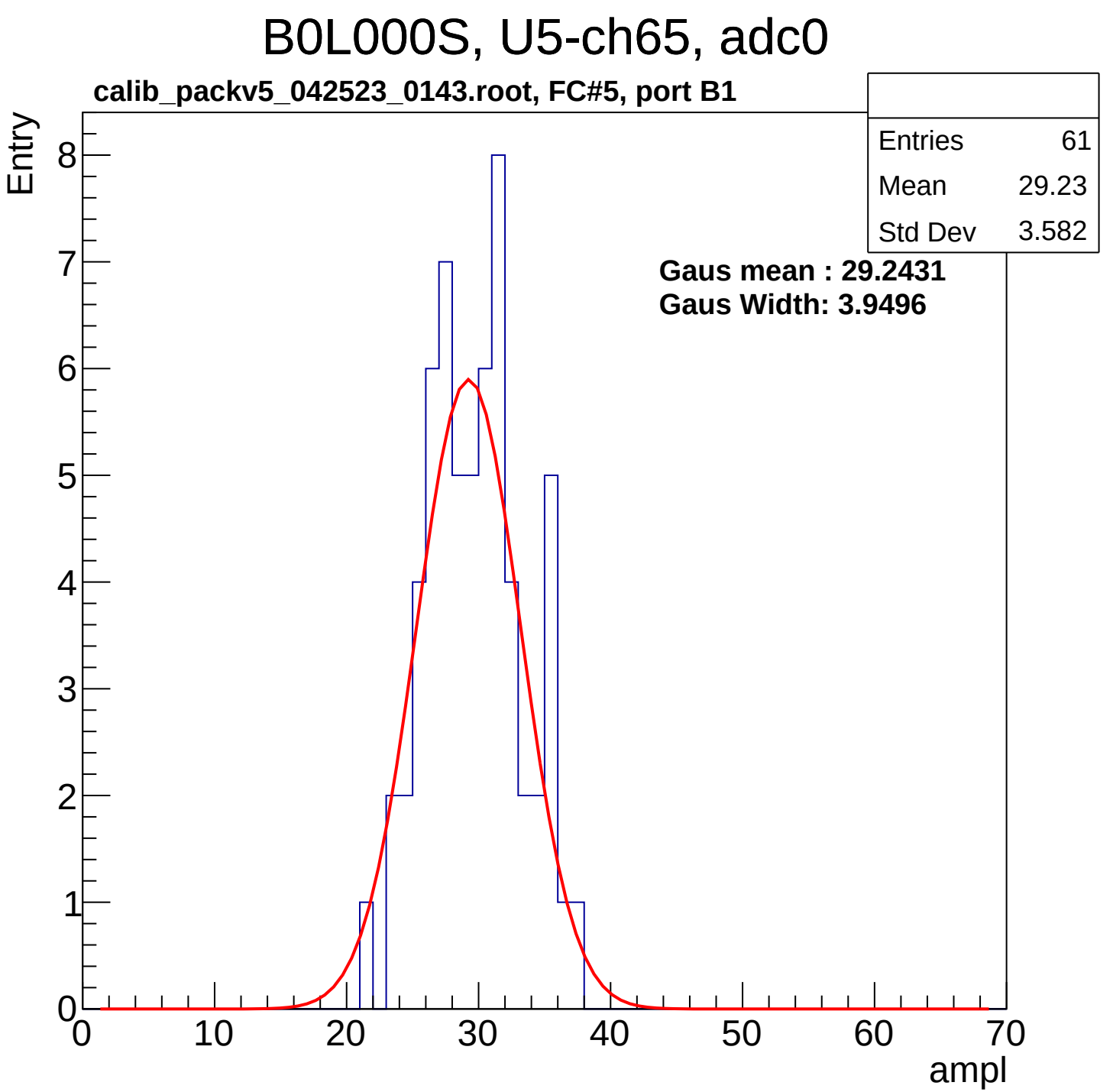
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch64, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch65, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 29.23
Std Dev 3.582
Gaus mean : 29.2431
Gaus Width: 3.9496
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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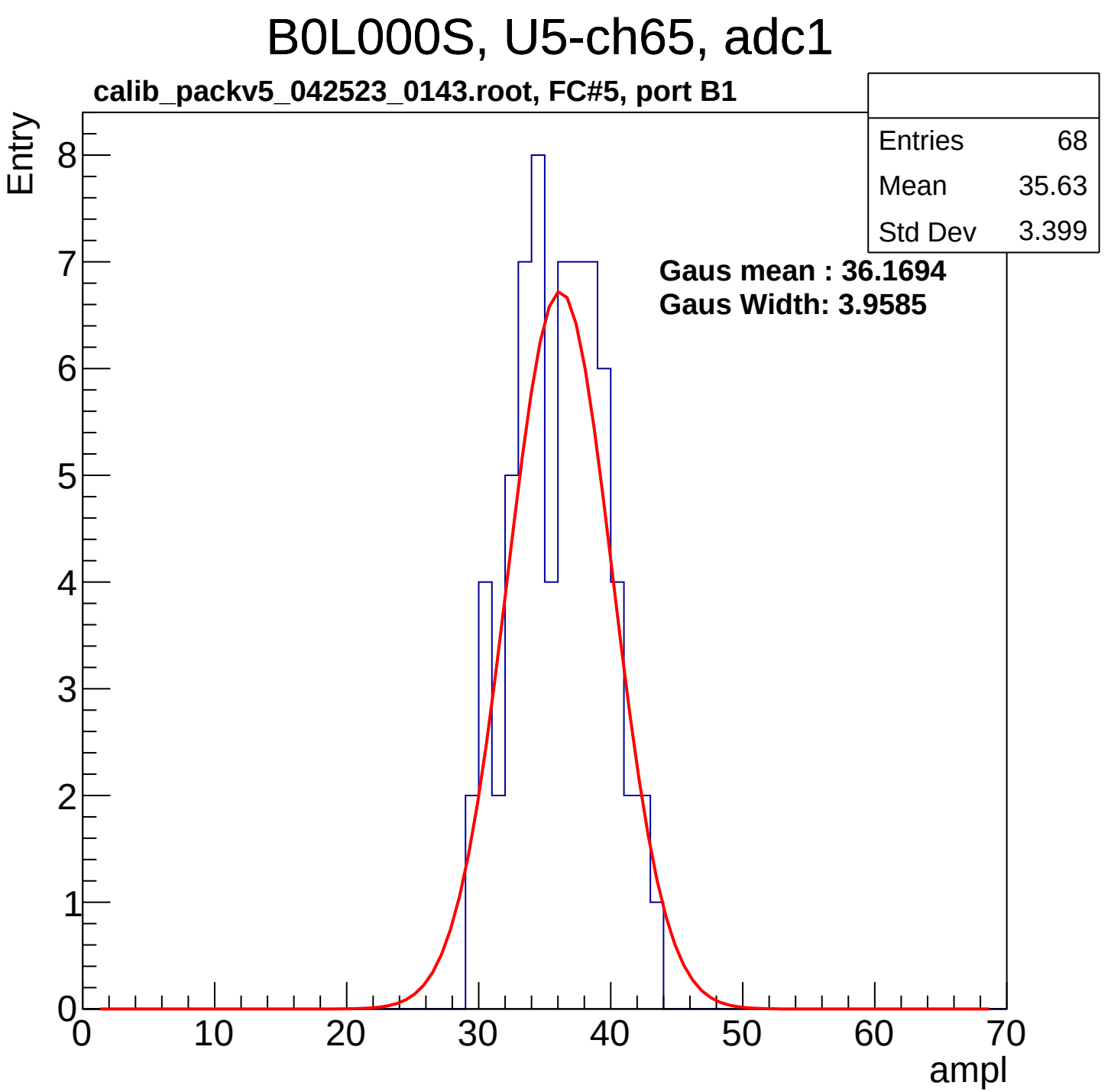
B0L000S, U5-ch65, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 35.63
Std Dev 3.399
Gaus mean : 36.1694
Gaus Width: 3.9585
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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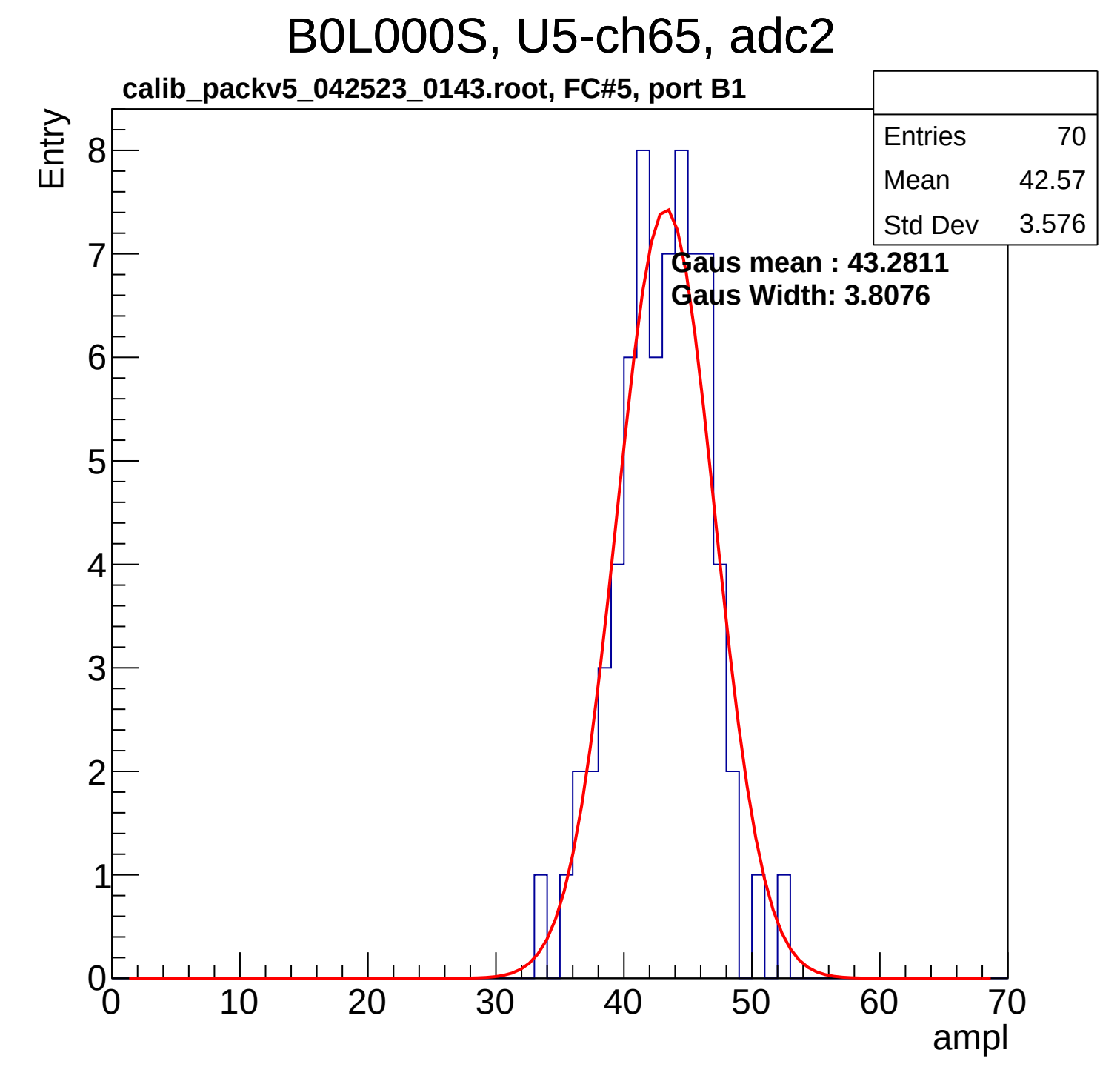
B0L000S, U5-ch65, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 42.57
Std Dev 3.576
Gaus mean : 43.2811
Gaus Width: 3.8076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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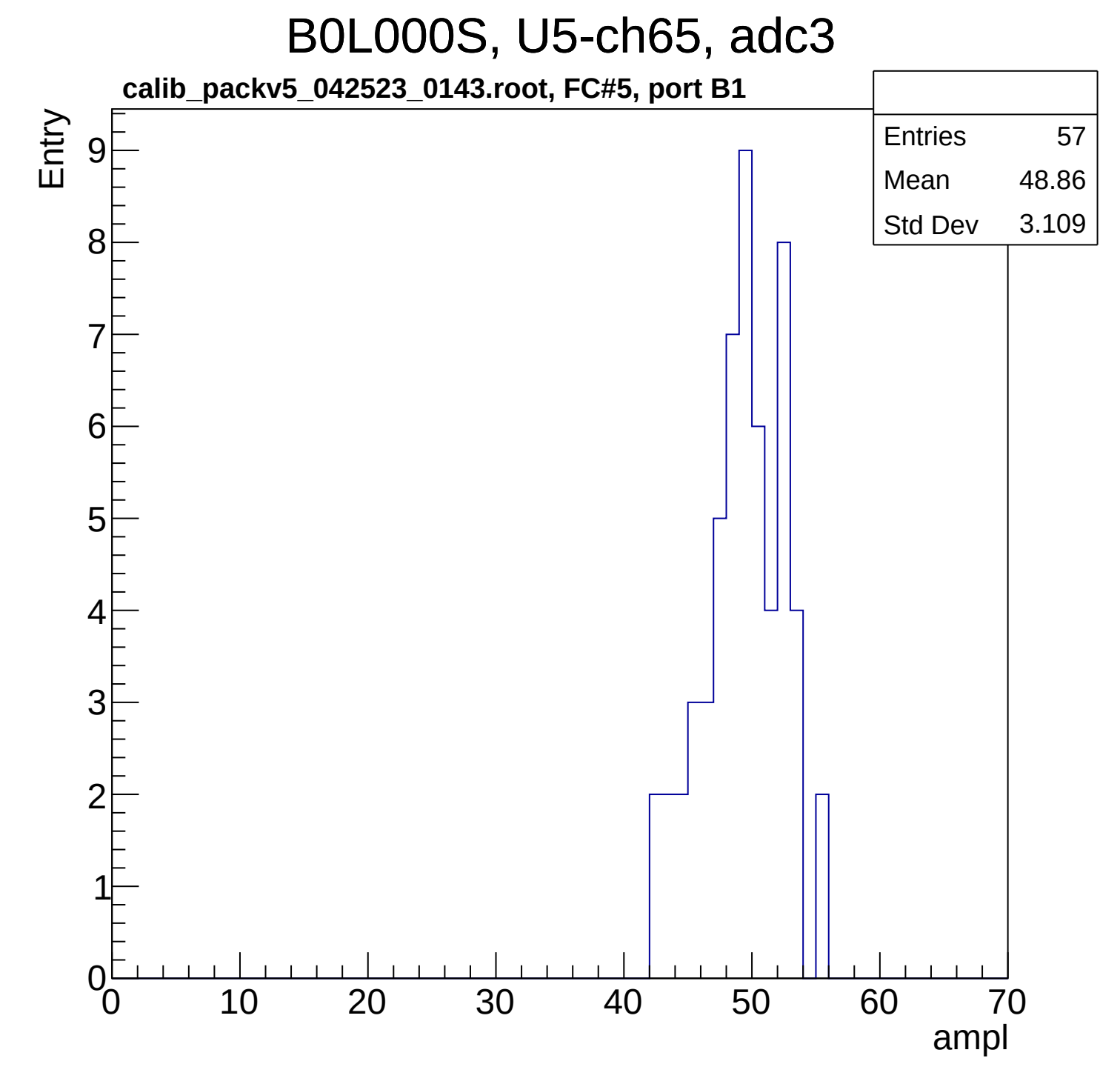
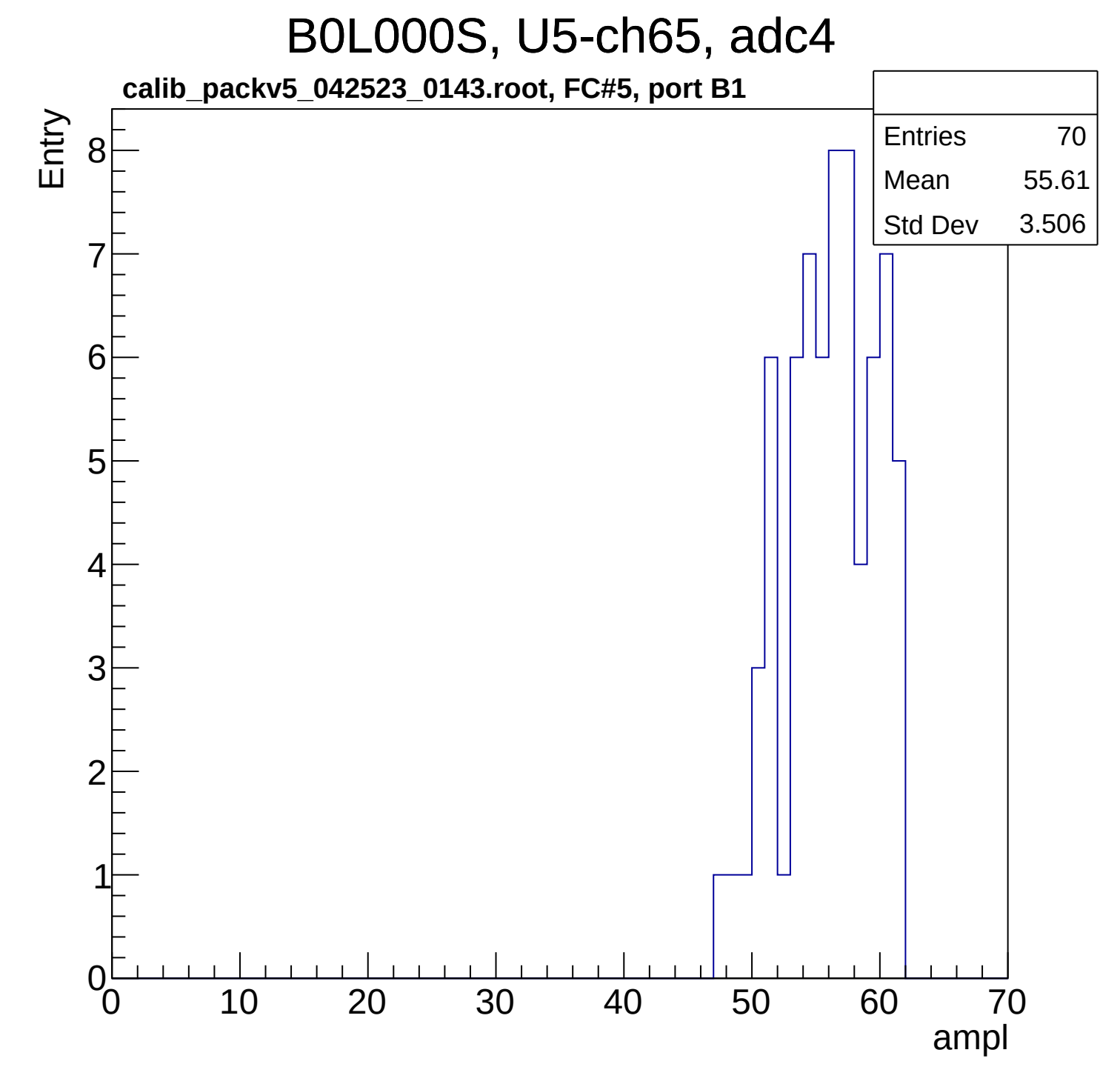
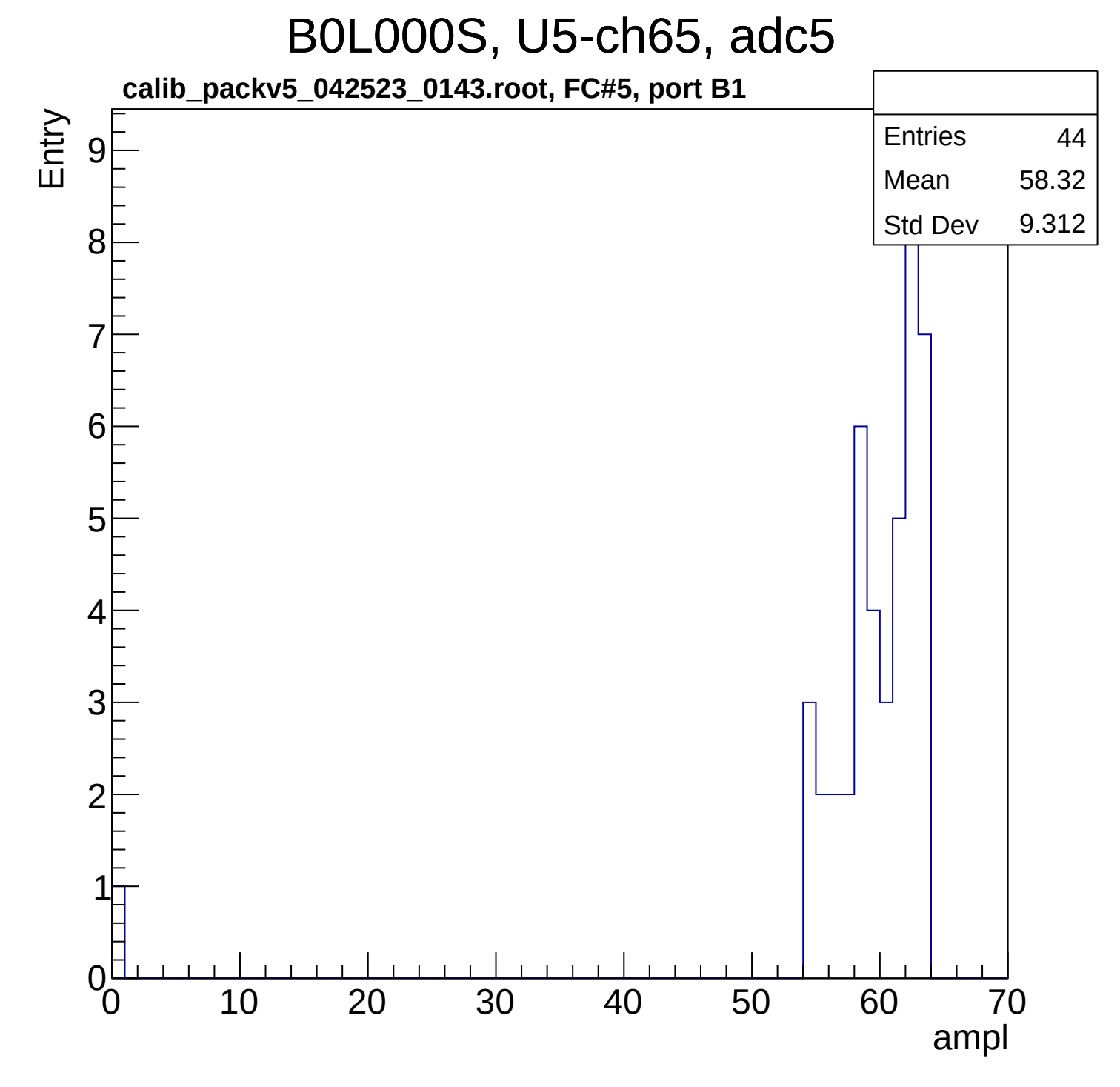
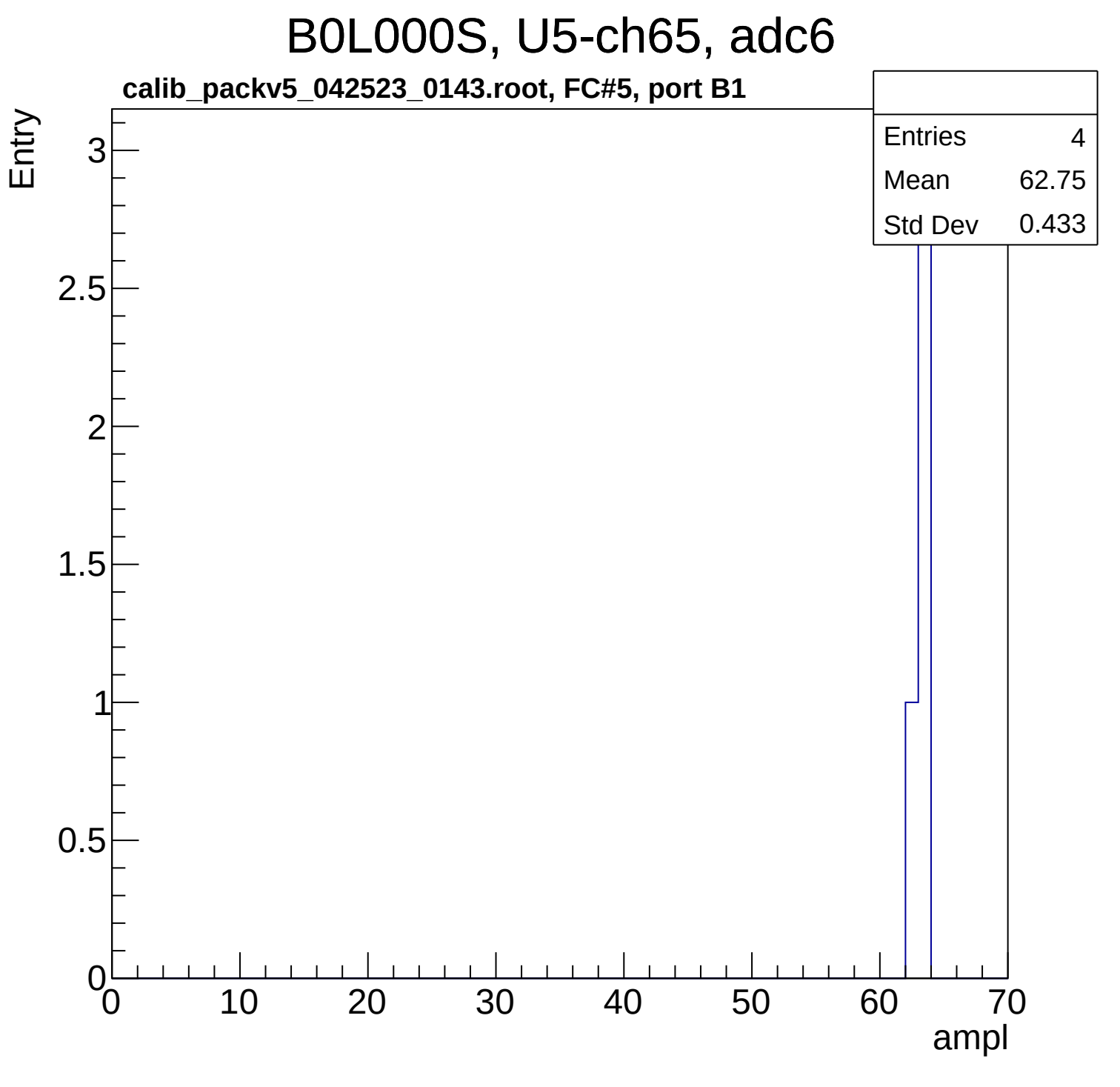
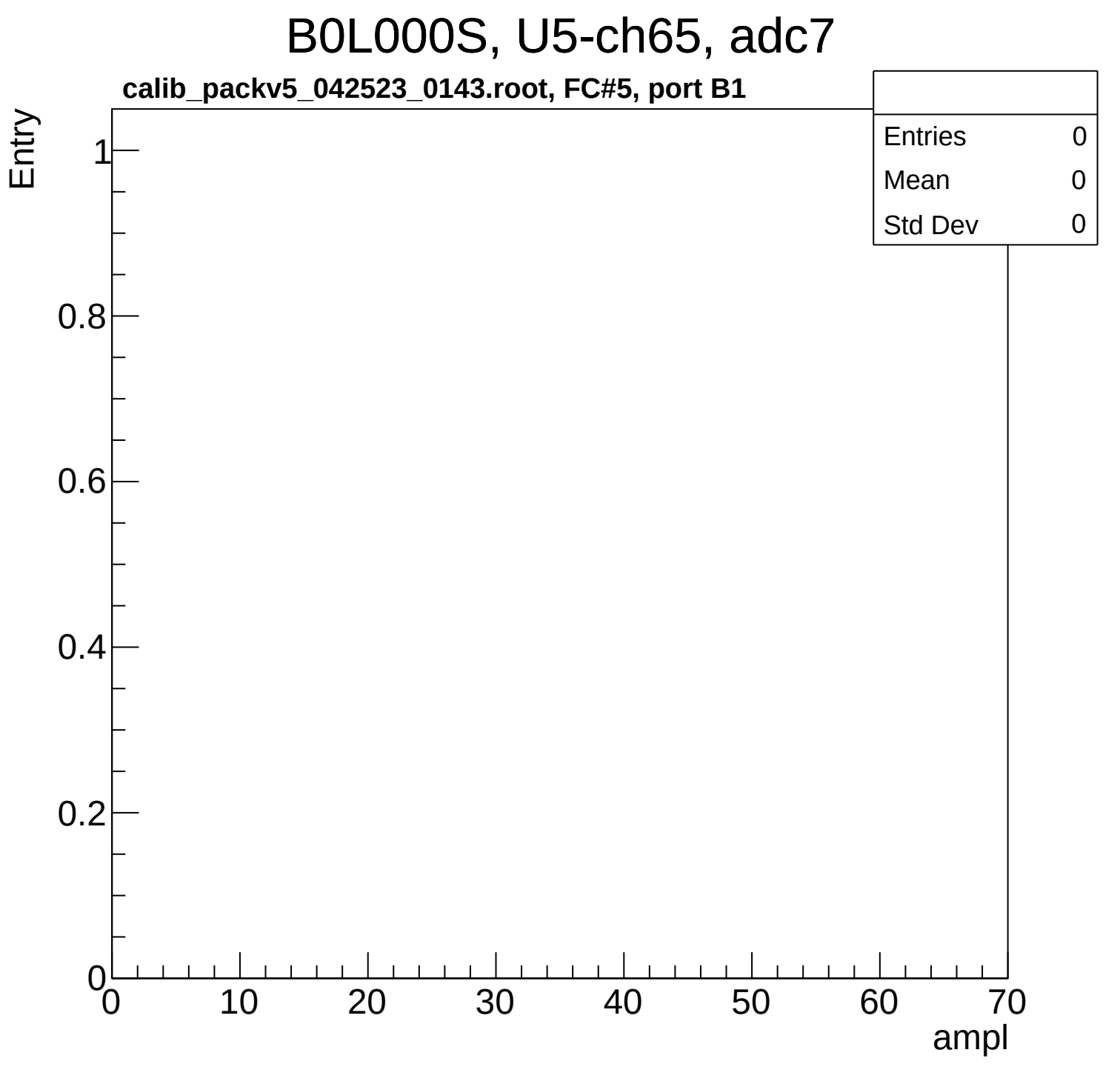
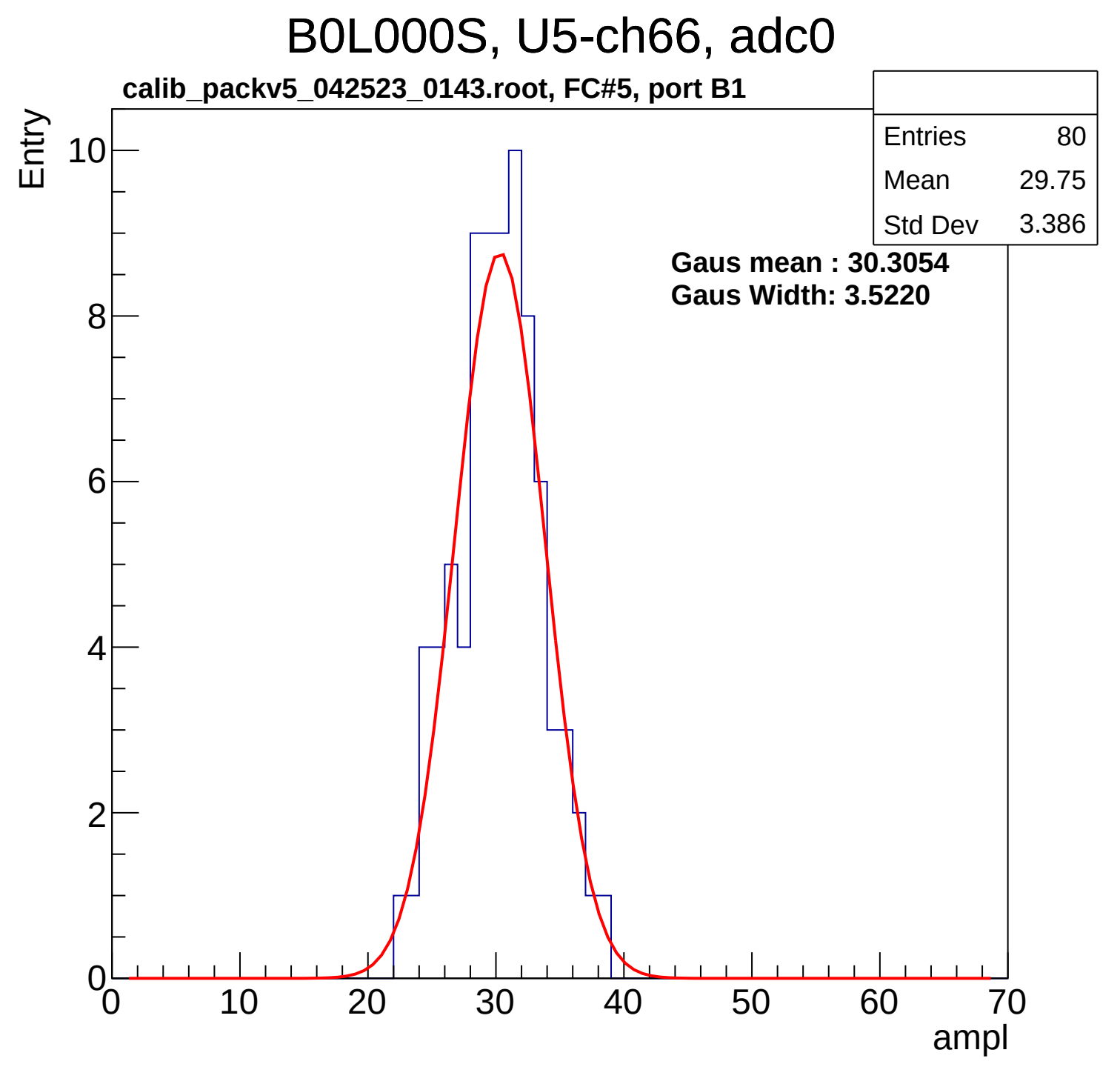

B0L000S, U5-ch65, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 48.86
Std Dev 3.109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

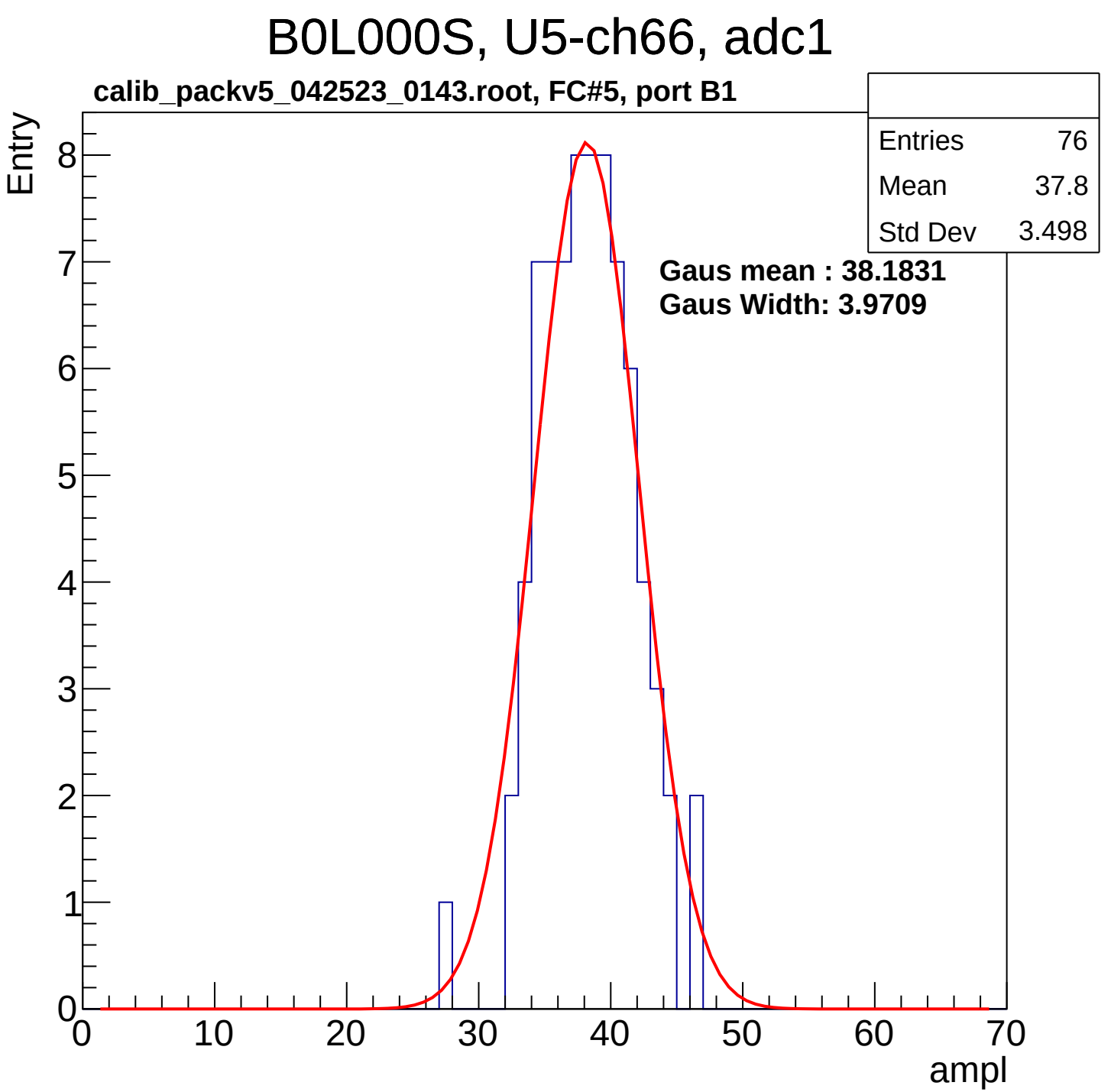

B0L000S, U5-ch65, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 55.61
Std Dev 3.506
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

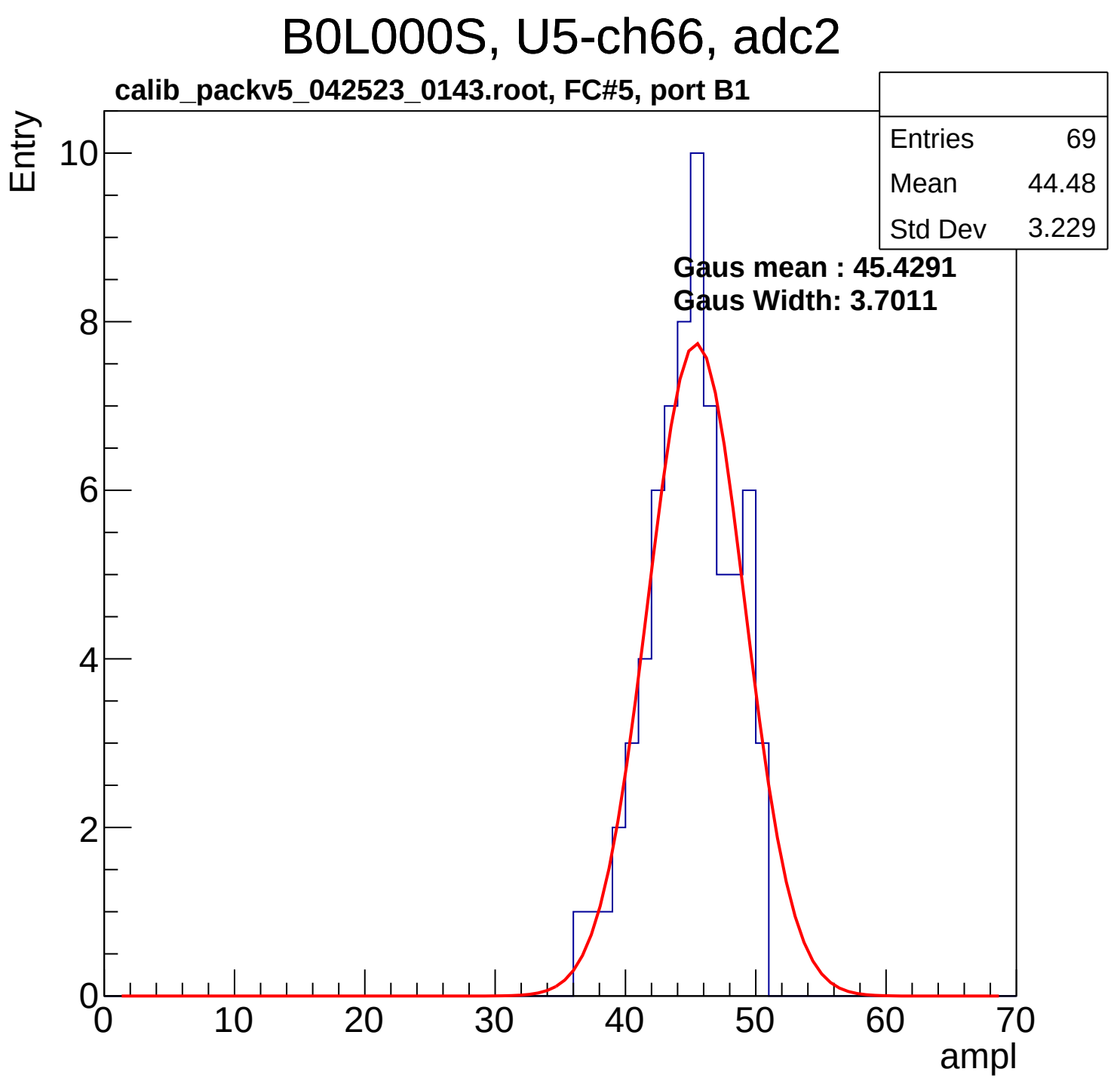

B0L000S, U5-ch65, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.32
Std Dev 9.312
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

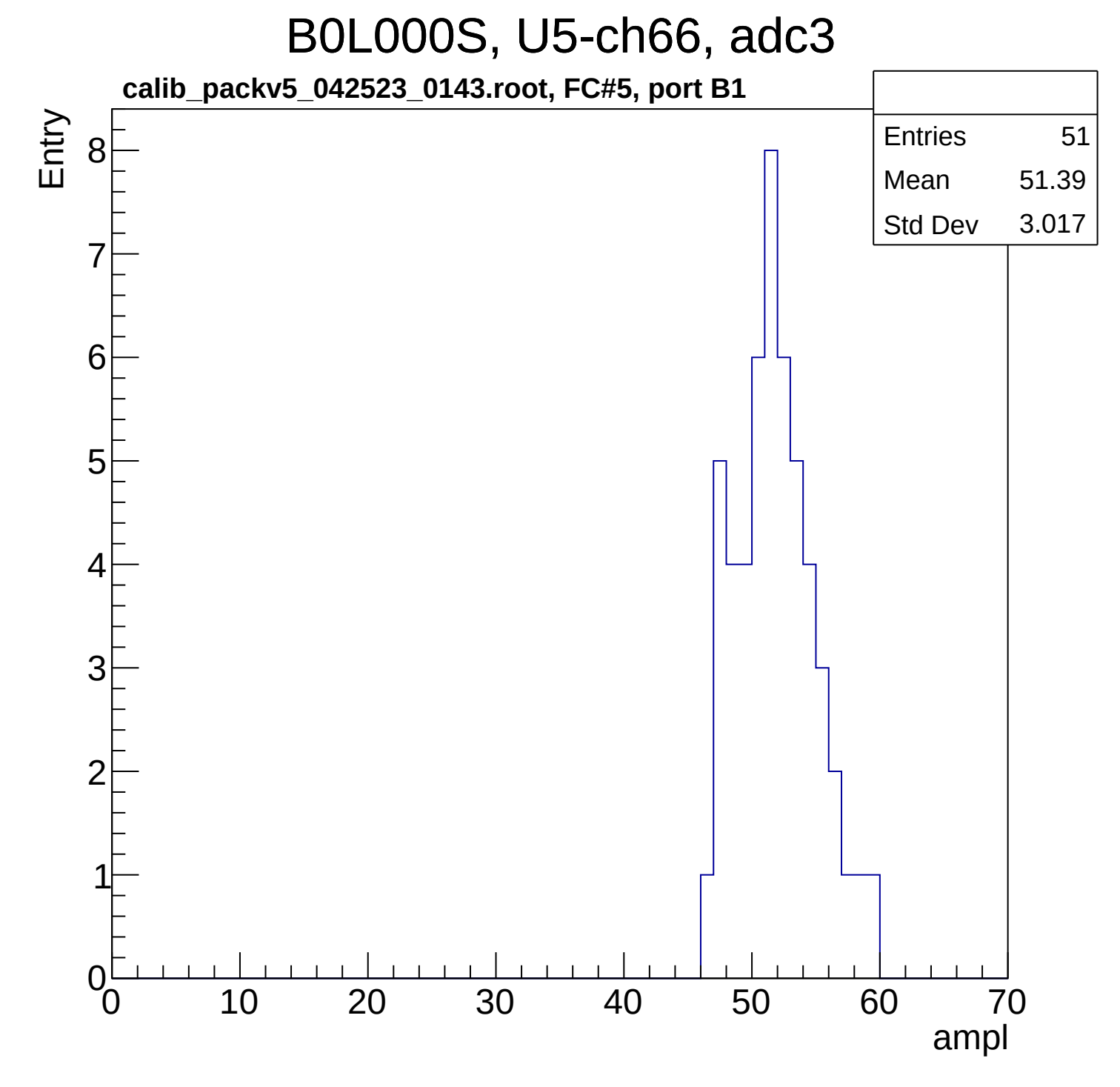

B0L000S, U5-ch65, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

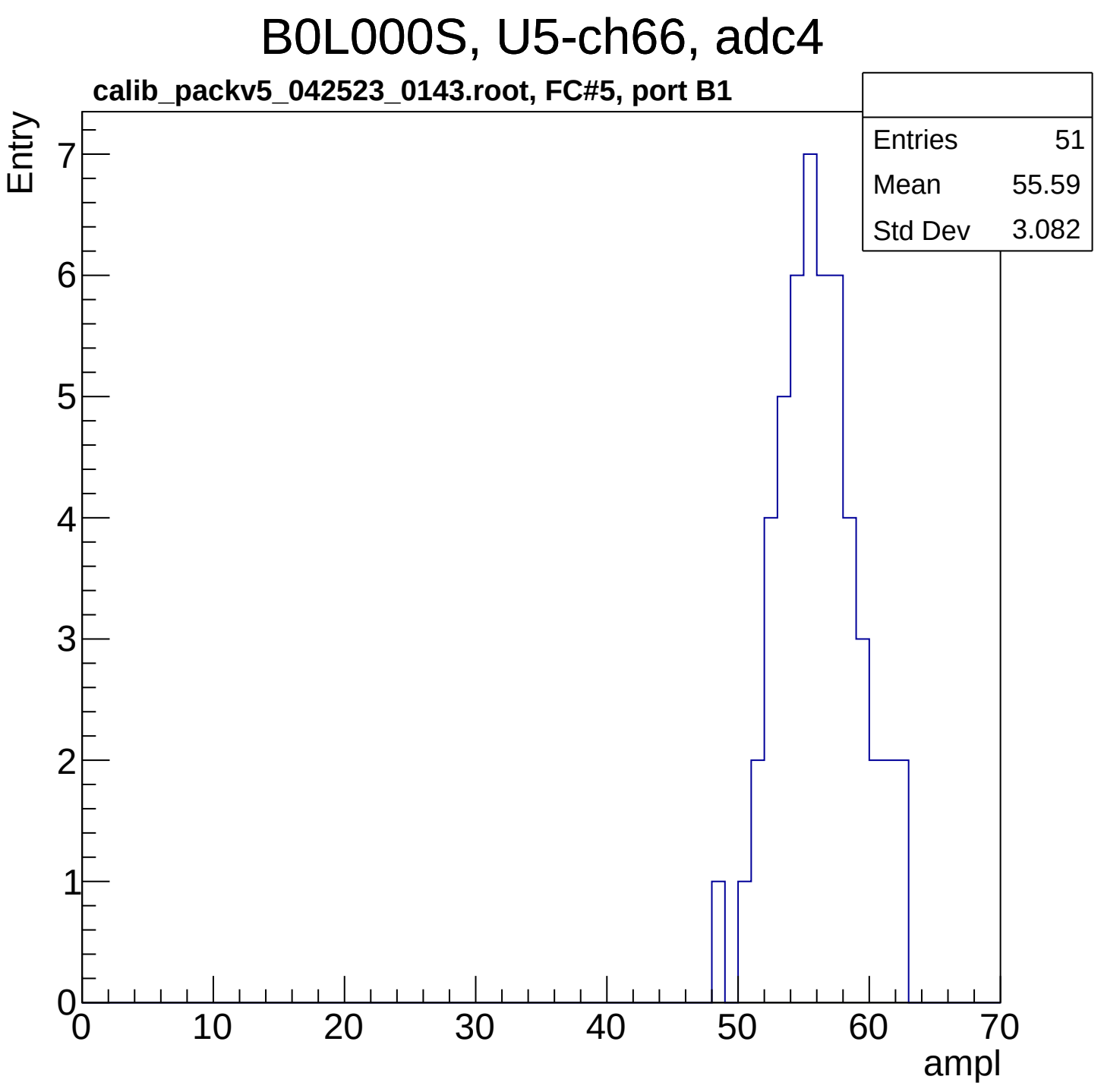

B0L000S, U5-ch65, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

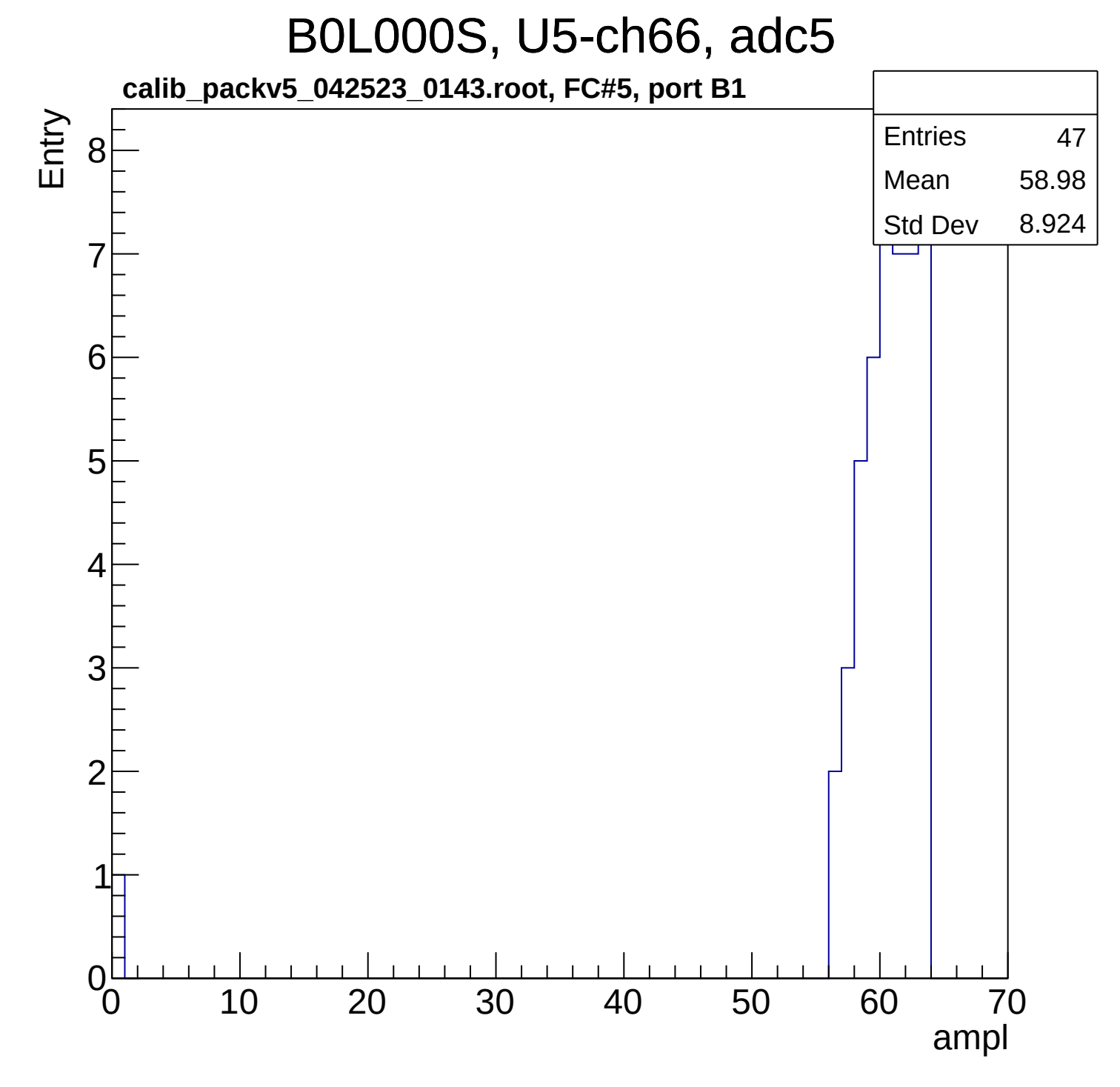

B0L000S, U5-ch66, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 29.75
Std Dev 3.386
Gaus mean : 30.3054
Gaus Width: 3.5220
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

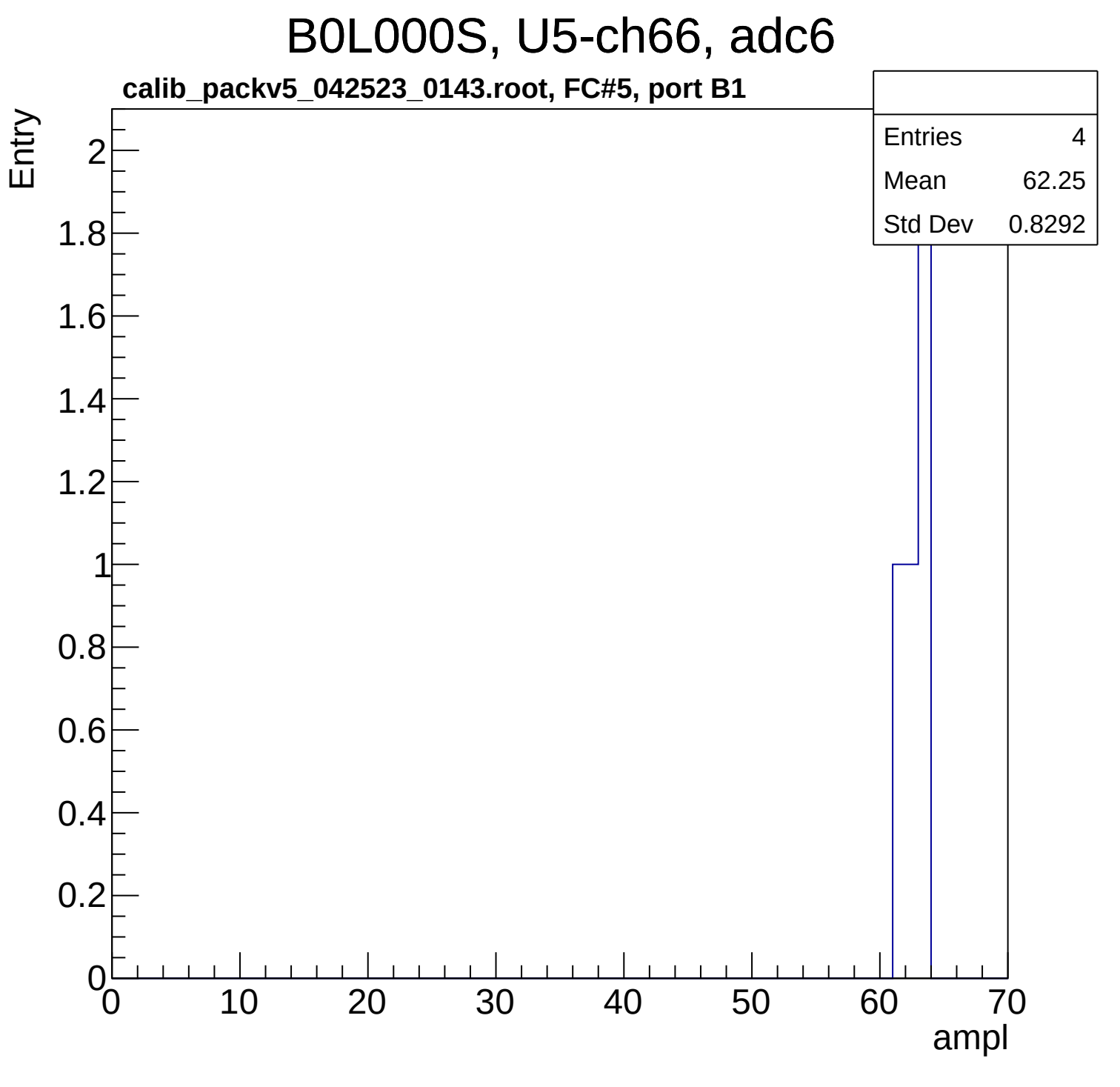

B0L000S, U5-ch66, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 37.8
Std Dev 3.498
Gaus mean : 38.1831
Gaus Width: 3.9709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

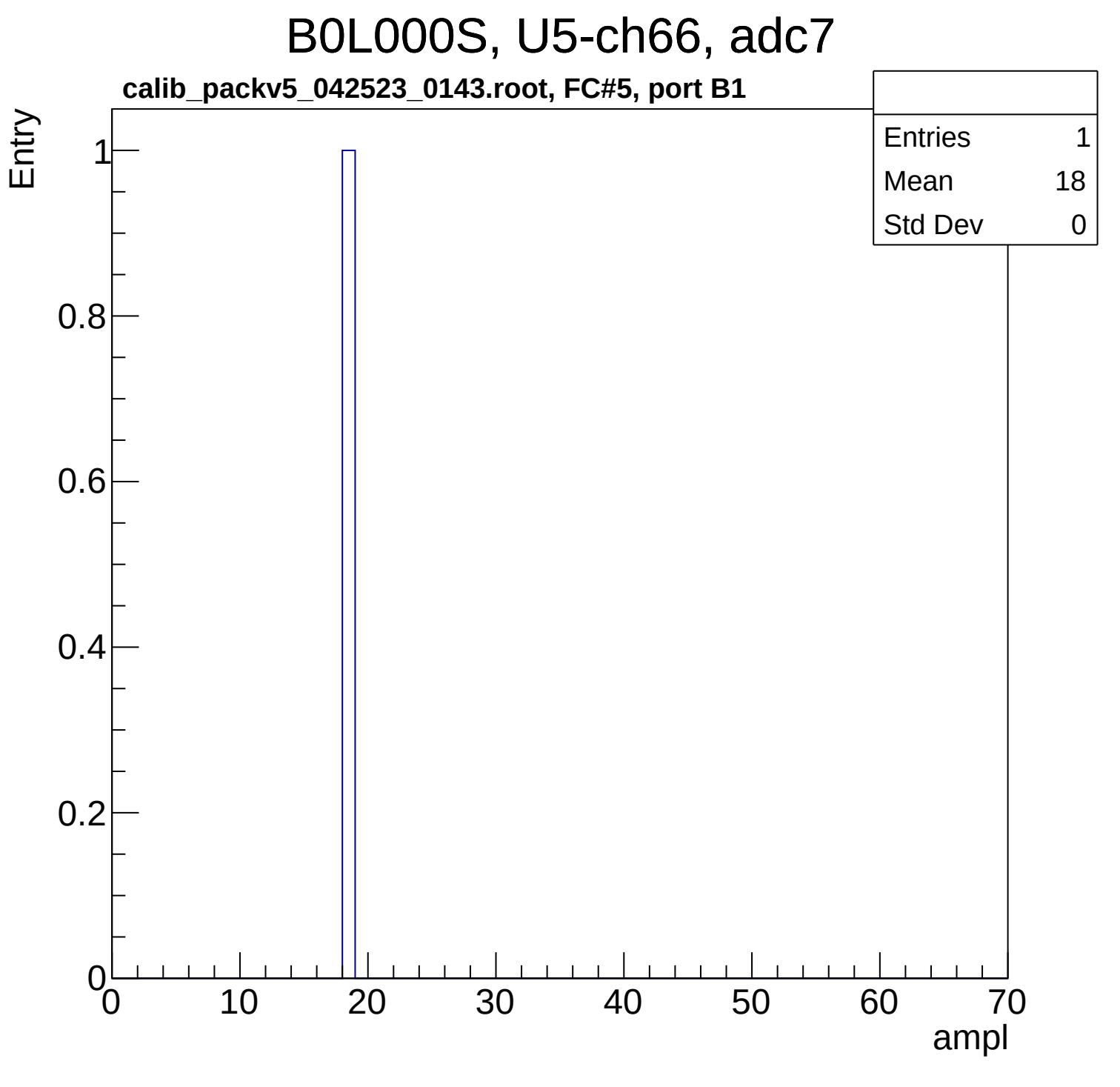

B0L000S, U5-ch66, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 44.48
Std Dev 3.229
Gaus mean : 45.4291
Gaus Width: 3.7011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

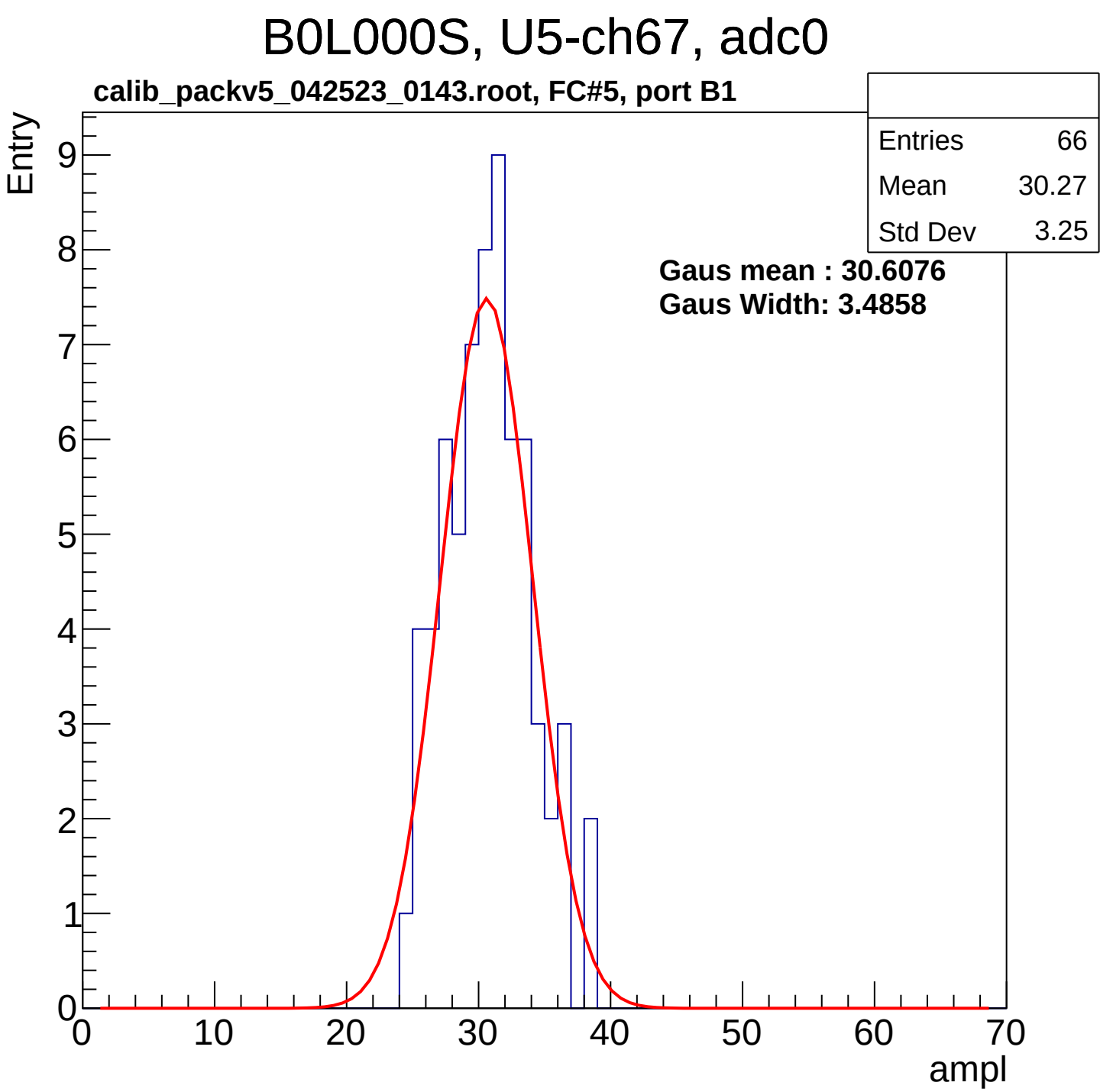

B0L000S, U5-ch66, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 51.39
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

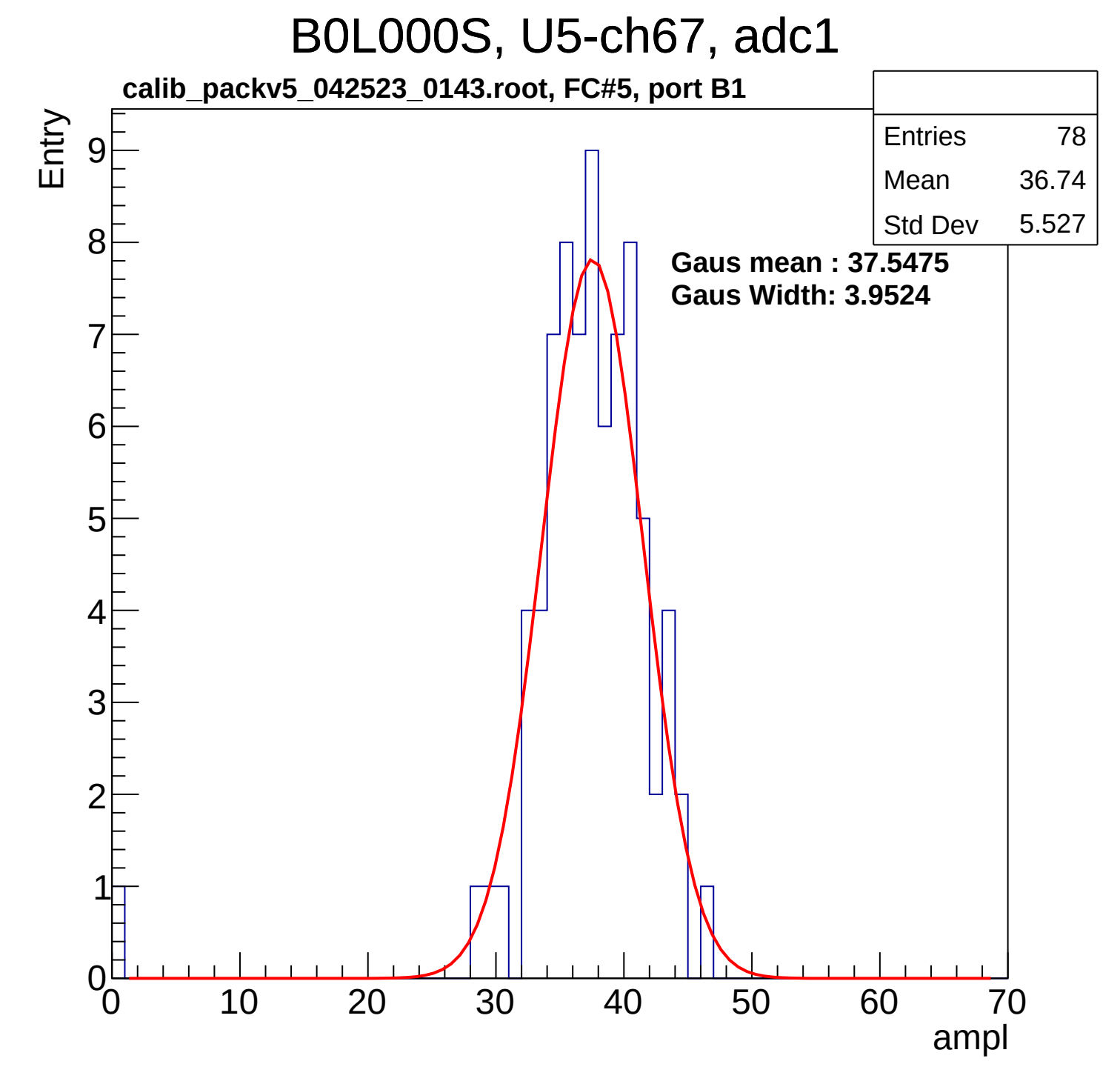

B0L000S, U5-ch66, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 55.59
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

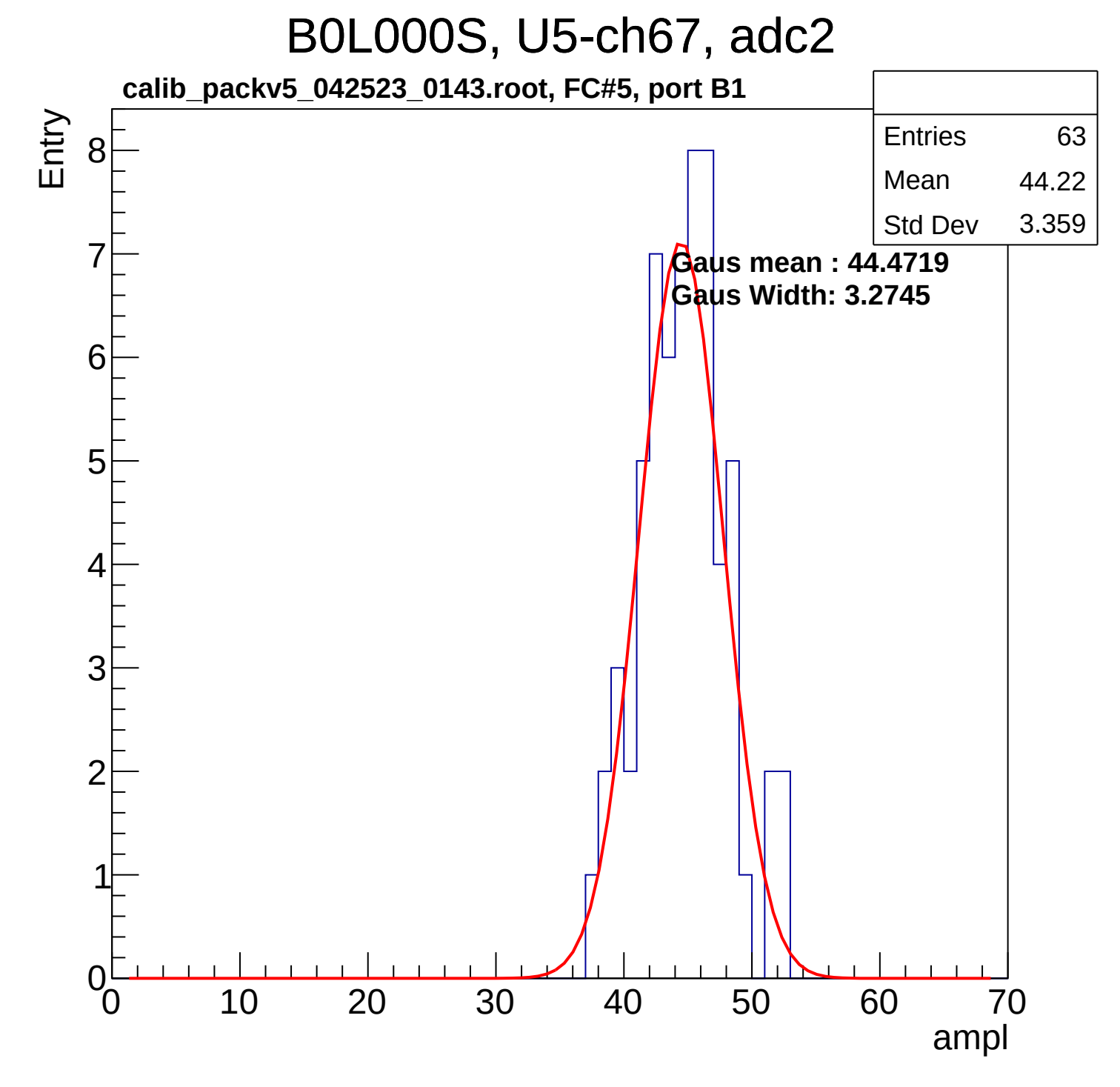

B0L000S, U5-ch66, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.98
Std Dev 8.924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

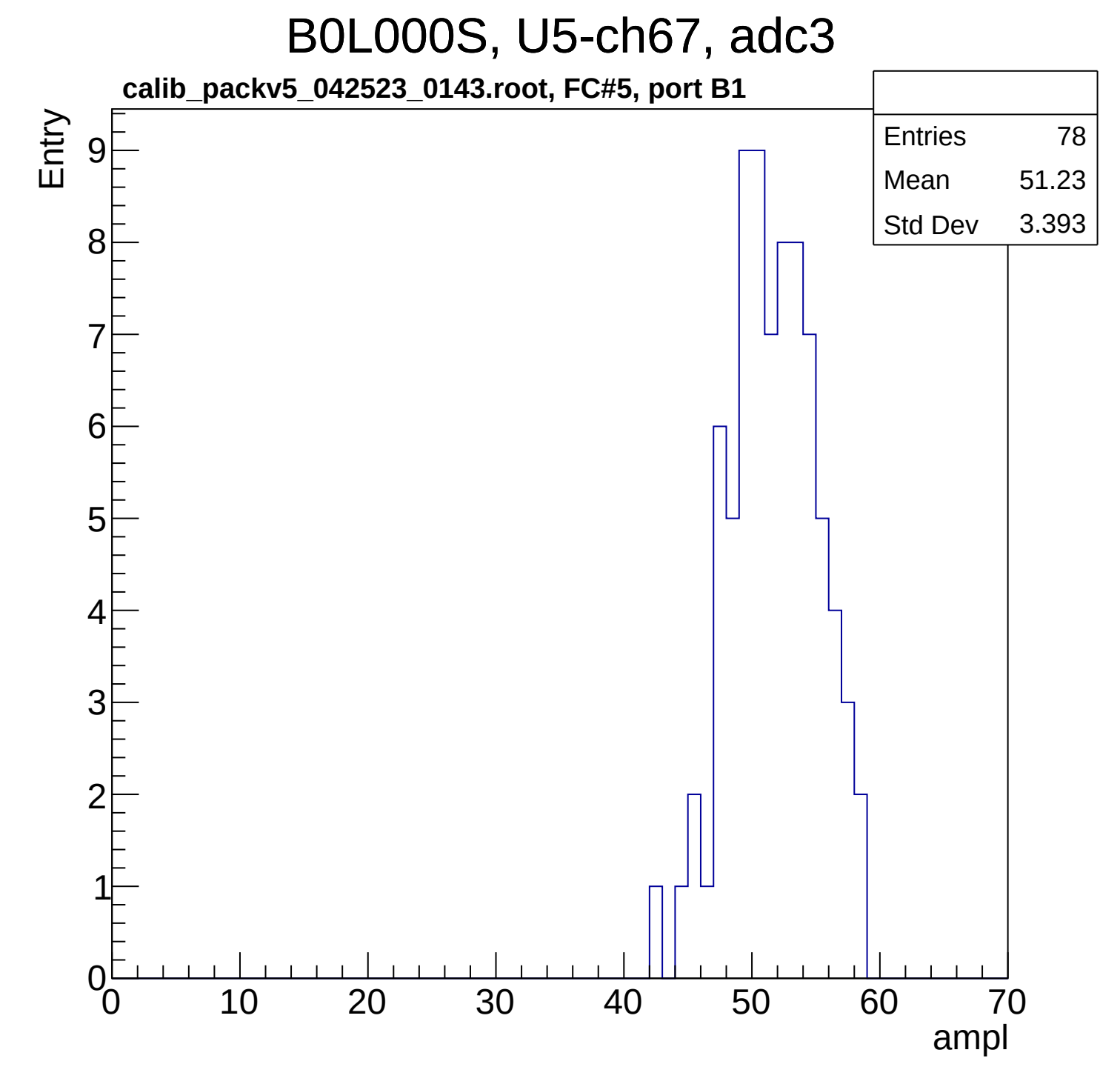

B0L000S, U5-ch66, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

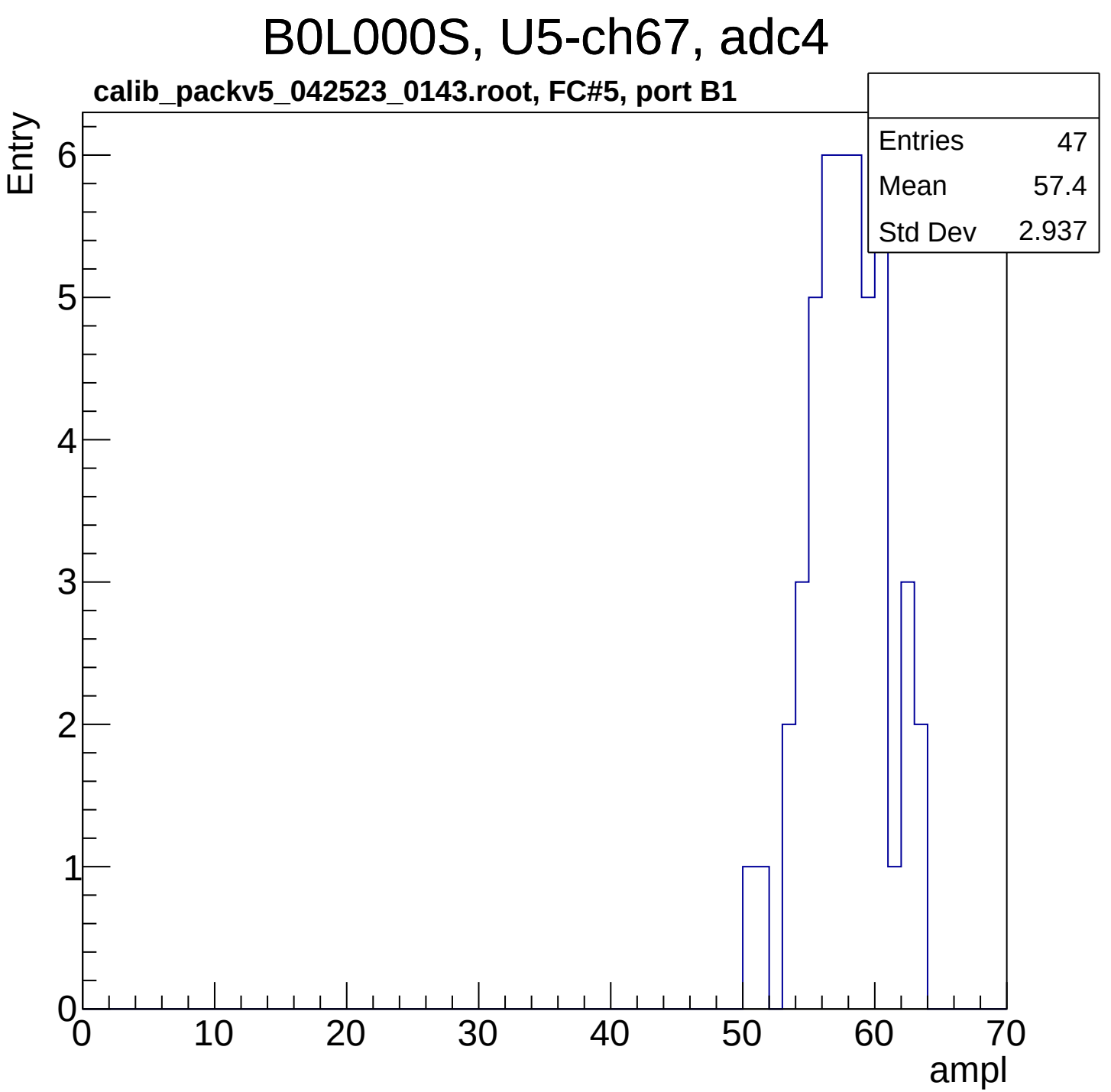

B0L000S, U5-ch66, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

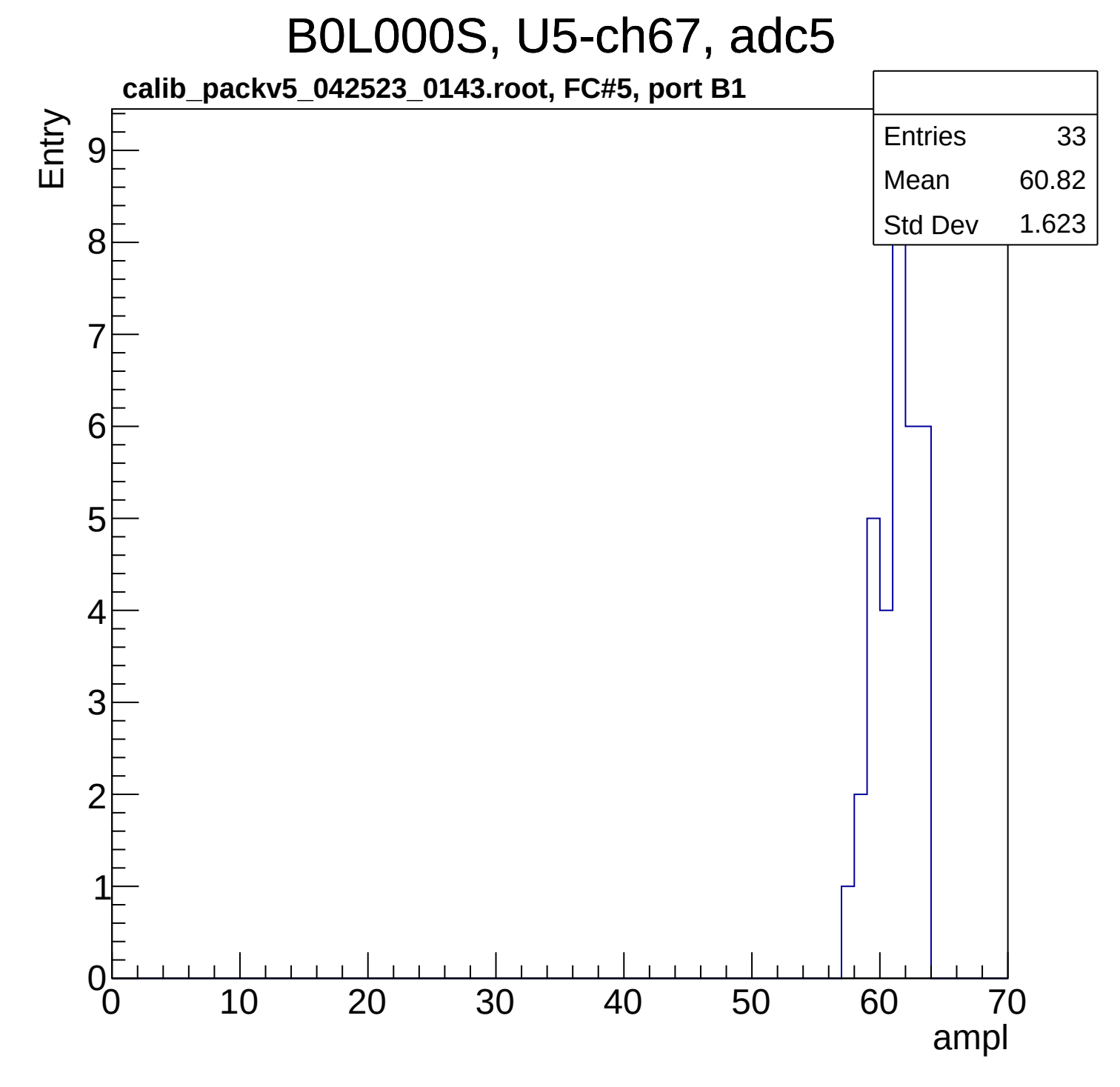

B0L000S, U5-ch67, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 30.27
Std Dev 3.25
Gaus mean : 30.6076
Gaus Width: 3.4858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

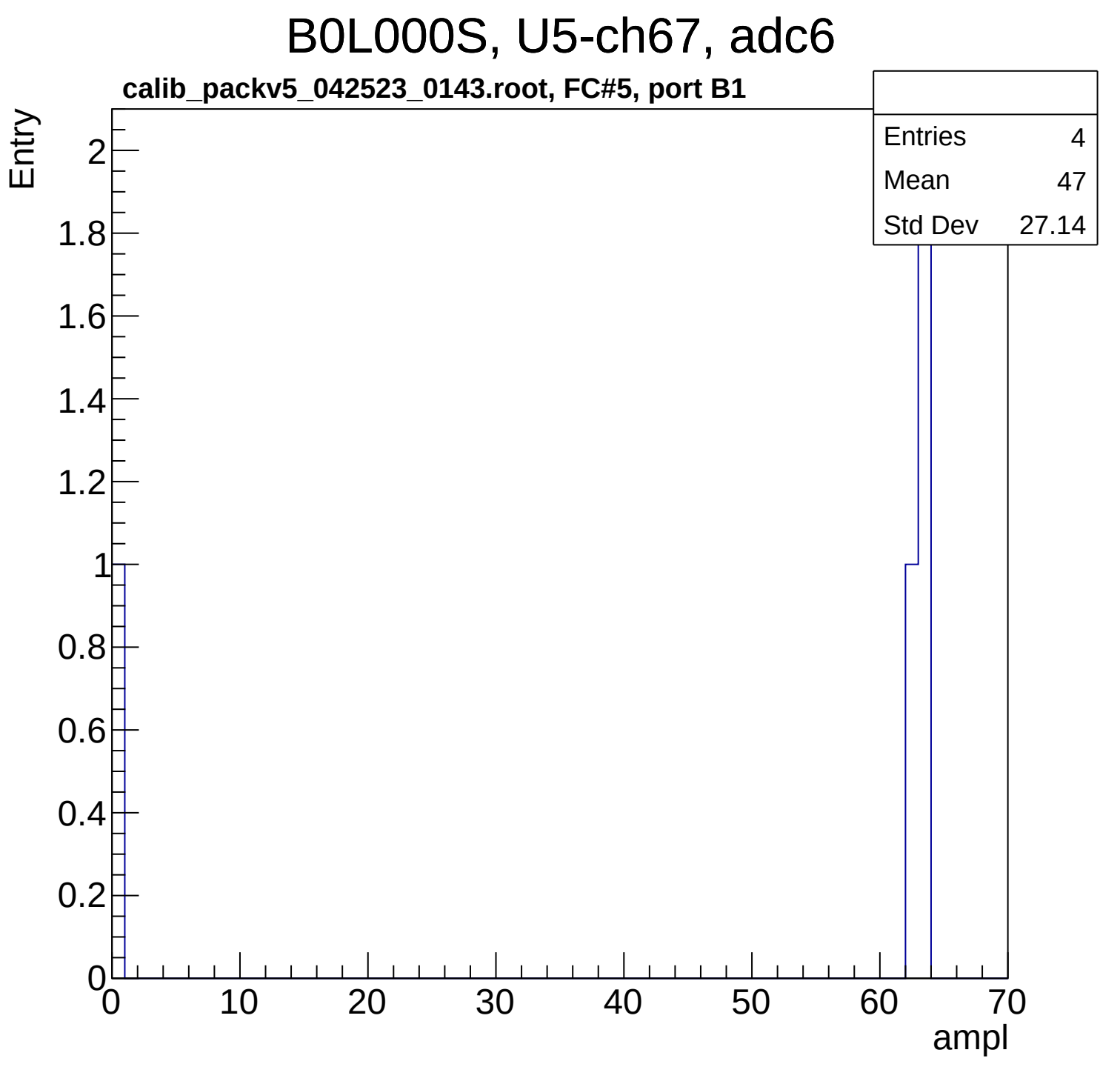

B0L000S, U5-ch67, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 36.74
Std Dev 5.527
Gaus mean : 37.5475
Gaus Width: 3.9524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

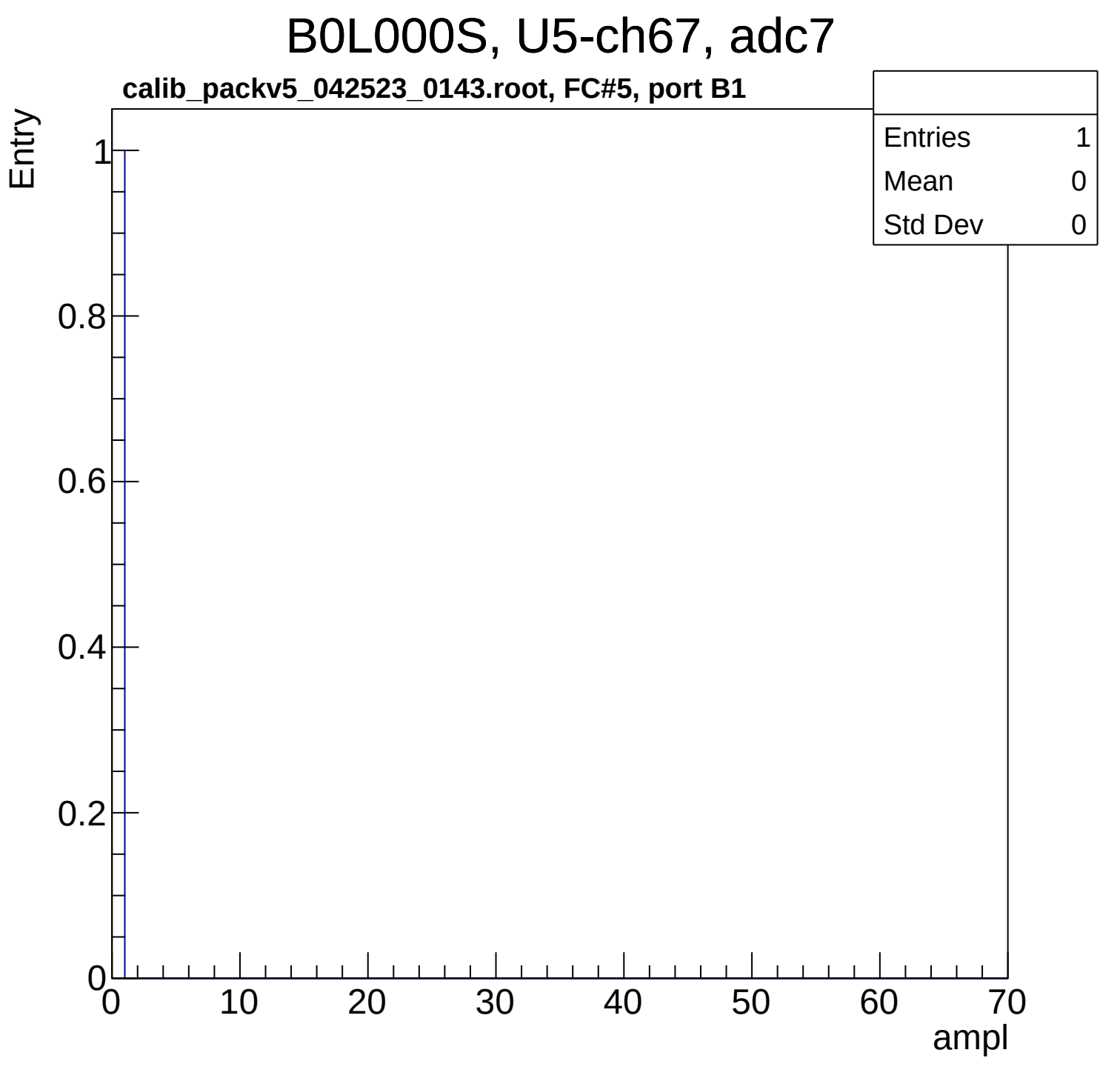

B0L000S, U5-ch67, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 44.22
Std Dev 3.359
Gaus mean : 44.4719
Gaus Width: 3.2745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

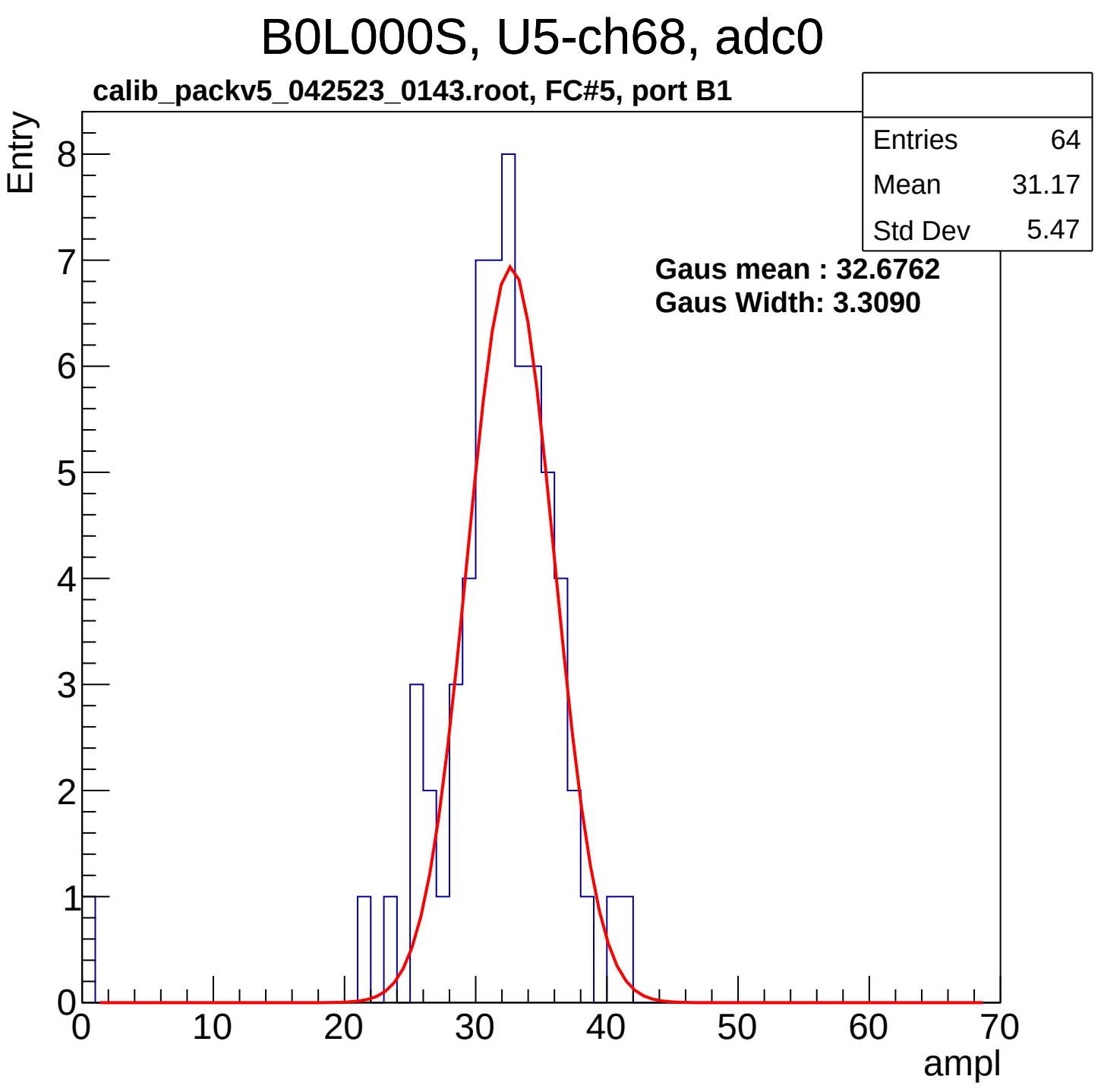

B0L000S, U5-ch67, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 51.23
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

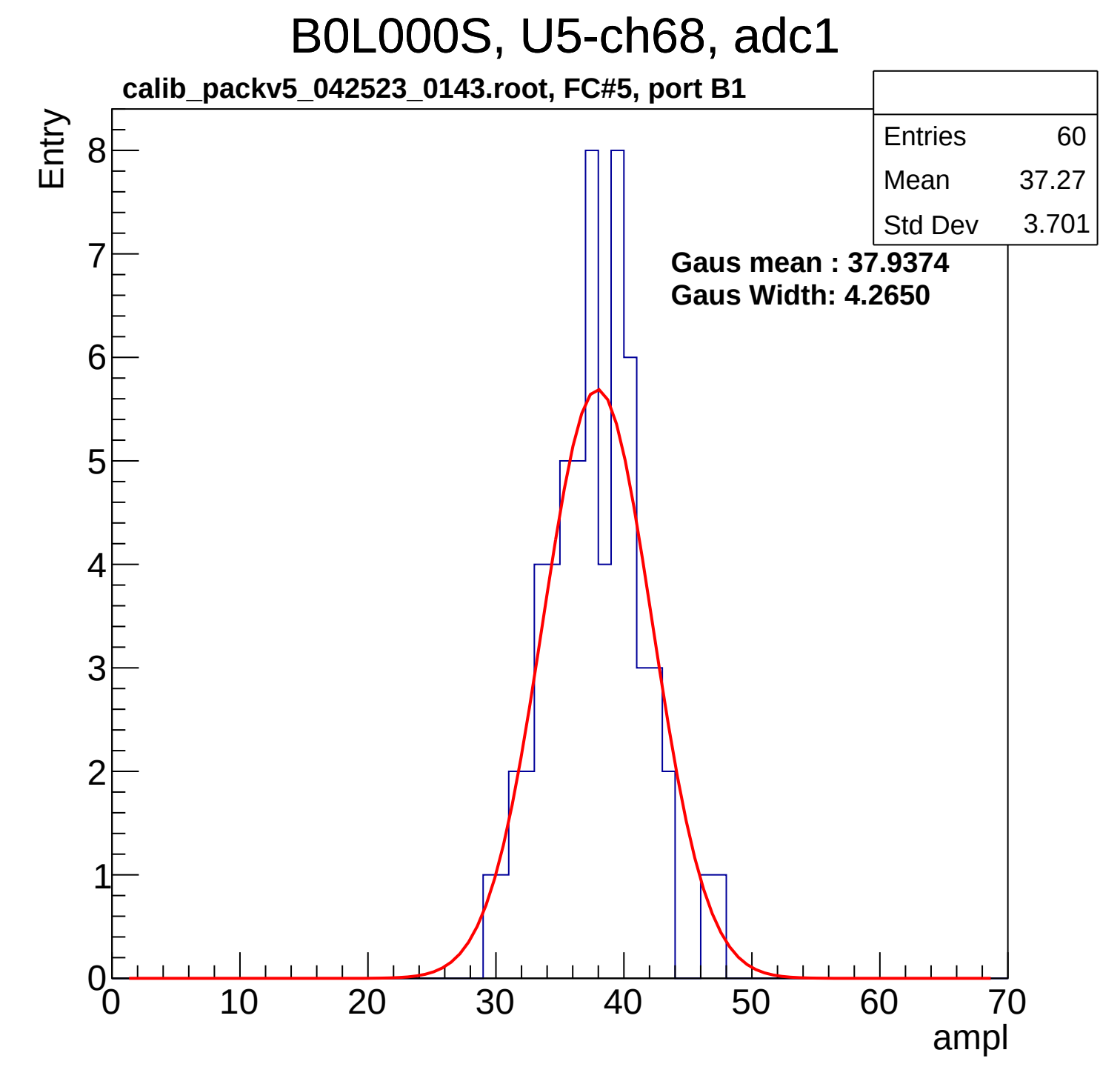

B0L000S, U5-ch67, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 57.4
Std Dev 2.937
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

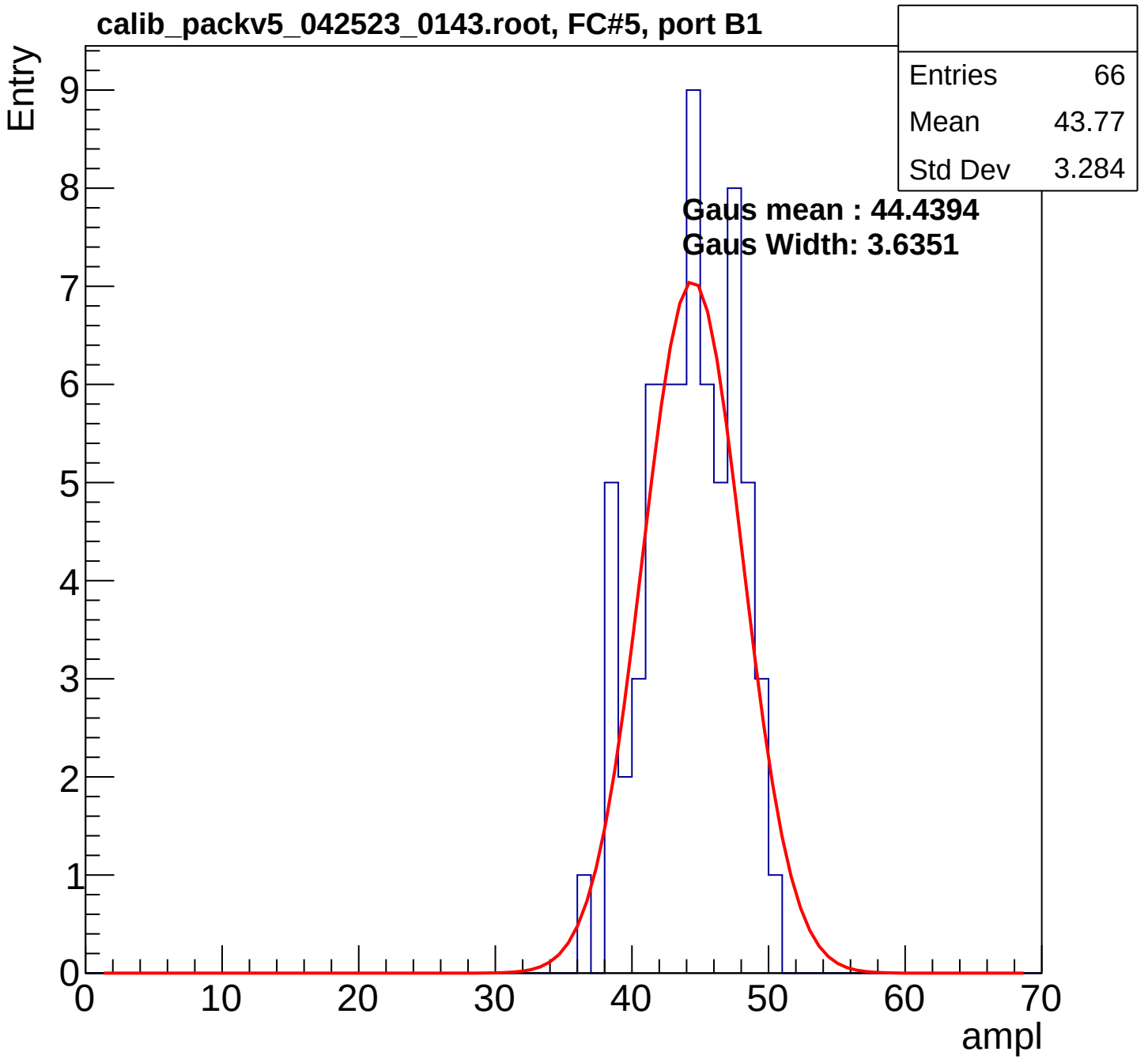

B0L000S, U5-ch67, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 60.82
Std Dev 1.623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

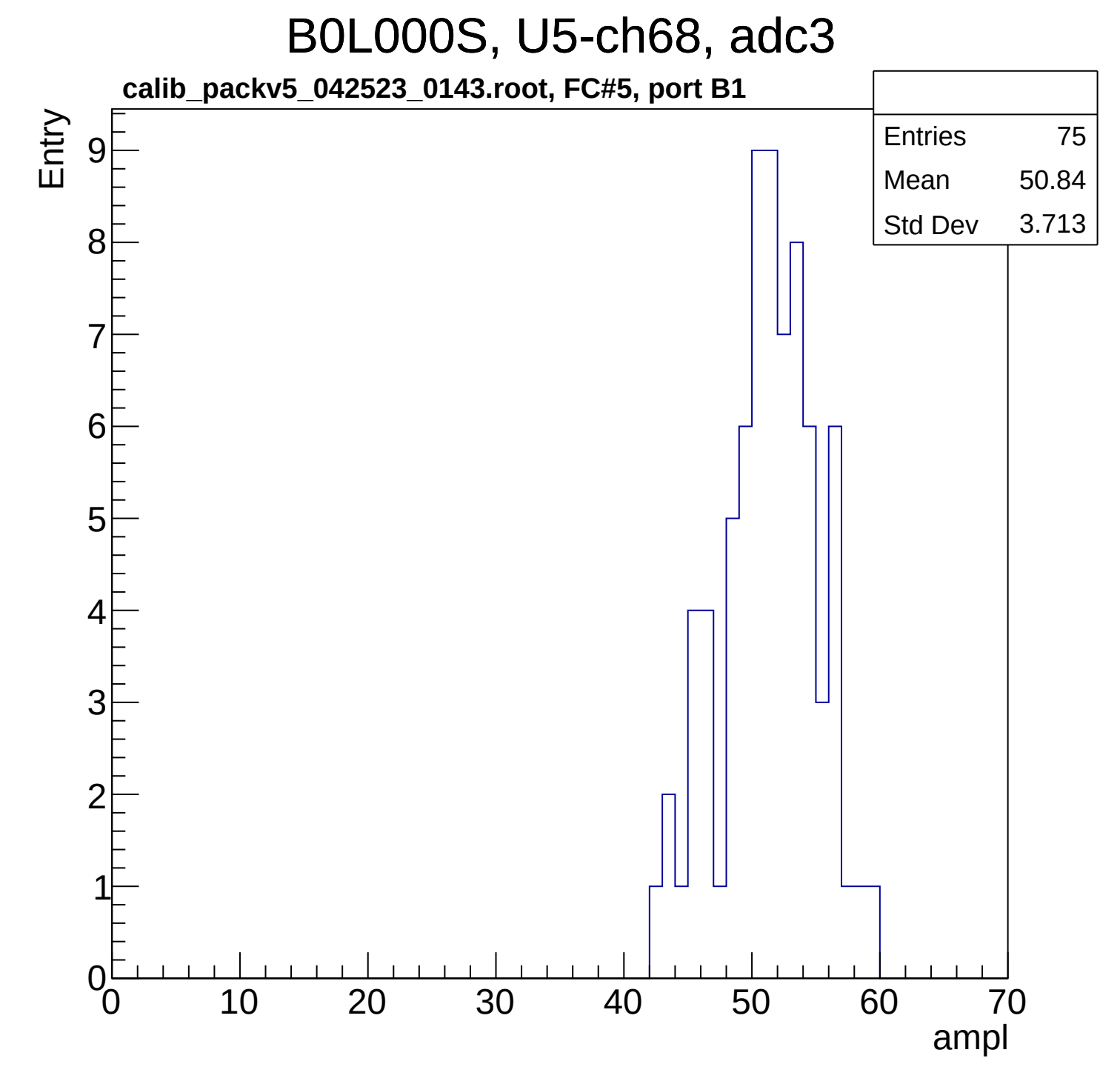

B0L000S, U5-ch67, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch67, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch68, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 31.17
Std Dev 5.47
Gaus mean : 32.6762
Gaus Width: 3.3090
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch68, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.27
Std Dev 3.701
Gaus mean : 37.9374
Gaus Width: 4.2650
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch68, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.77
Std Dev 3.284
Gaus mean : 44.4394
Gaus Width: 3.6351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch68, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.84
Std Dev 3.713
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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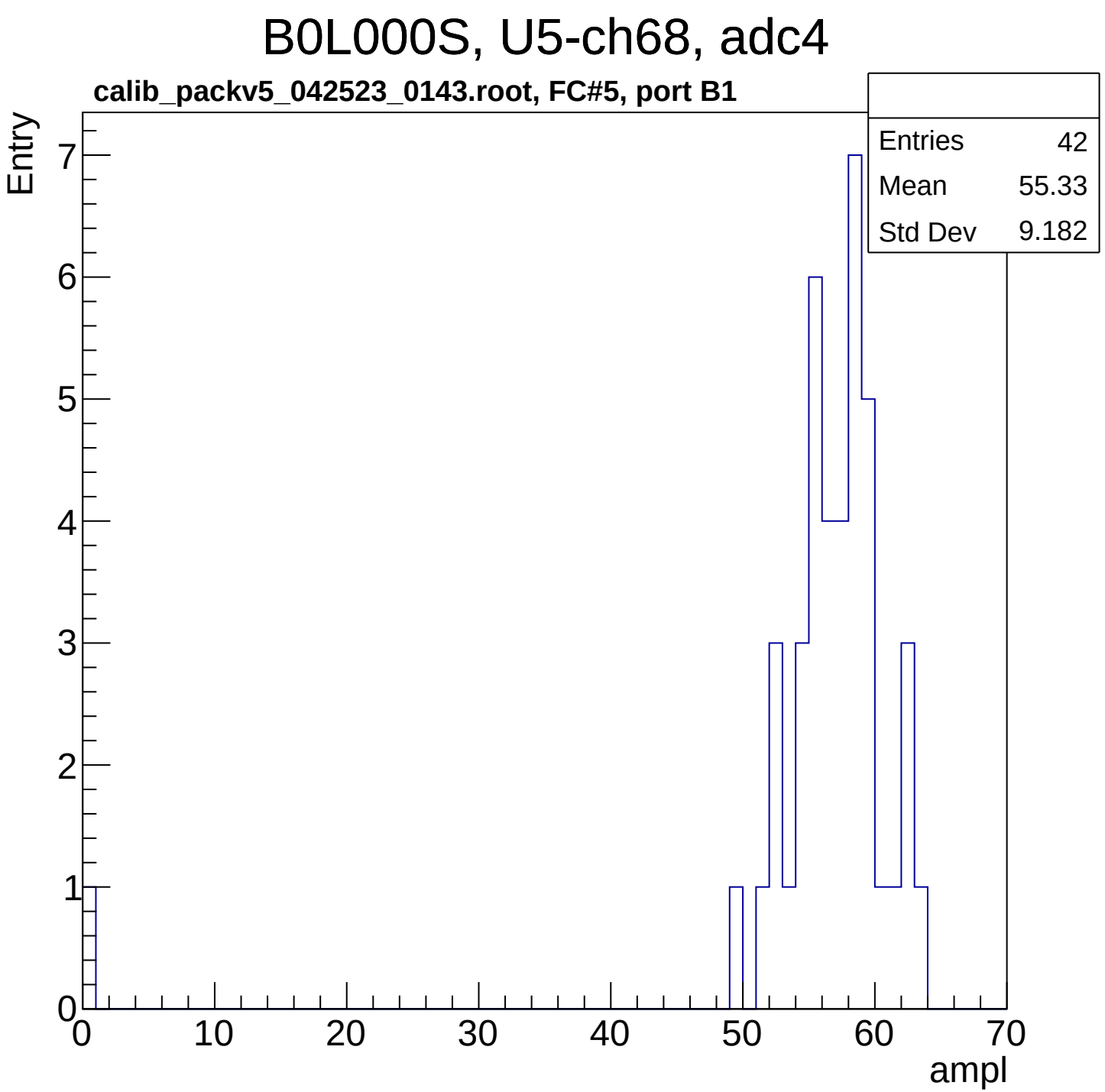
40
50
60
70


B0L000S, U5-ch68, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 55.33
Std Dev 9.182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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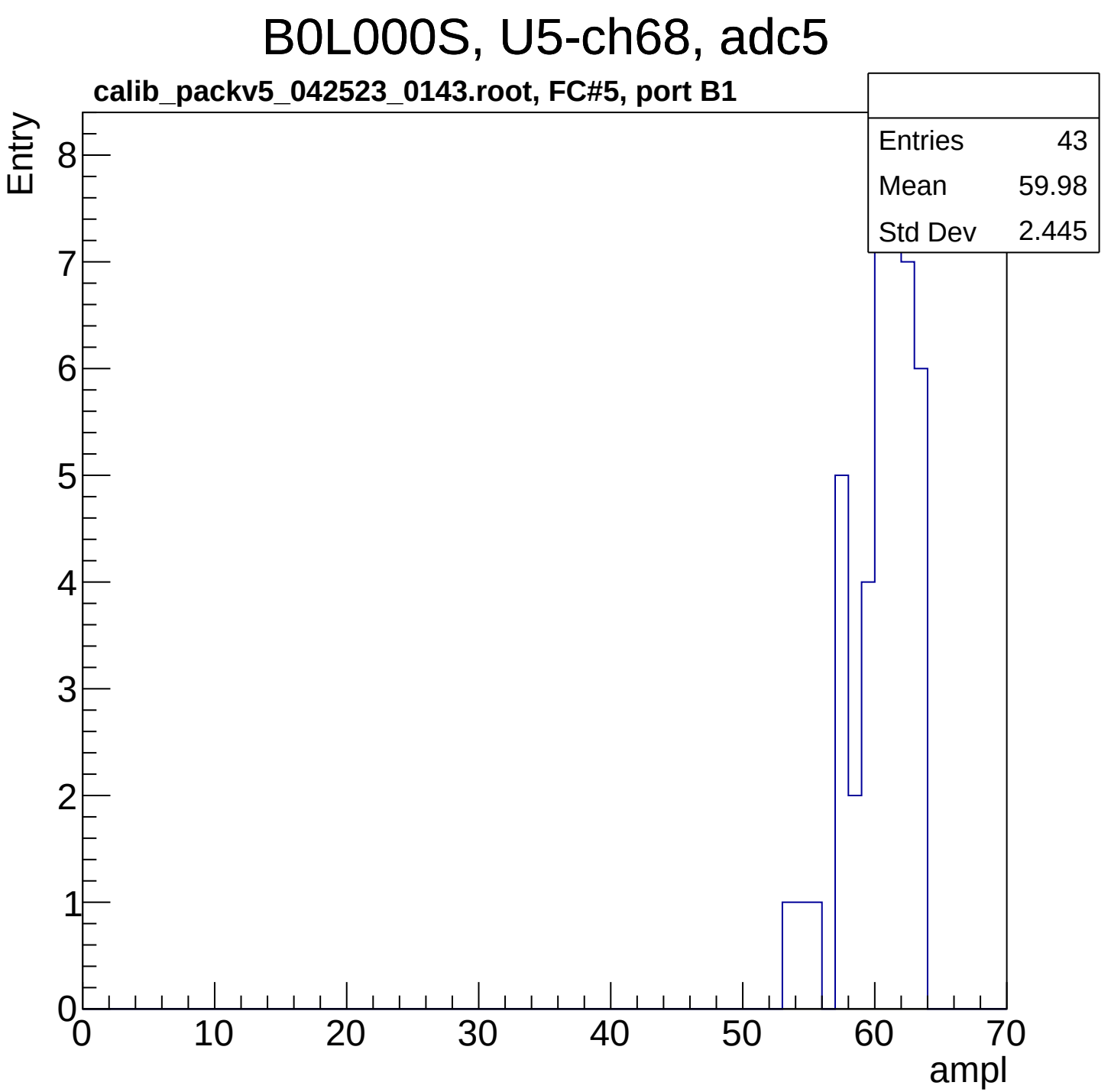
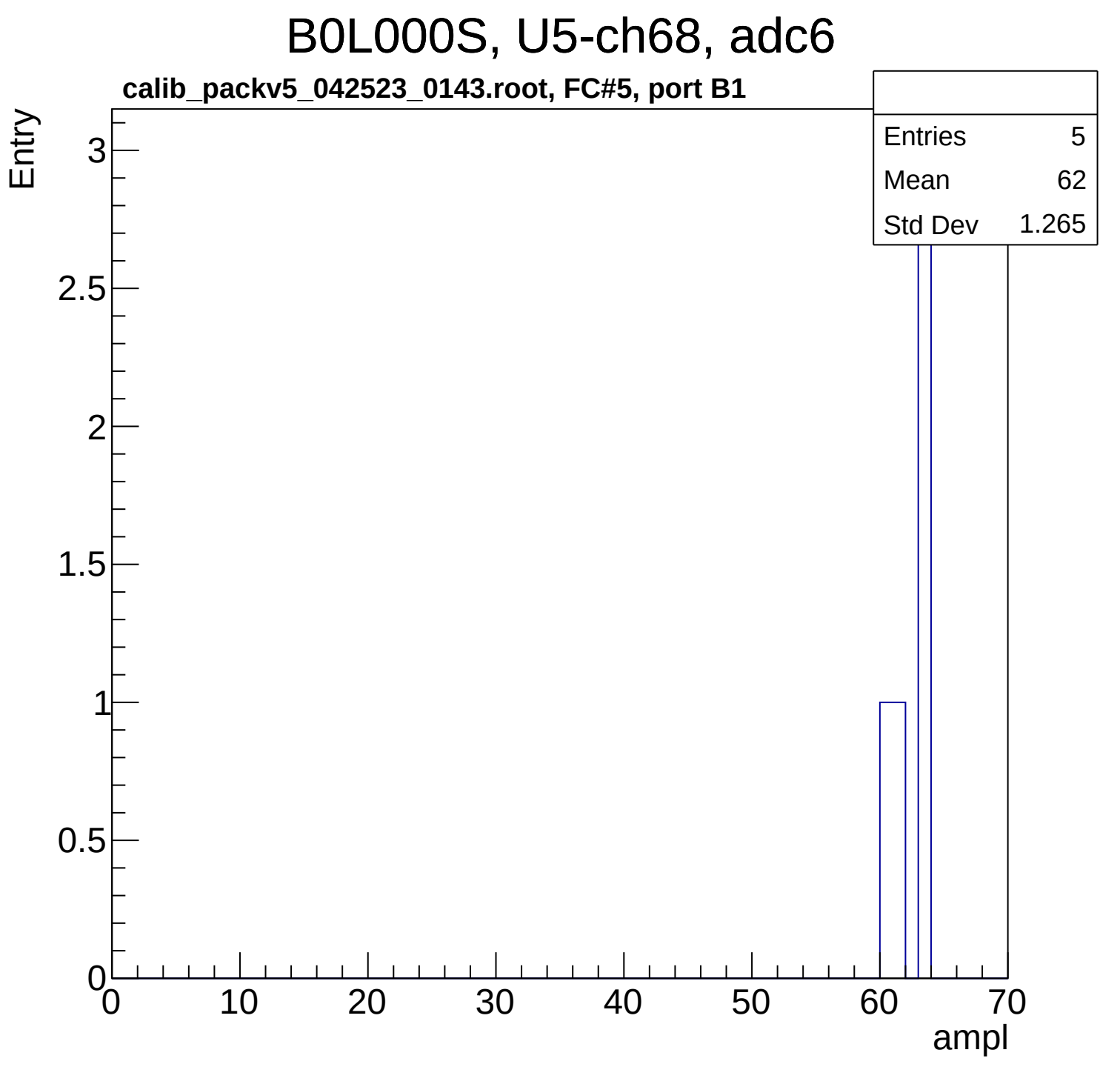
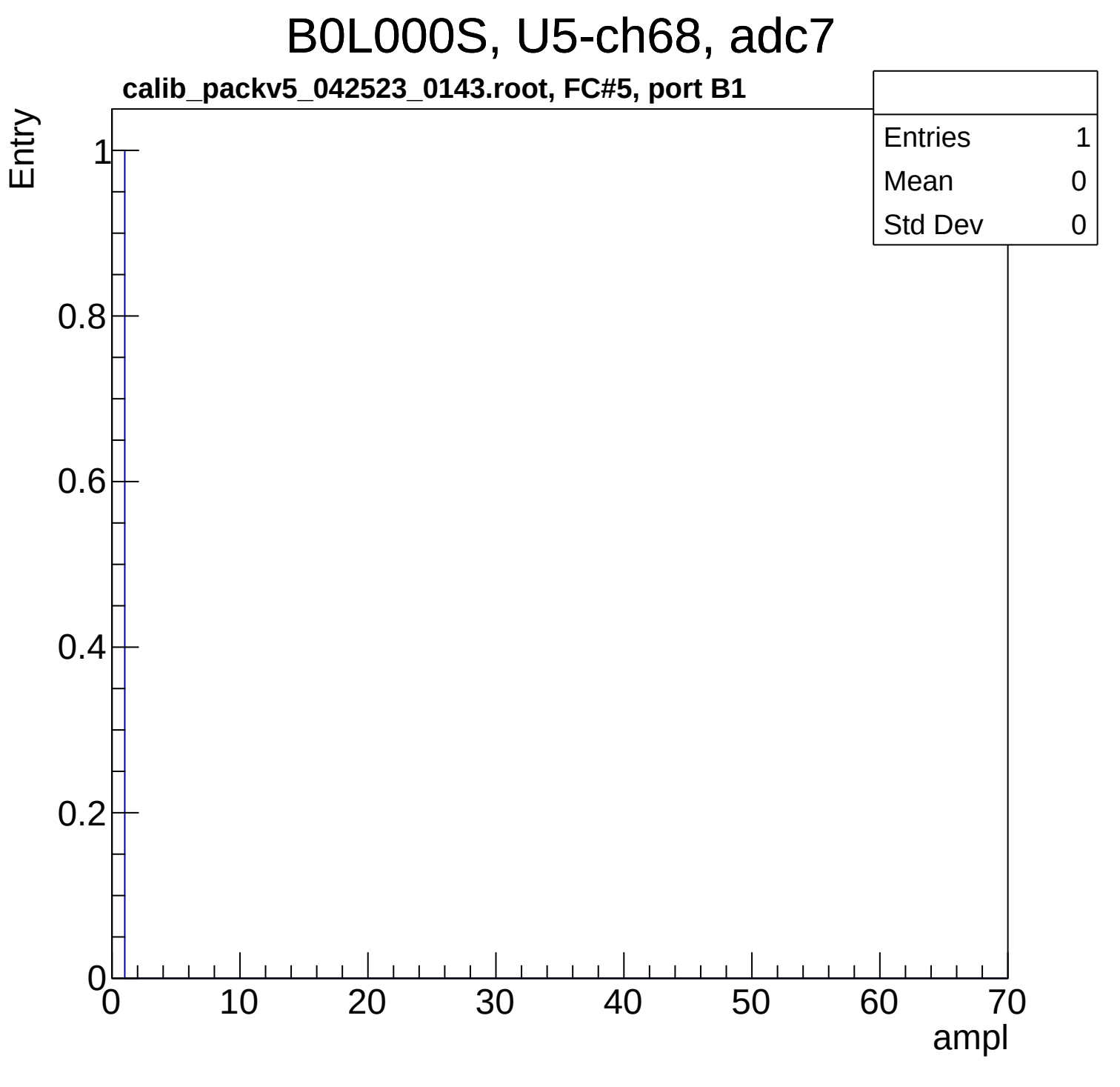
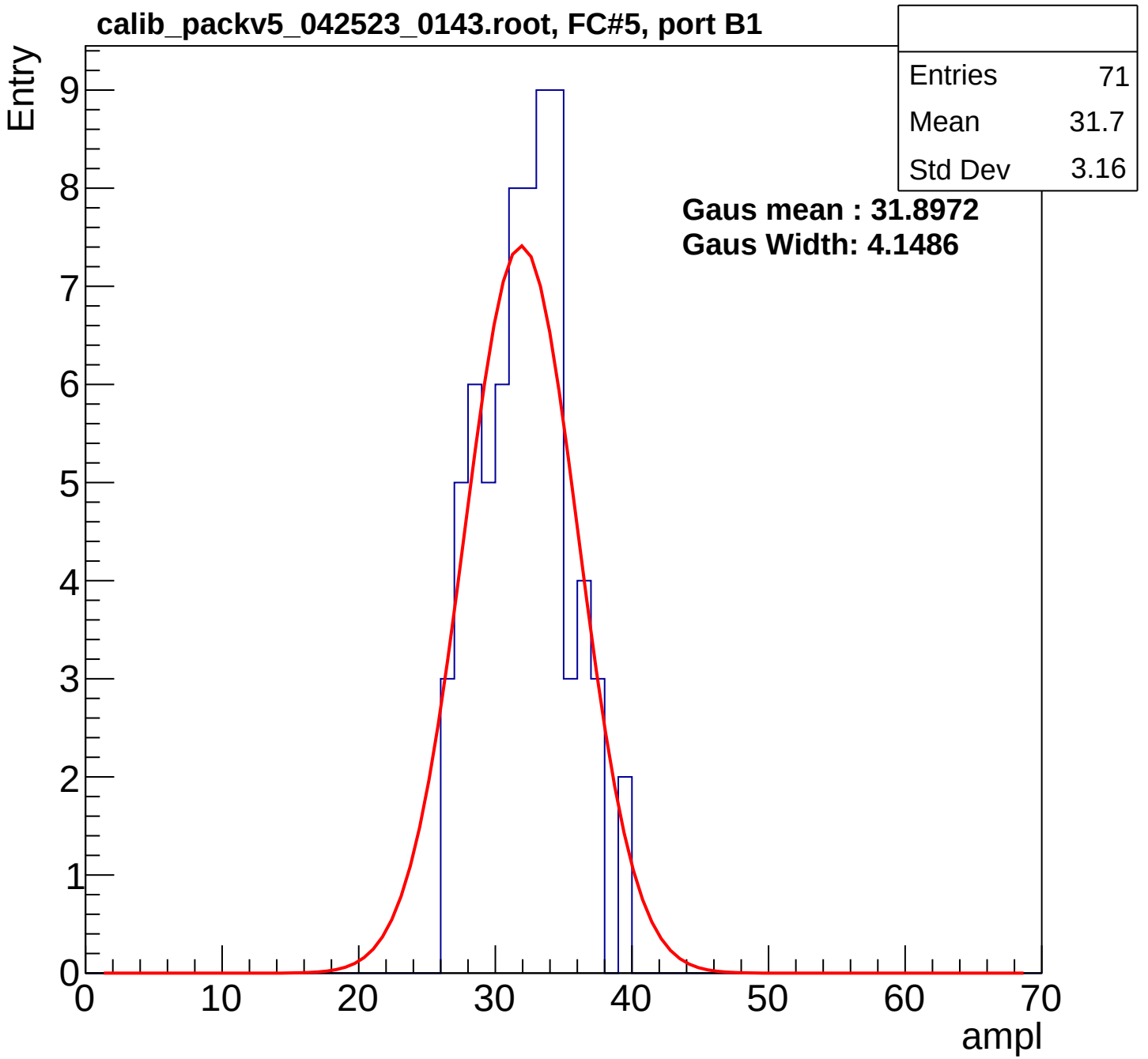

B0L000S, U5-ch68, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.98
Std Dev 2.445
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch68, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch68, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch69, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 31.7
Std Dev 3.16
Gaus mean : 31.8972
Gaus Width: 4.1486
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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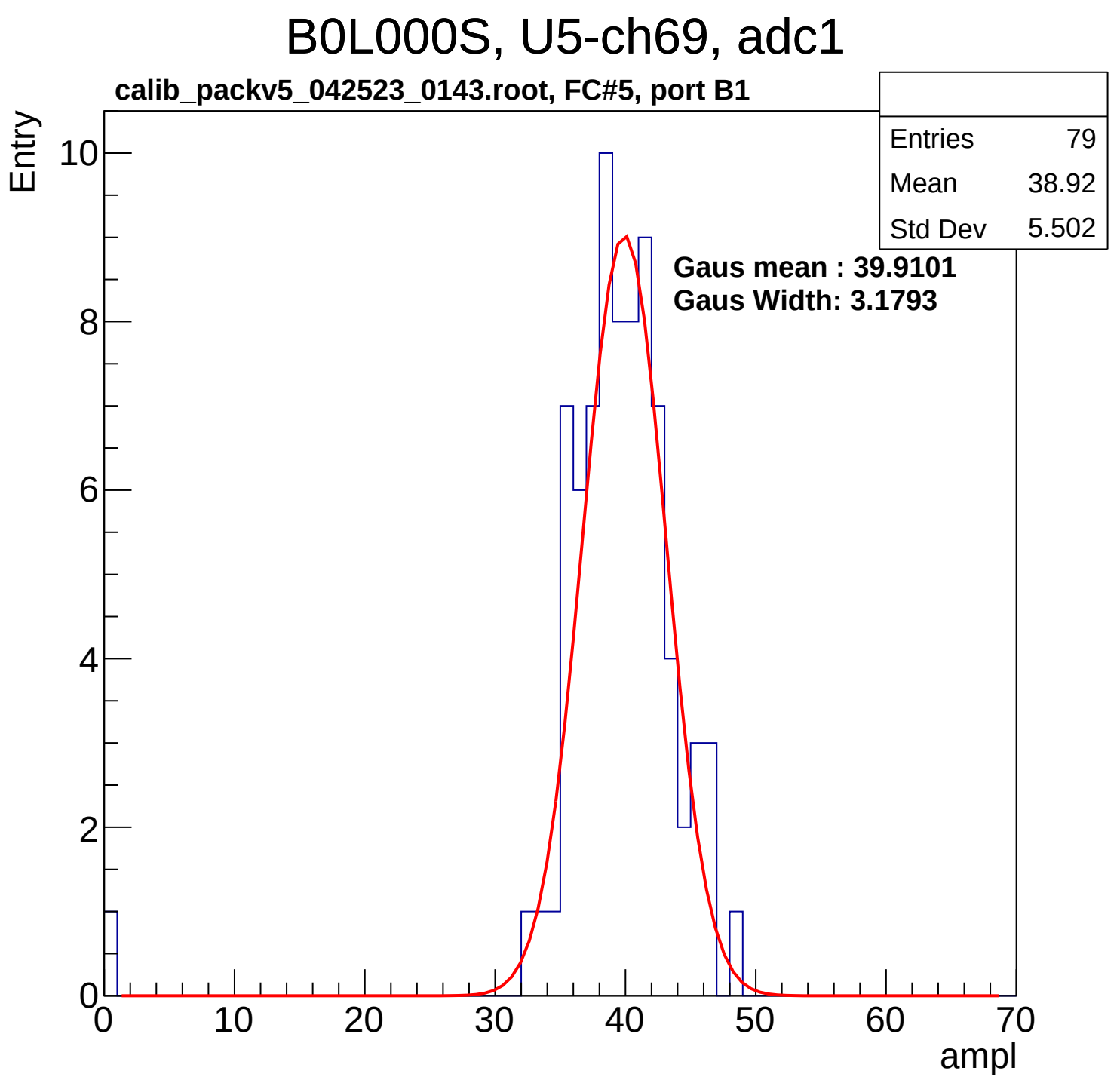
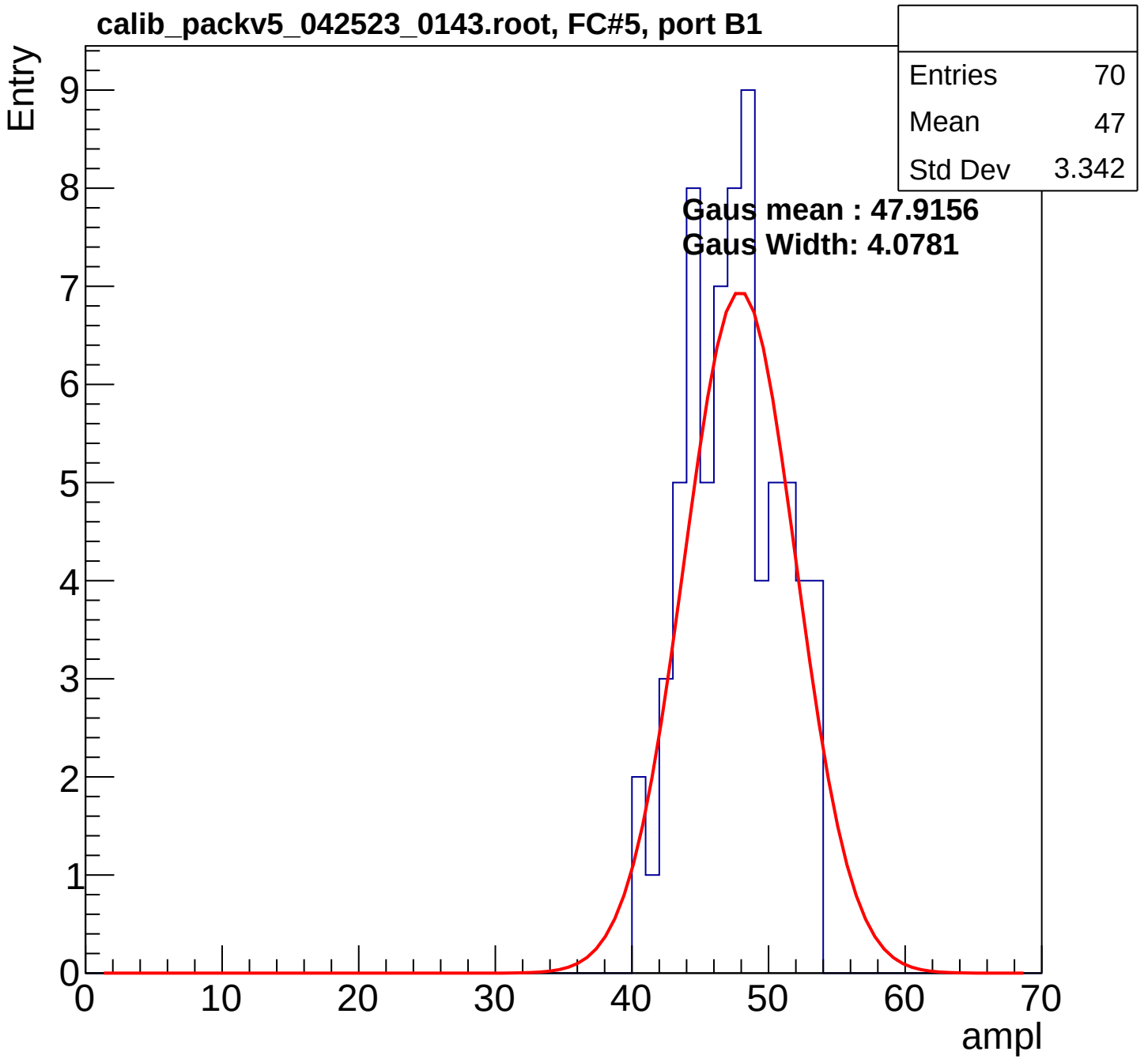

B0L000S, U5-ch69, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 38.92
Std Dev 5.502
Gaus mean : 39.9101
Gaus Width: 3.1793
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch69, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 47
Std Dev 3.342
Gaus mean : 47.9156
Gaus Width: 4.0781
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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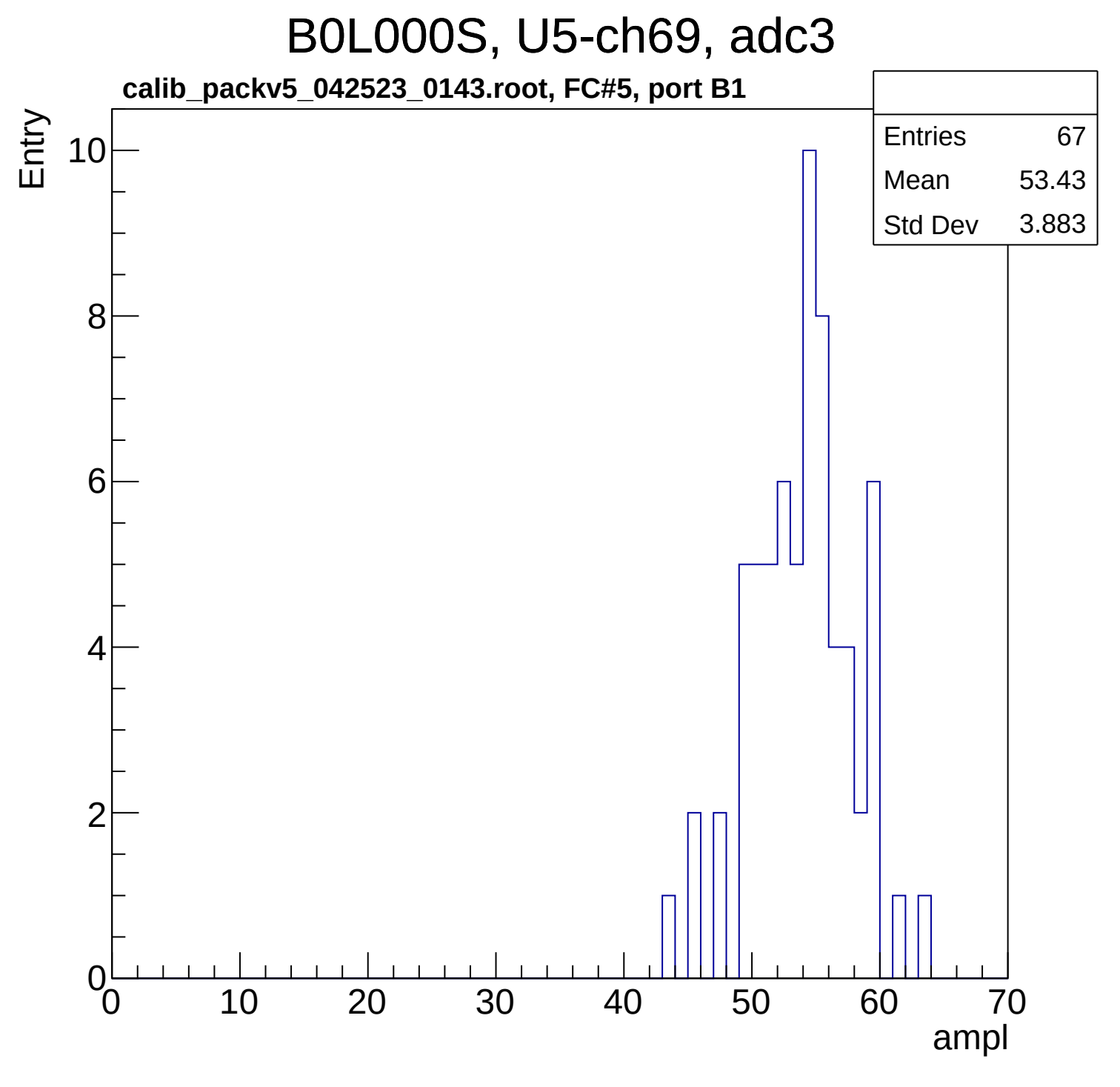
B0L000S, U5-ch69, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 53.43
Std Dev 3.883
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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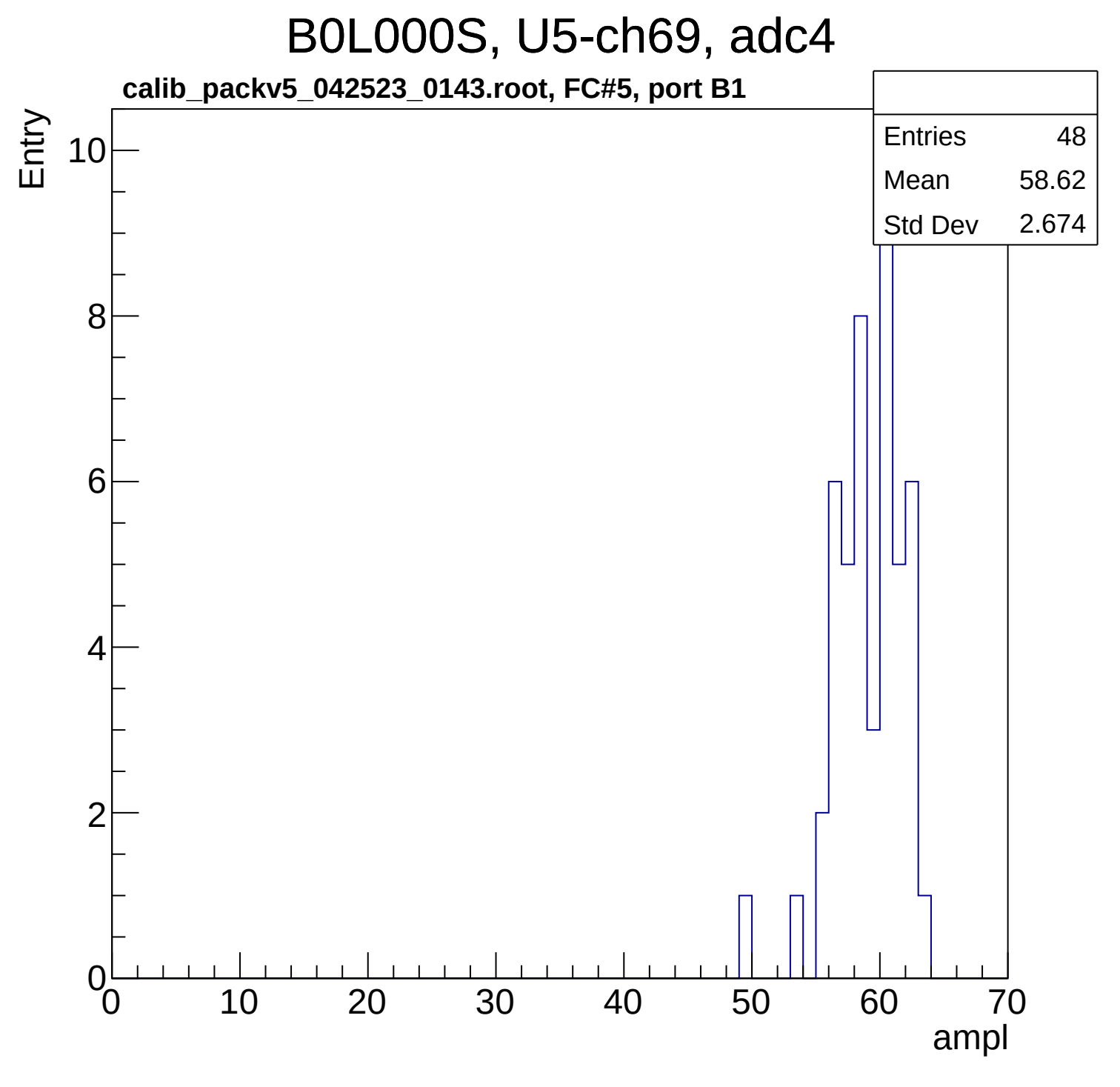
B0L000S, U5-ch69, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.62
Std Dev 2.674
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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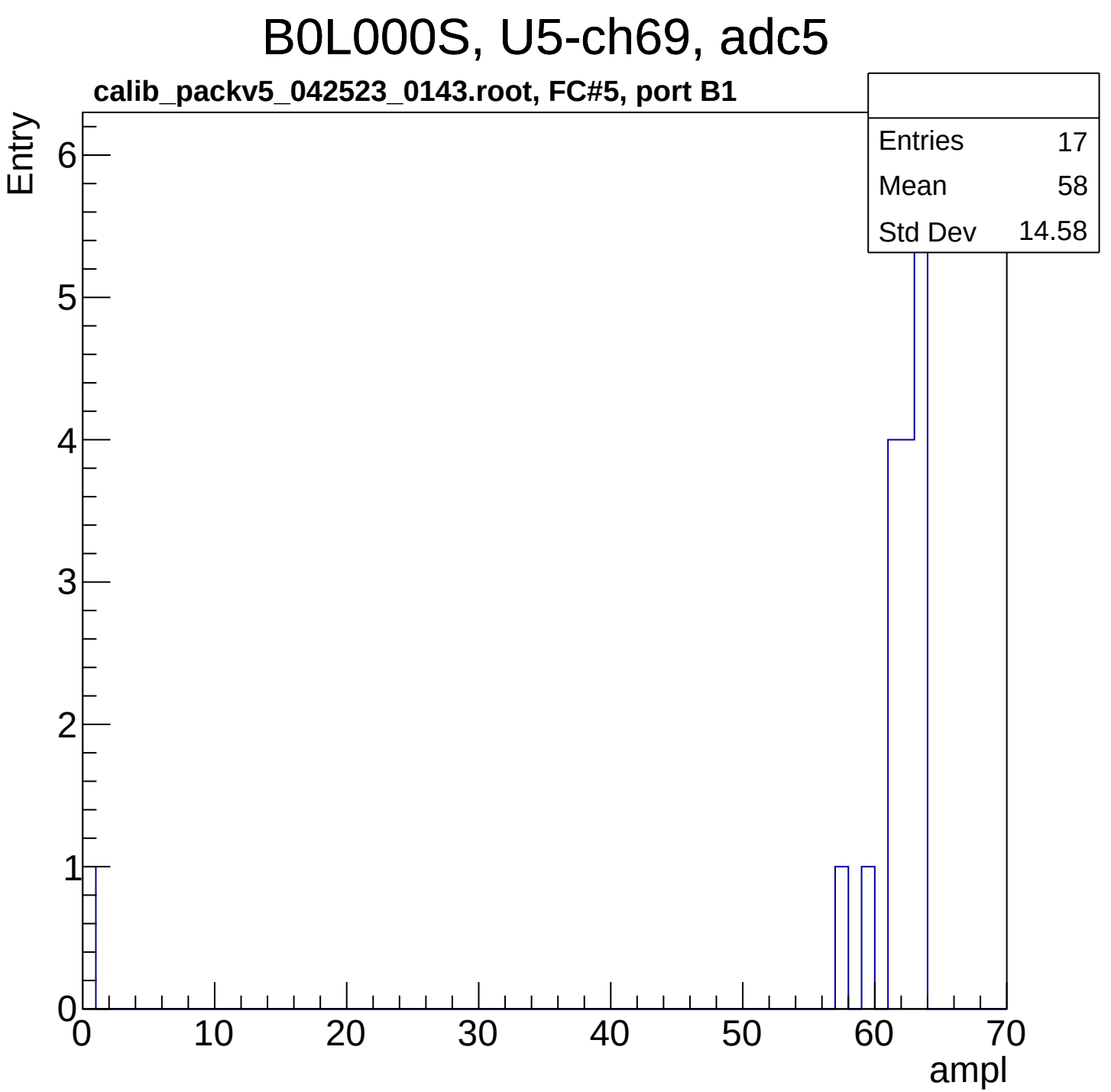
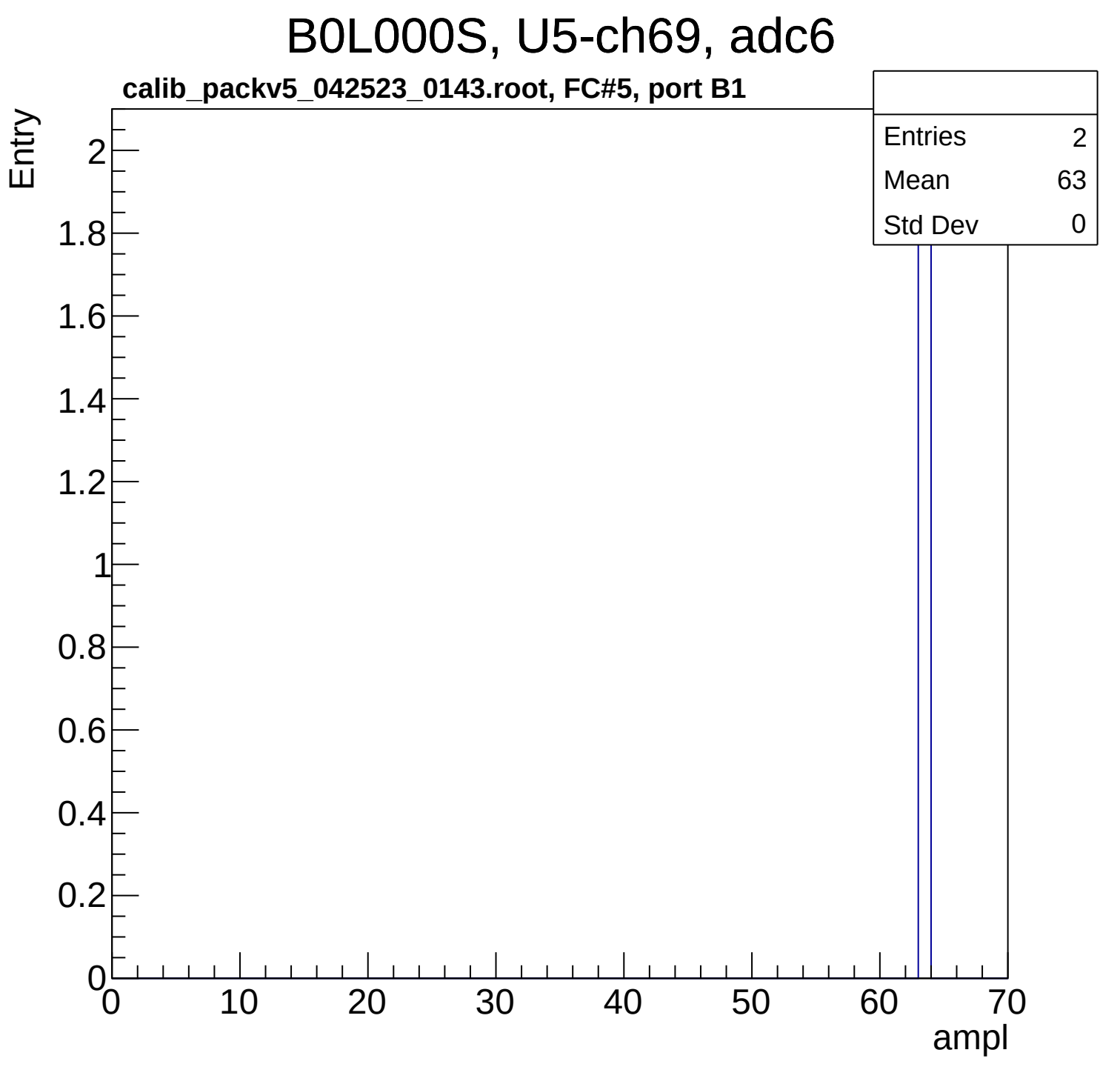

B0L000S, U5-ch69, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 58
Std Dev 14.58
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch69, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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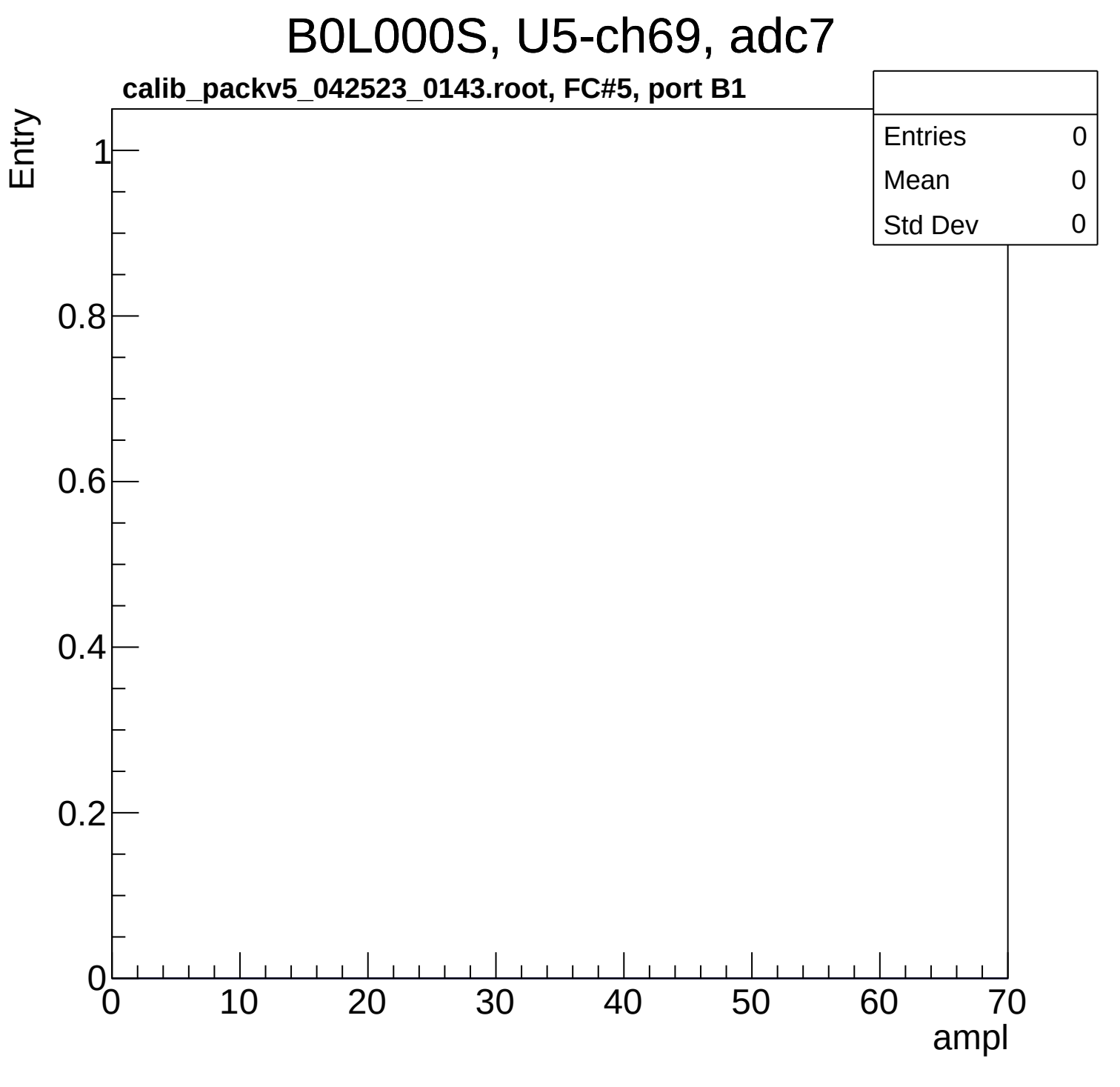
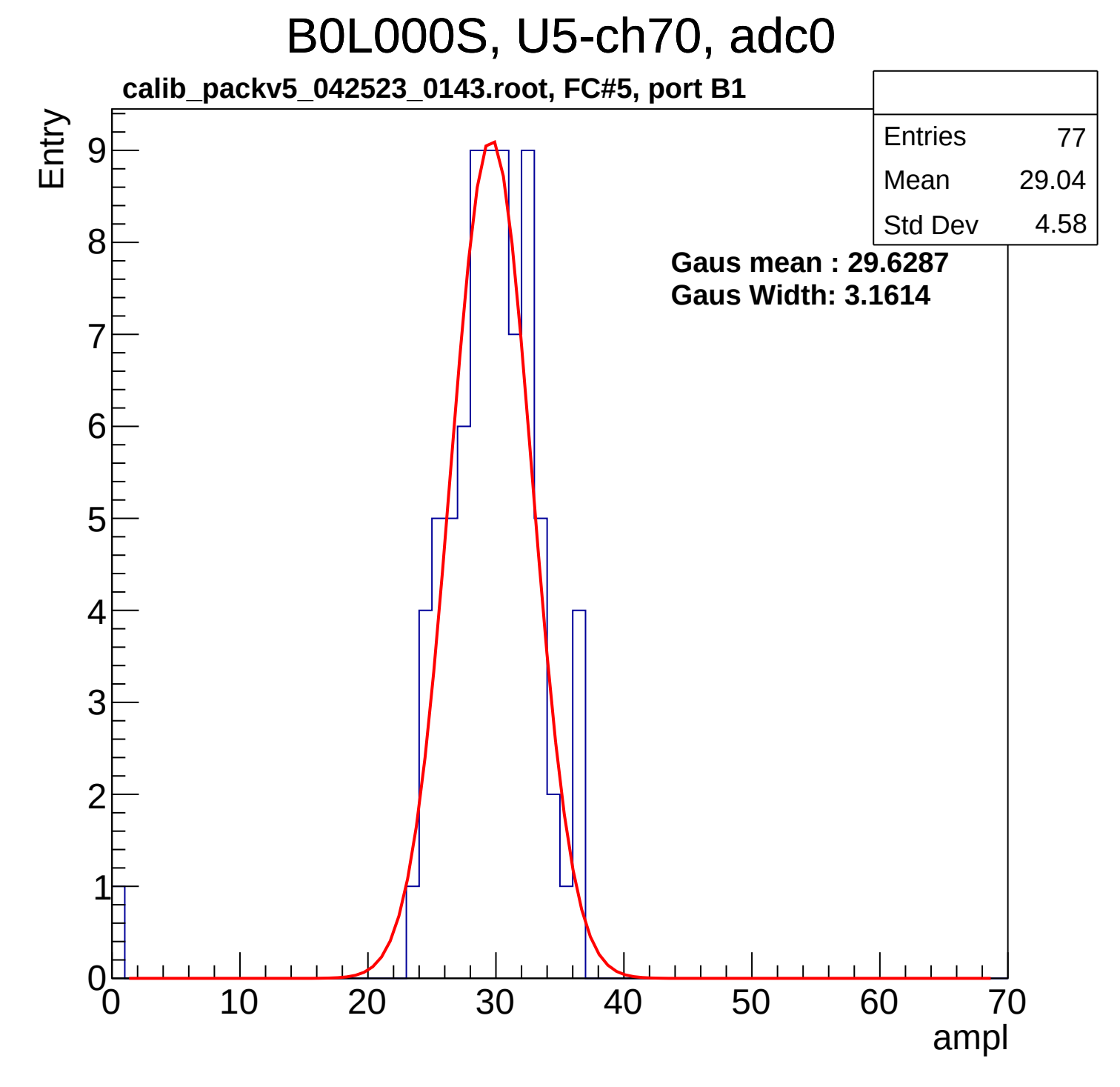

B0L000S, U5-ch69, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch70, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 29.04
Std Dev 4.58
Gaus mean : 29.6287
Gaus Width: 3.1614
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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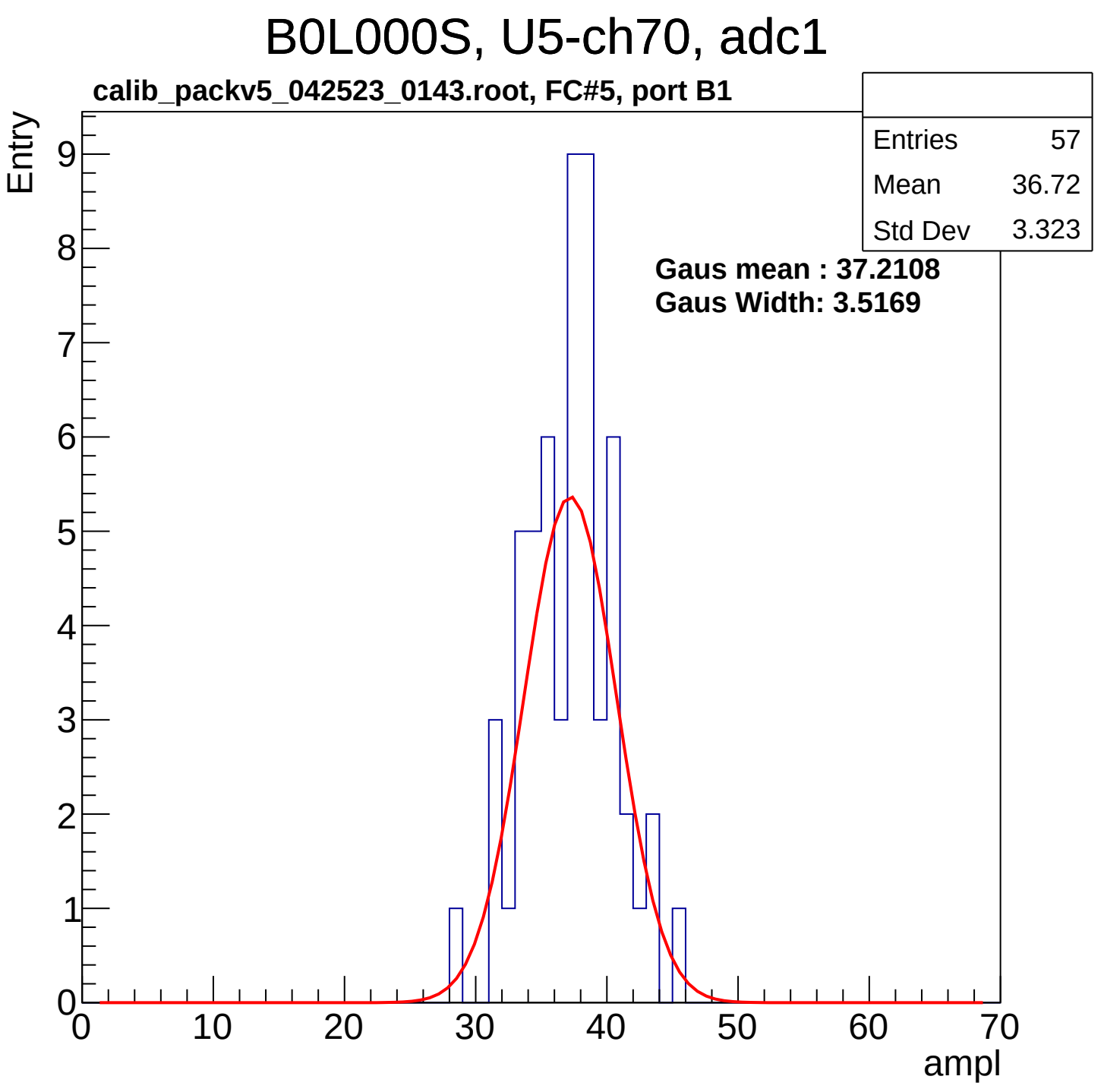
B0L000S, U5-ch70, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 36.72
Std Dev 3.323
Gaus mean : 37.2108
Gaus Width: 3.5169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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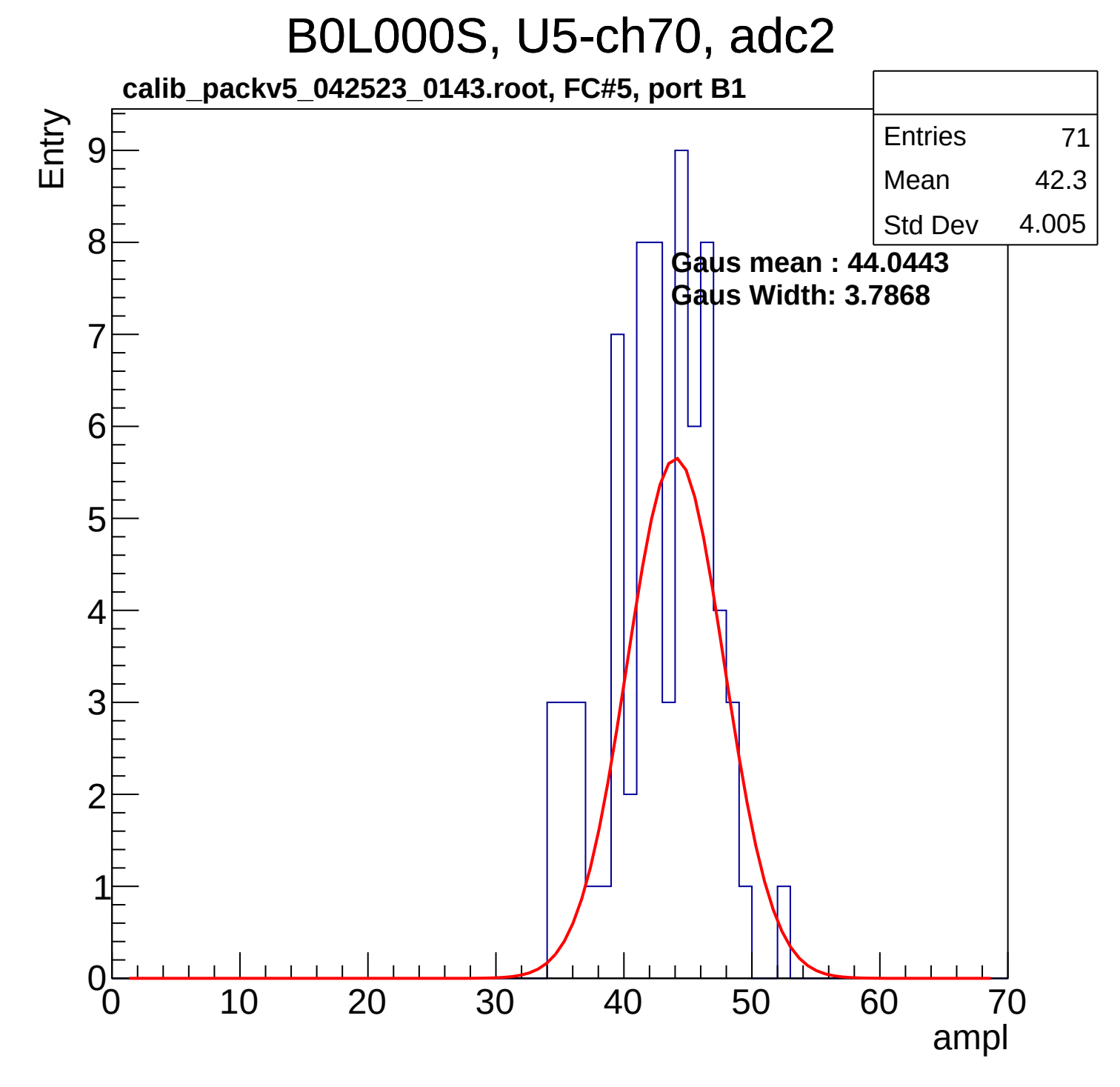
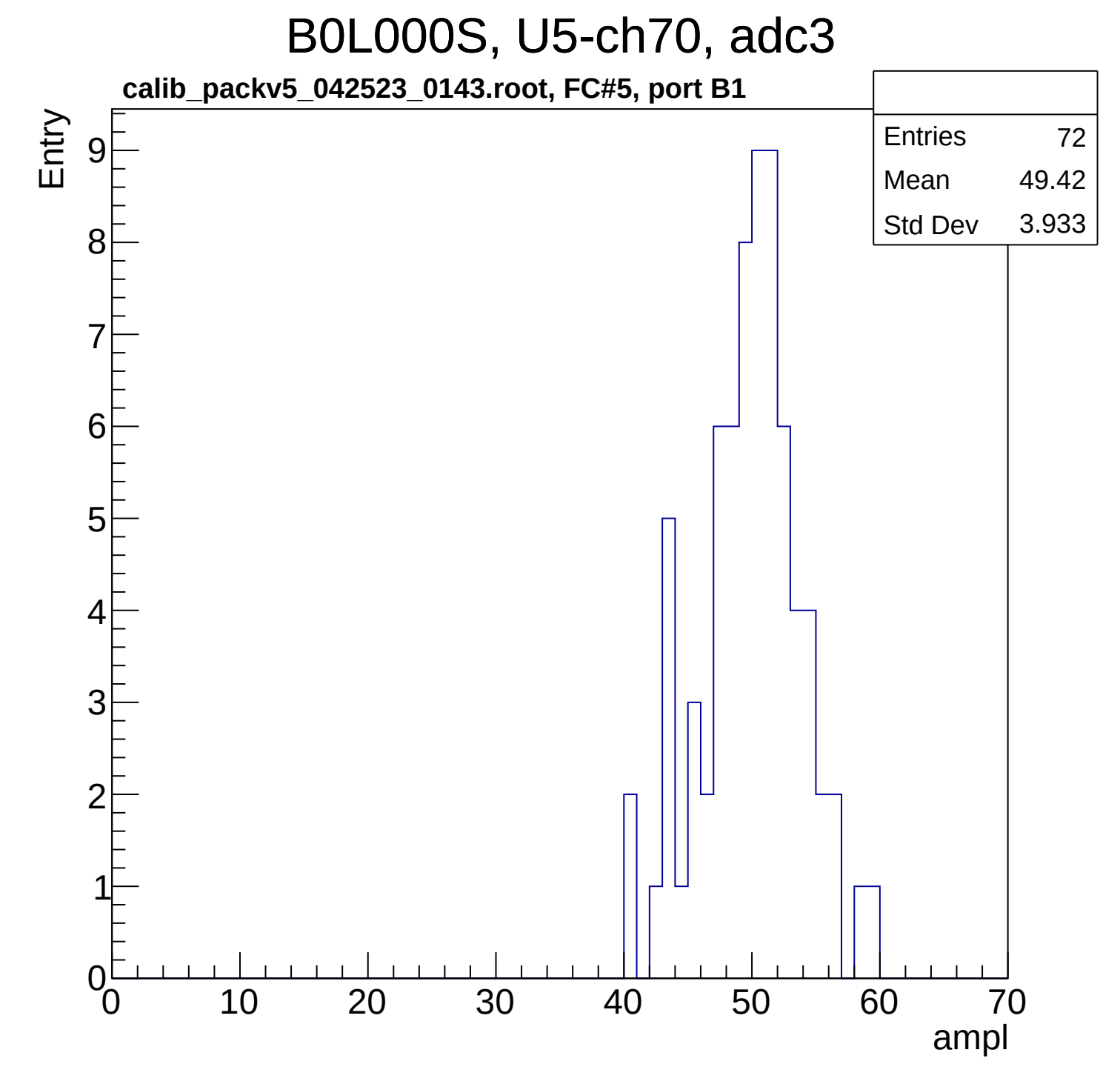
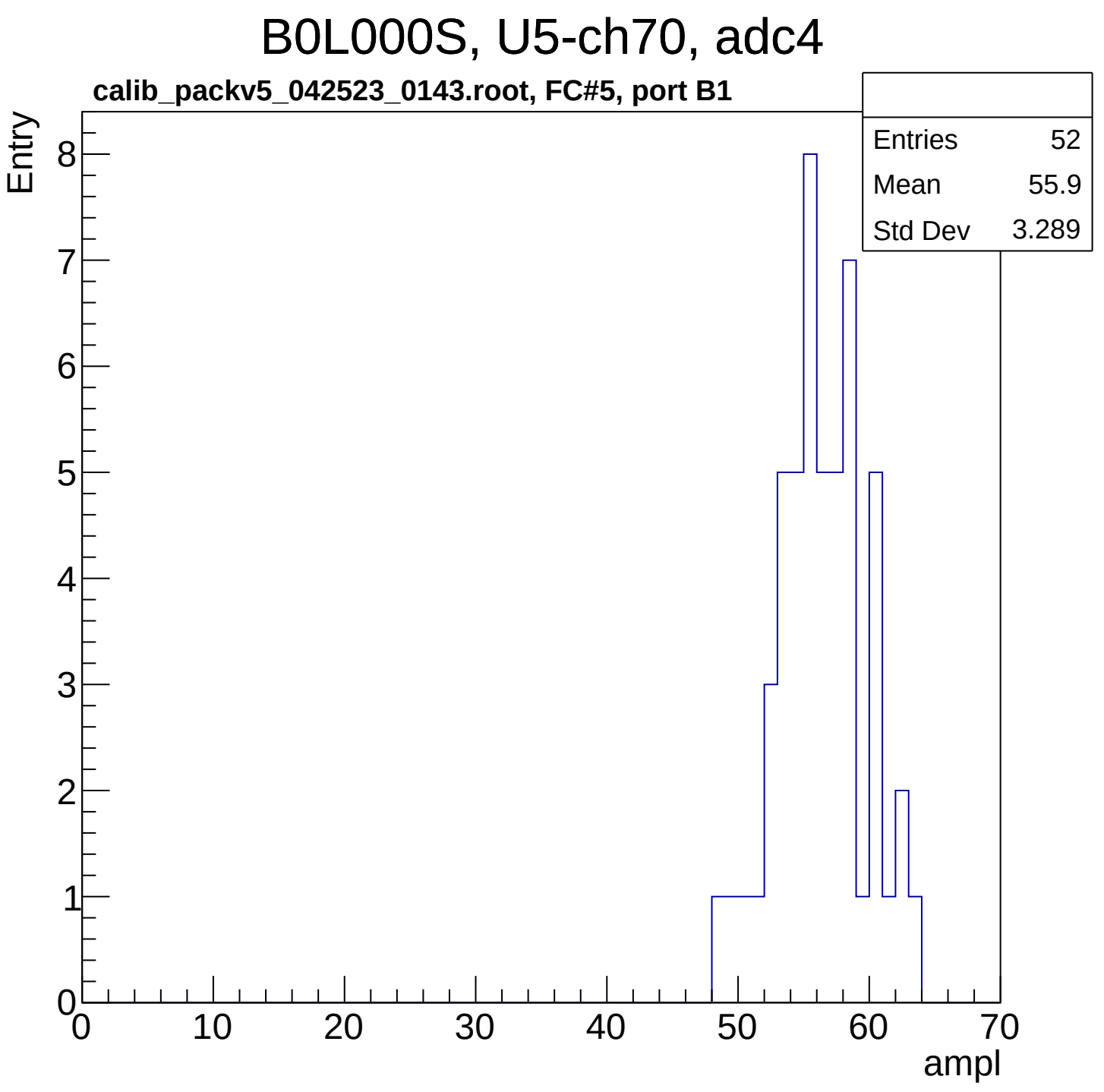

B0L000S, U5-ch70, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 42.3
Std Dev 4.005
Gaus mean : 44.0443
Gaus Width: 3.7868
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch70, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 49.42
Std Dev 3.933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch70, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 55.9
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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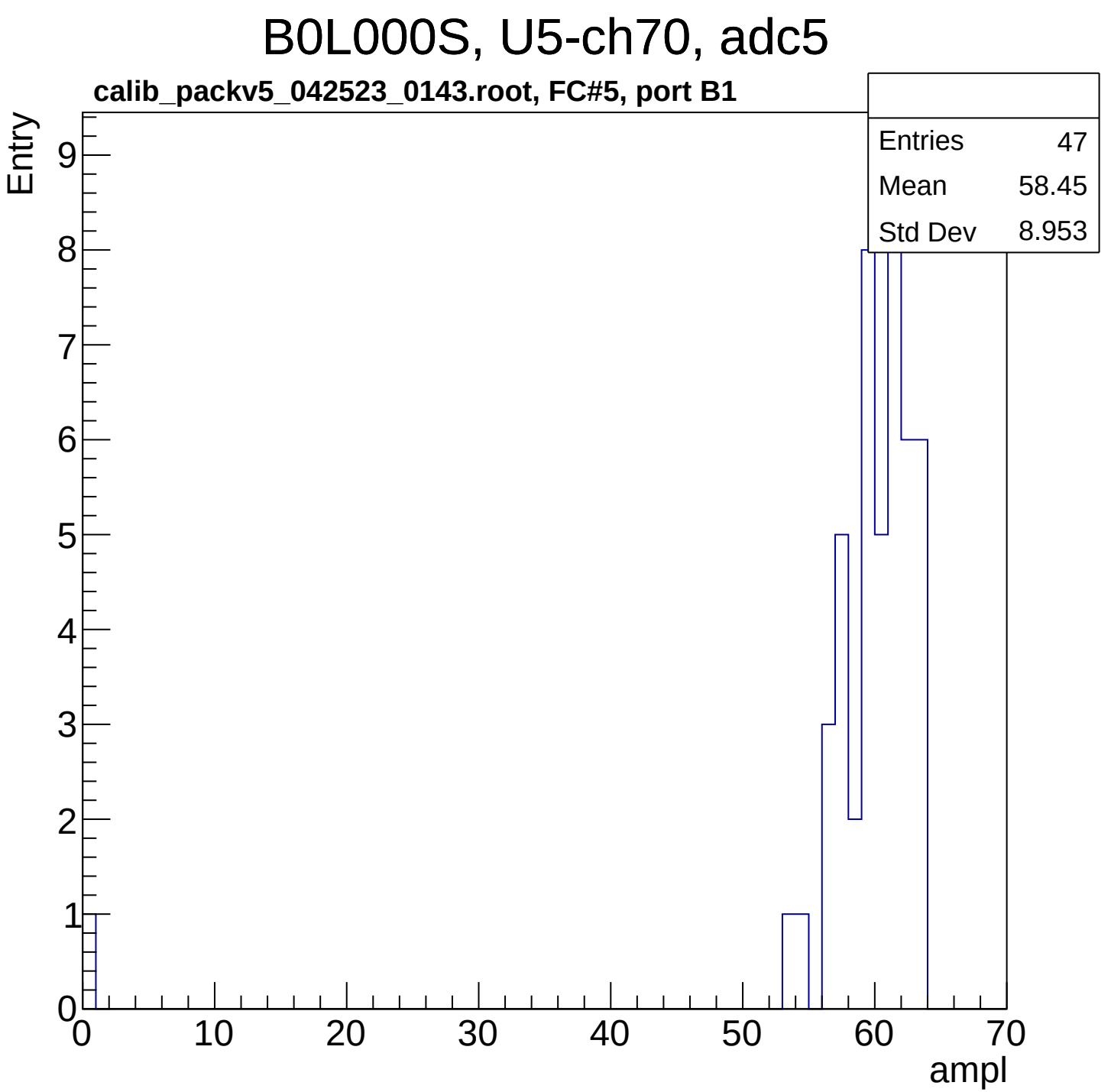
B0L000S, U5-ch70, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 58.45
Std Dev 8.953
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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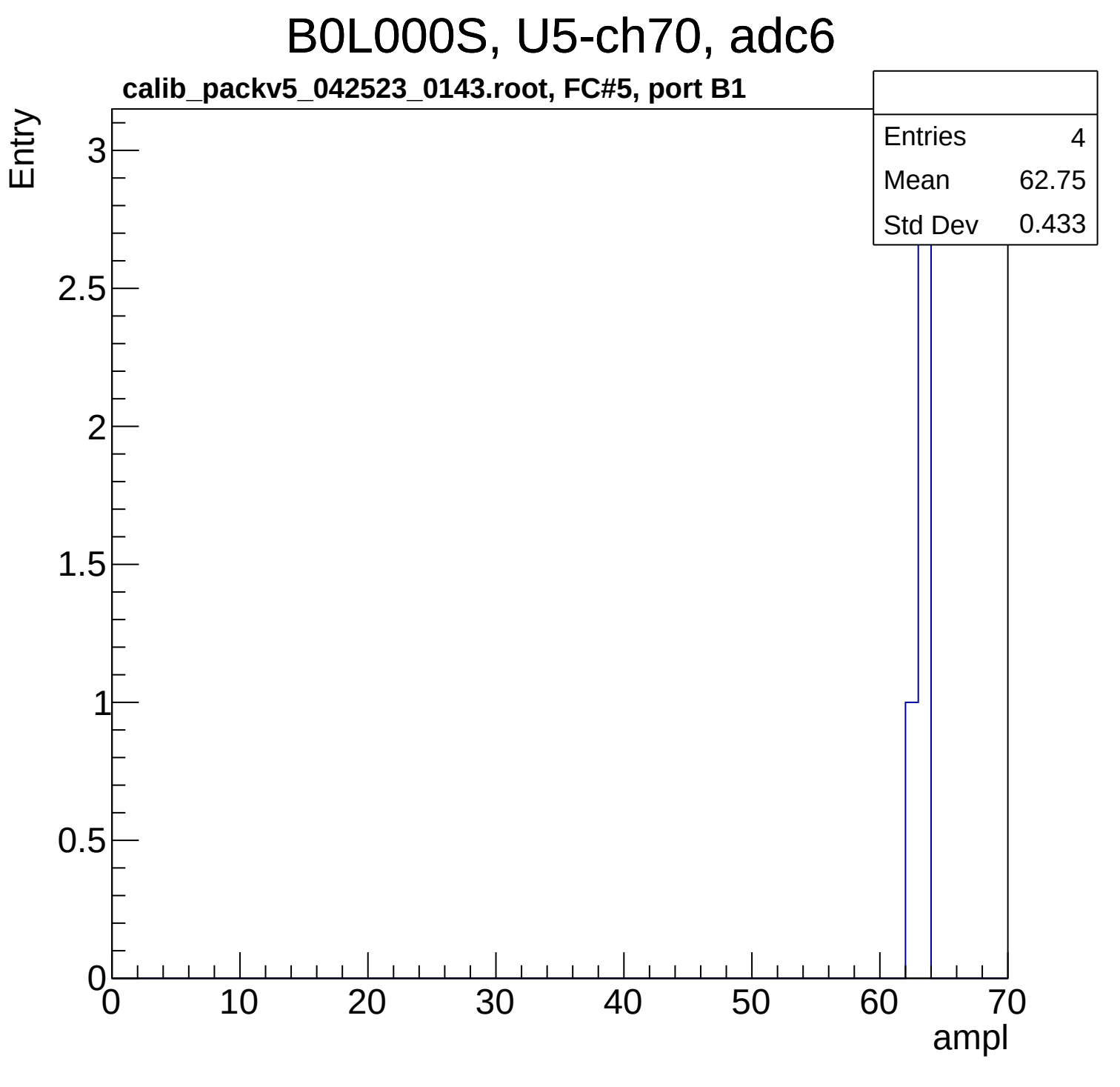
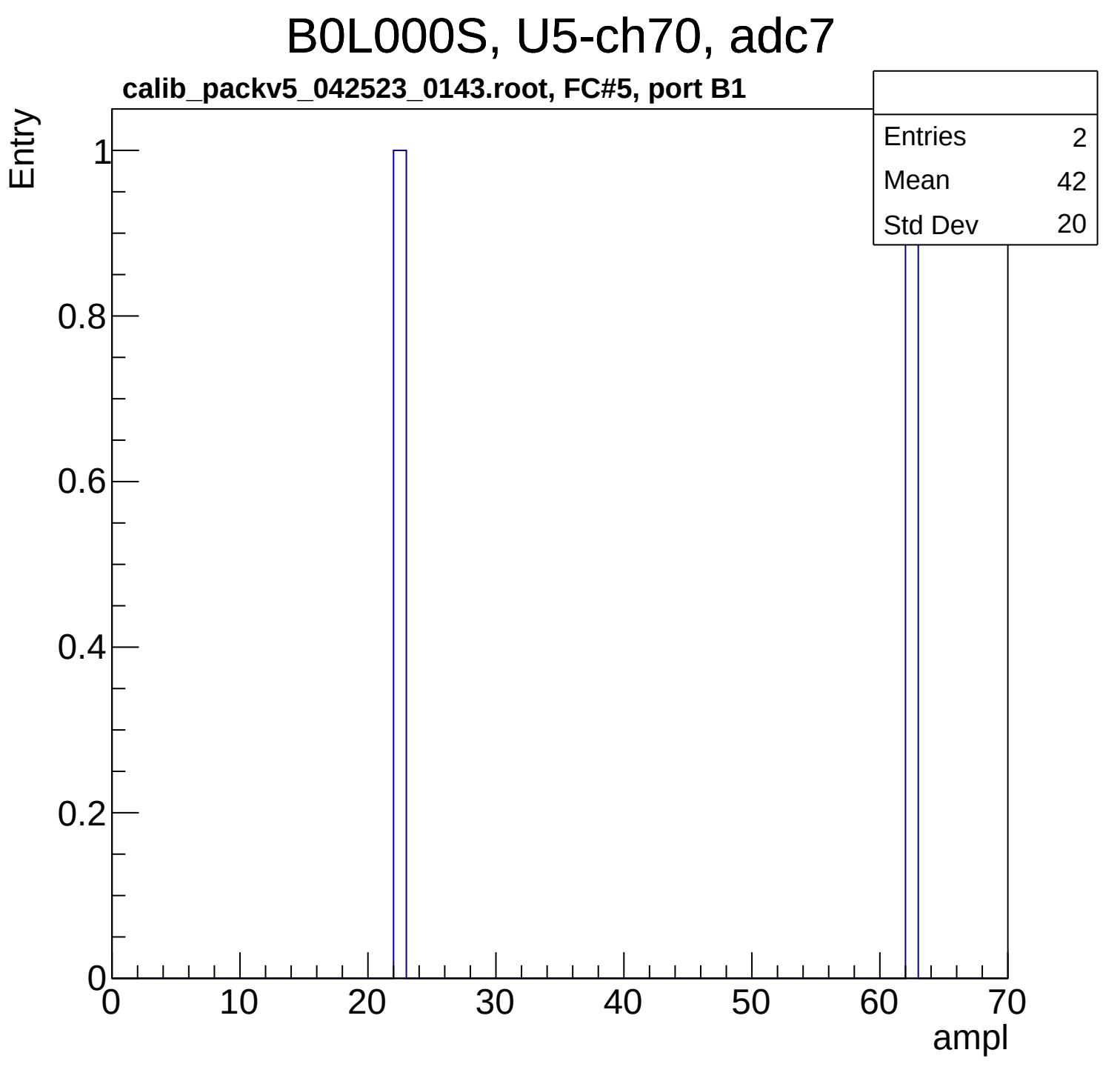

B0L000S, U5-ch70, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch70, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42
Std Dev 20
Entry
ampl
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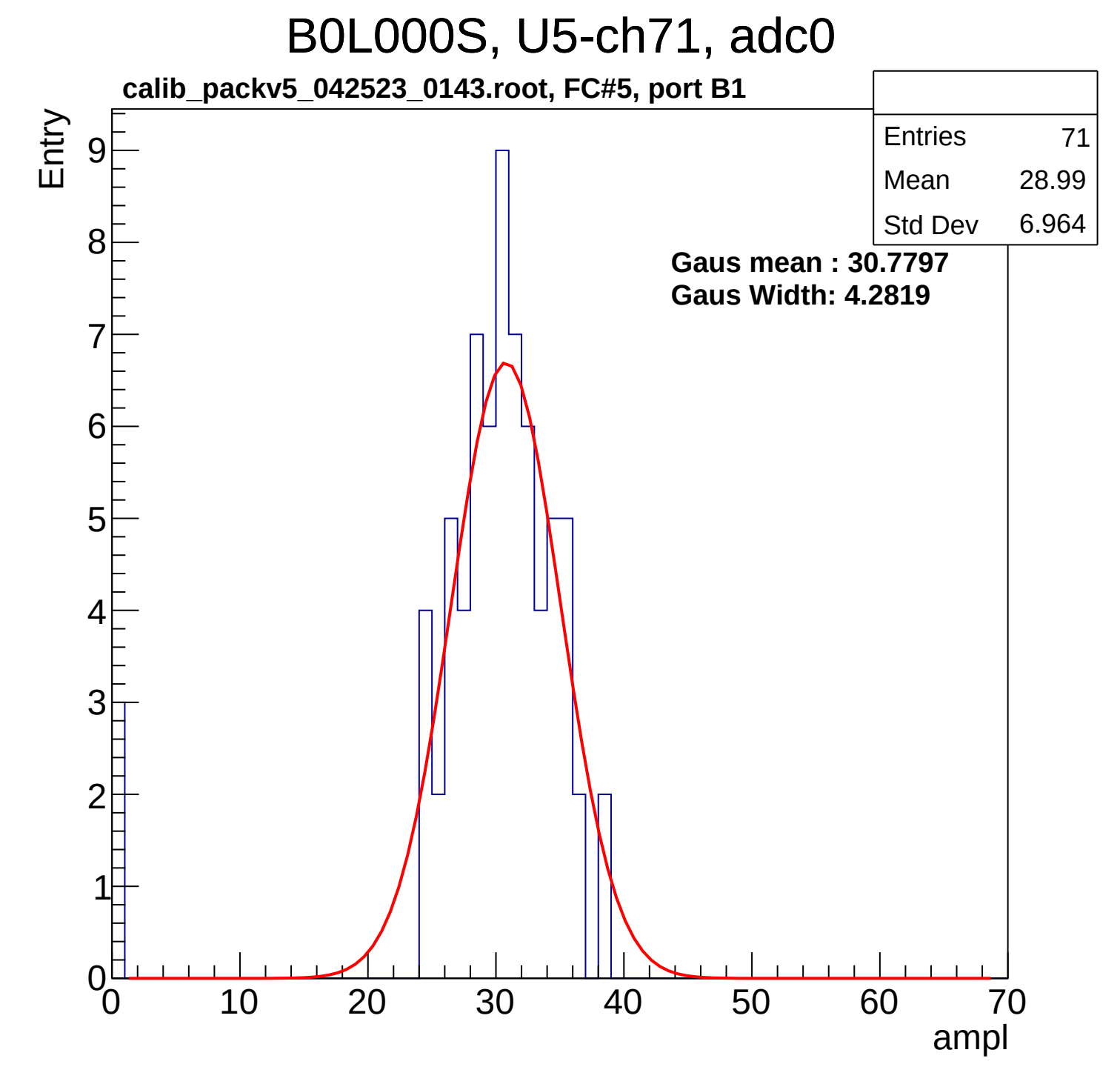
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U5-ch71, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 28.99
Std Dev 6.964
Gaus mean : 30.7797
Gaus Width: 4.2819
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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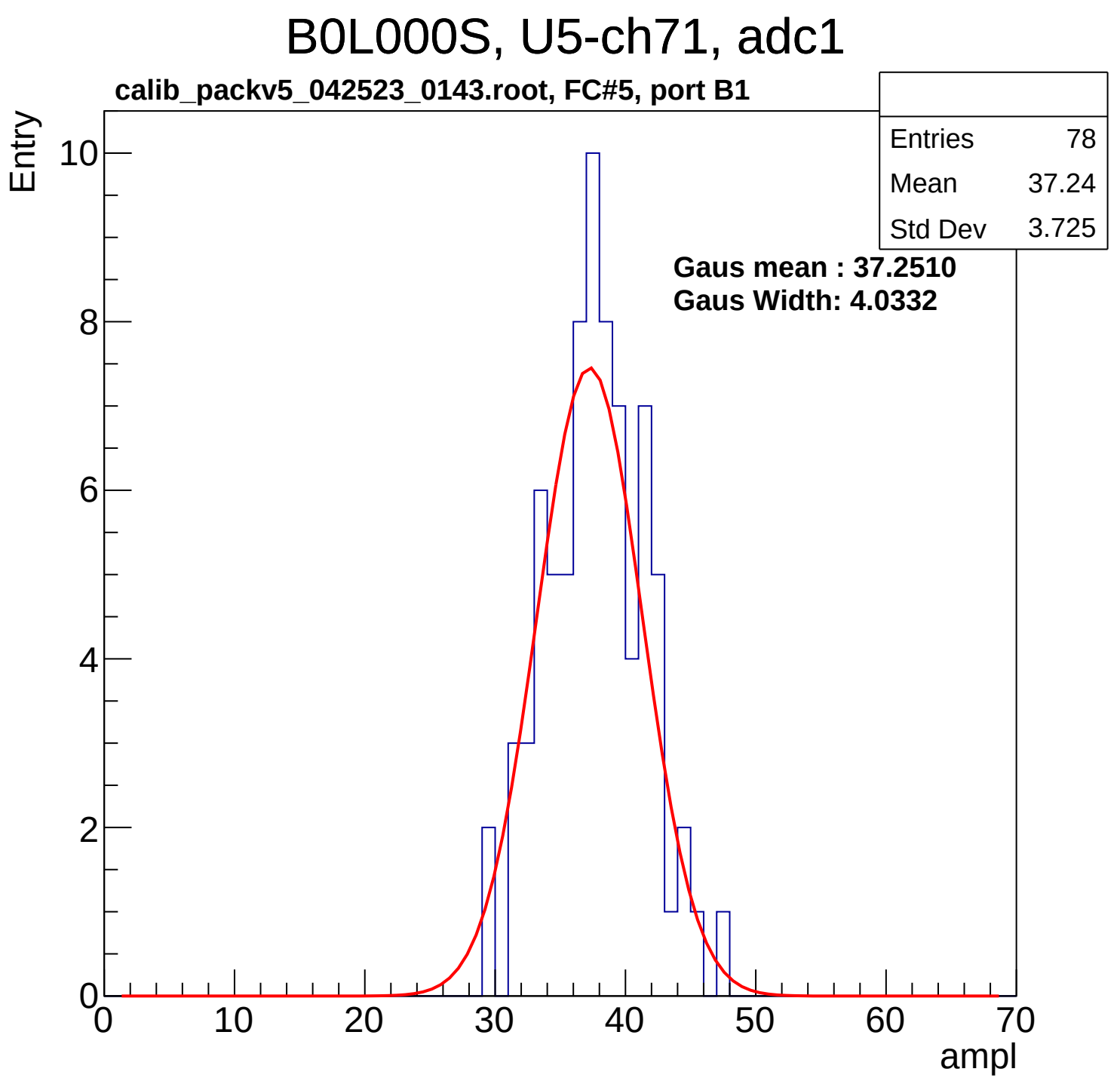
B0L000S, U5-ch71, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 37.24
Std Dev 3.725
Gaus mean : 37.2510
Gaus Width: 4.0332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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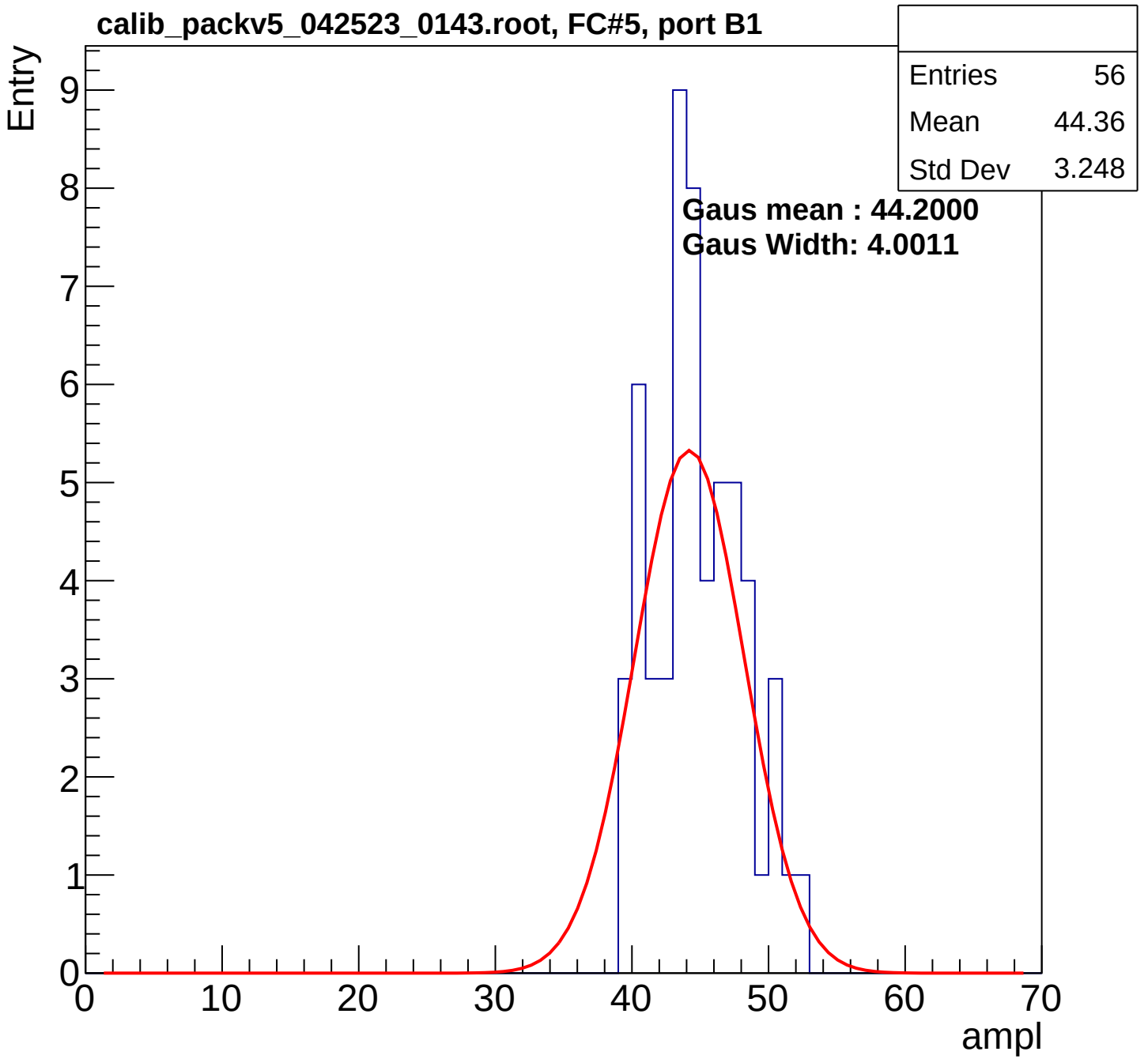
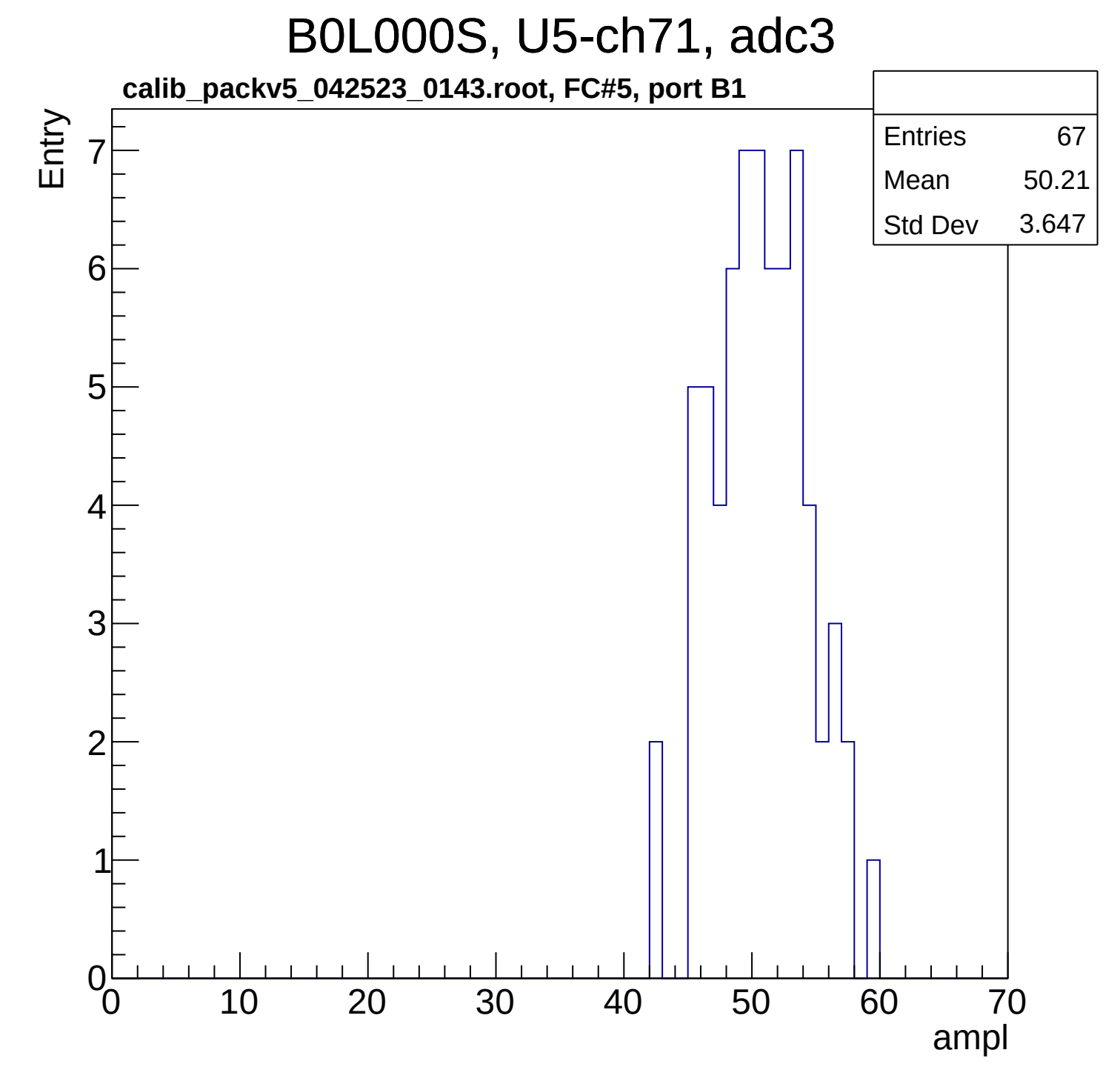

B0L000S, U5-ch71, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 44.36
Std Dev 3.248
Gaus mean : 44.2000
Gaus Width: 4.0011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch71, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 50.21
Std Dev 3.647
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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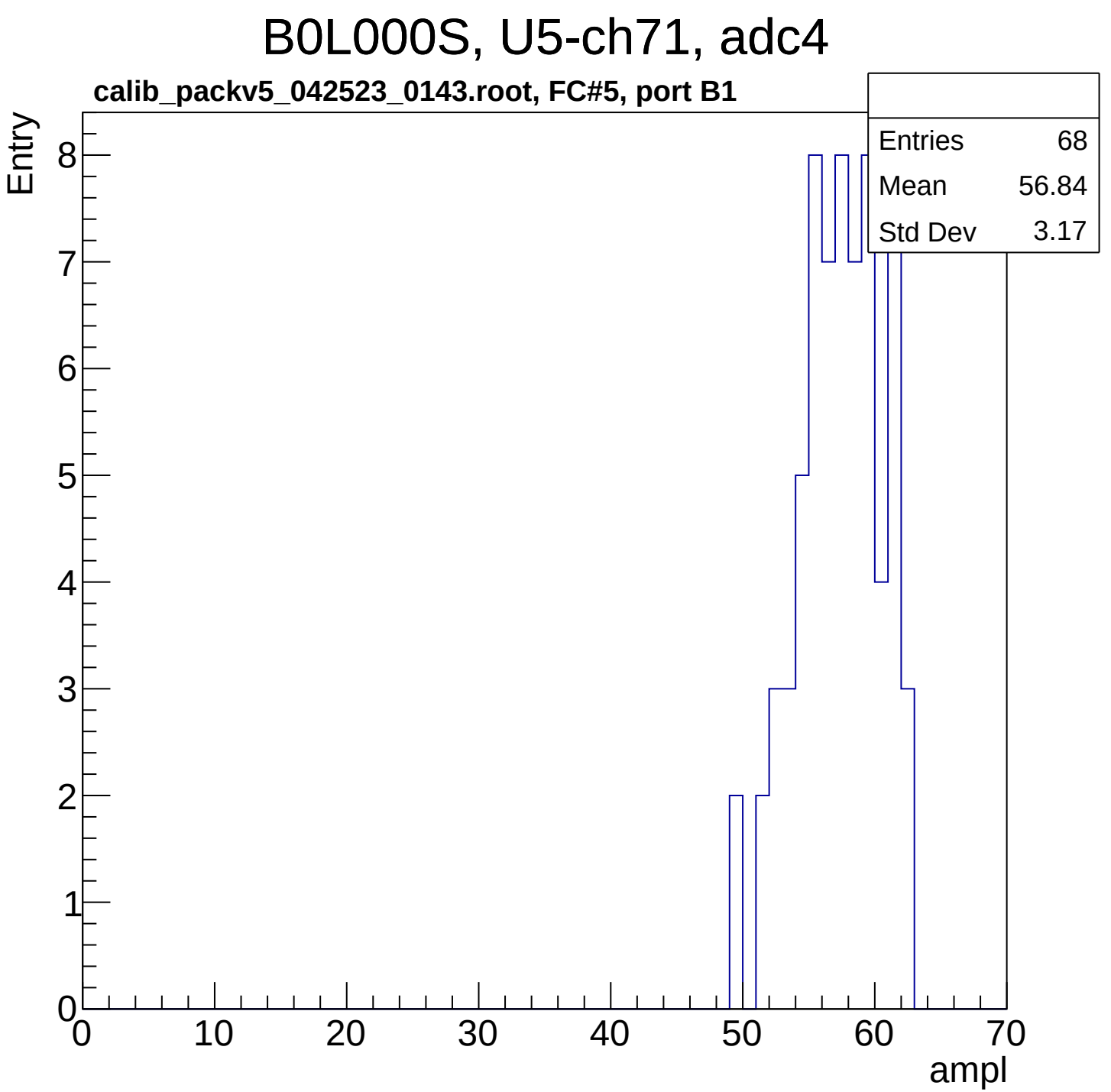
B0L000S, U5-ch71, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 56.84
Std Dev 3.17
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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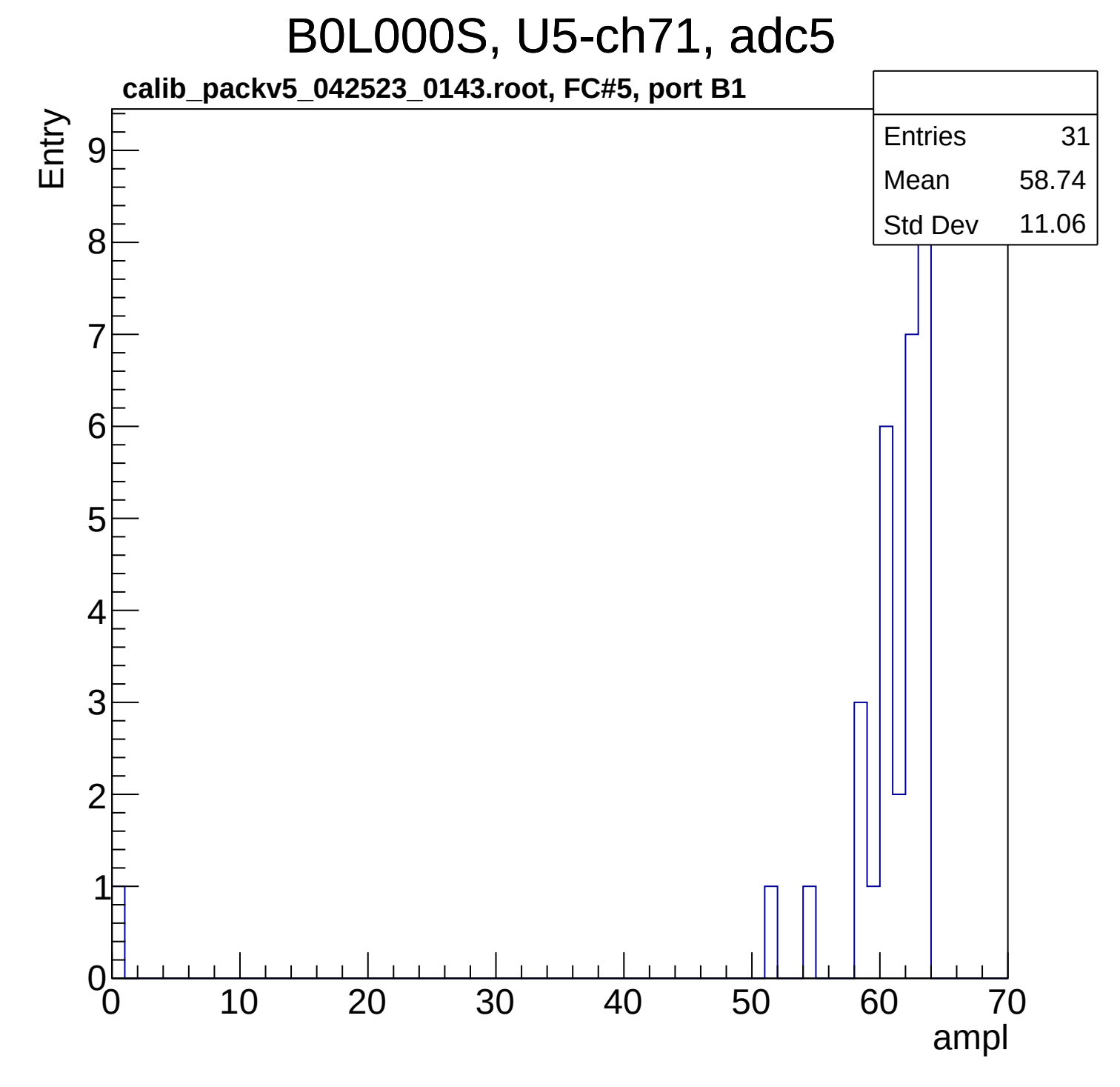
B0L000S, U5-ch71, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.74
Std Dev 11.06
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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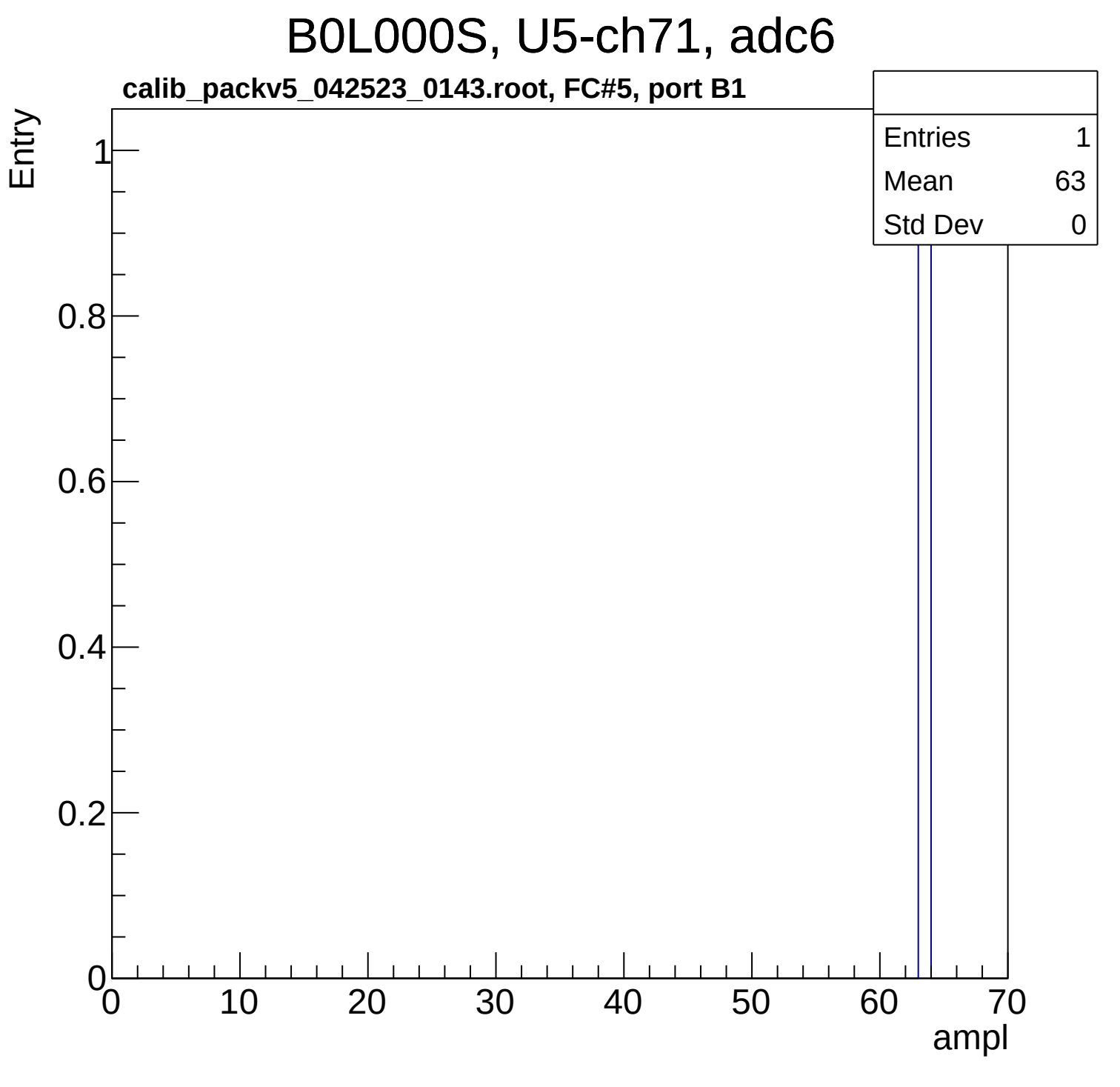
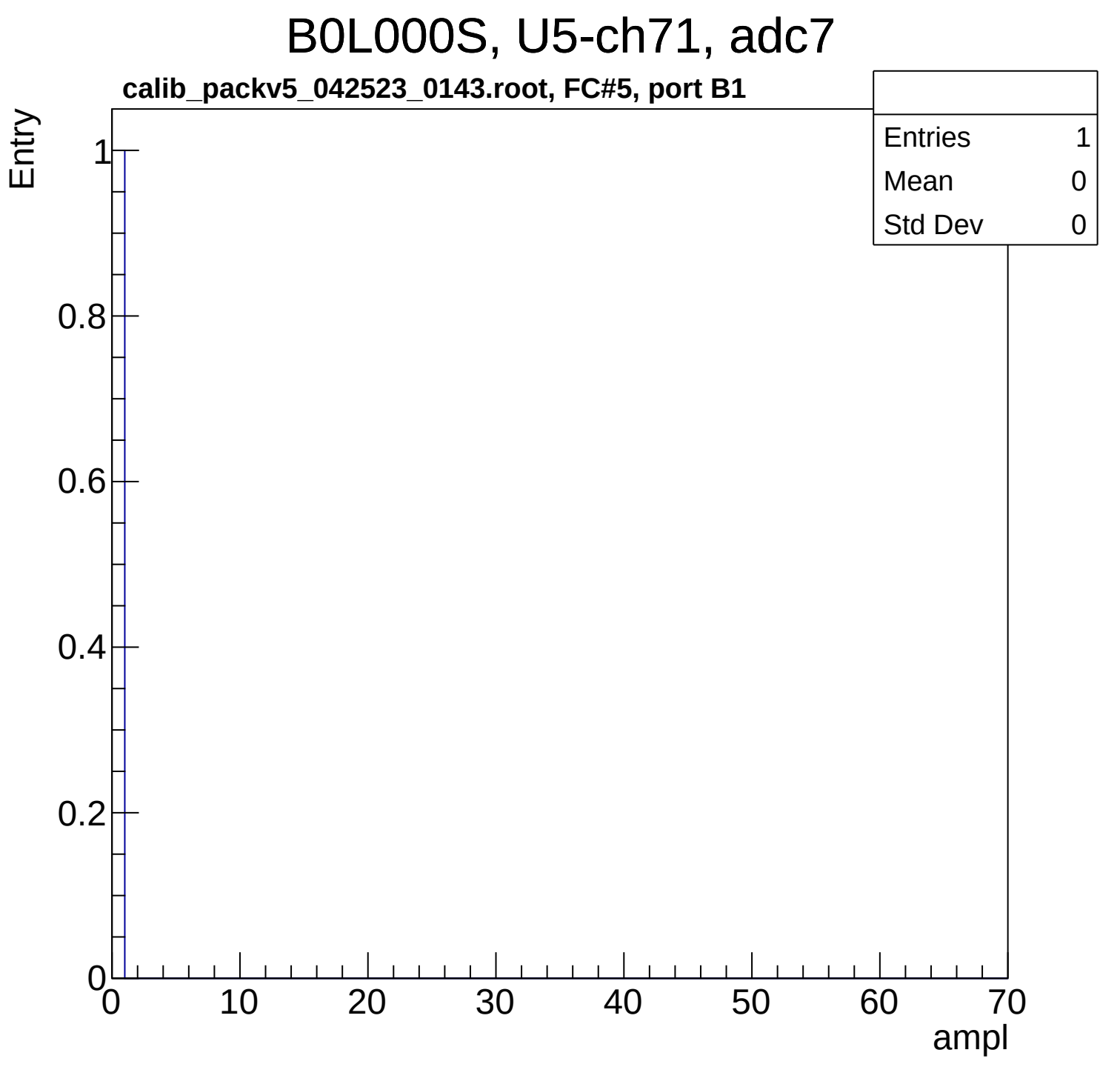
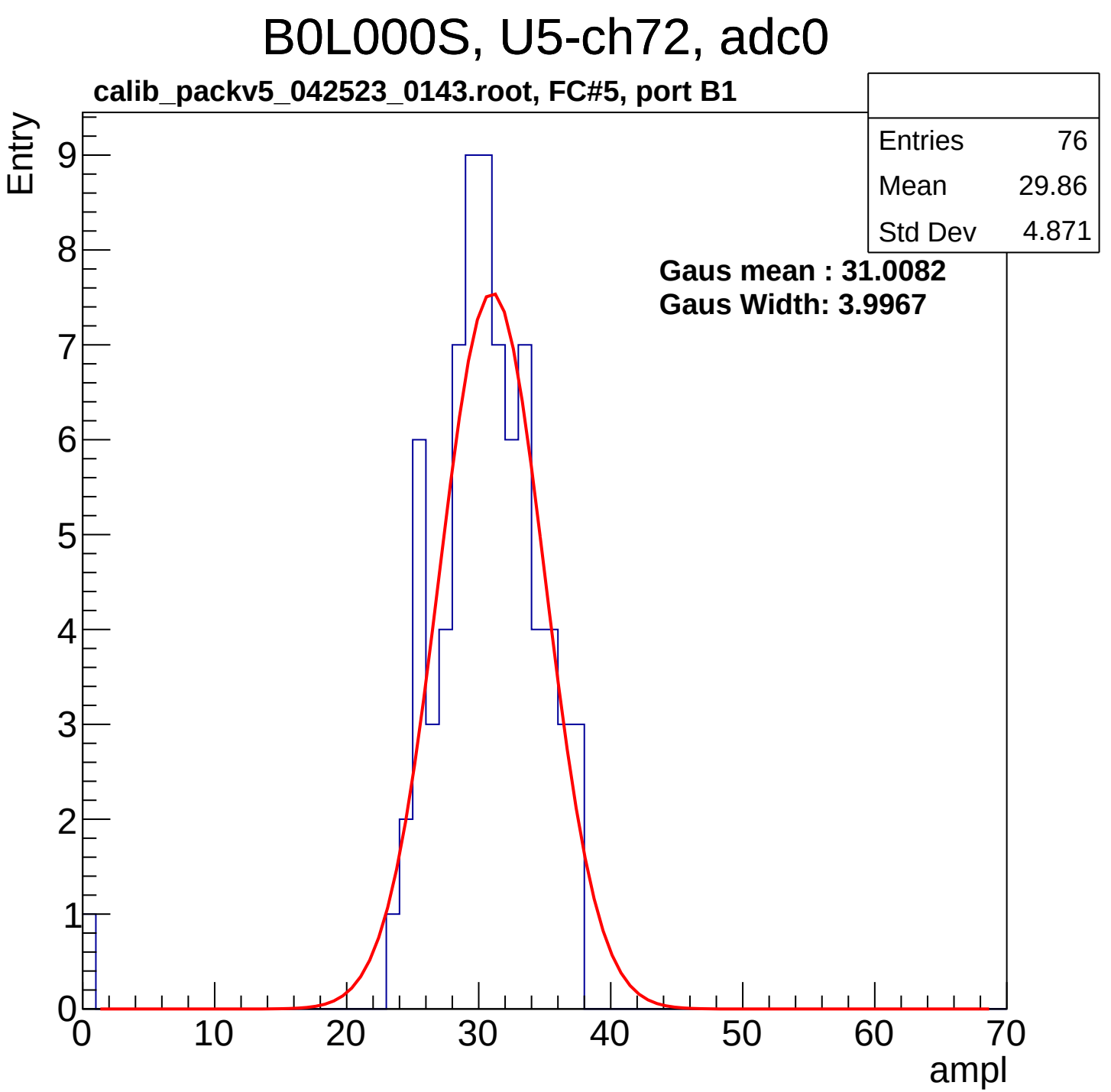

B0L000S, U5-ch71, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch71, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch72, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.86
Std Dev 4.871
Gaus mean : 31.0082
Gaus Width: 3.9967
Entry
ampl
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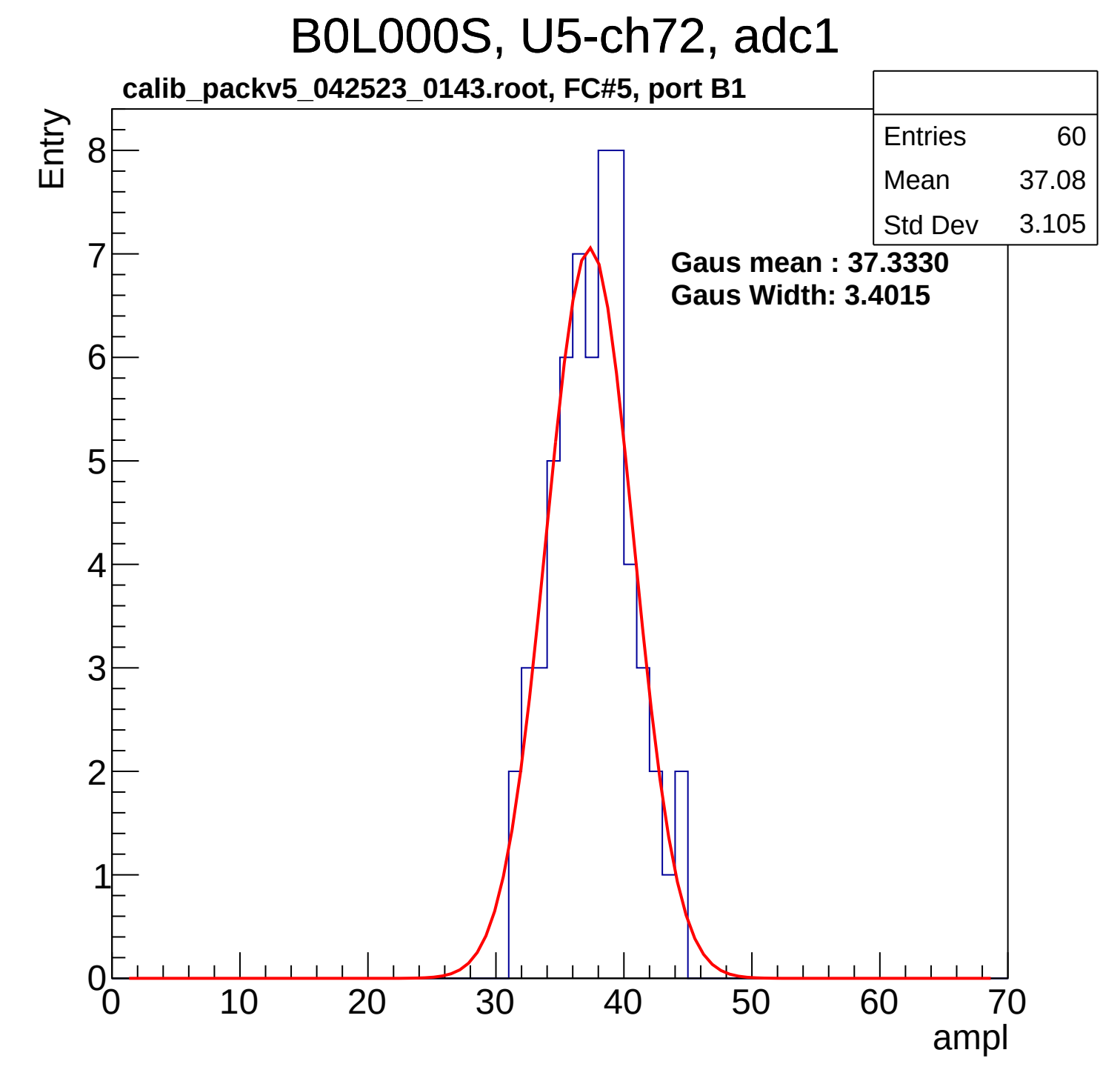
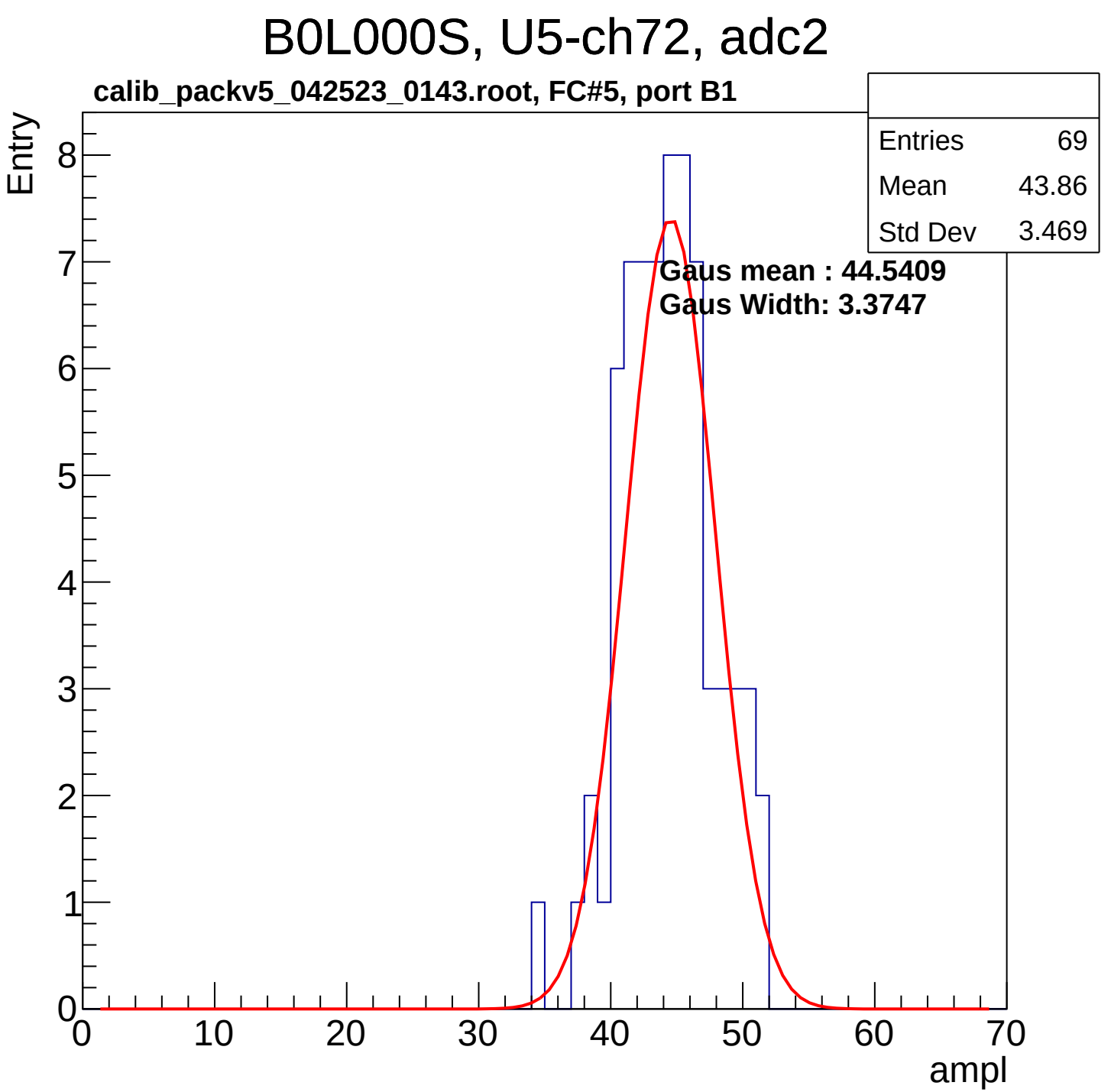
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch72, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.08
Std Dev 3.105
Gaus mean : 37.3330
Gaus Width: 3.4015
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch72, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 43.86
Std Dev 3.469
Gaus mean : 44.5409
Gaus Width: 3.3747
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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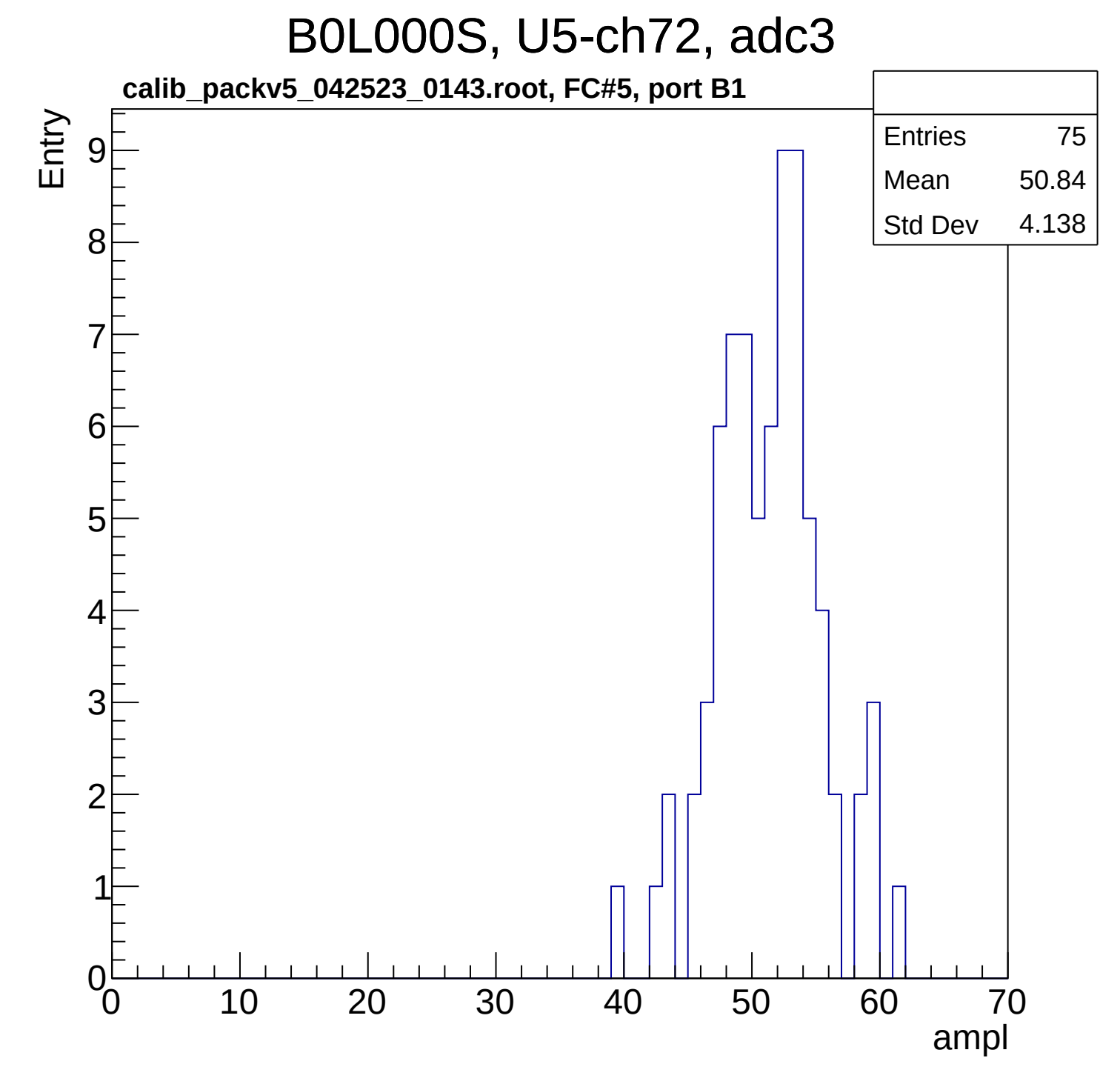
60
70


B0L000S, U5-ch72, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.84
Std Dev 4.138
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
50
60
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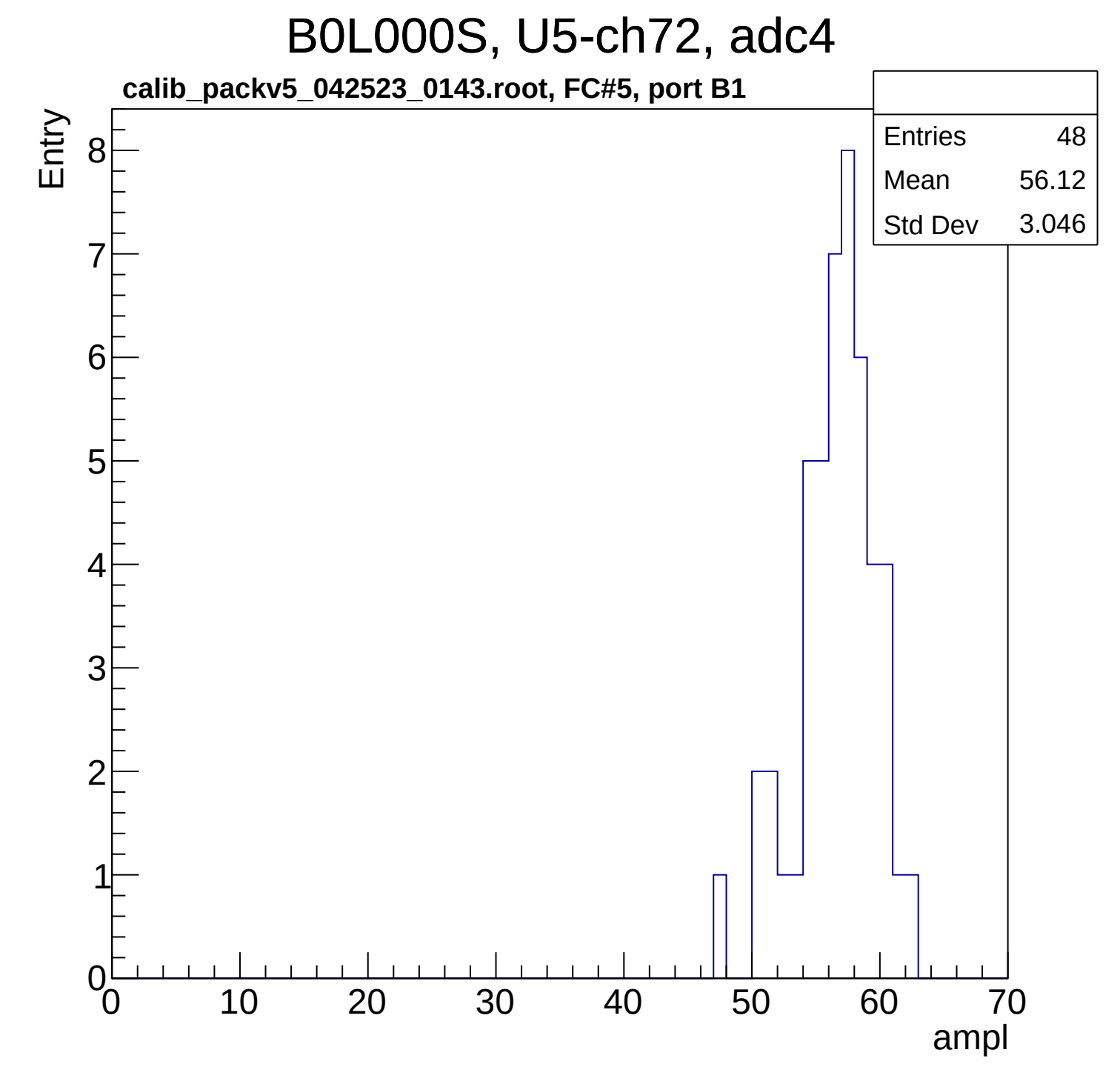
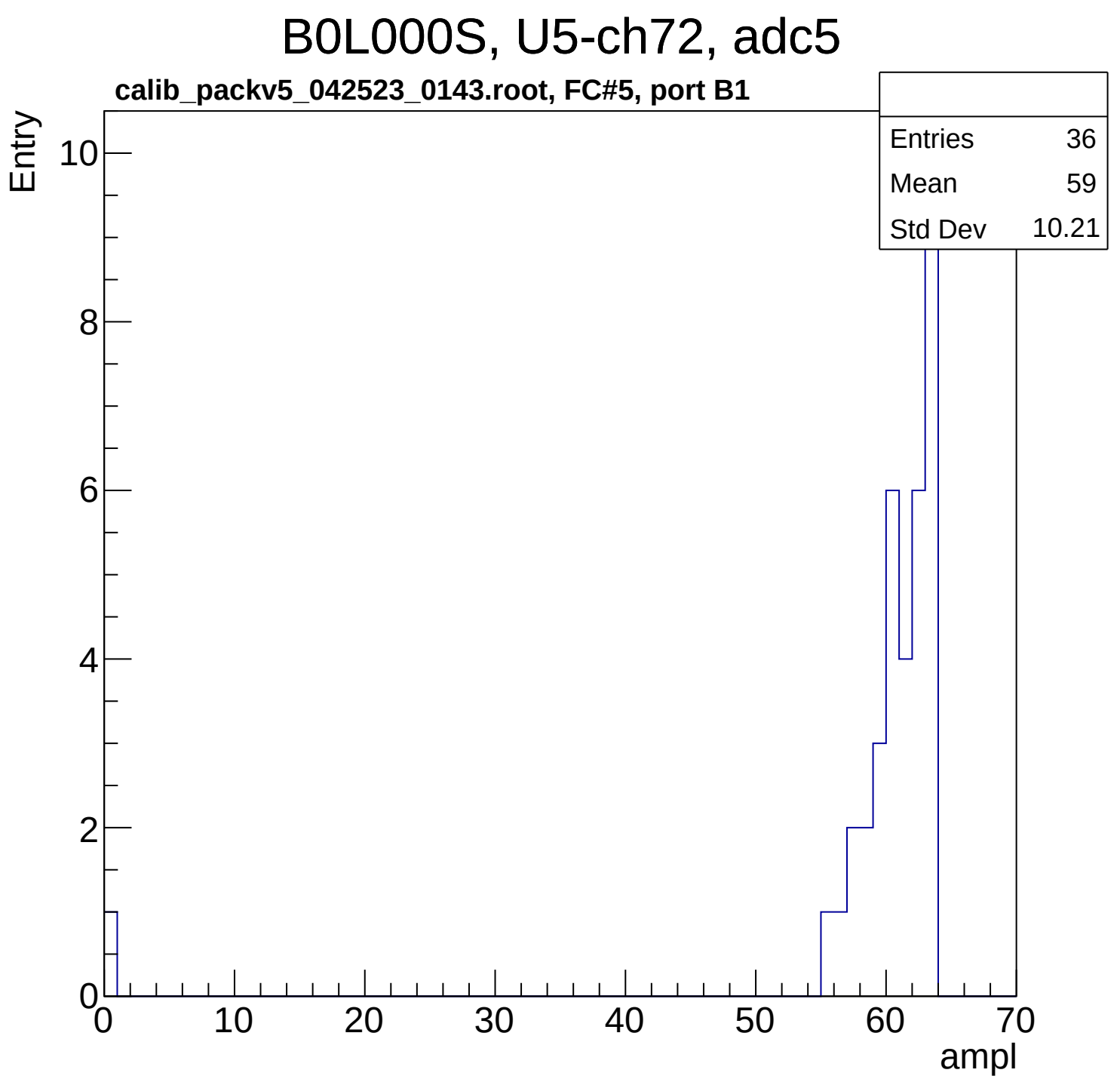
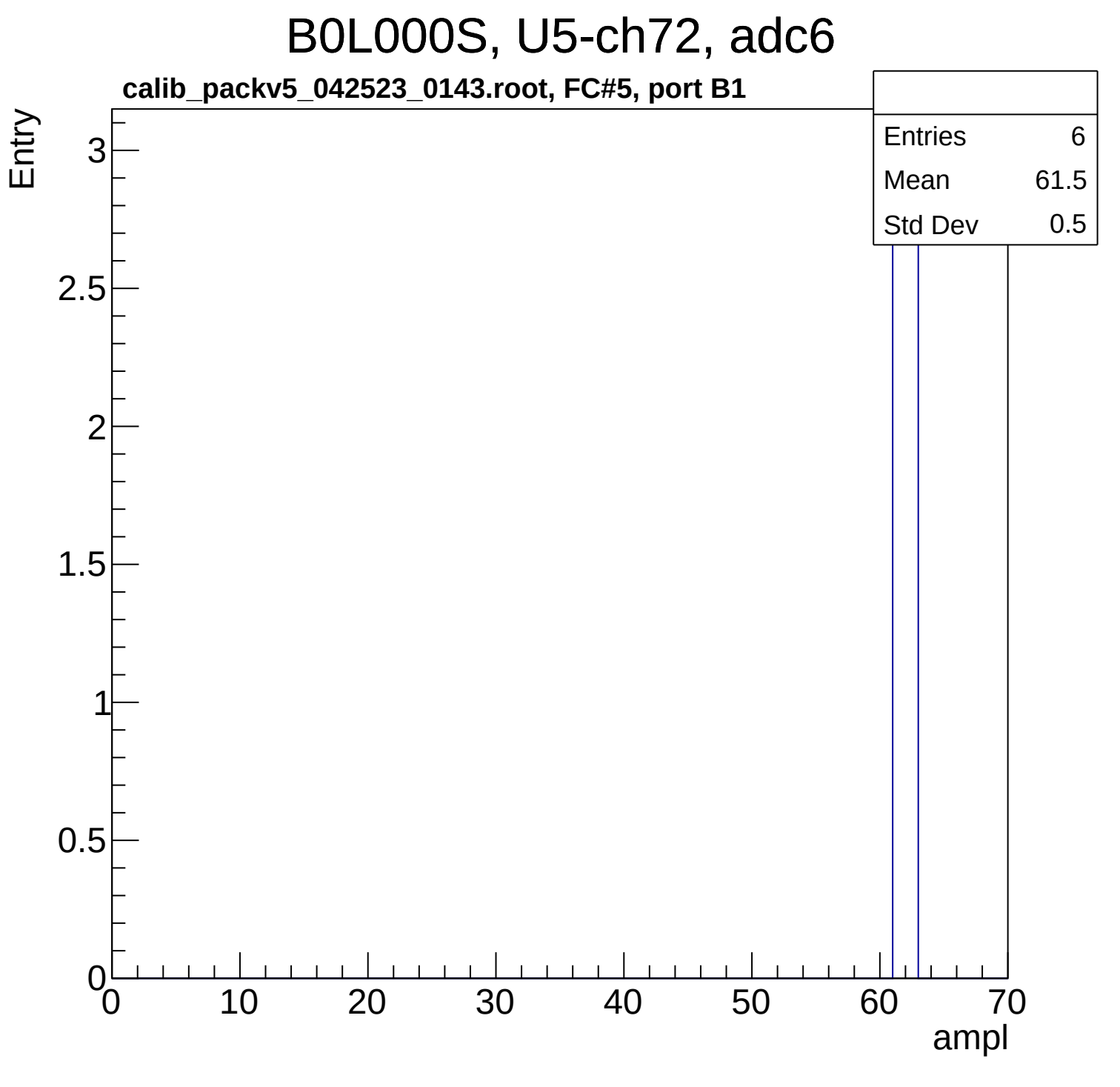

B0L000S, U5-ch72, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.12
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch72, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 36
Mean 59
Std Dev 10.21
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch72, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.5
Std Dev 0.5
Entry
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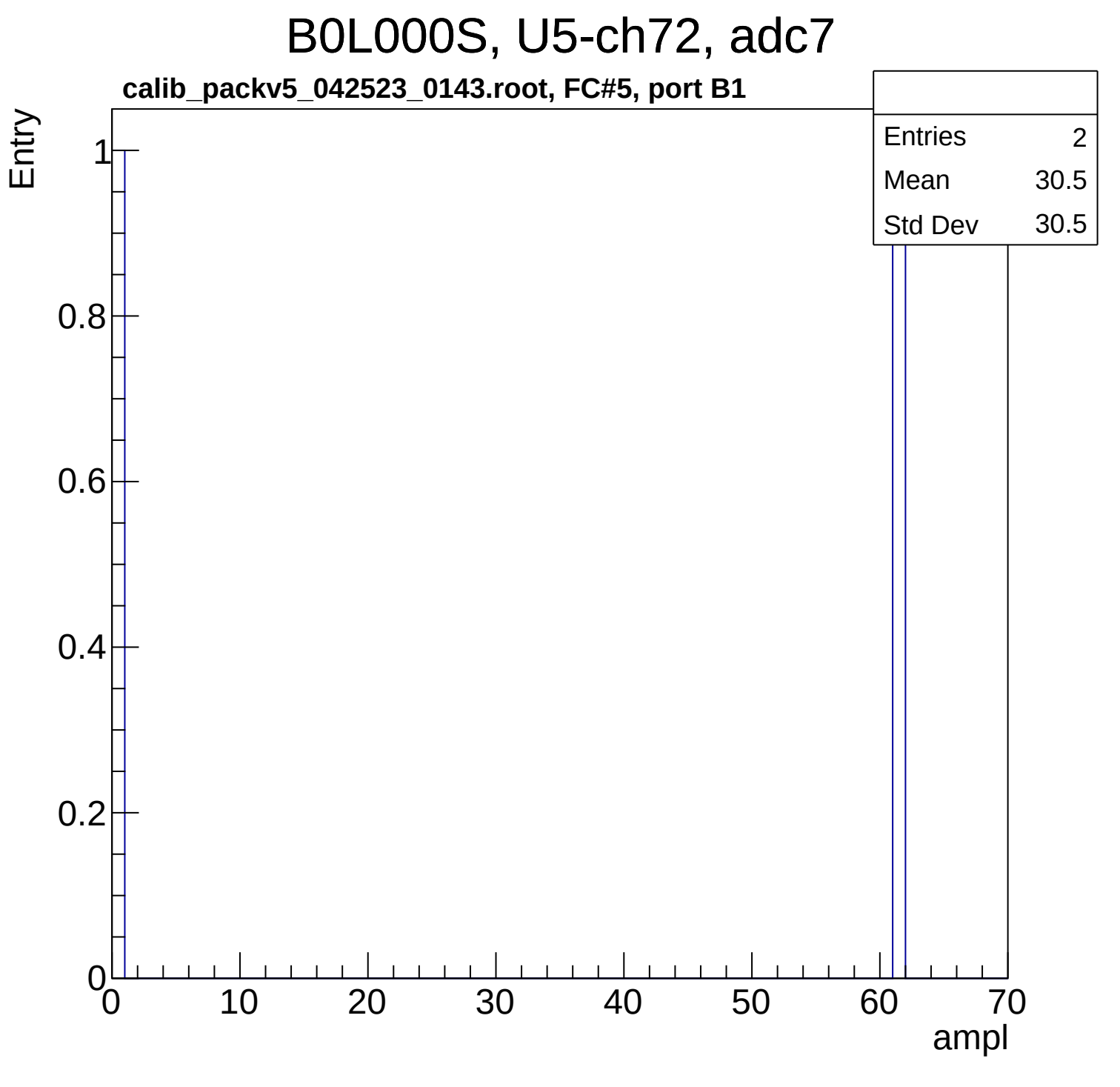
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch72, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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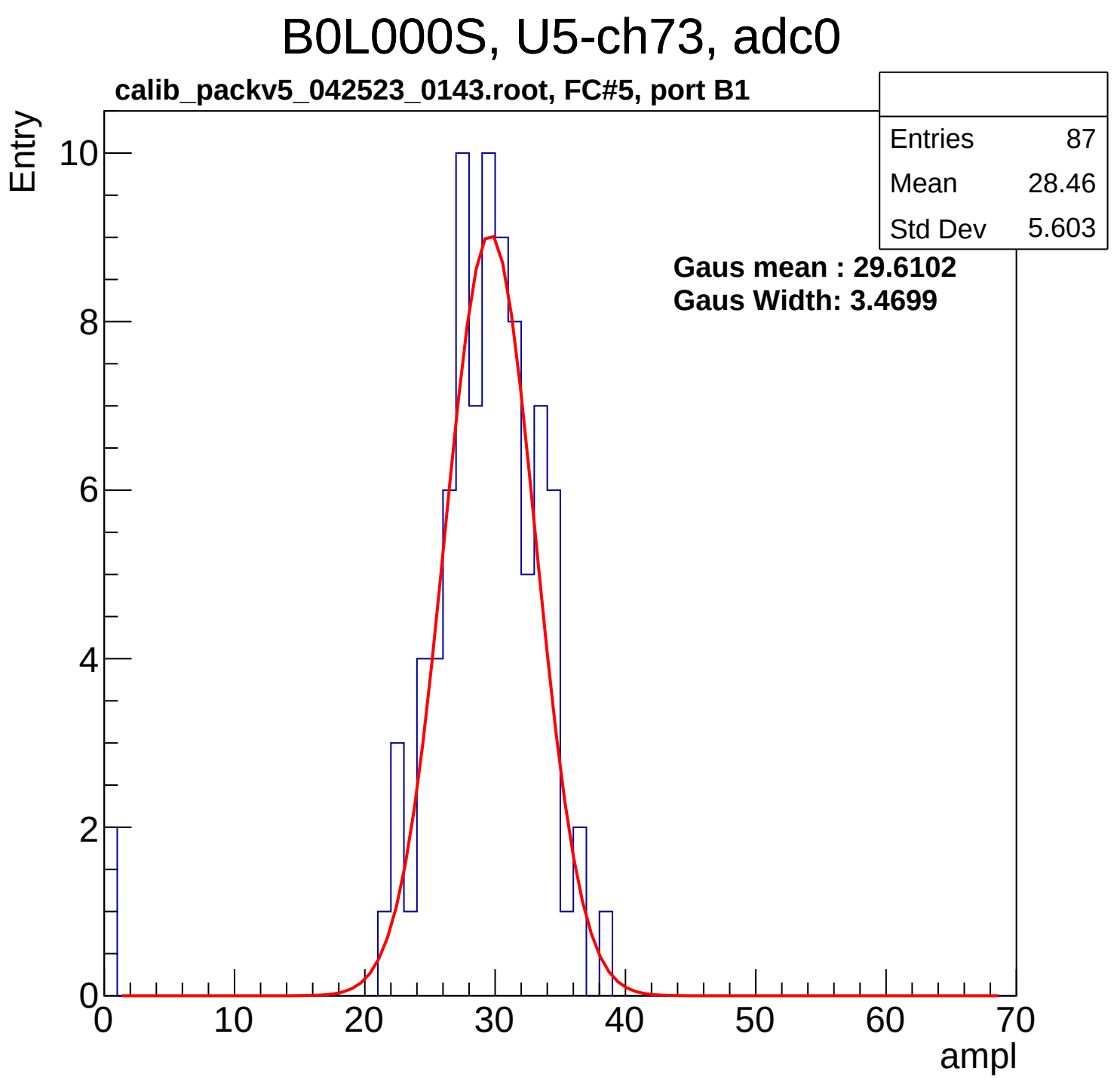
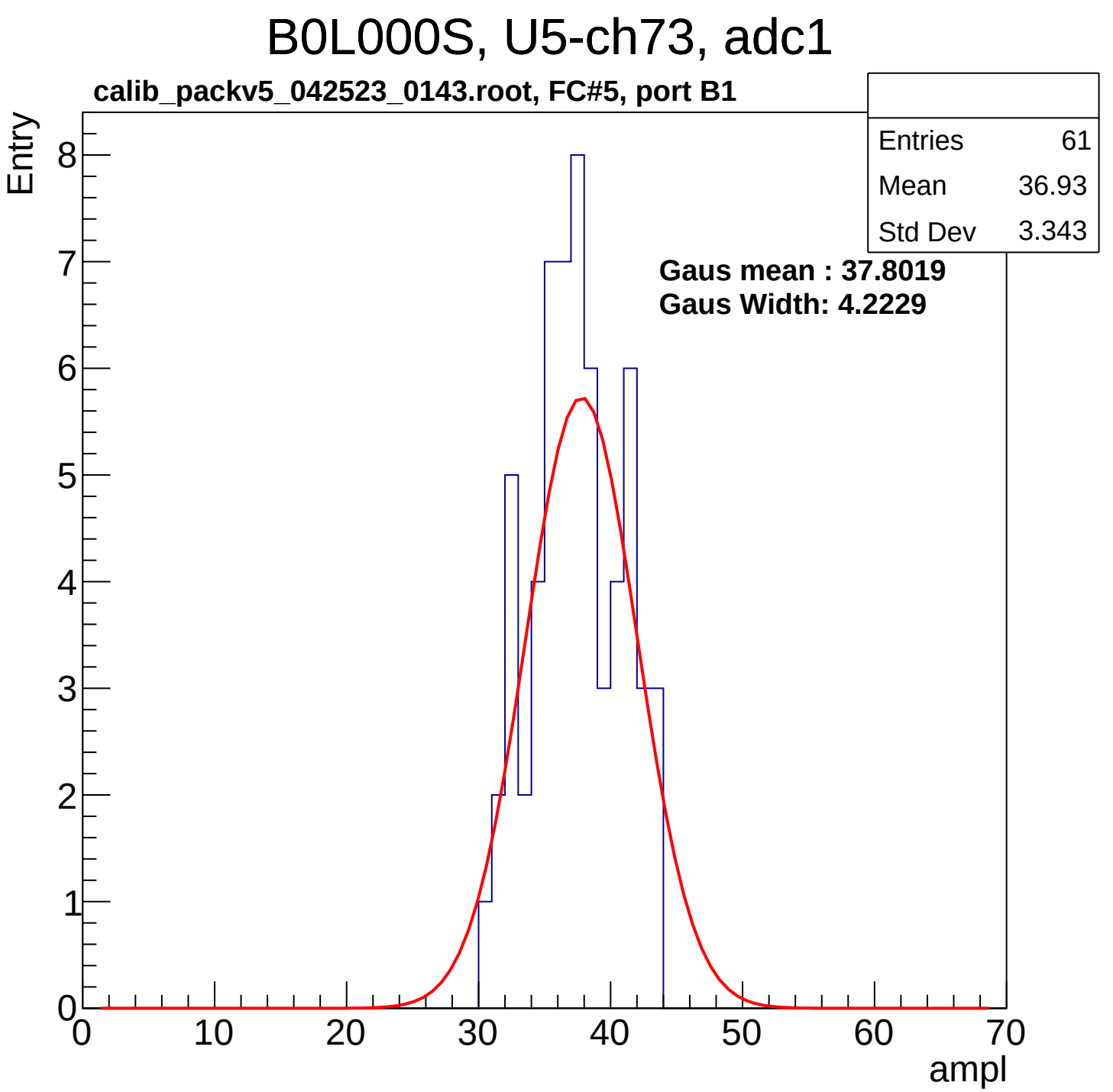
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch73, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 87
Mean 28.46
Std Dev 5.603
Gaus mean : 29.6102
Gaus Width: 3.4699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch73, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 36.93
Std Dev 3.343
Gaus mean : 37.8019
Gaus Width: 4.2229
Entry
ampl
0
1
2
3
4
5
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7
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0
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40
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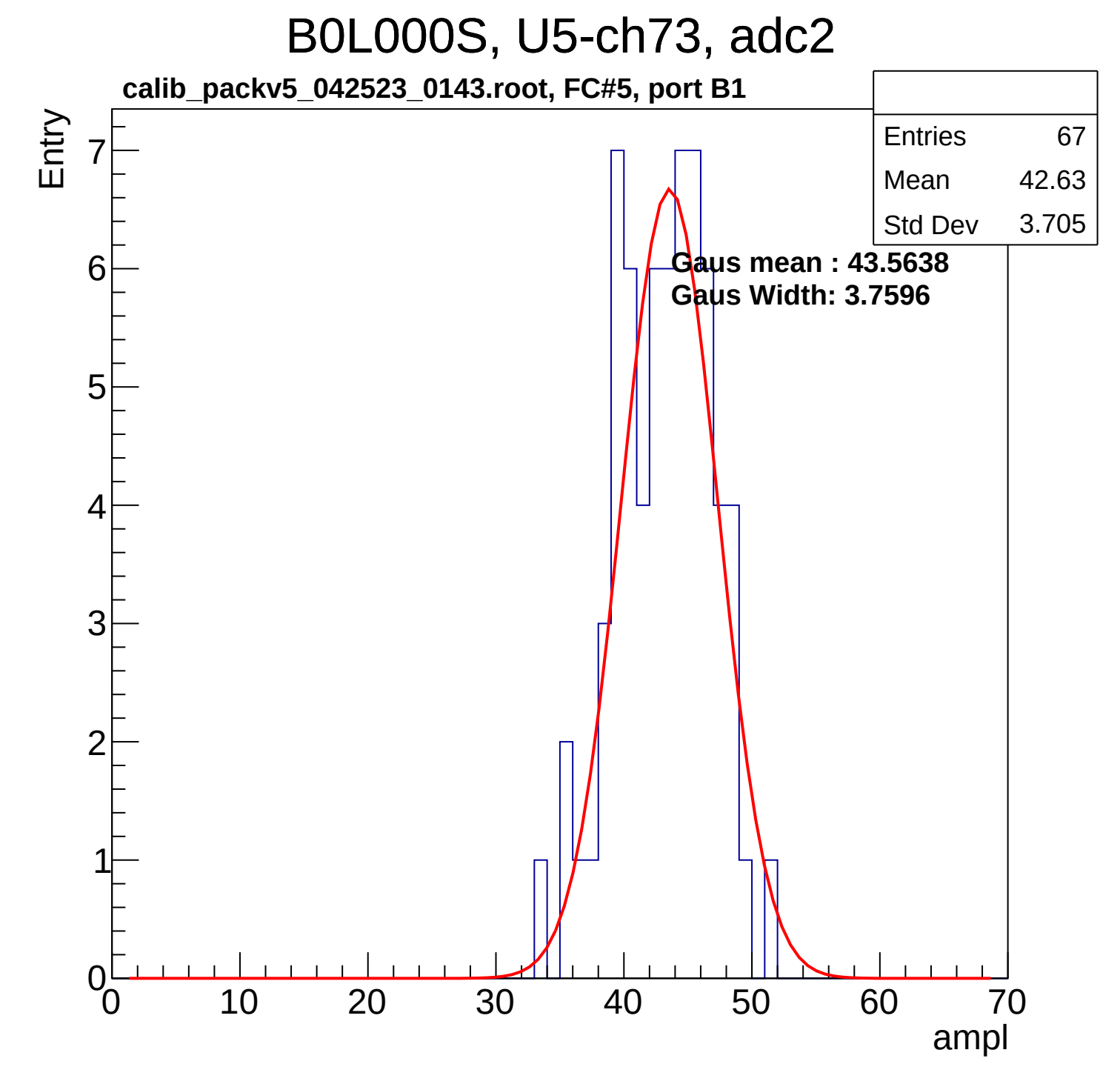
B0L000S, U5-ch73, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 42.63
Std Dev 3.705
Gaus mean : 43.5638
Gaus Width: 3.7596
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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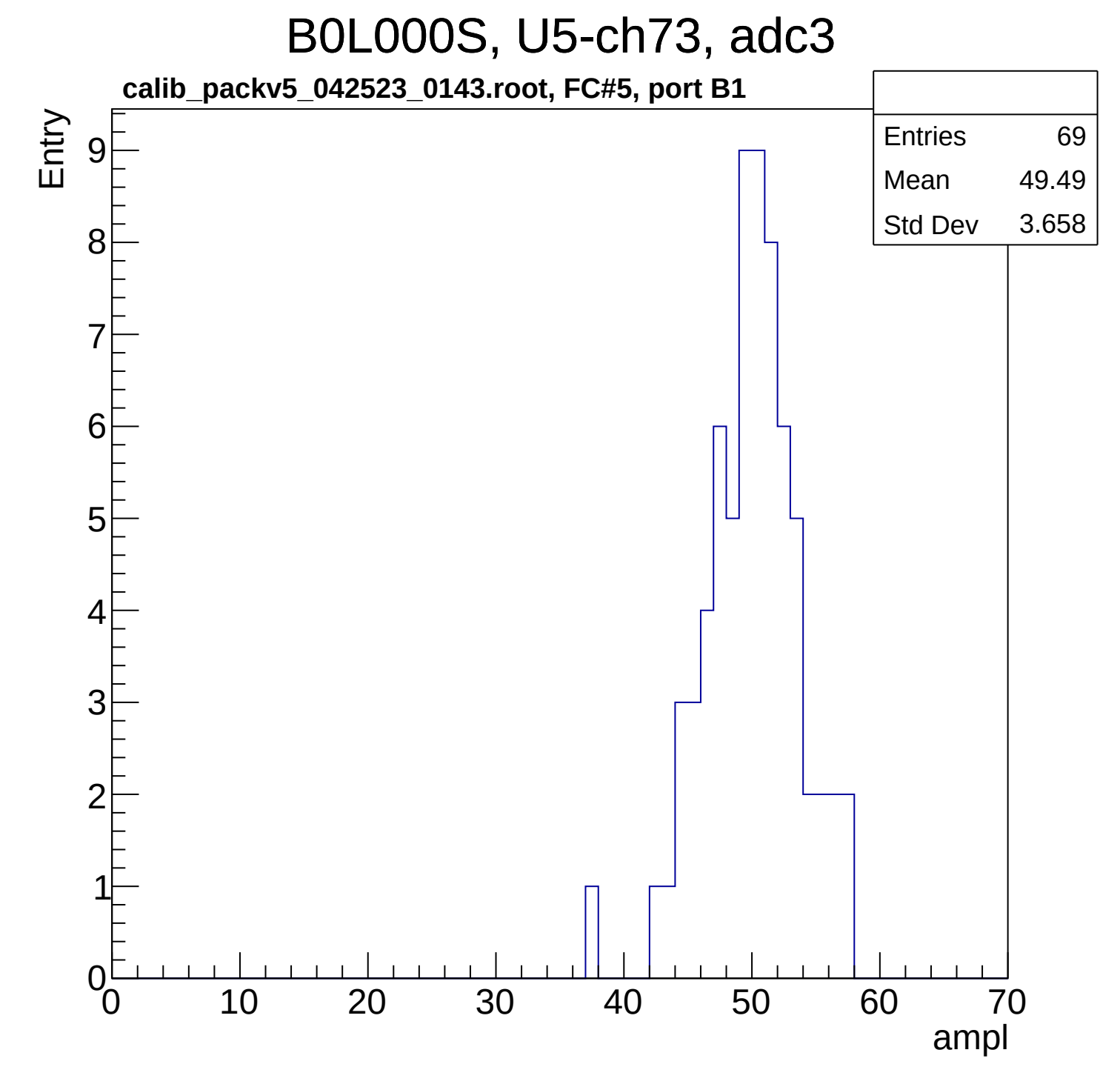
B0L000S, U5-ch73, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 49.49
Std Dev 3.658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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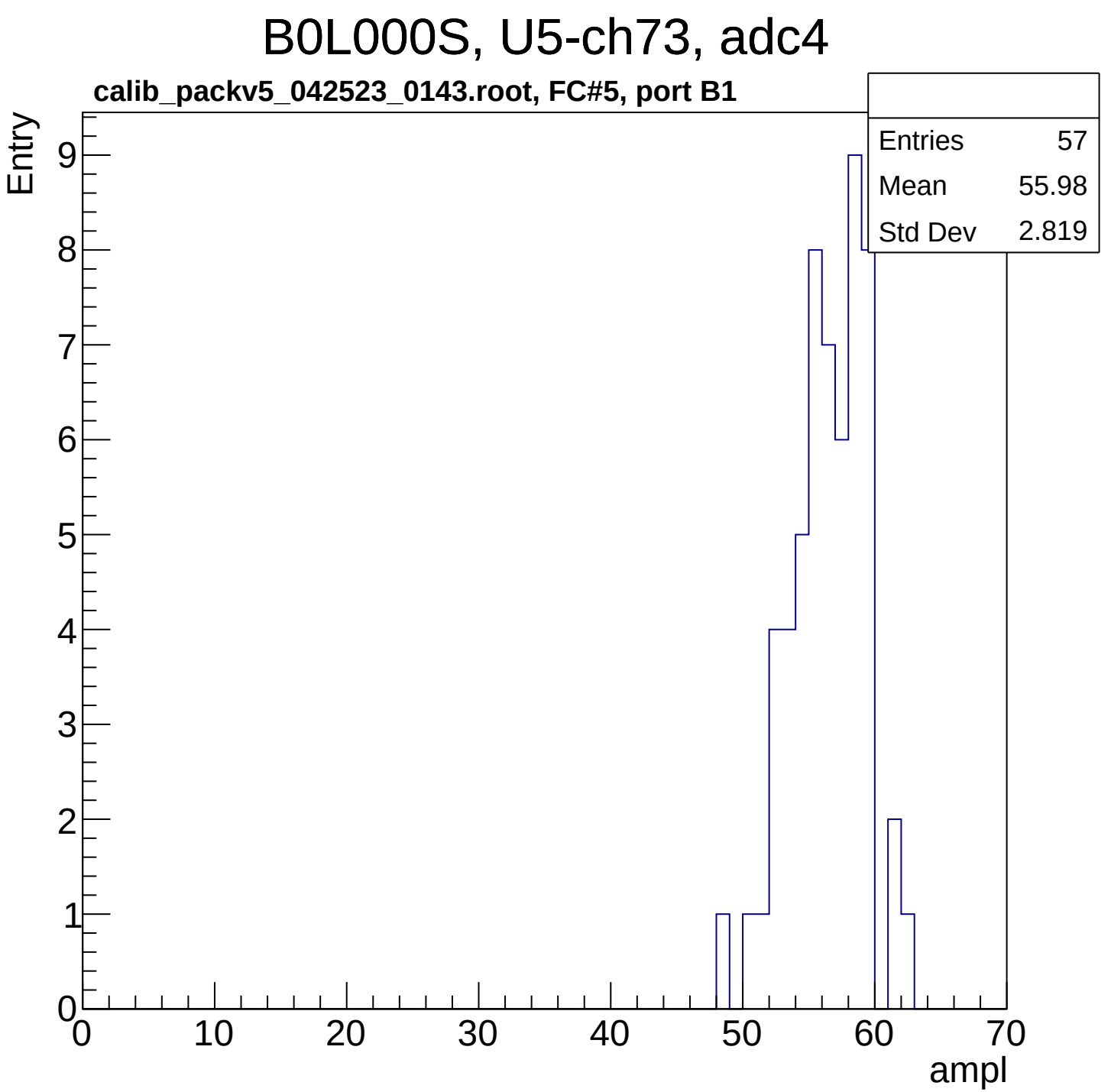
B0L000S, U5-ch73, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.98
Std Dev 2.819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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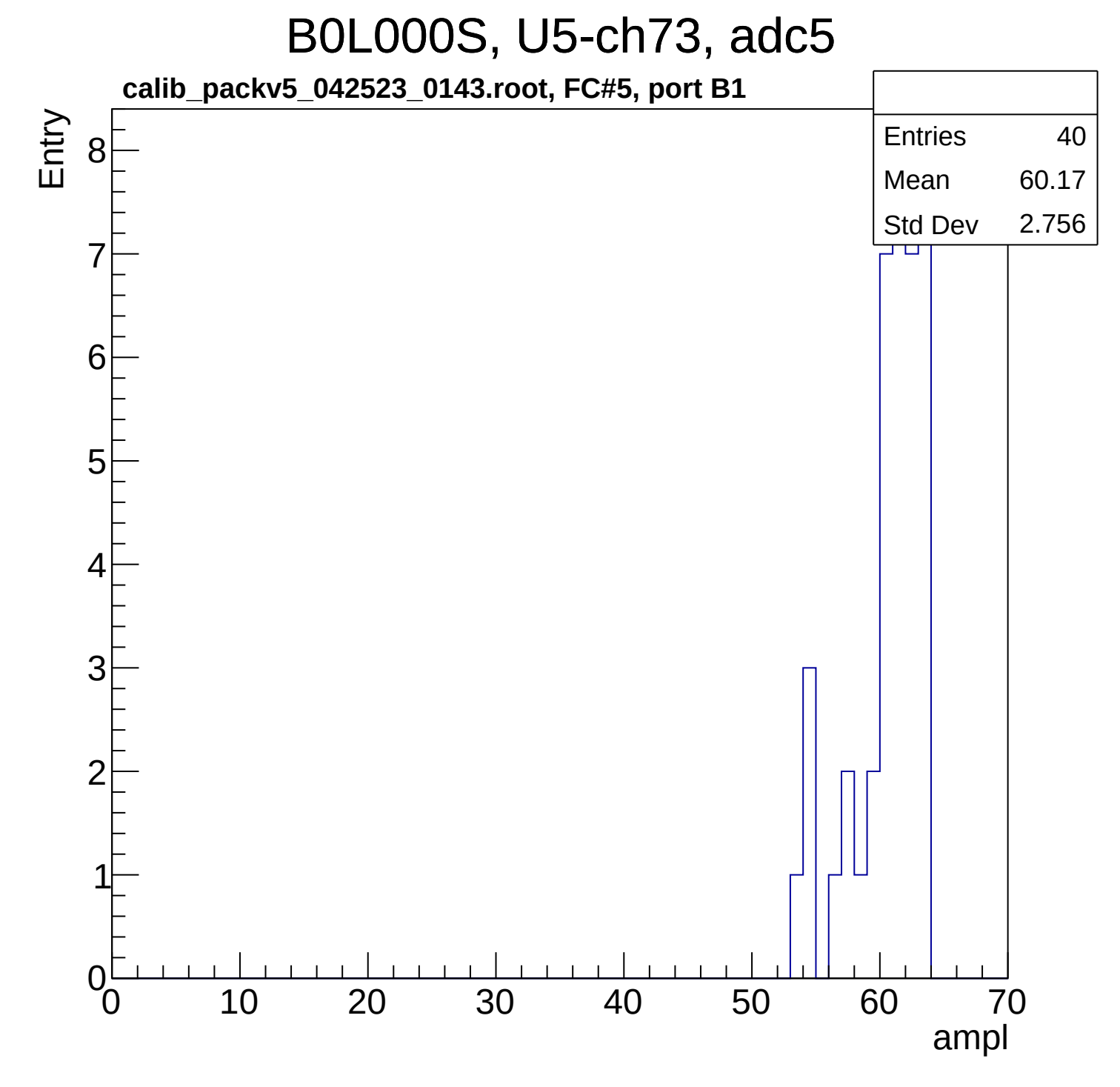
B0L000S, U5-ch73, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 60.17
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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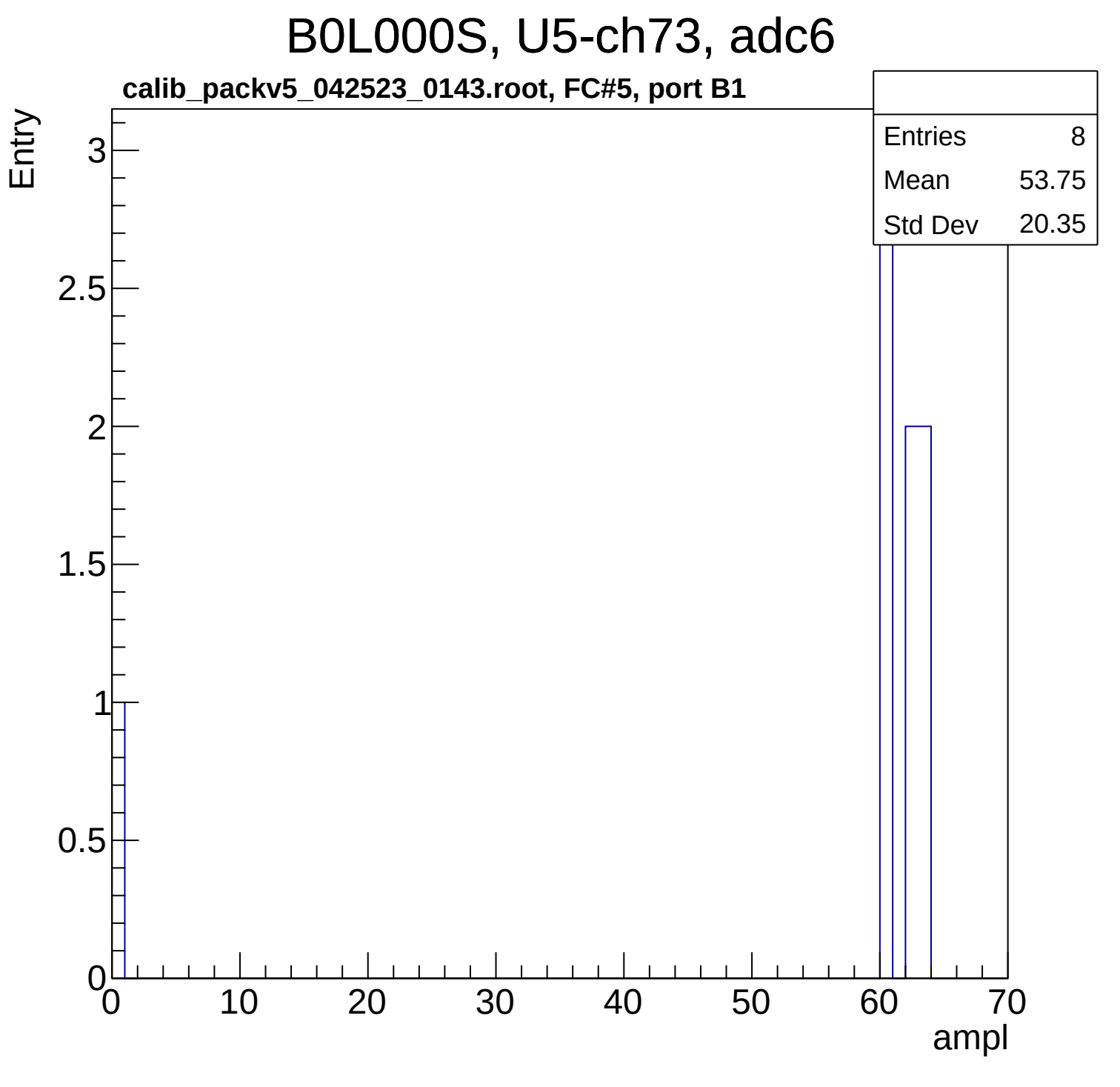
B0L000S, U5-ch73, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 53.75
Std Dev 20.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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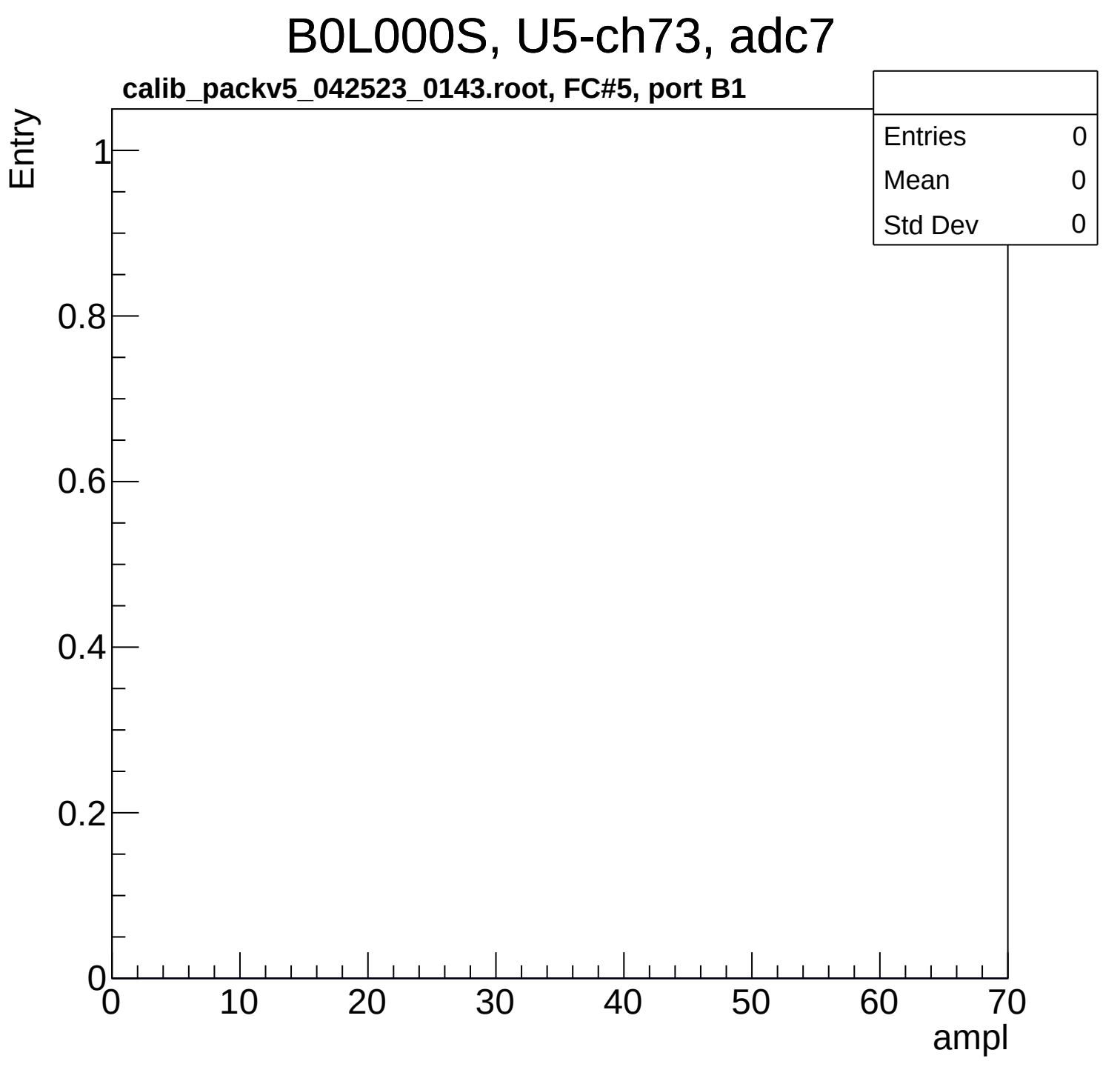
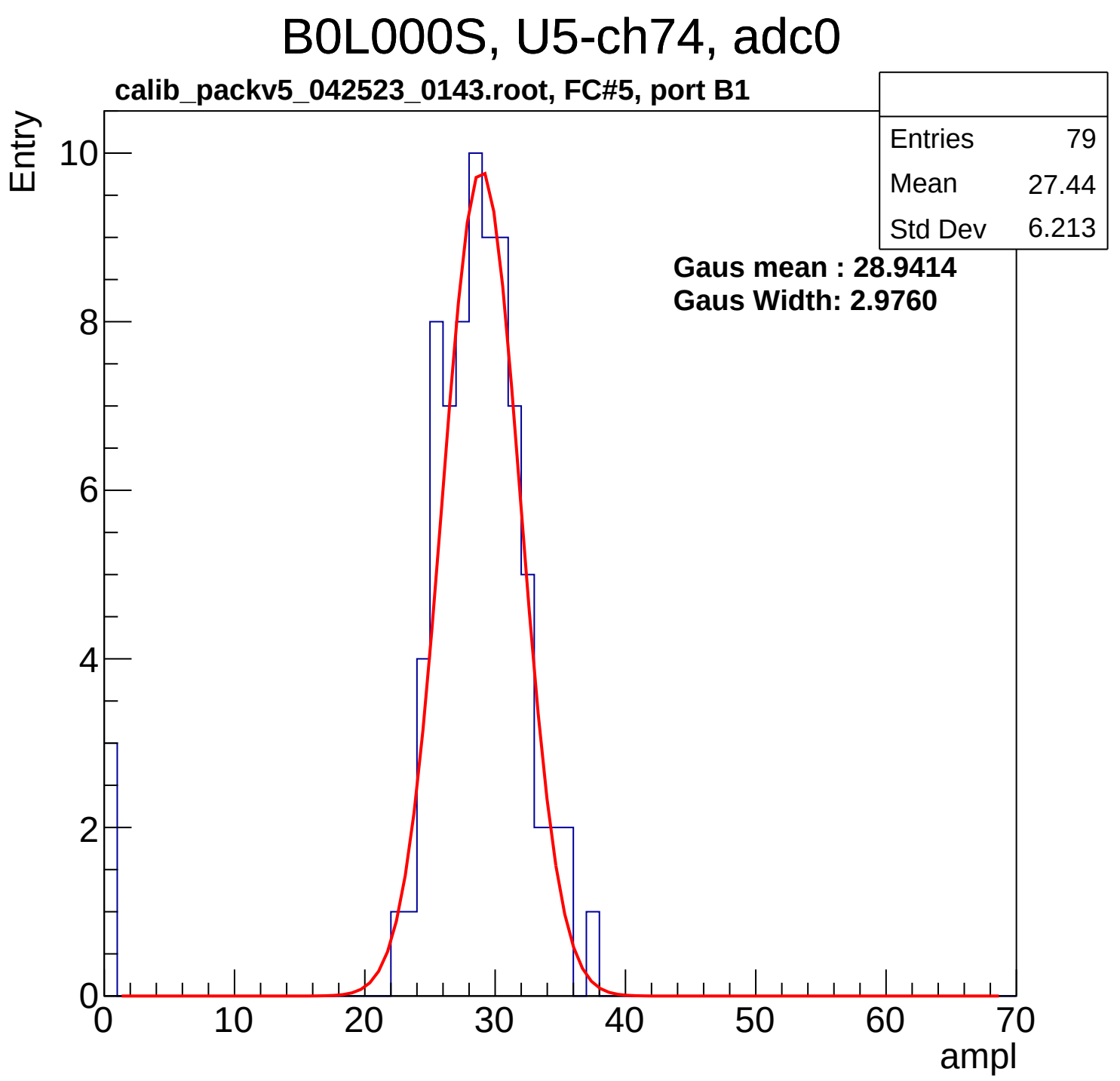
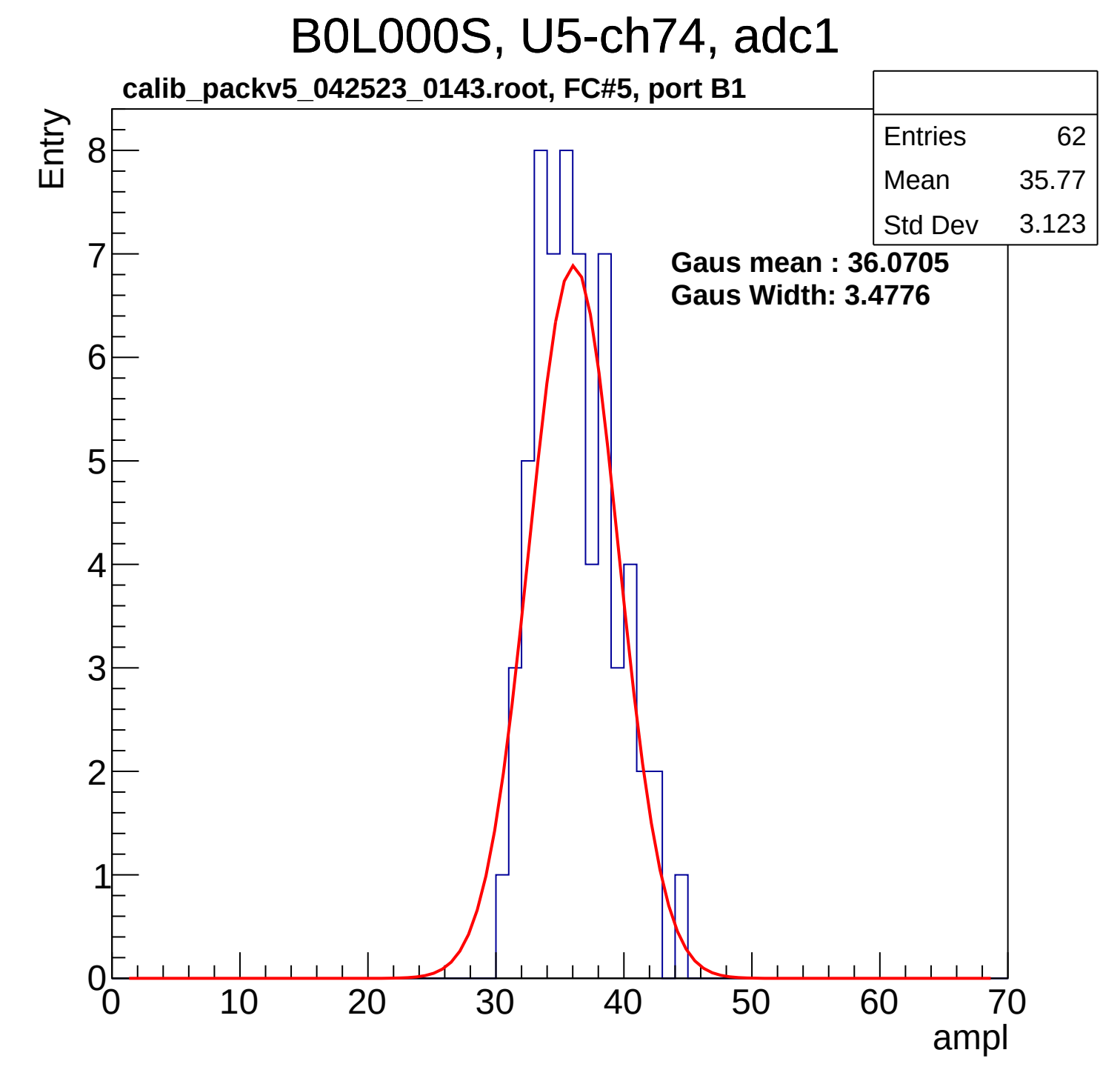
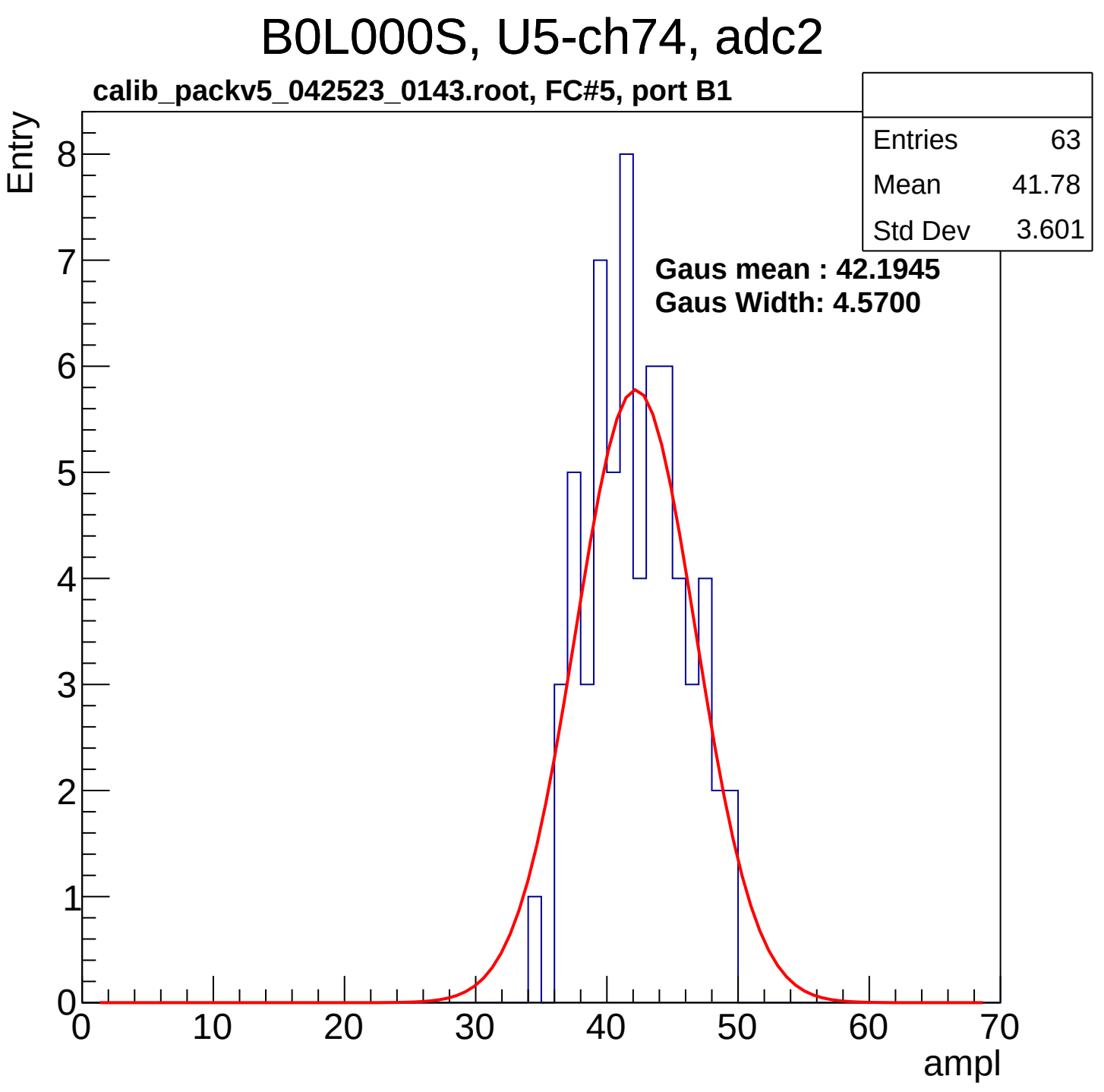
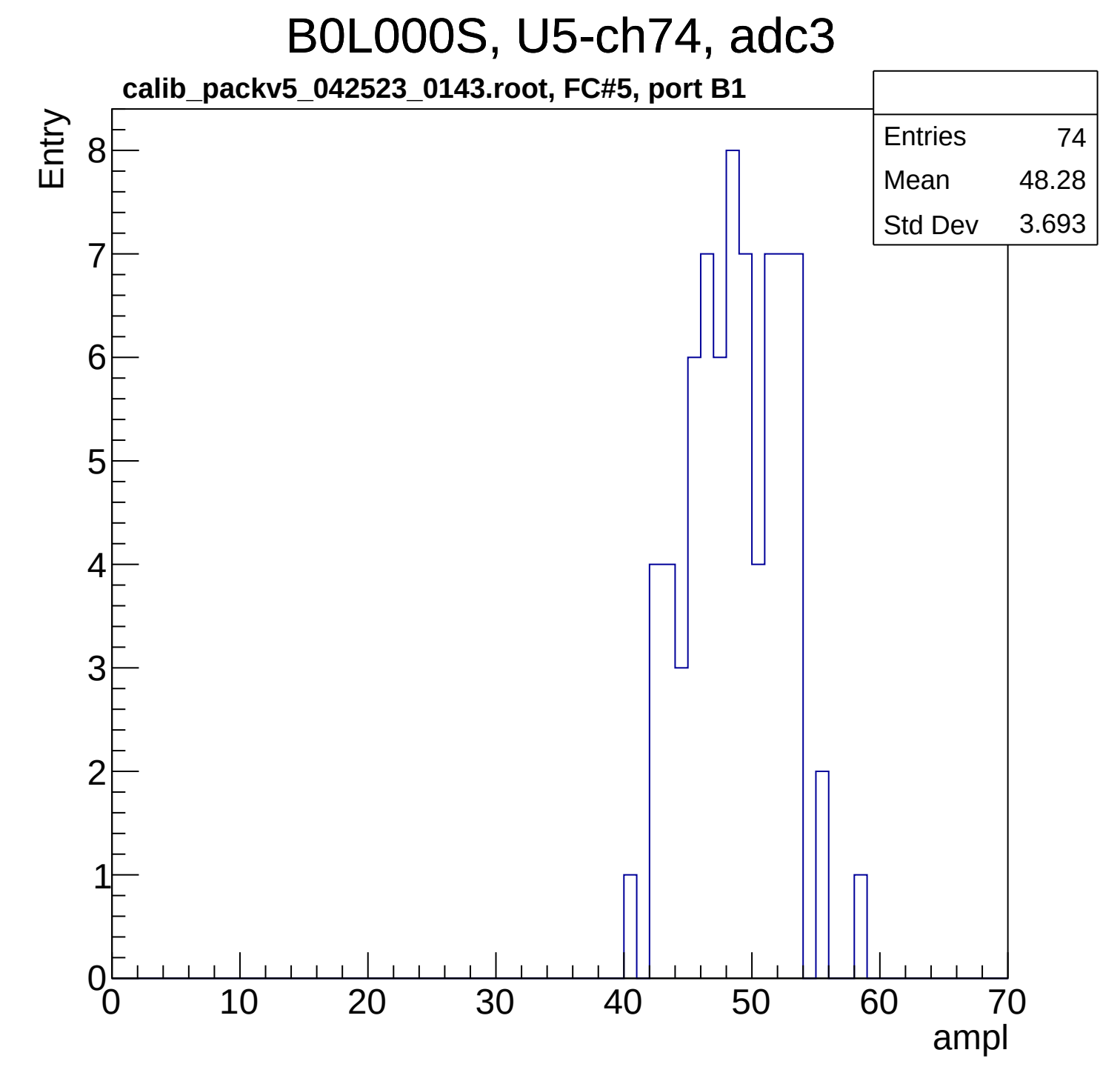

B0L000S, U5-ch73, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch74, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 27.44
Std Dev 6.213
Gaus mean : 28.9414
Gaus Width: 2.9760
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch74, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 35.77
Std Dev 3.123
Gaus mean : 36.0705
Gaus Width: 3.4776
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch74, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.78
Std Dev 3.601
Gaus mean : 42.1945
Gaus Width: 4.5700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch74, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 48.28
Std Dev 3.693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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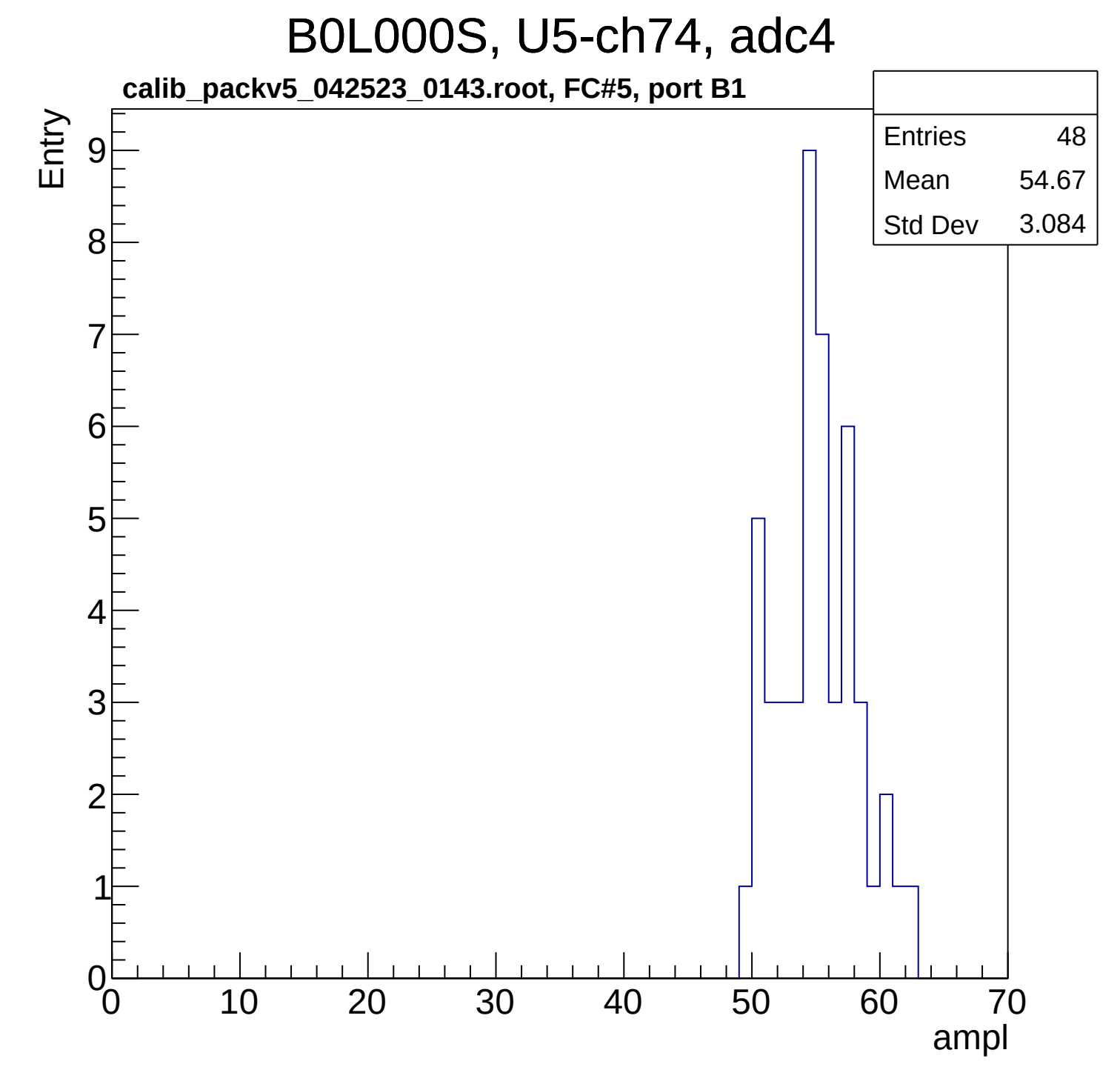
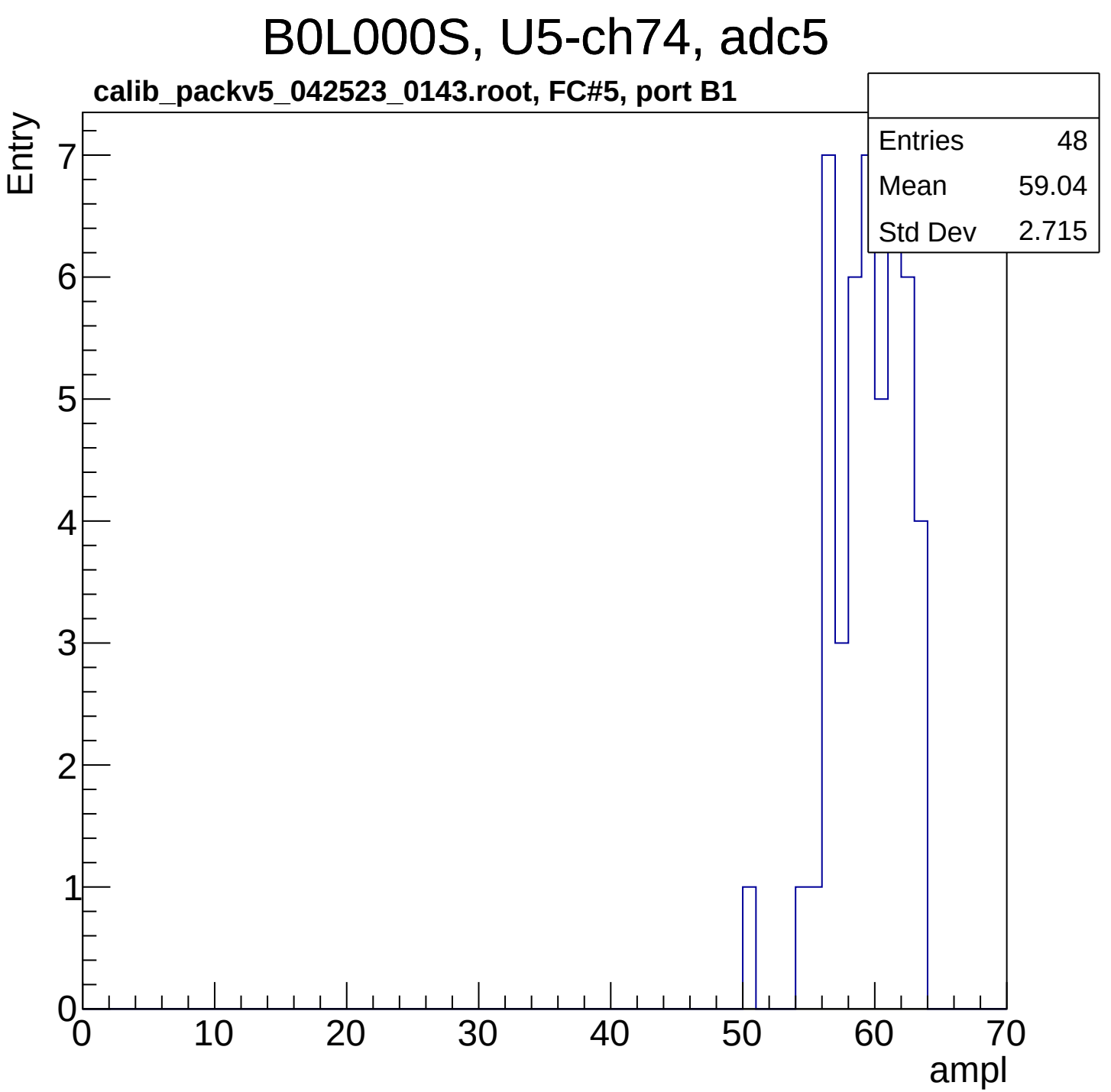
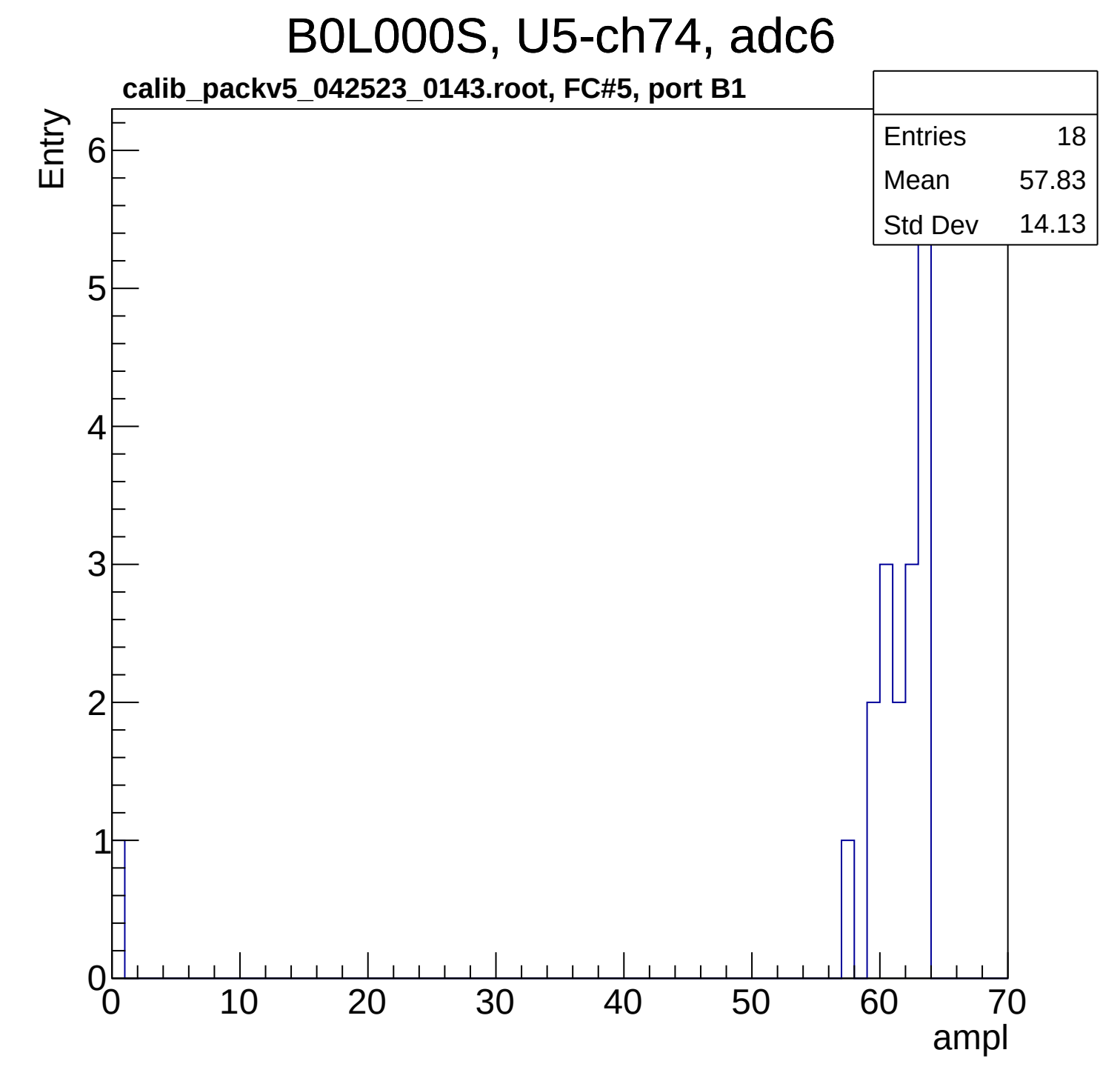
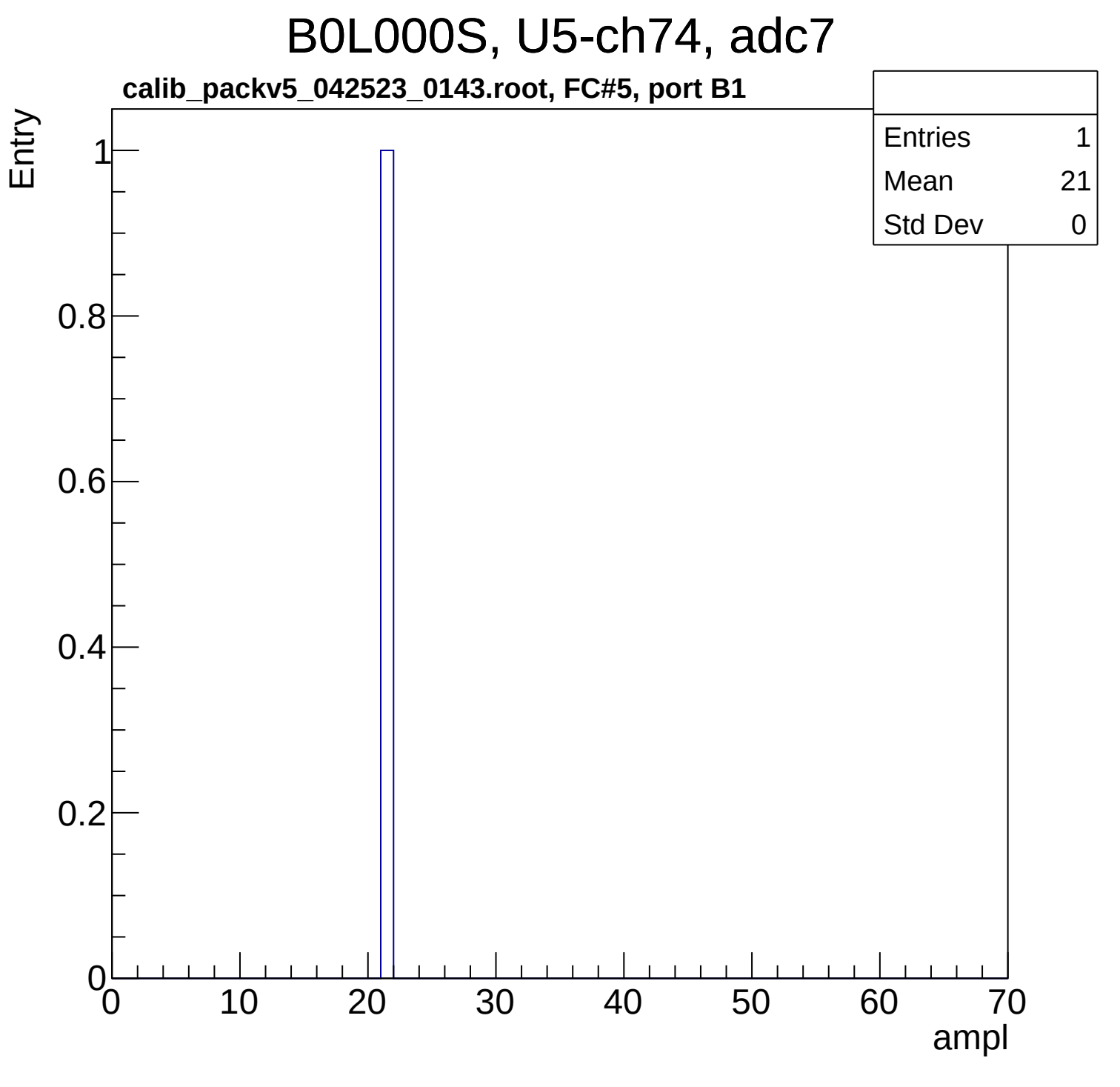
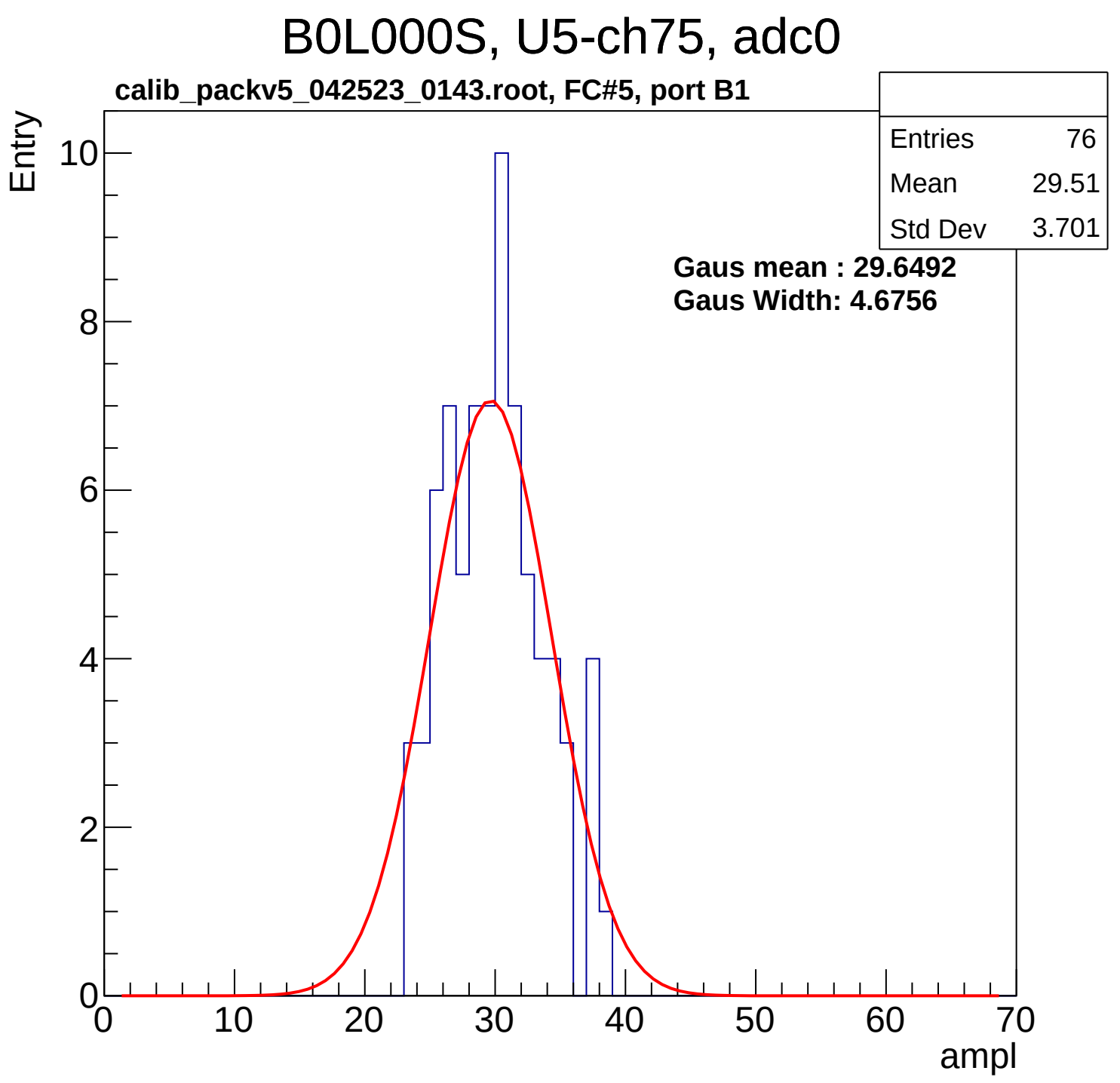
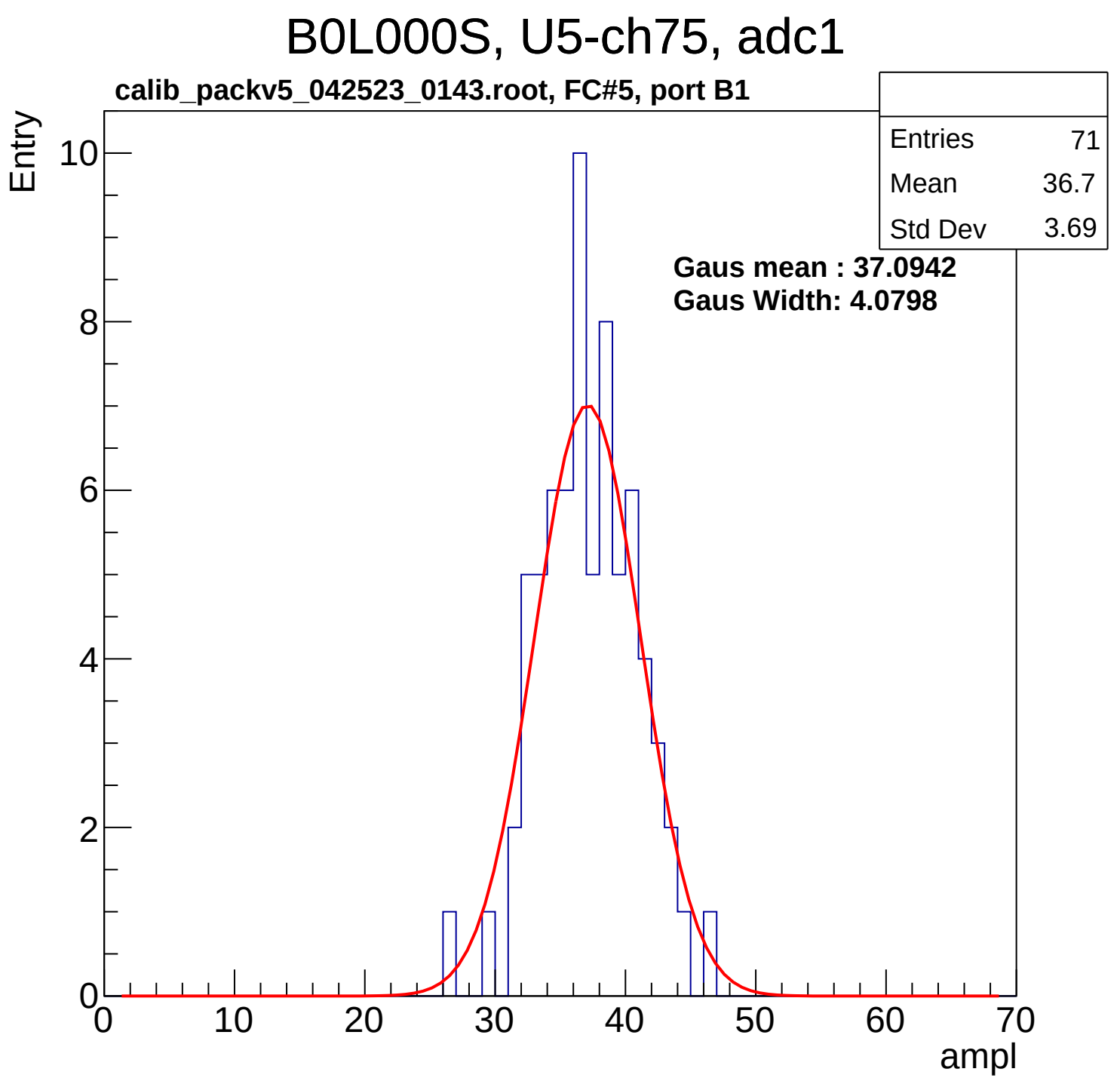

B0L000S, U5-ch74, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.67
Std Dev 3.084
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch74, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.04
Std Dev 2.715
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch74, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 18
Mean 57.83
Std Dev 14.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch74, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch75, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 29.51
Std Dev 3.701
Gaus mean : 29.6492
Gaus Width: 4.6756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch75, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 36.7
Std Dev 3.69
Gaus mean : 37.0942
Gaus Width: 4.0798
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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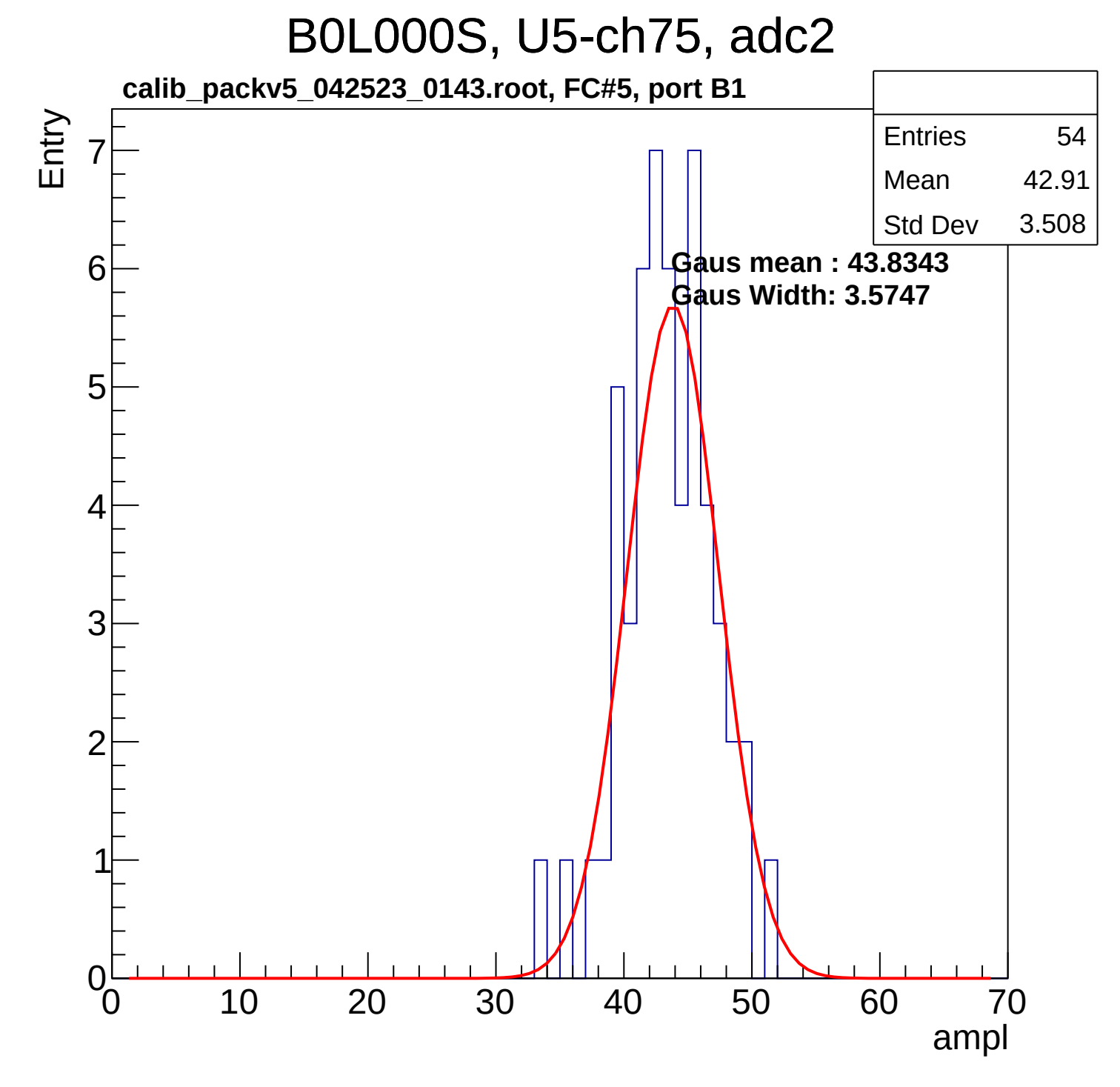
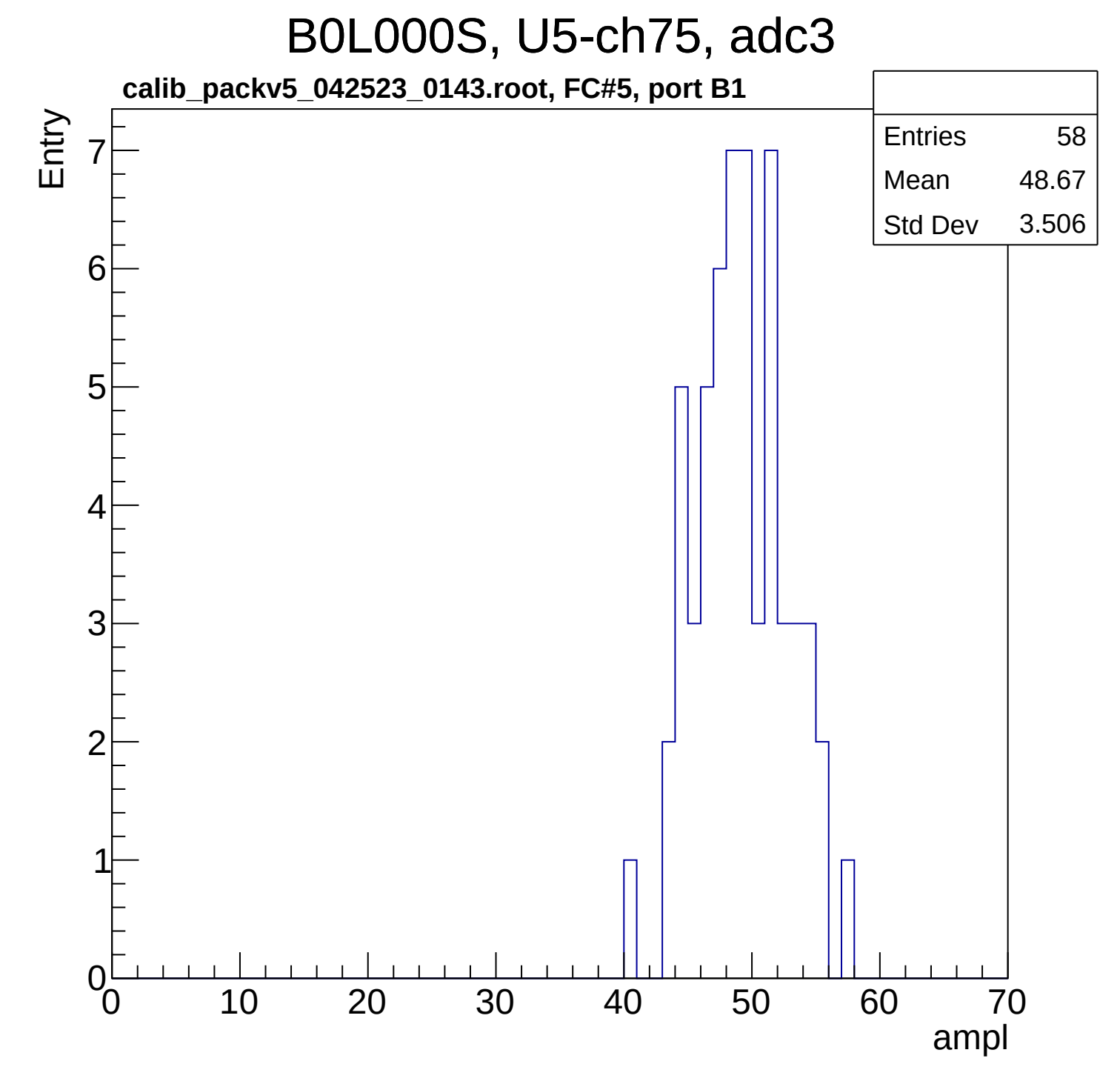

B0L000S, U5-ch75, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 42.91
Std Dev 3.508
Gaus mean : 43.8343
Gaus Width: 3.5747
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch75, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 48.67
Std Dev 3.506
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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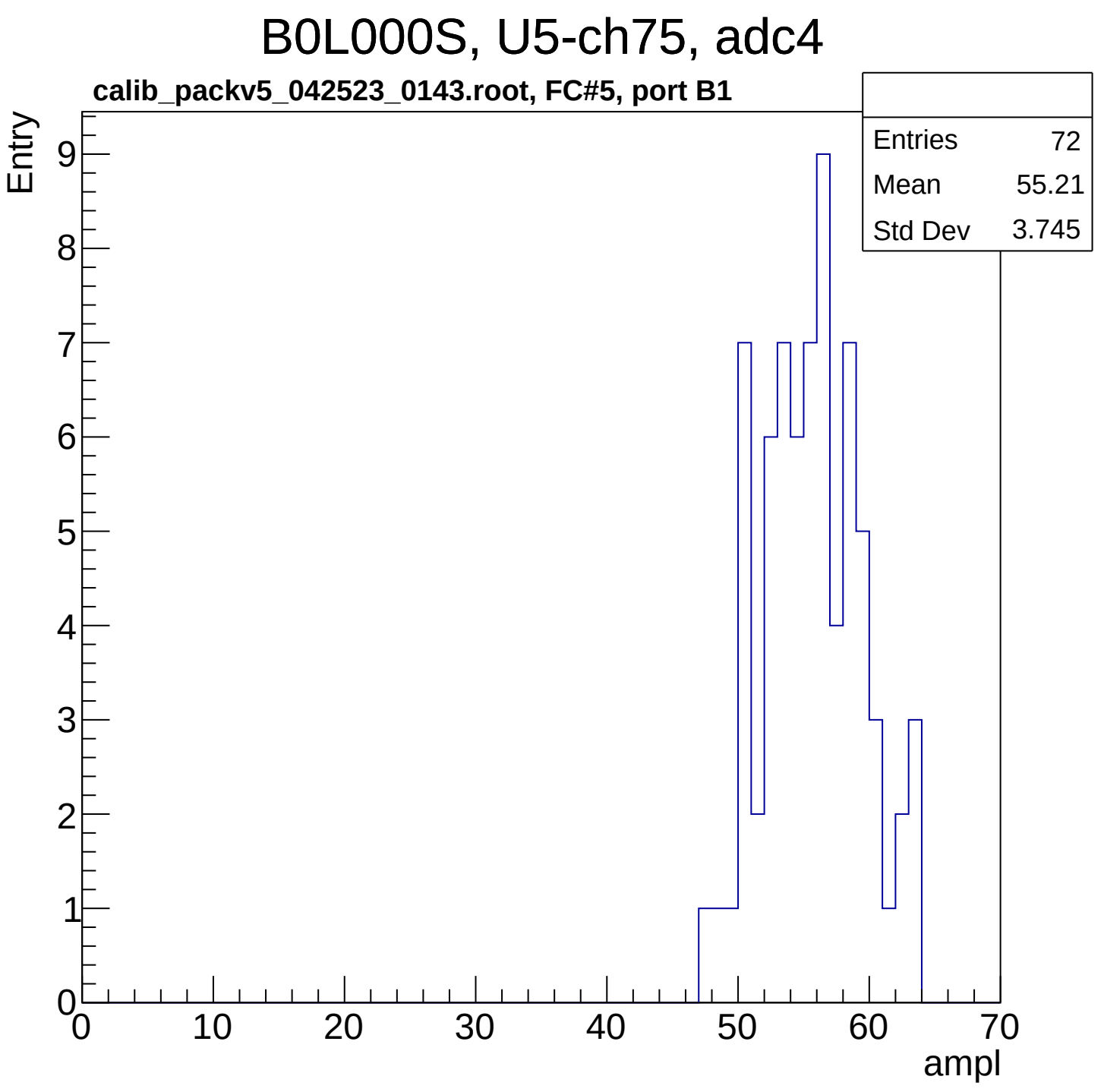
40
50
60
70


B0L000S, U5-ch75, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 55.21
Std Dev 3.745
Entry
ampl
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3
4
5
6
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9
0
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60
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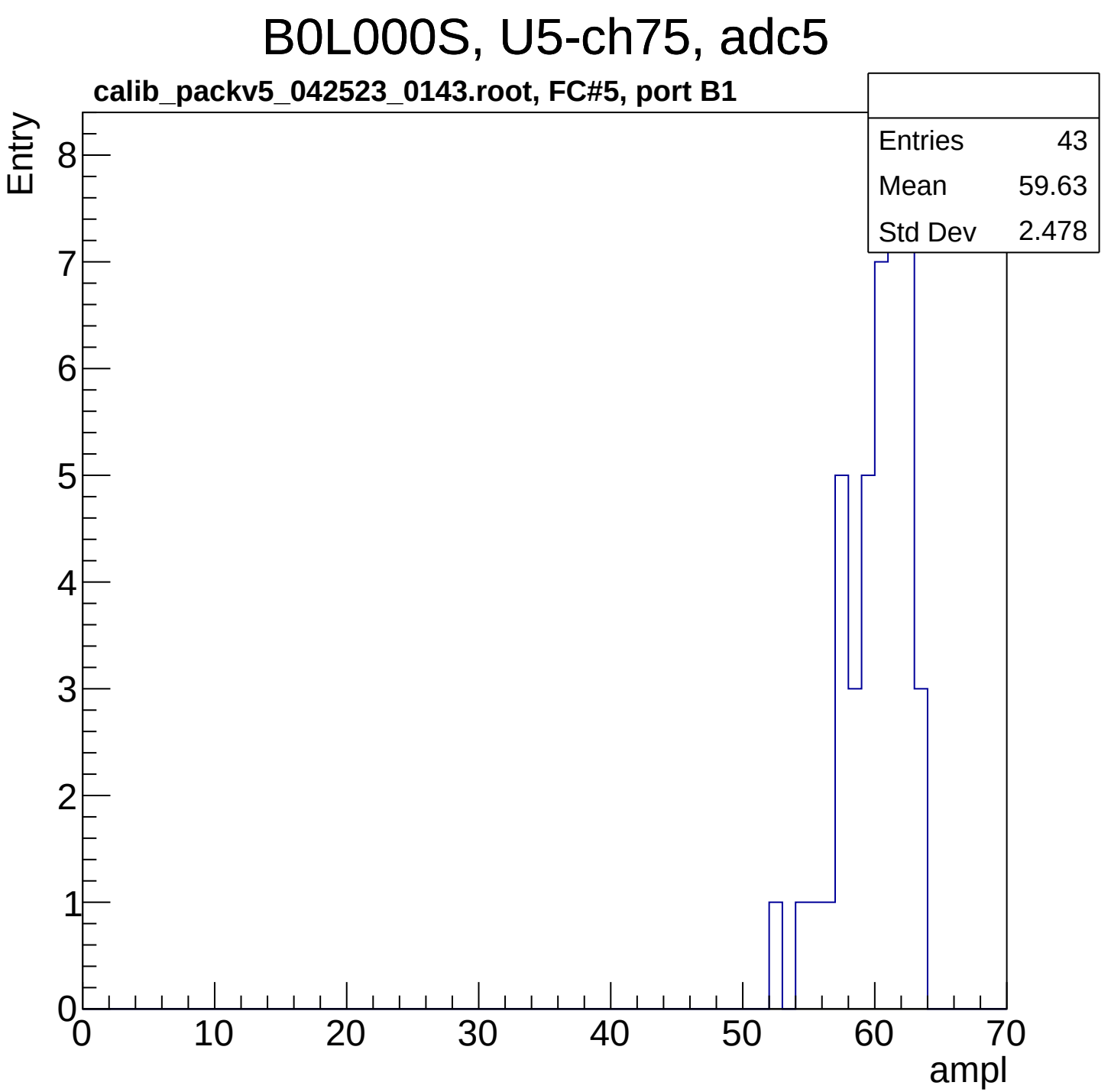
B0L000S, U5-ch75, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 59.63
Std Dev 2.478
Entry
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3
4
5
6
7
8
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10
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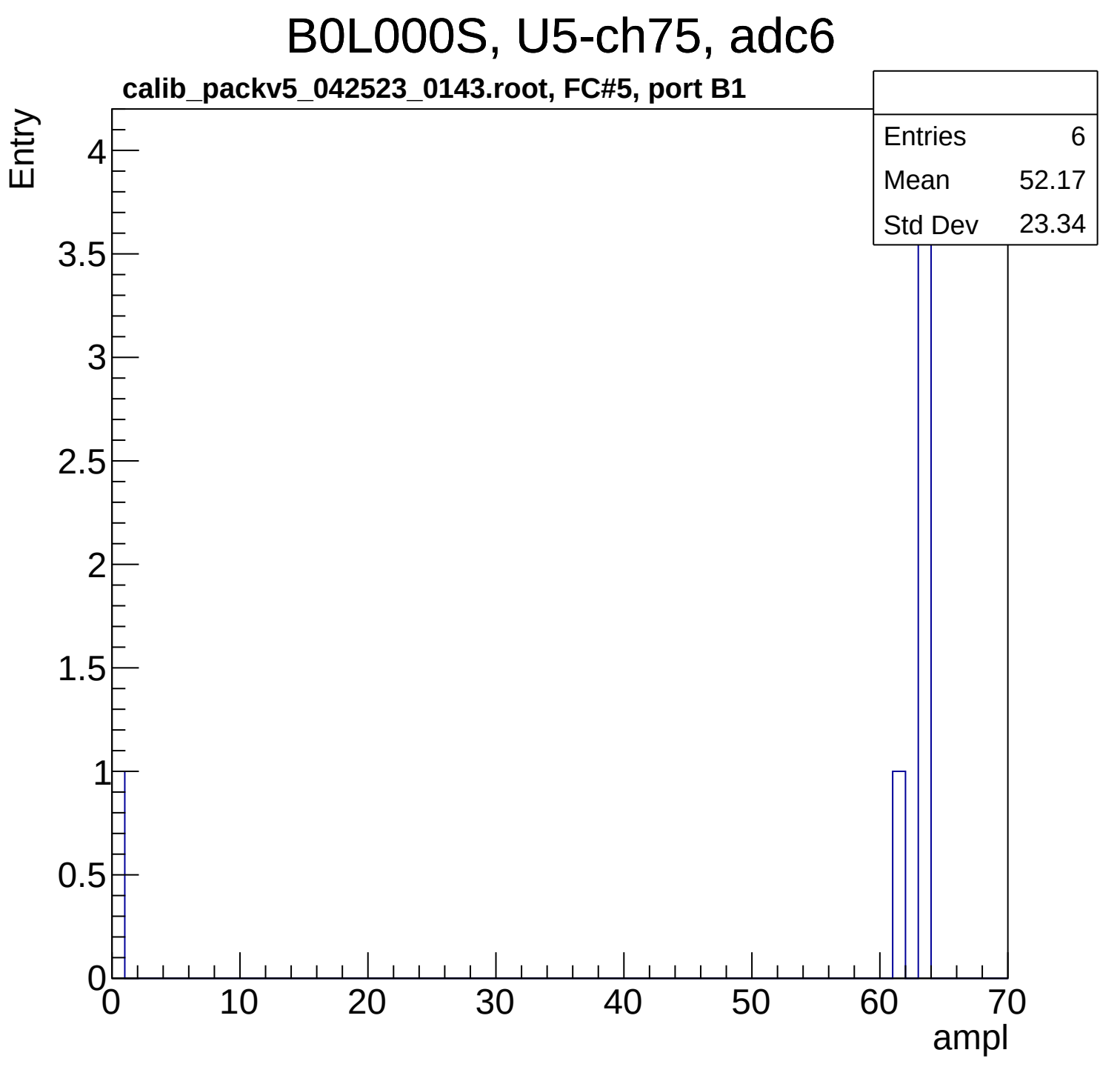
30
40
50
60
70
ampl


B0L000S, U5-ch75, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 52.17
Std Dev 23.34
Entry
ampl
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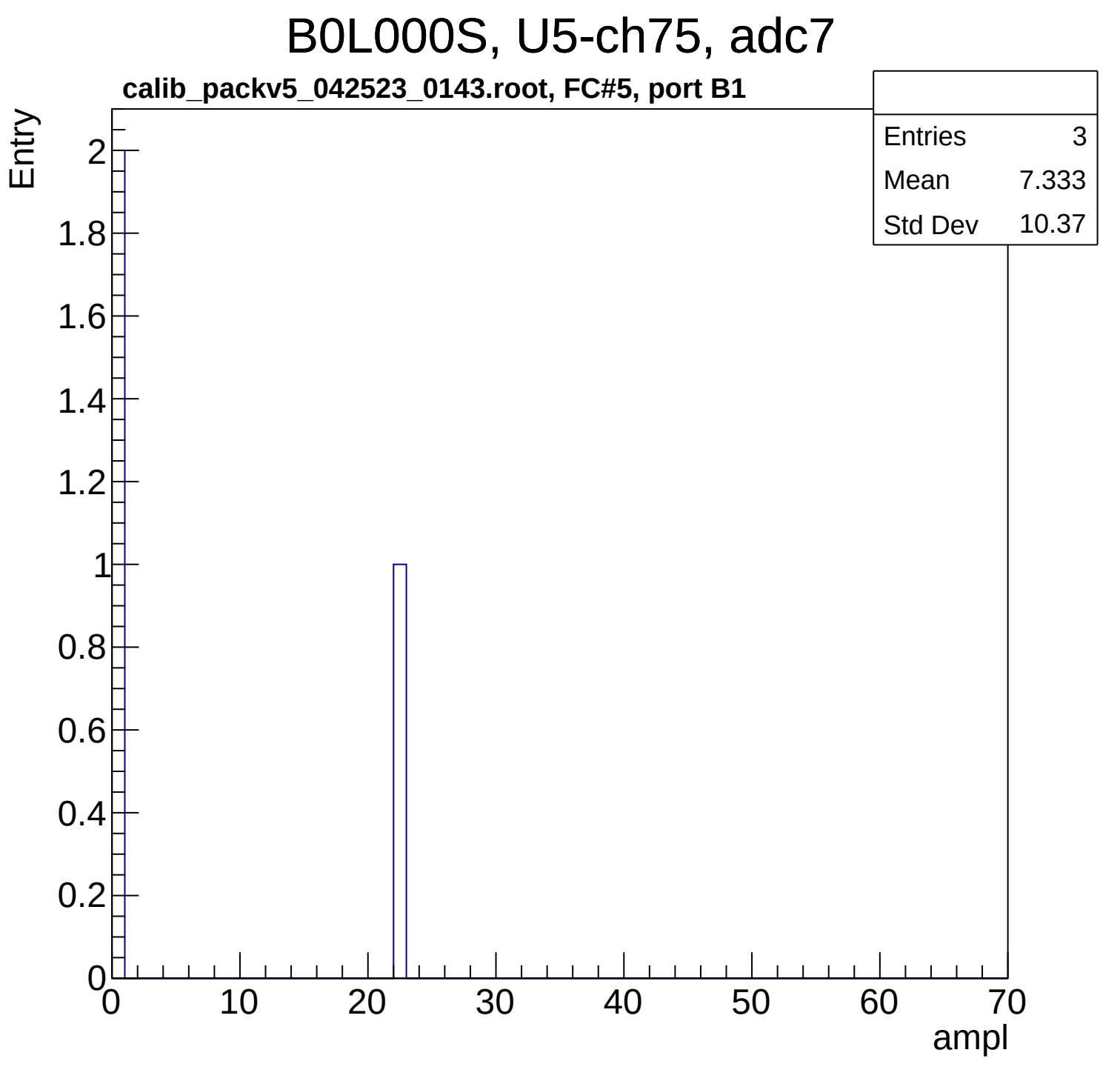
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U5-ch75, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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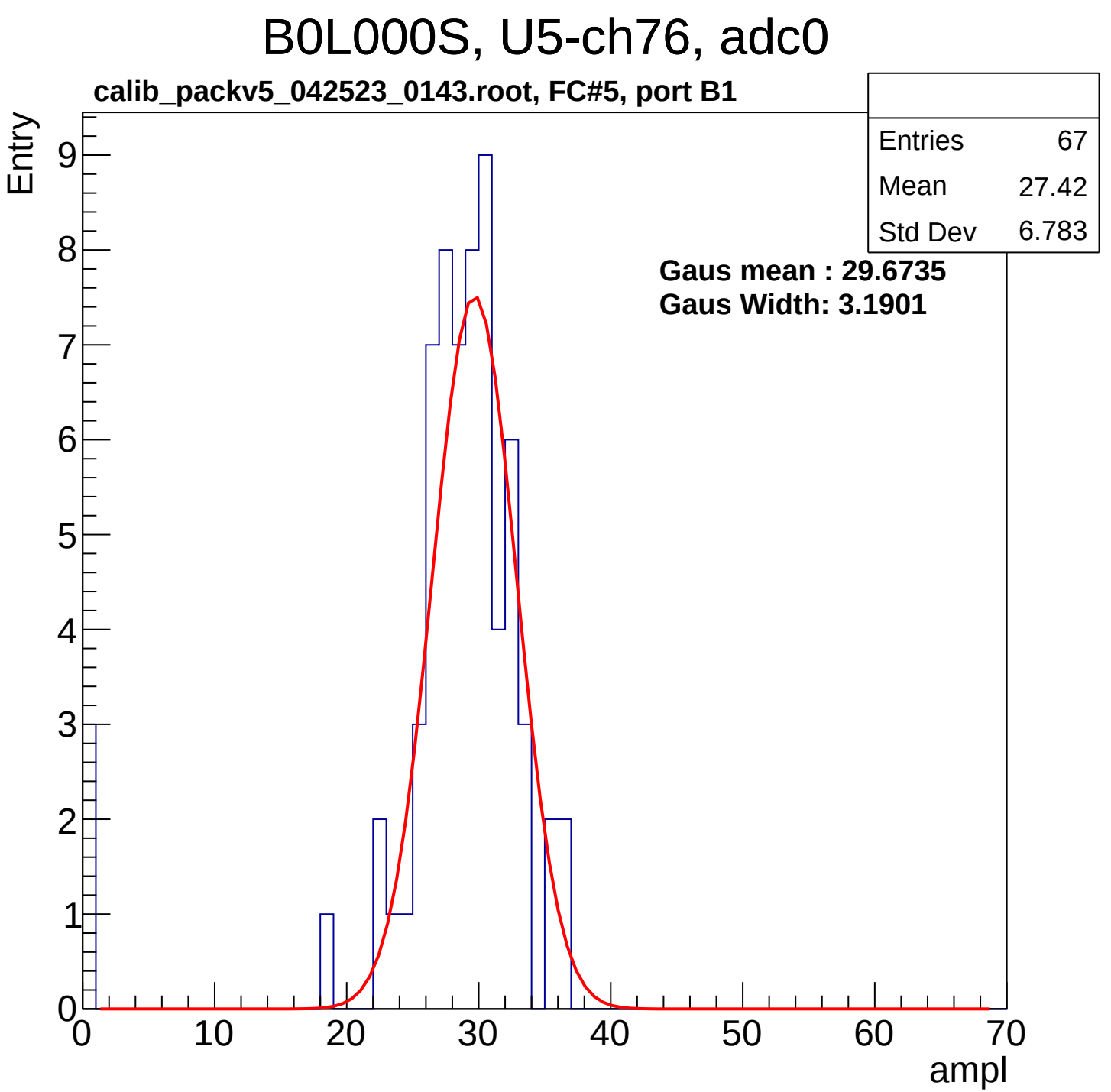
B0L000S, U5-ch76, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 27.42
Std Dev 6.783
Gaus mean : 29.6735
Gaus Width: 3.1901
Entry
ampl
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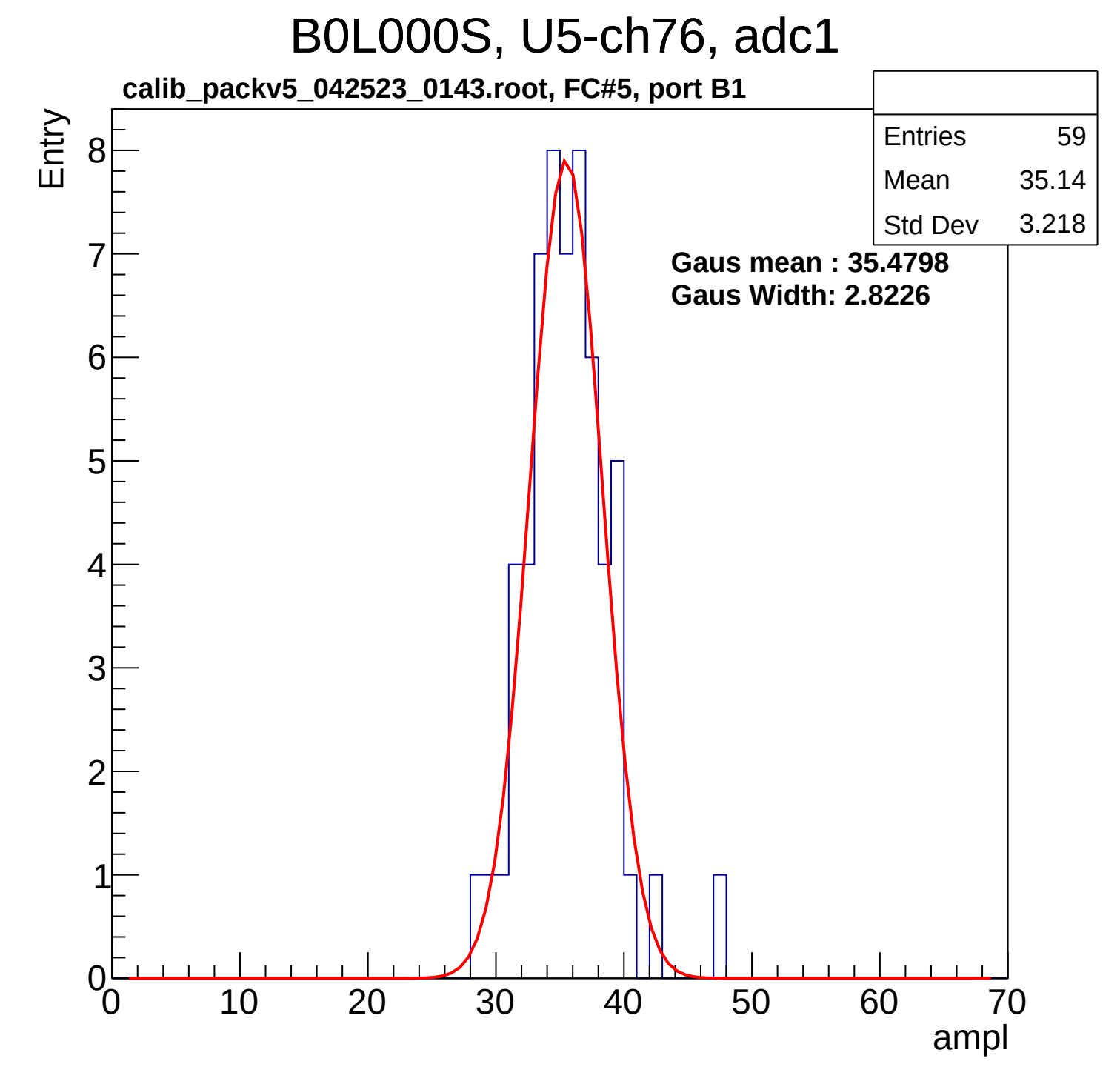
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch76, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.14
Std Dev 3.218
Gaus mean : 35.4798
Gaus Width: 2.8226
Entry
ampl
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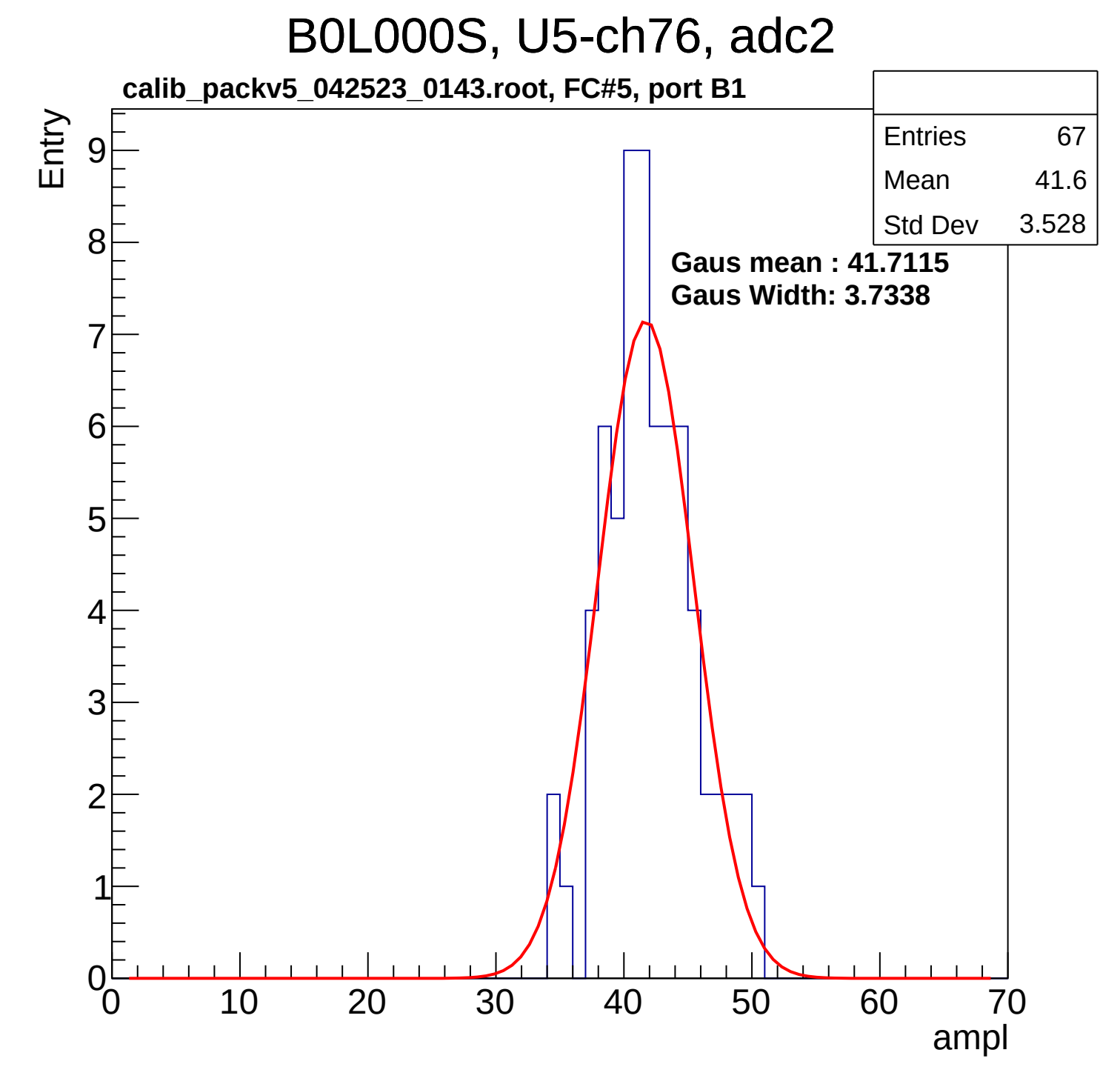
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch76, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 41.6
Std Dev 3.528
Gaus mean : 41.7115
Gaus Width: 3.7338
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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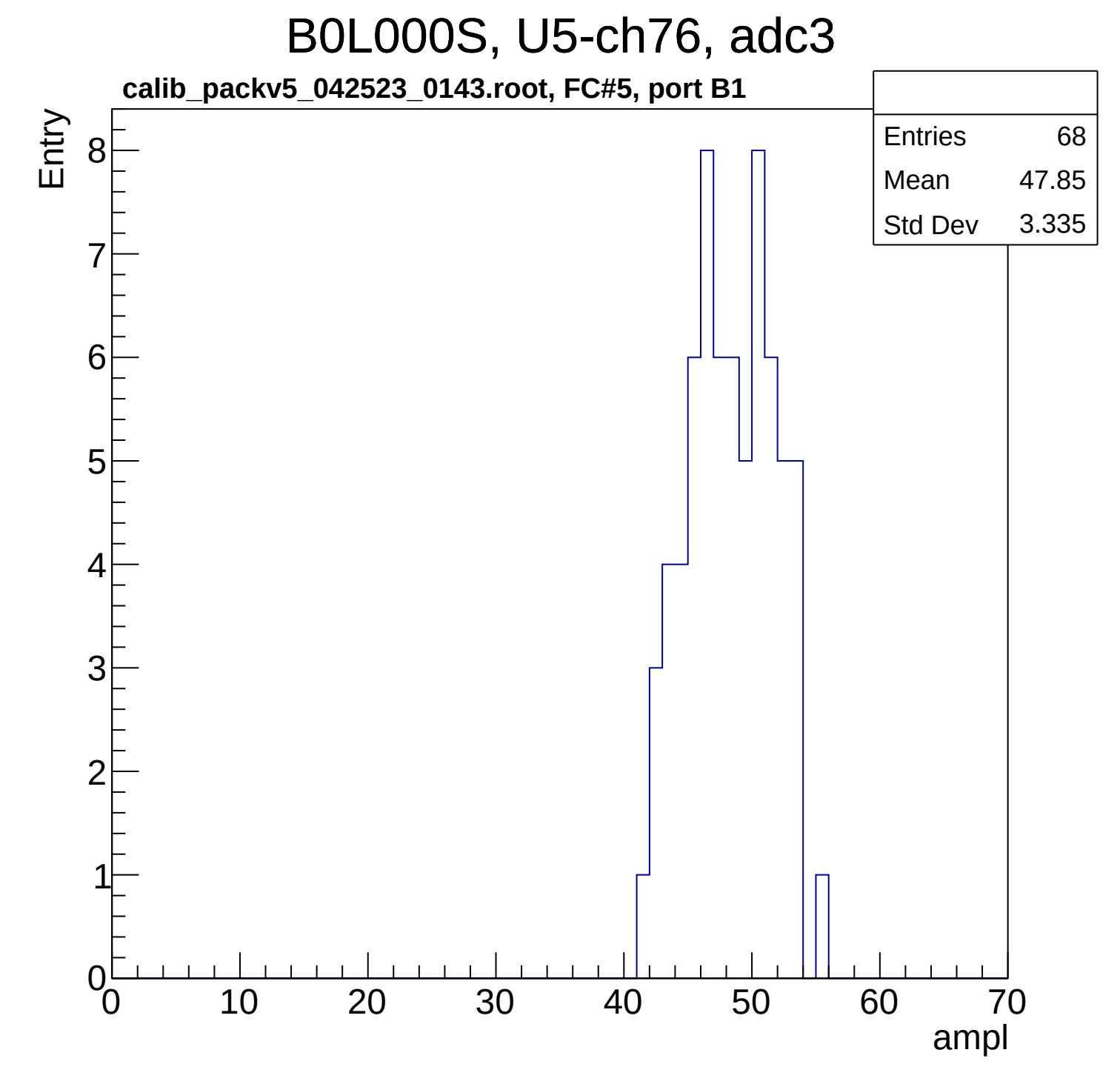
B0L000S, U5-ch76, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 47.85
Std Dev 3.335
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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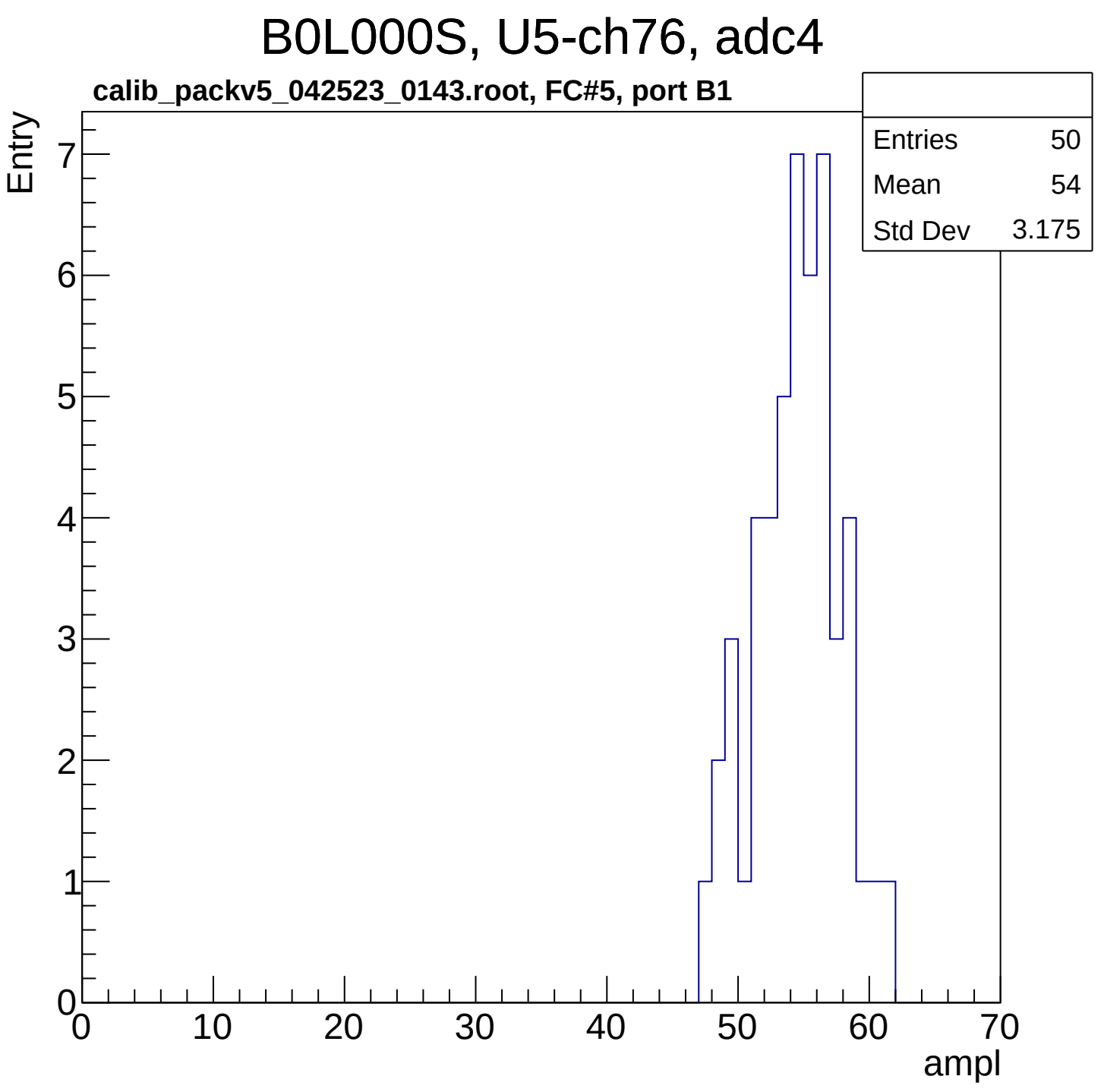
B0L000S, U5-ch76, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 54
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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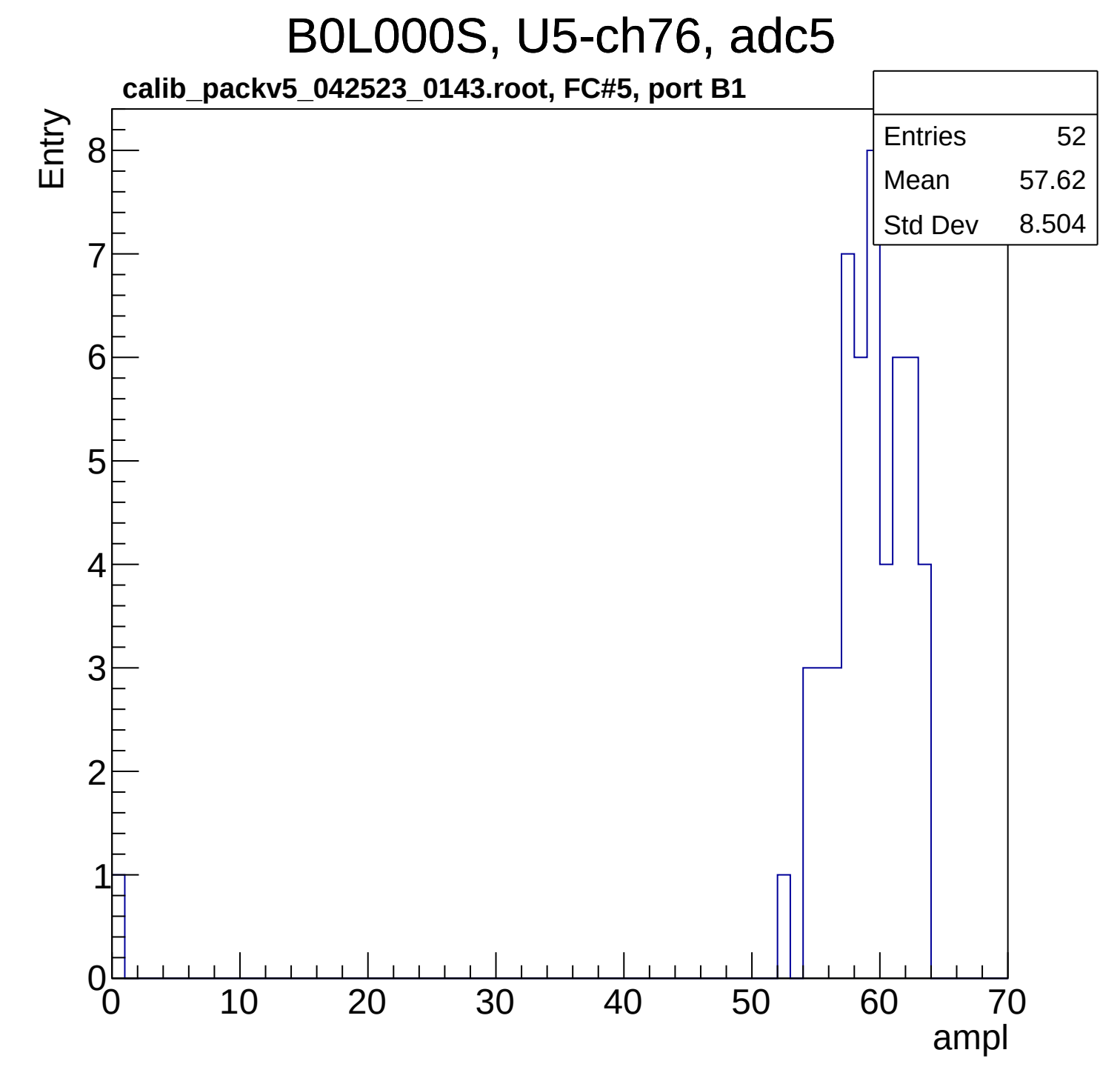
B0L000S, U5-ch76, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 57.62
Std Dev 8.504
Entry
ampl
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1
2
3
4
5
6
7
8
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10
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30
40
50
60
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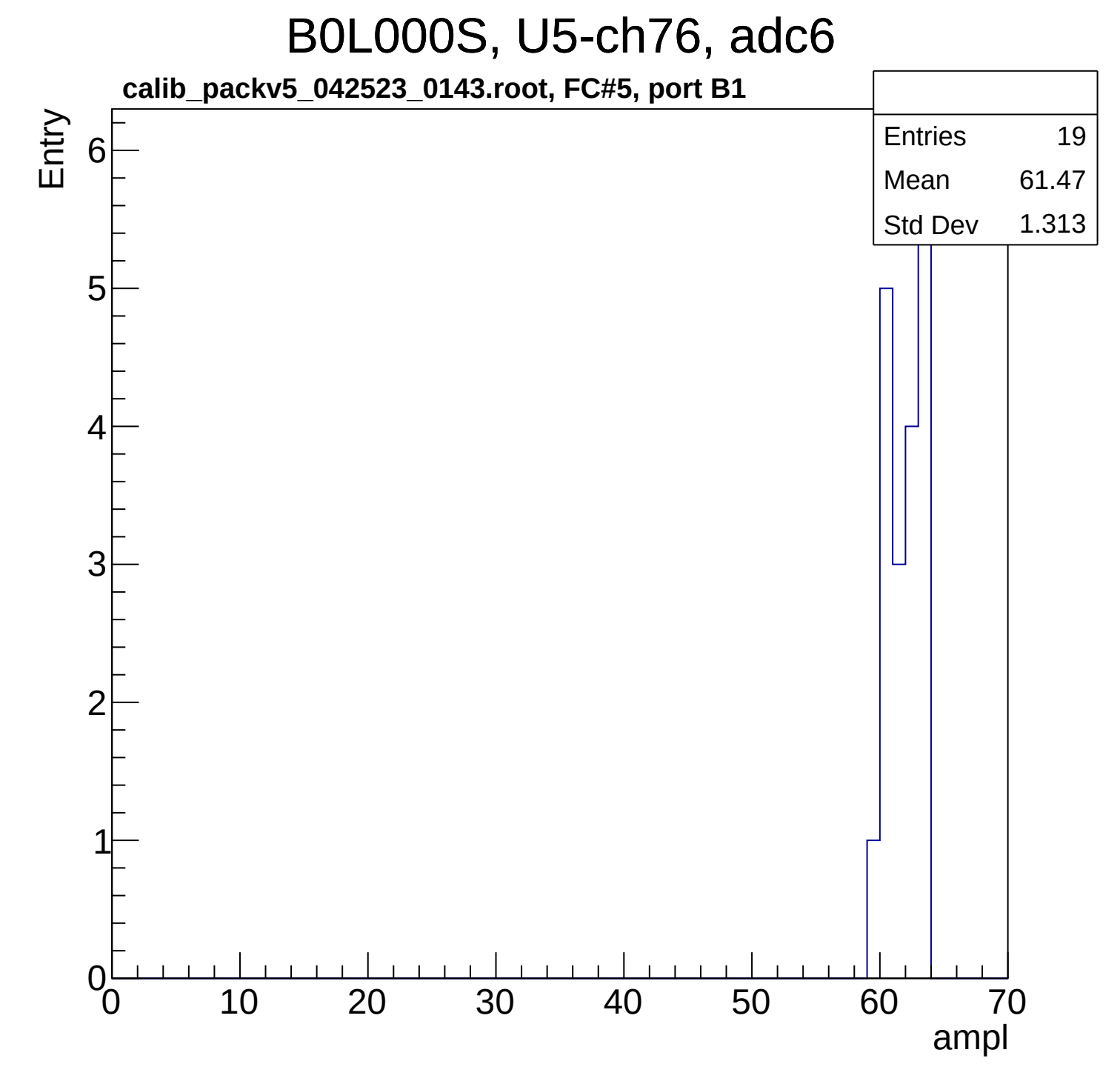
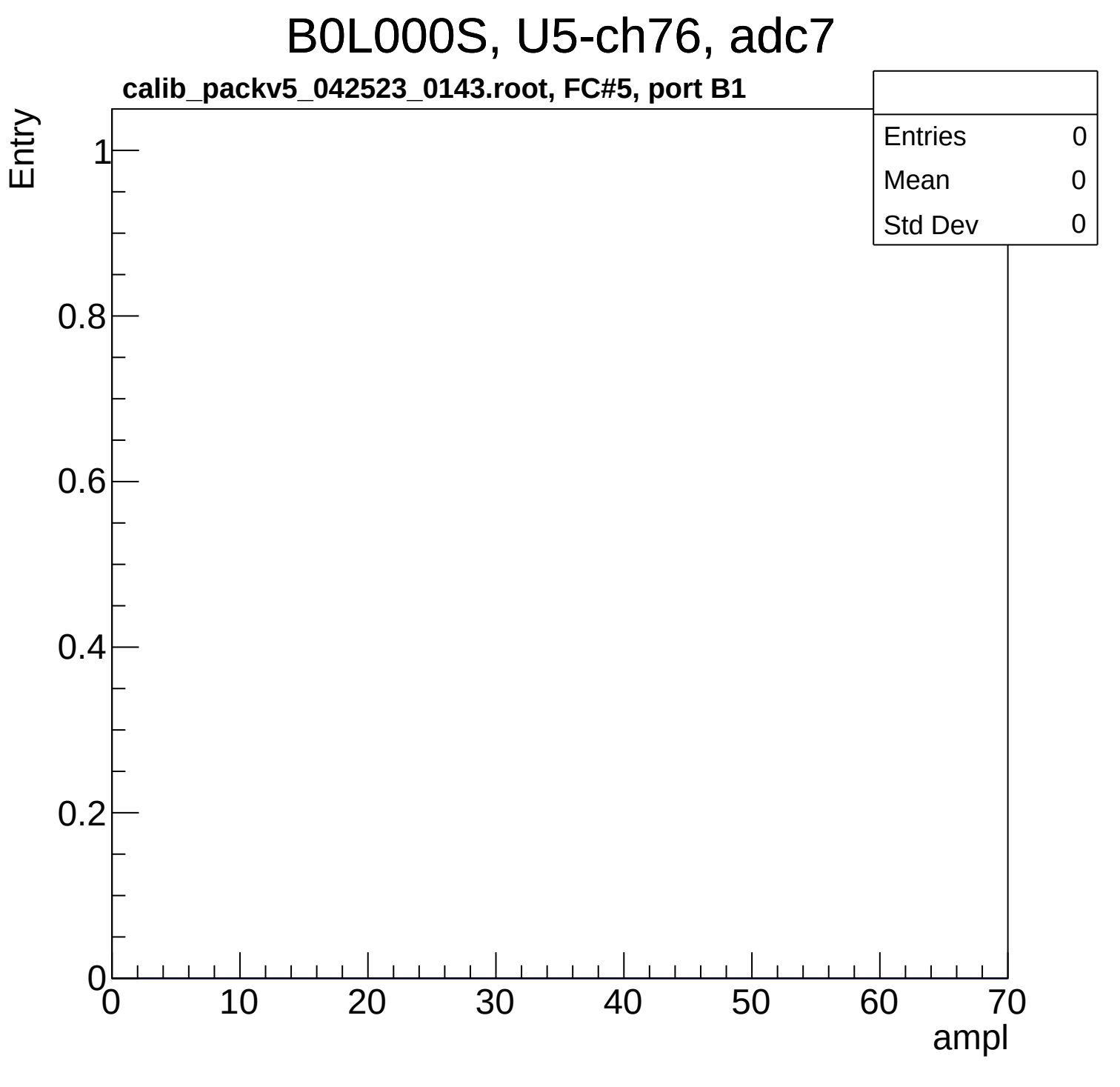
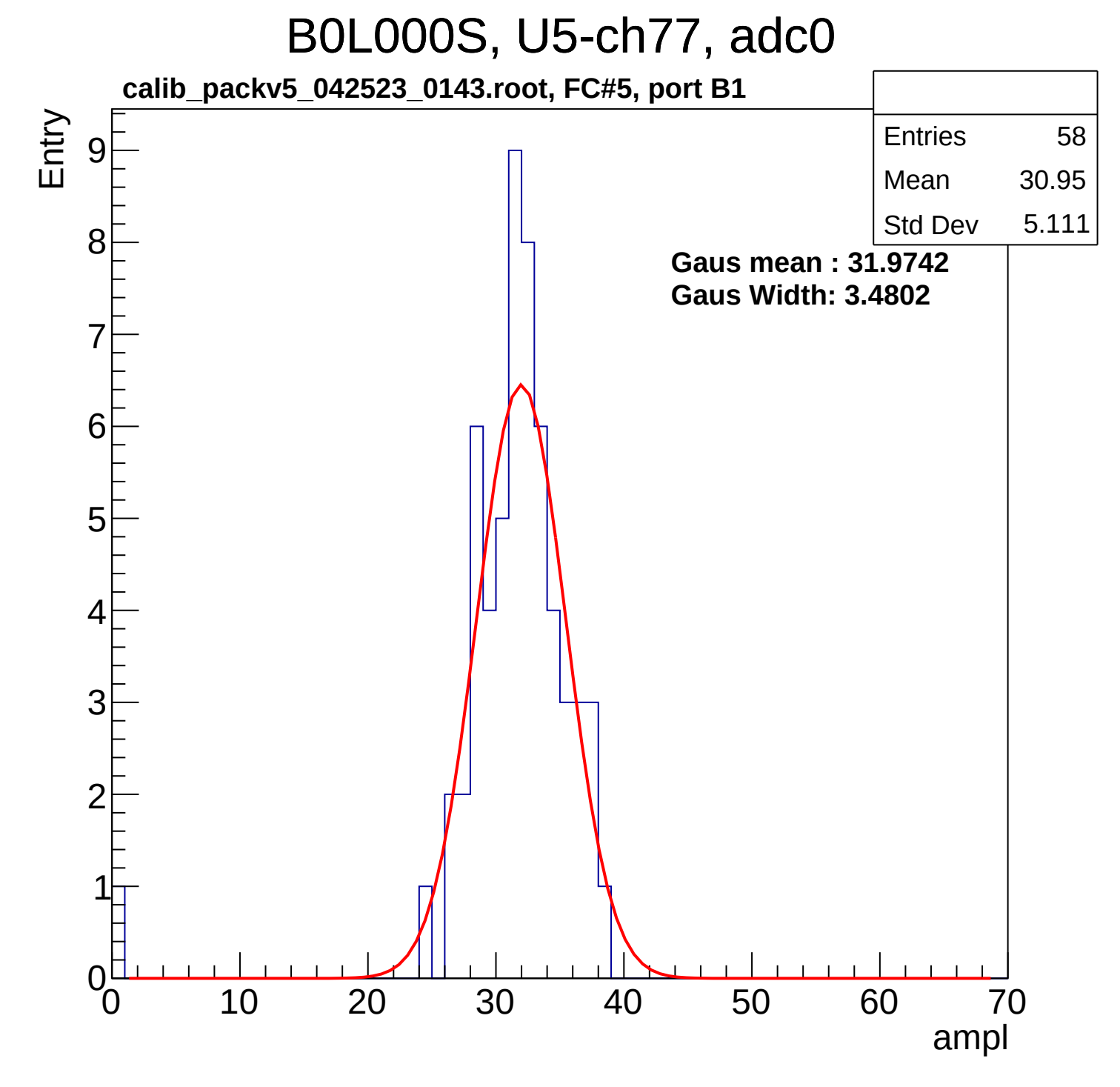
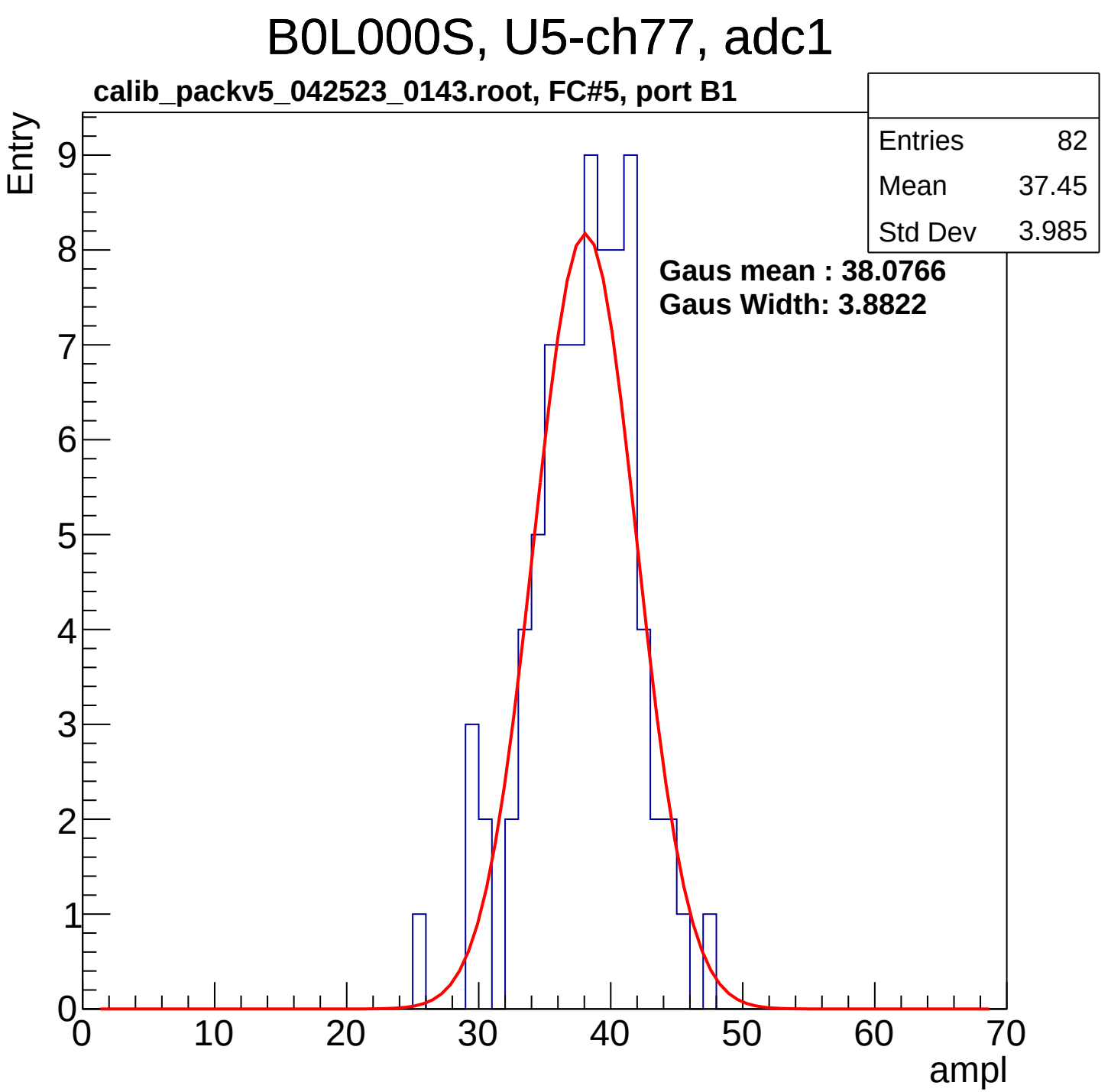
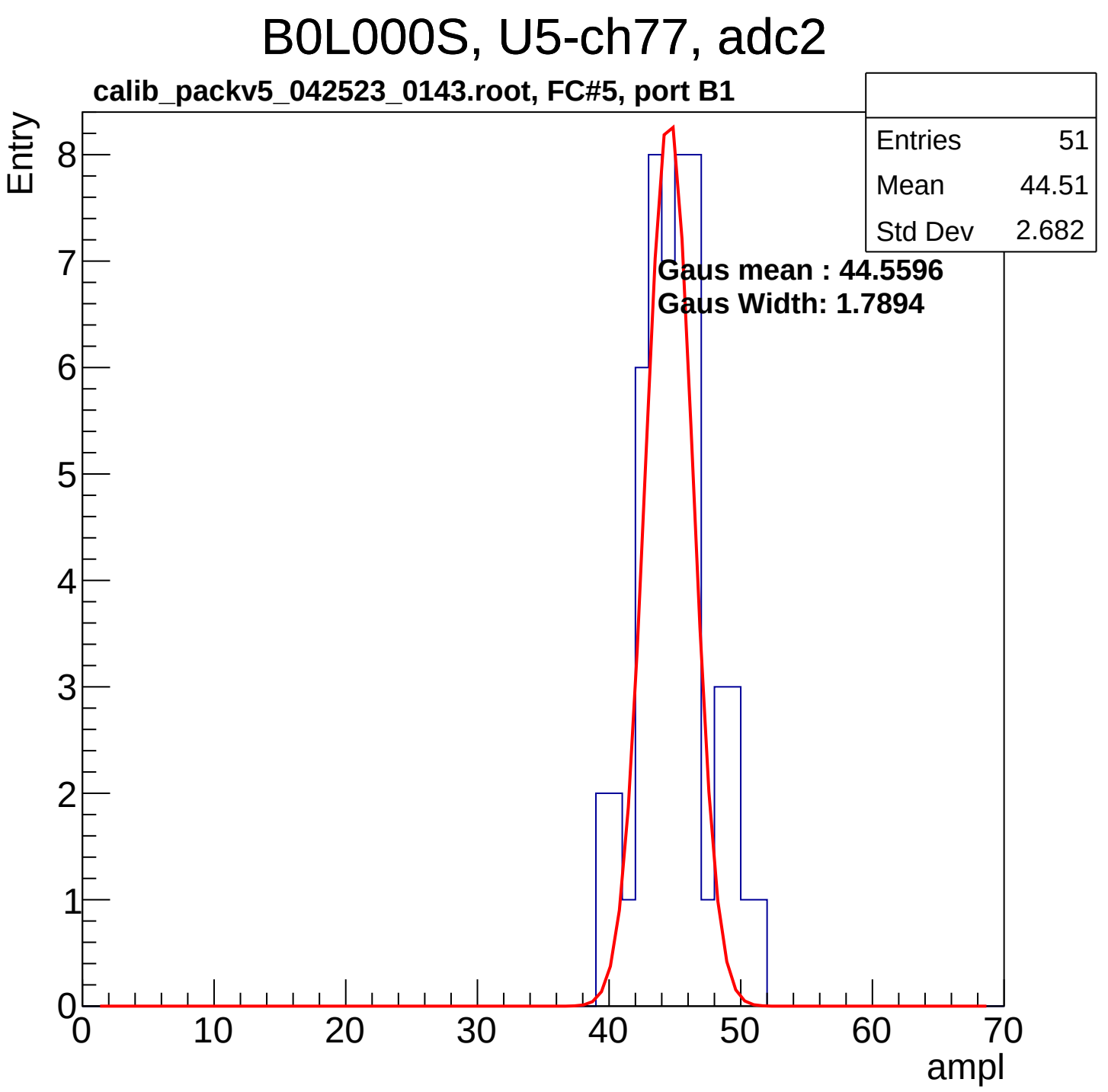

B0L000S, U5-ch76, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 19
Mean 61.47
Std Dev 1.313
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch76, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch77, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 30.95
Std Dev 5.111
Gaus mean : 31.9742
Gaus Width: 3.4802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch77, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 37.45
Std Dev 3.985
Gaus mean : 38.0766
Gaus Width: 3.8822
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U5-ch77, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 44.51
Std Dev 2.682
Gaus mean : 44.5596
Gaus Width: 1.7894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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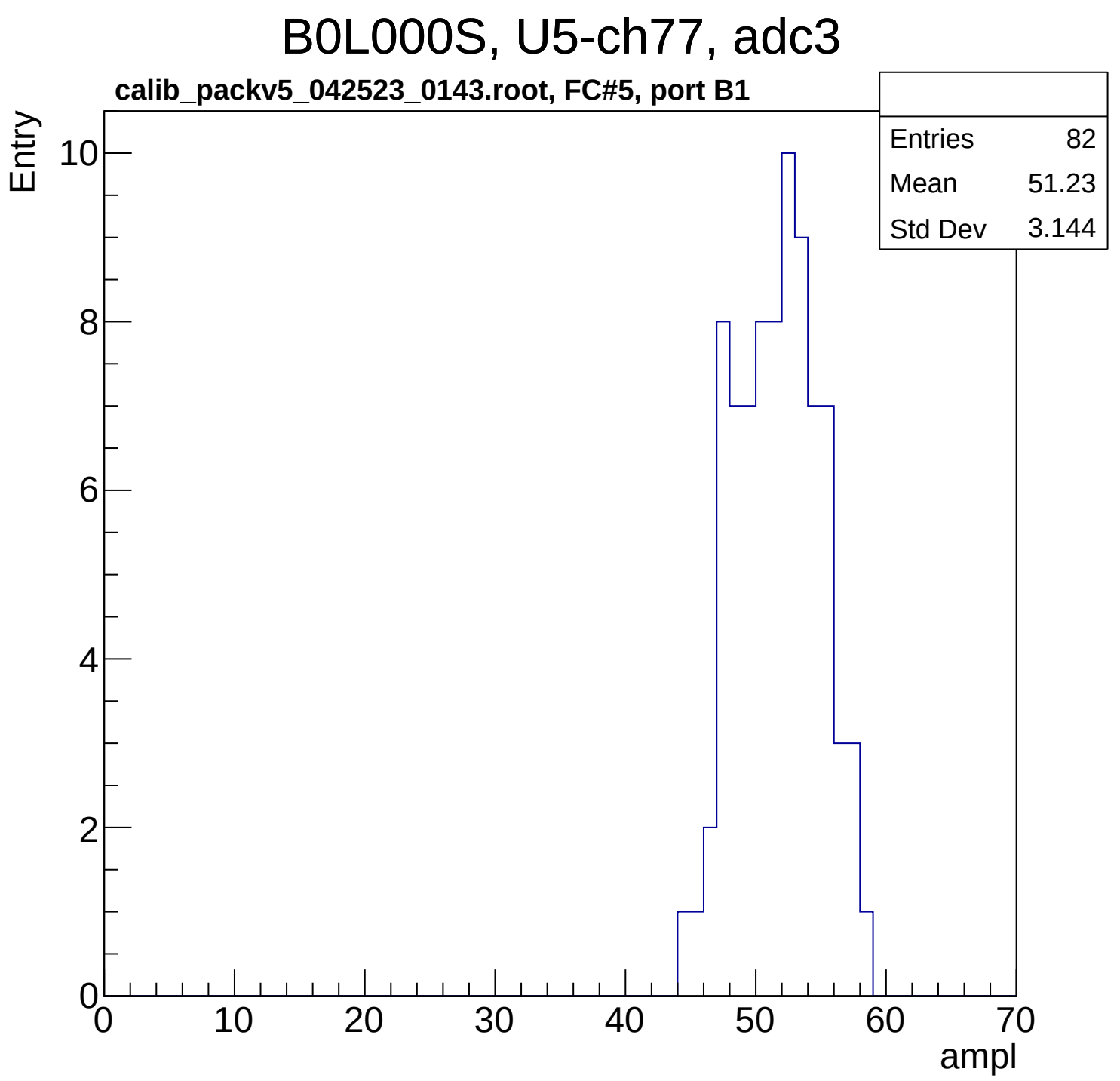
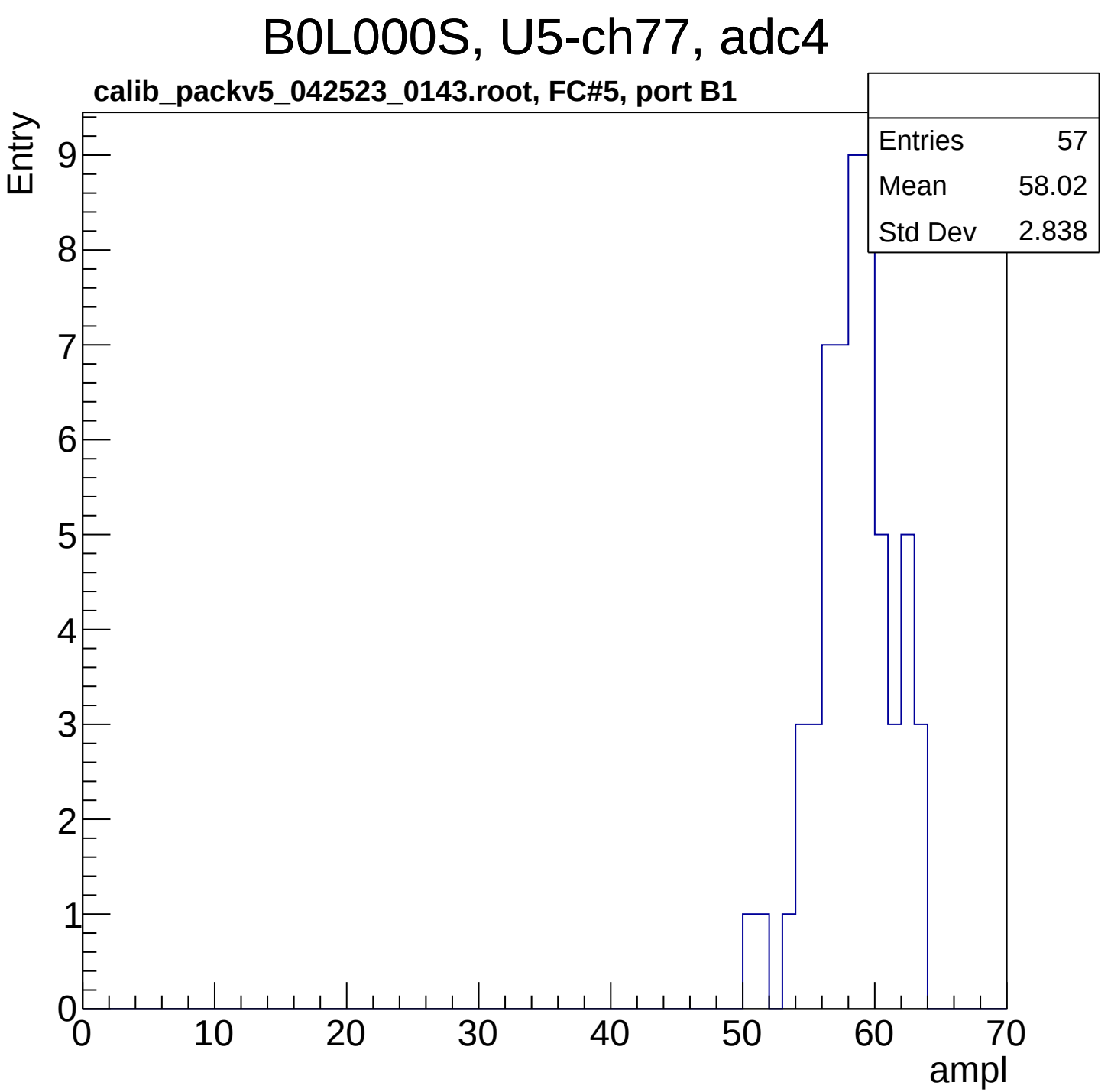

B0L000S, U5-ch77, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 51.23
Std Dev 3.144
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch77, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.02
Std Dev 2.838
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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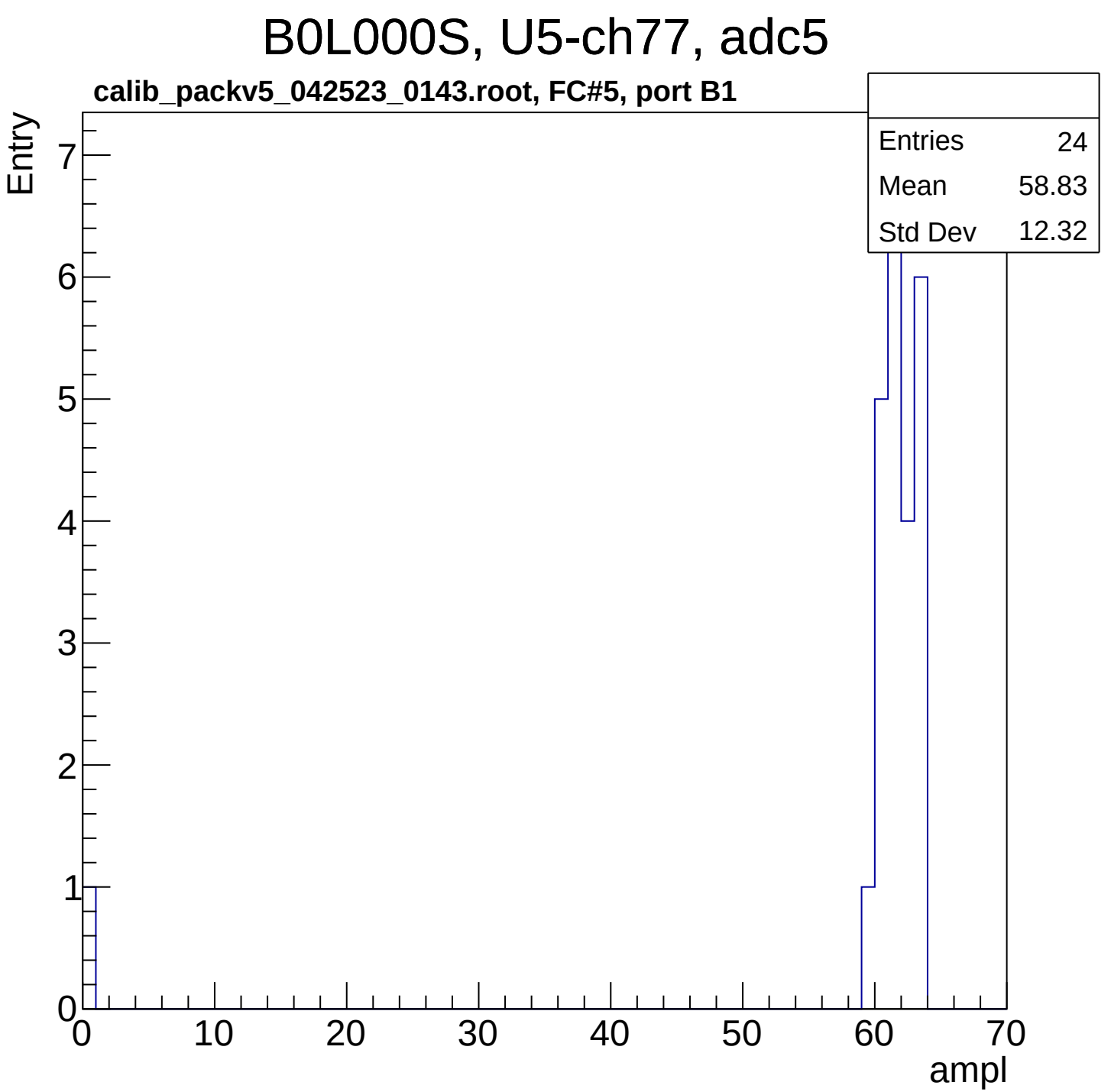
B0L000S, U5-ch77, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 24
Mean 58.83
Std Dev 12.32
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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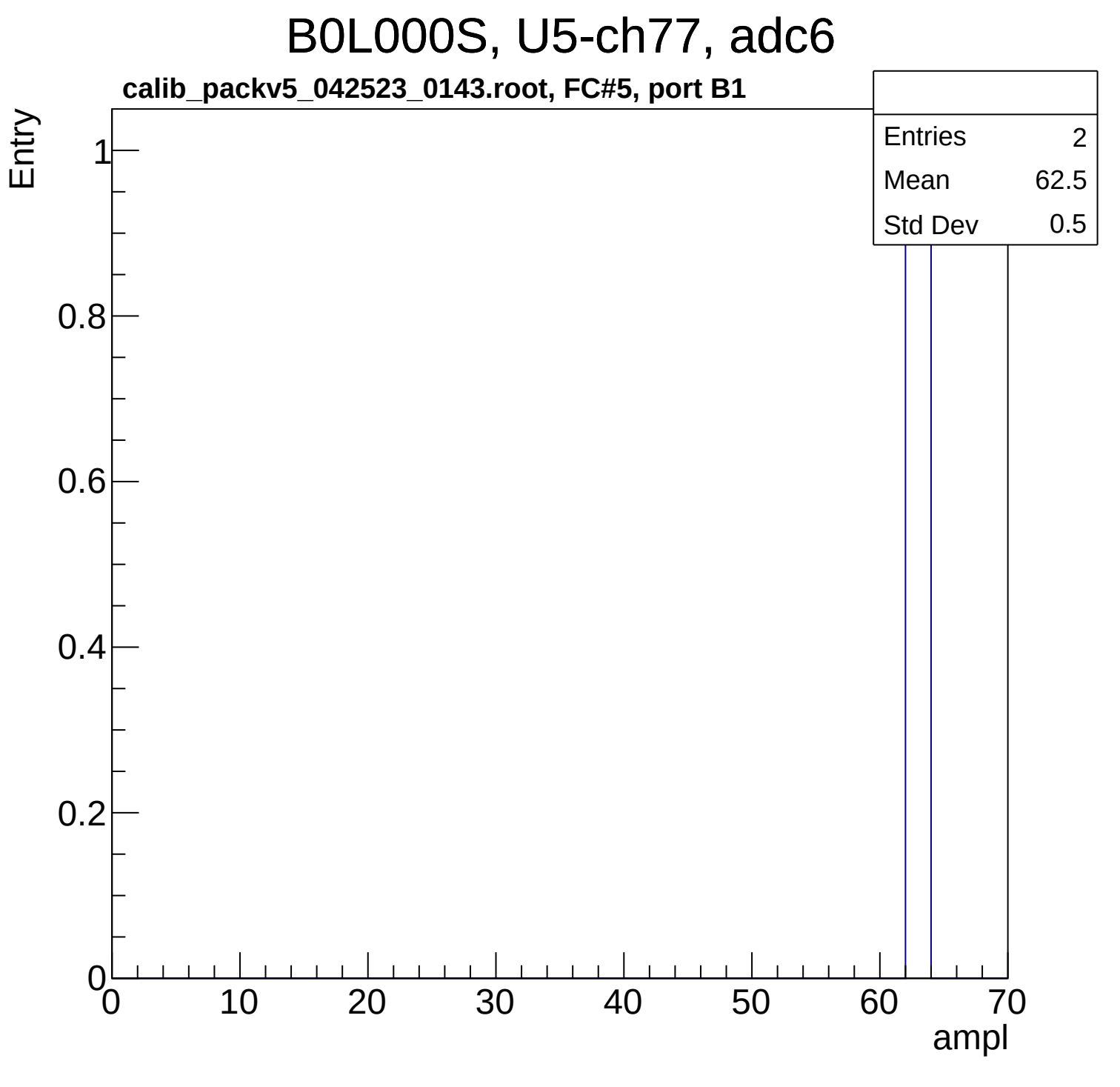
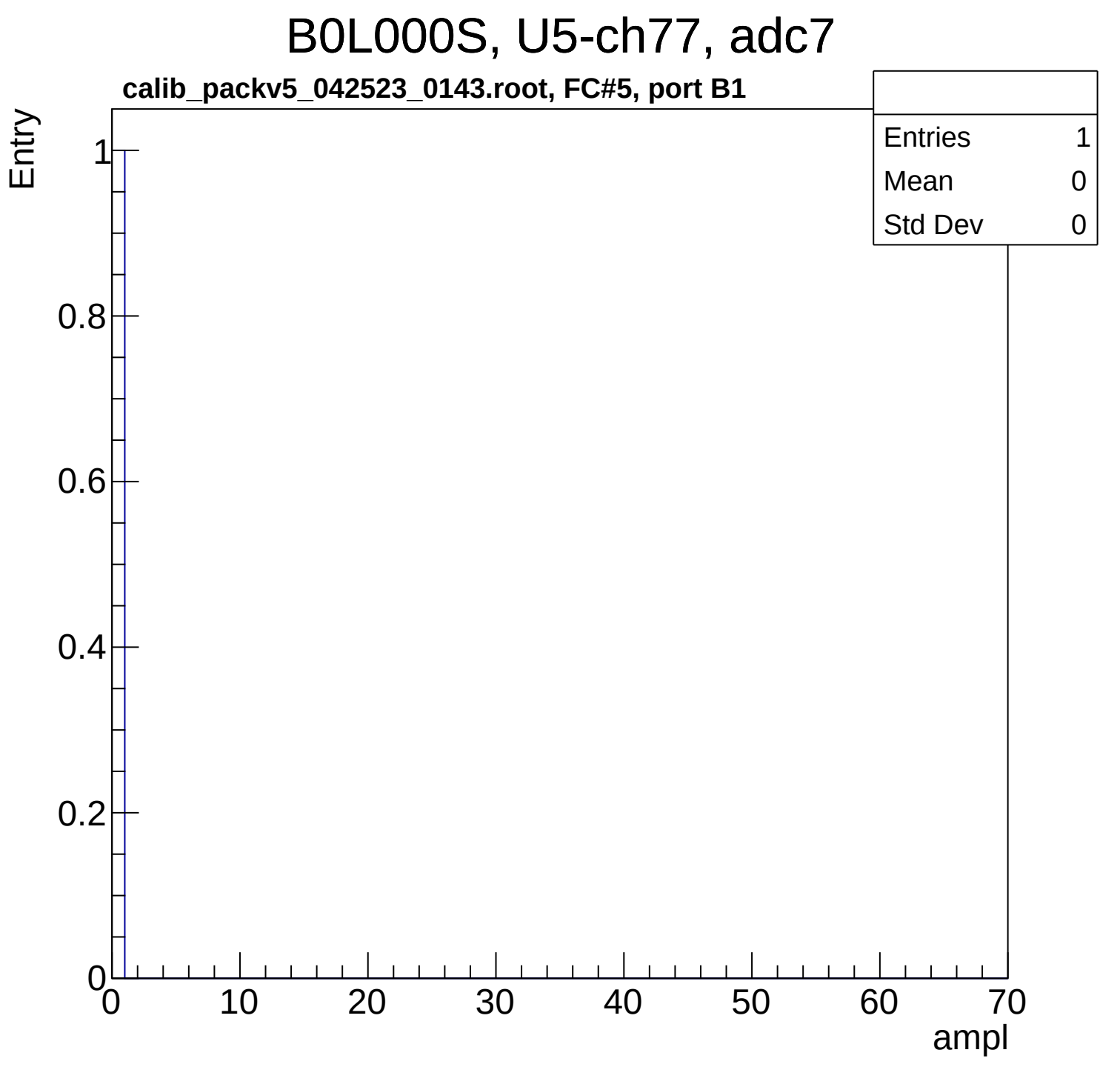
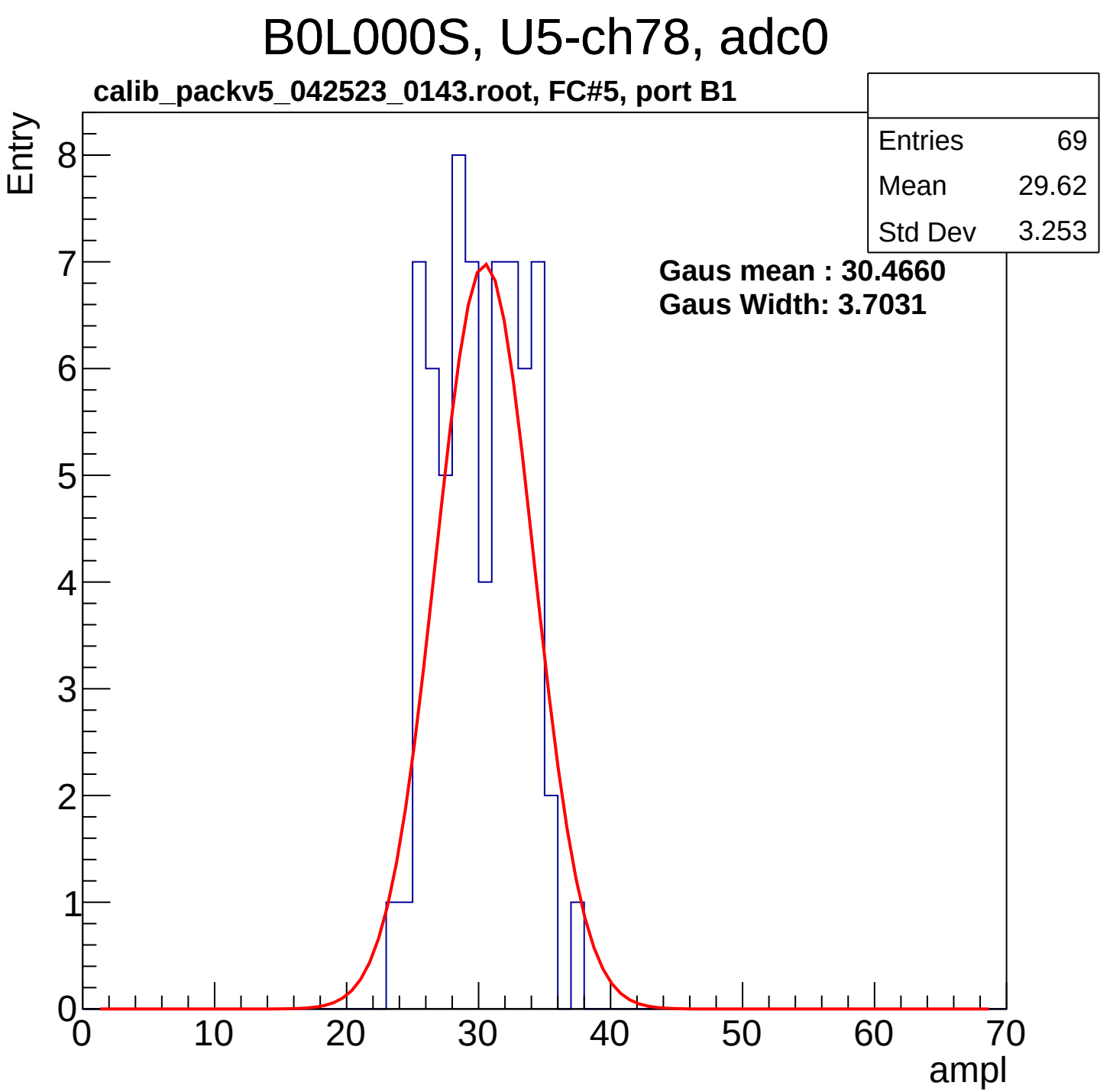
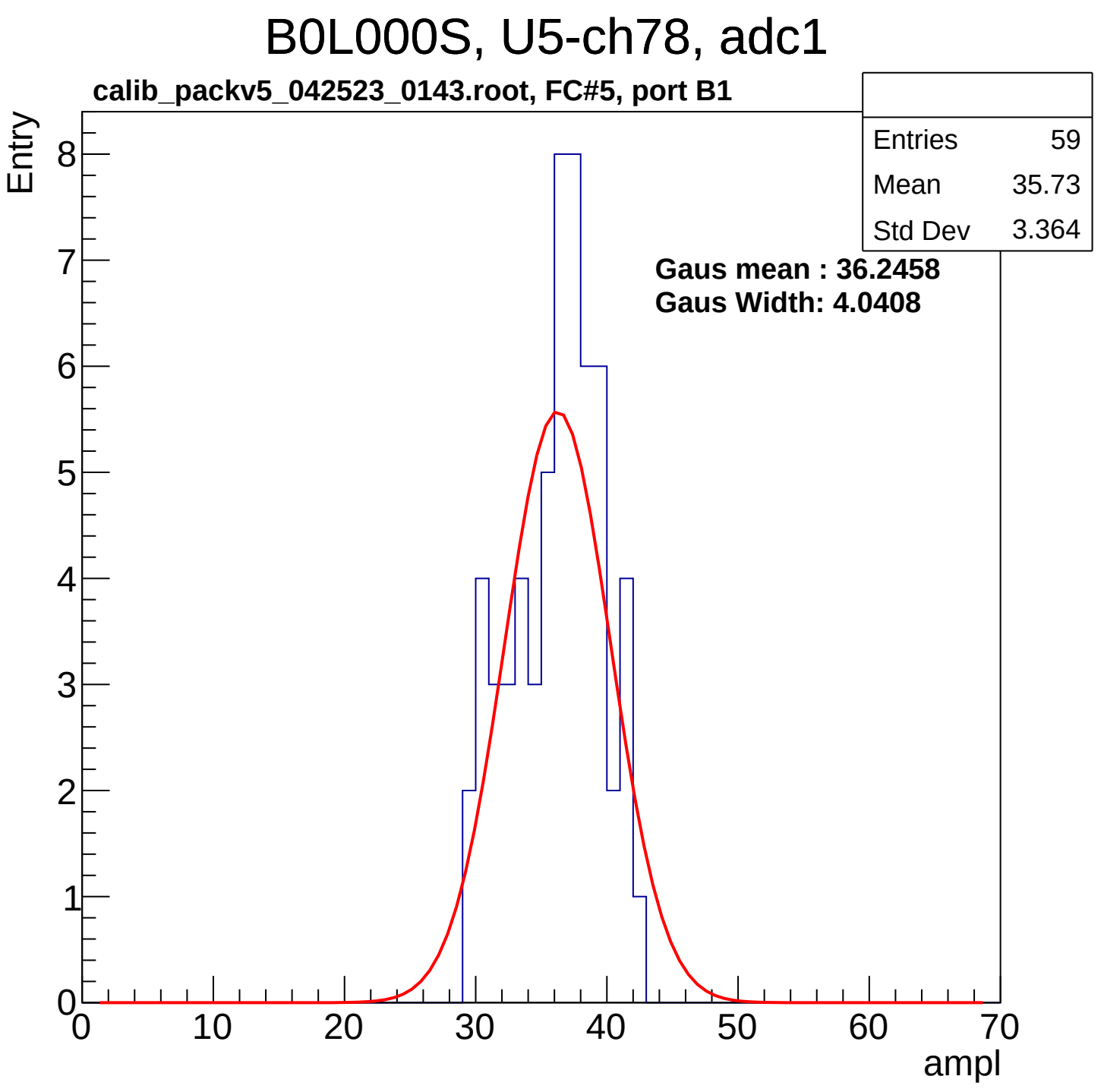
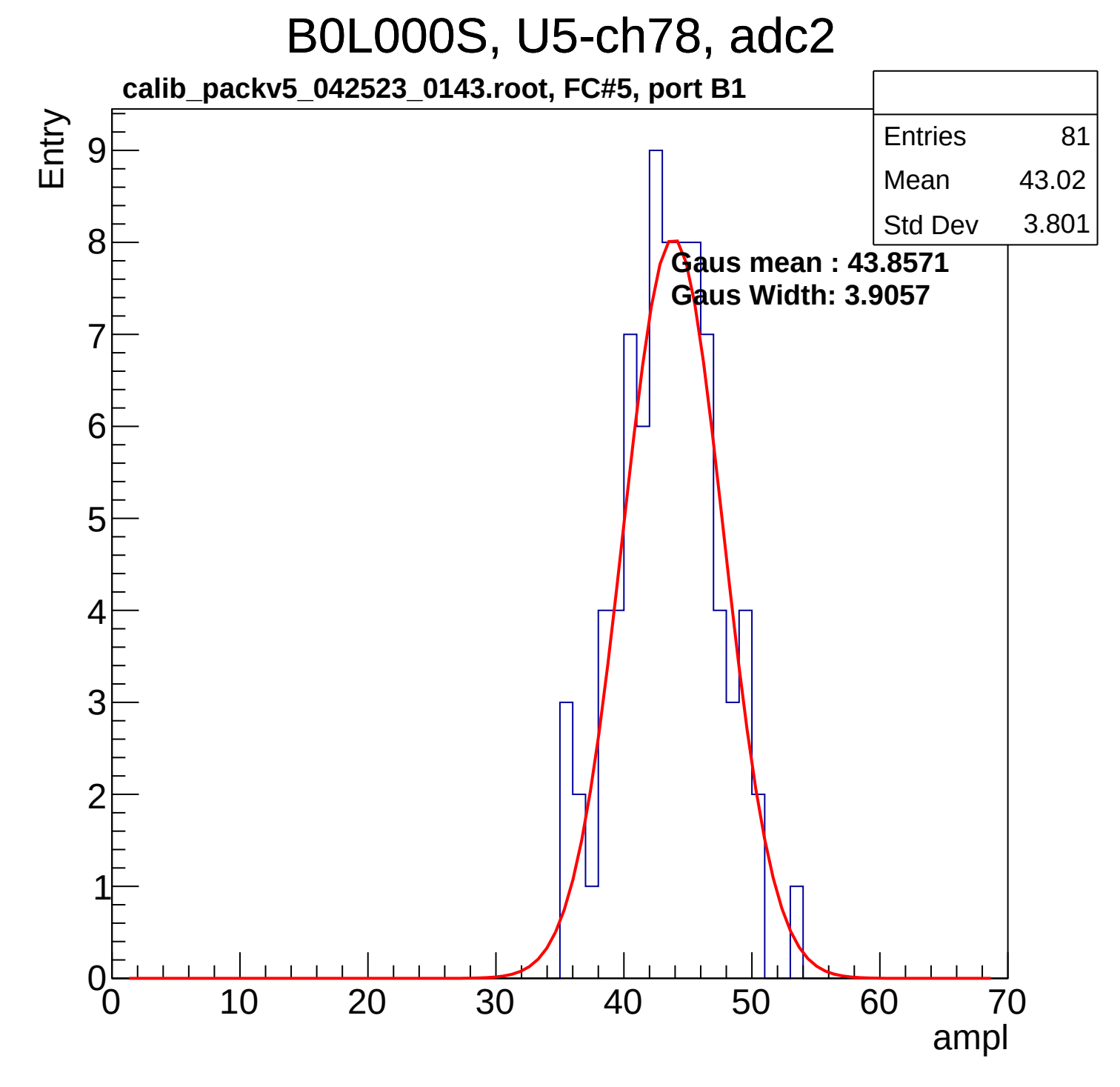
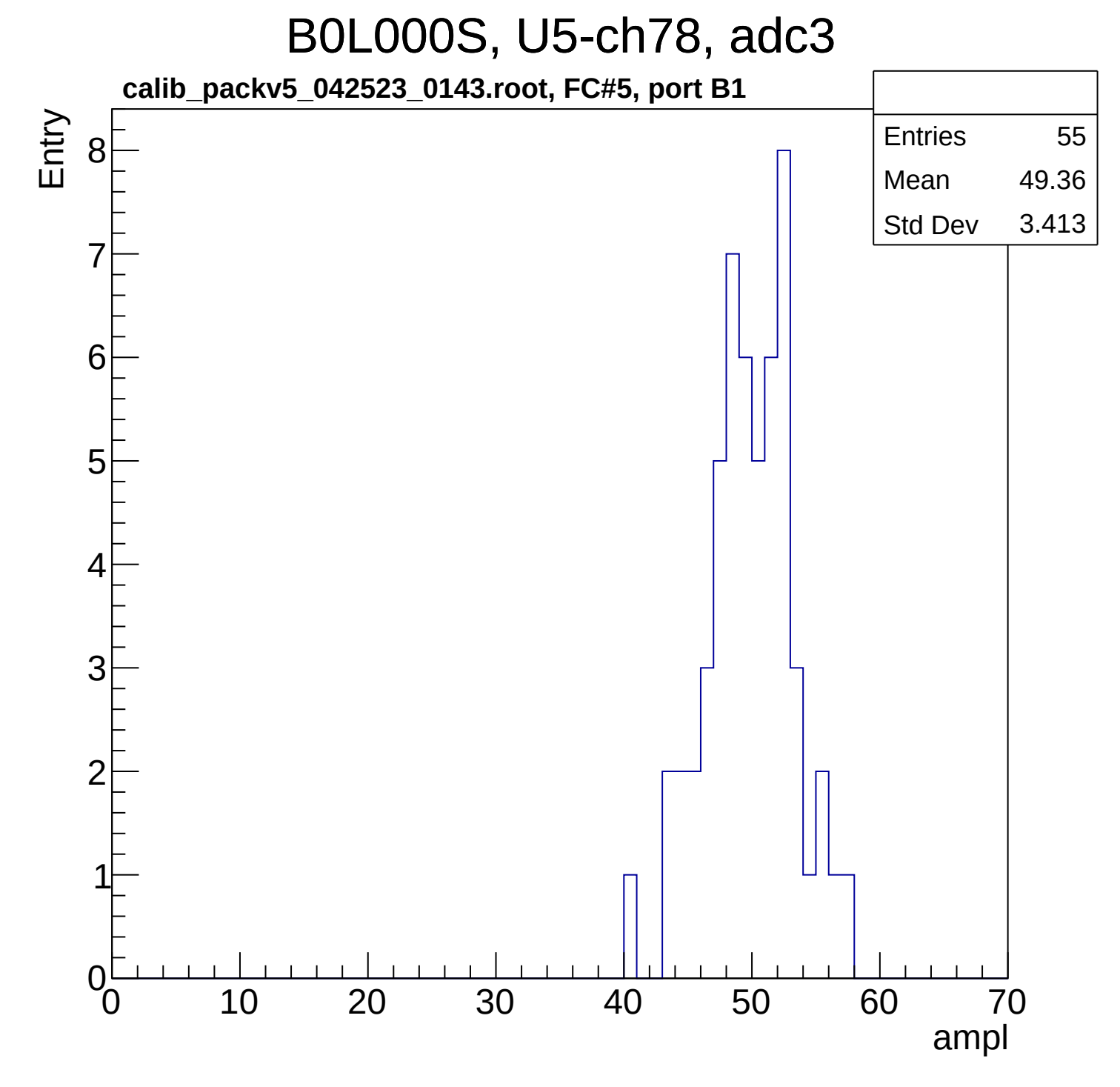

B0L000S, U5-ch77, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

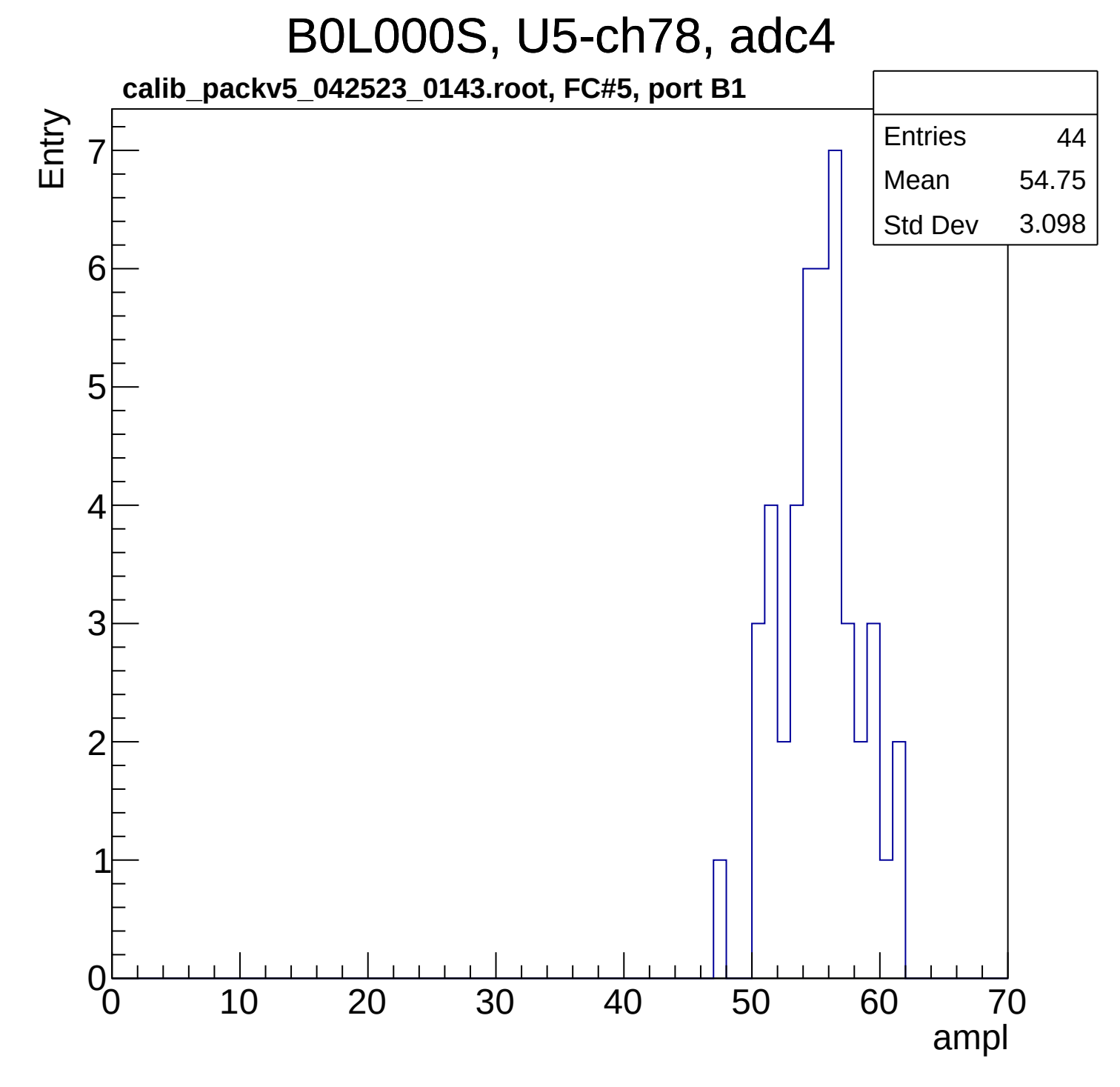

B0L000S, U5-ch77, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

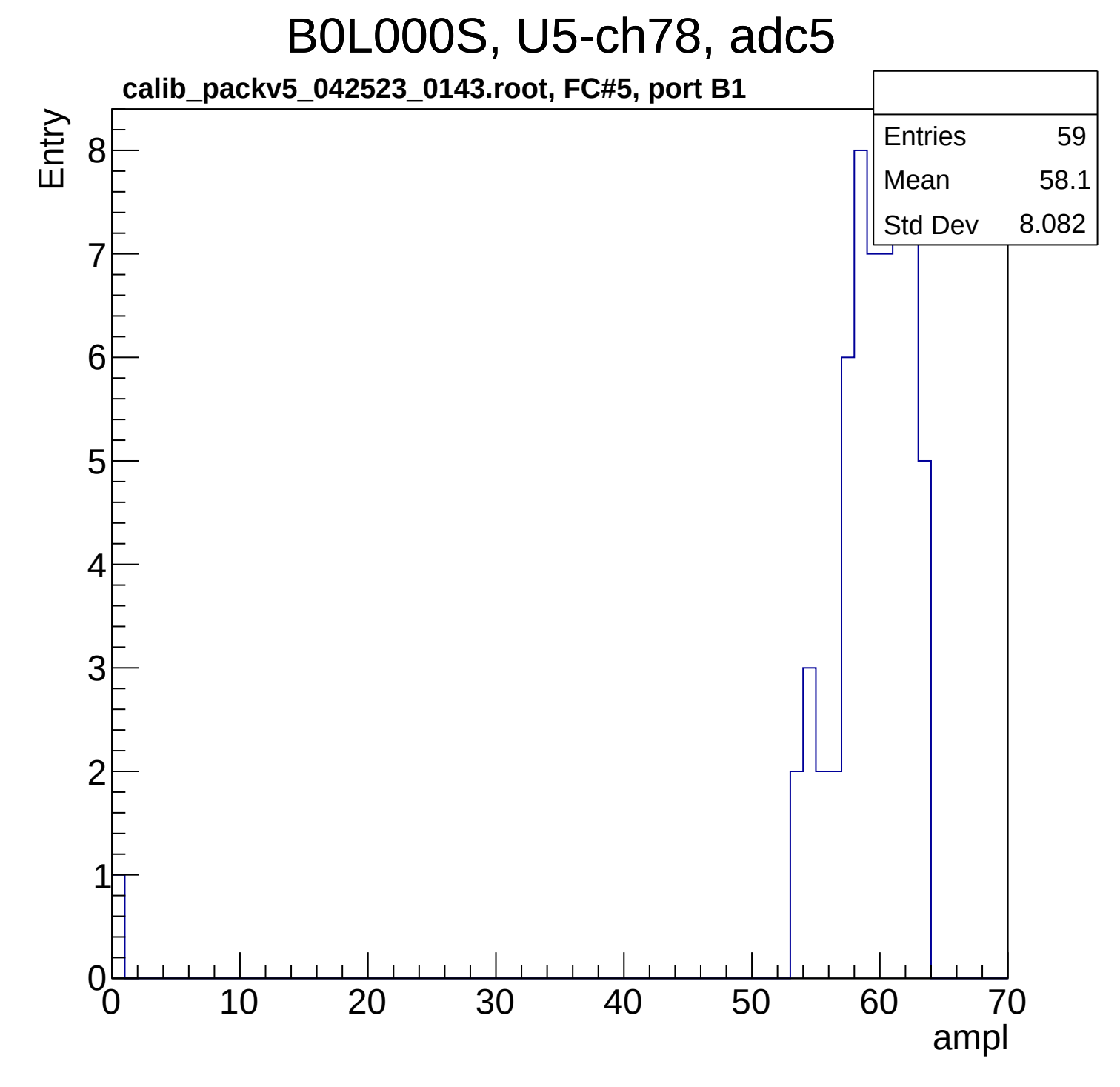

B0L000S, U5-ch78, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.62
Std Dev 3.253
Gaus mean : 30.4660
Gaus Width: 3.7031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

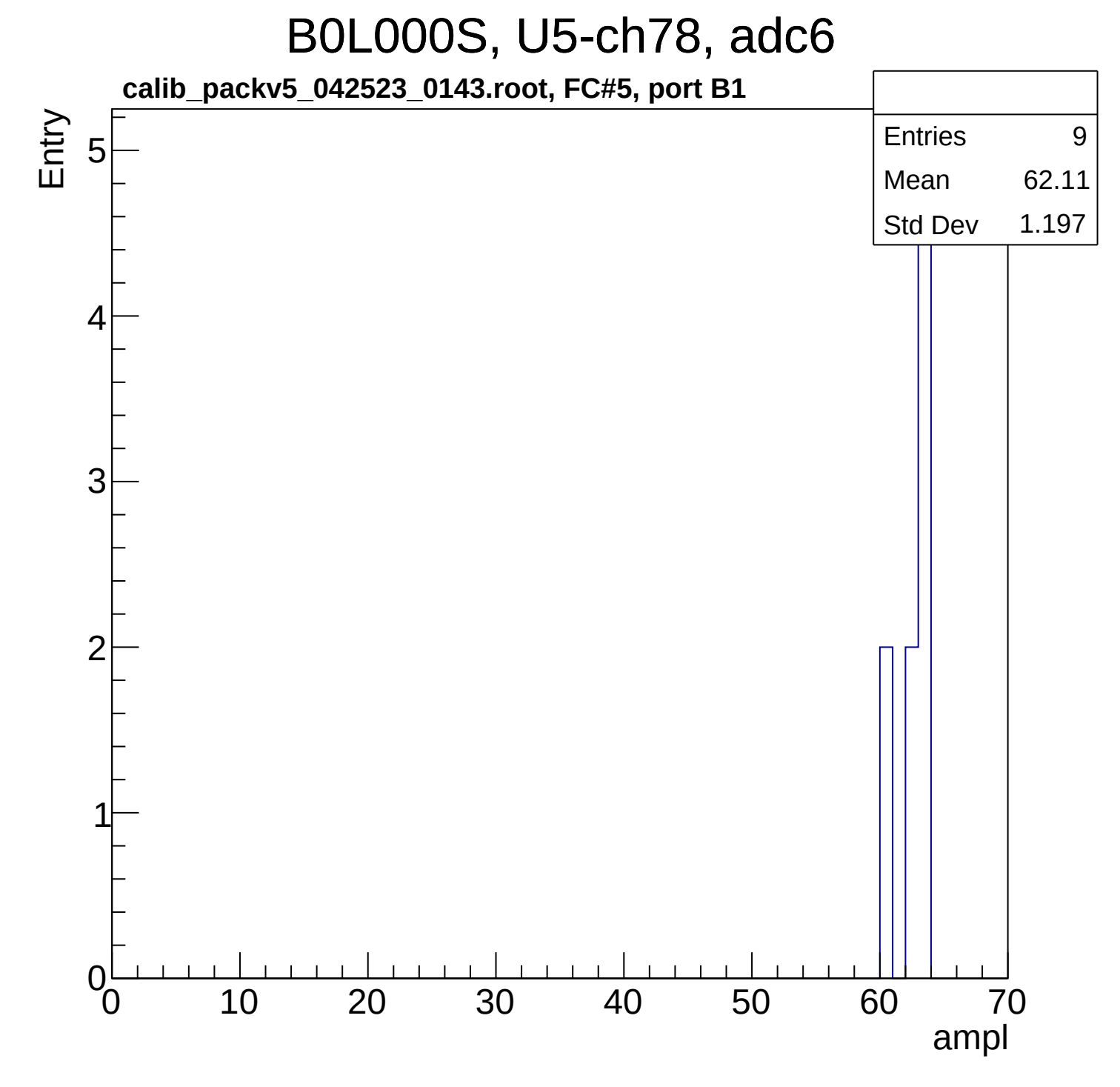

B0L000S, U5-ch78, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.73
Std Dev 3.364
Gaus mean : 36.2458
Gaus Width: 4.0408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

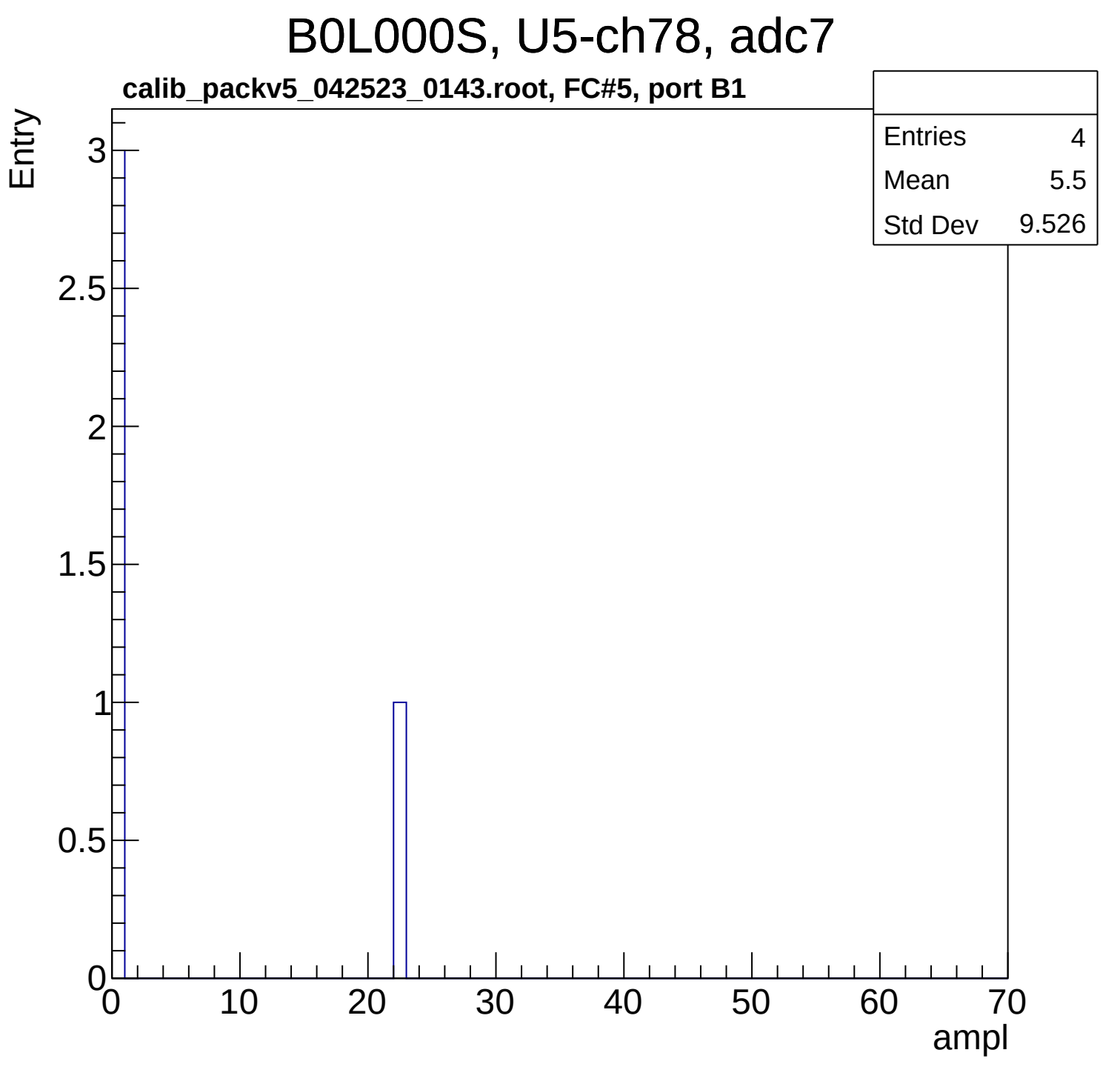

B0L000S, U5-ch78, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 43.02
Std Dev 3.801
Gaus mean : 43.8571
Gaus Width: 3.9057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

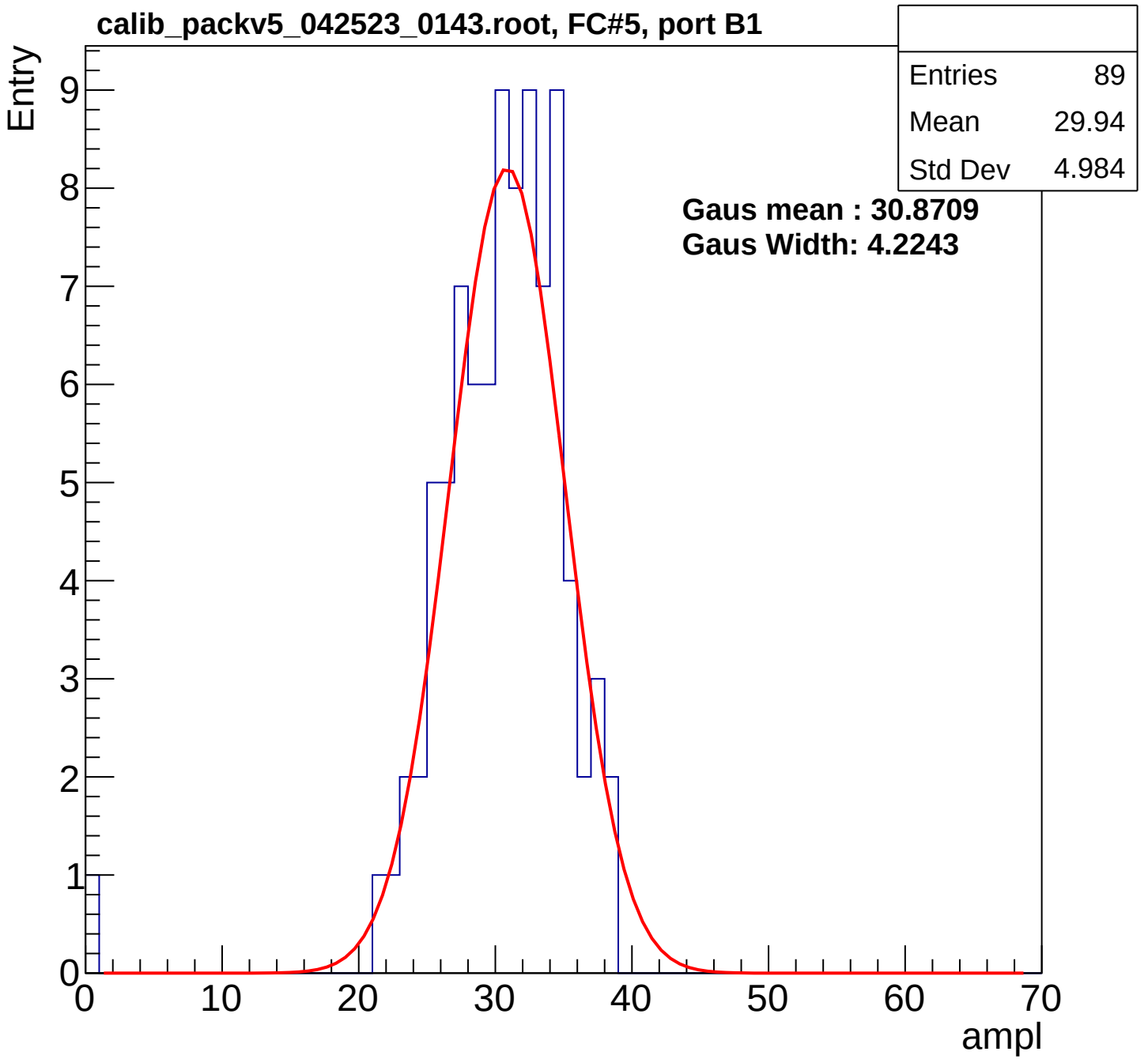

B0L000S, U5-ch78, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 49.36
Std Dev 3.413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

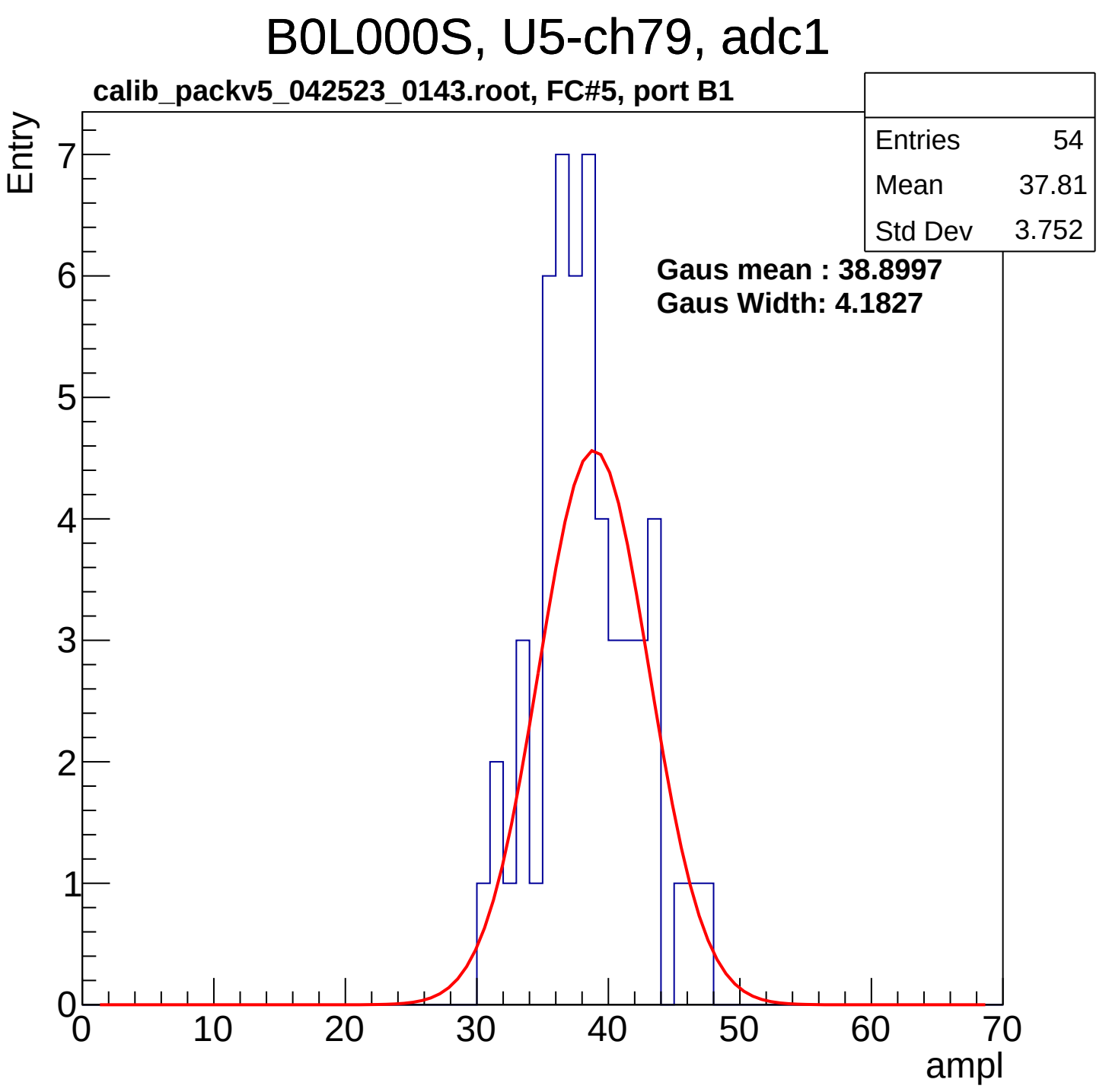

B0L000S, U5-ch78, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 54.75
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

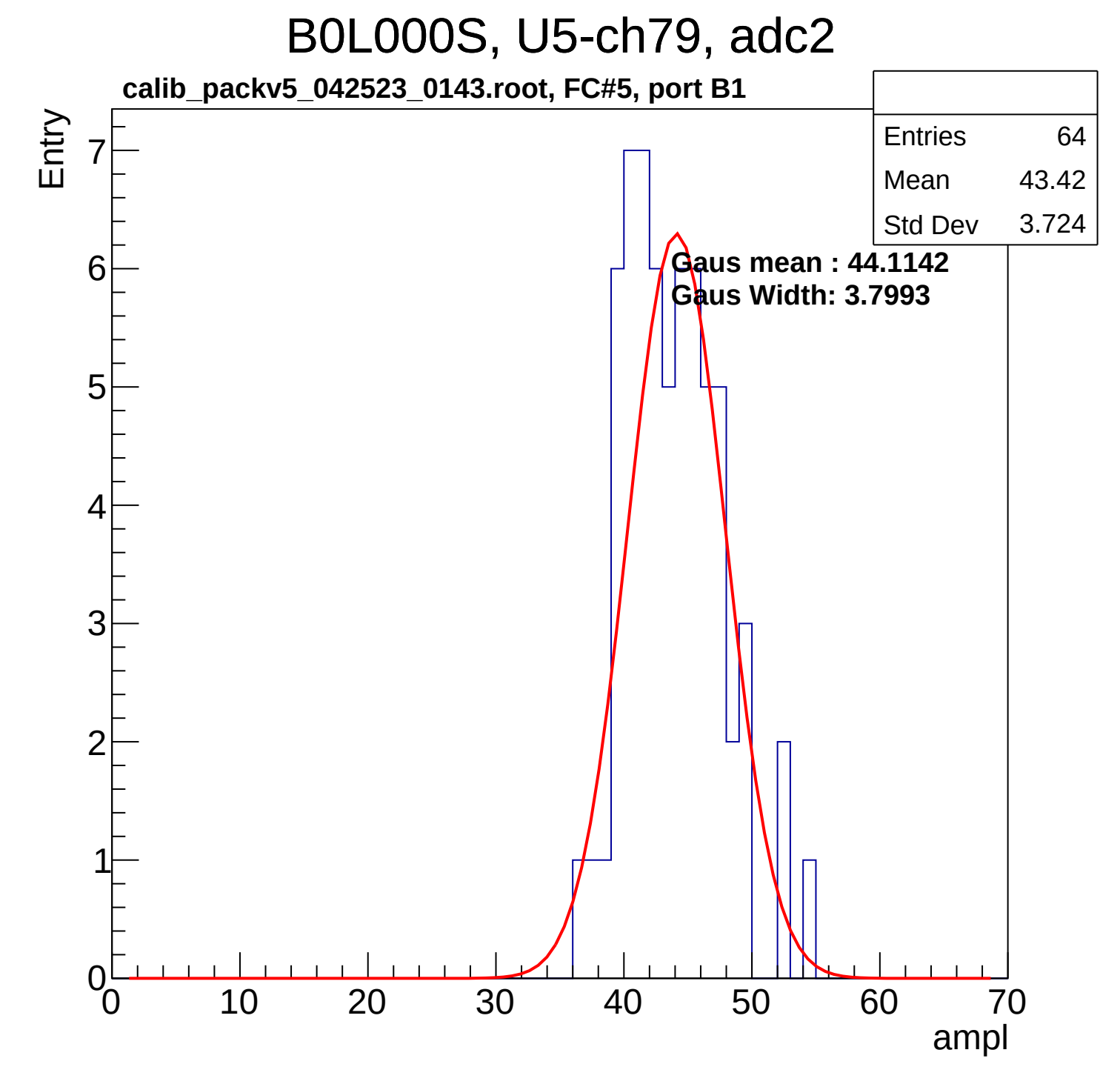

B0L000S, U5-ch78, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 58.1
Std Dev 8.082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

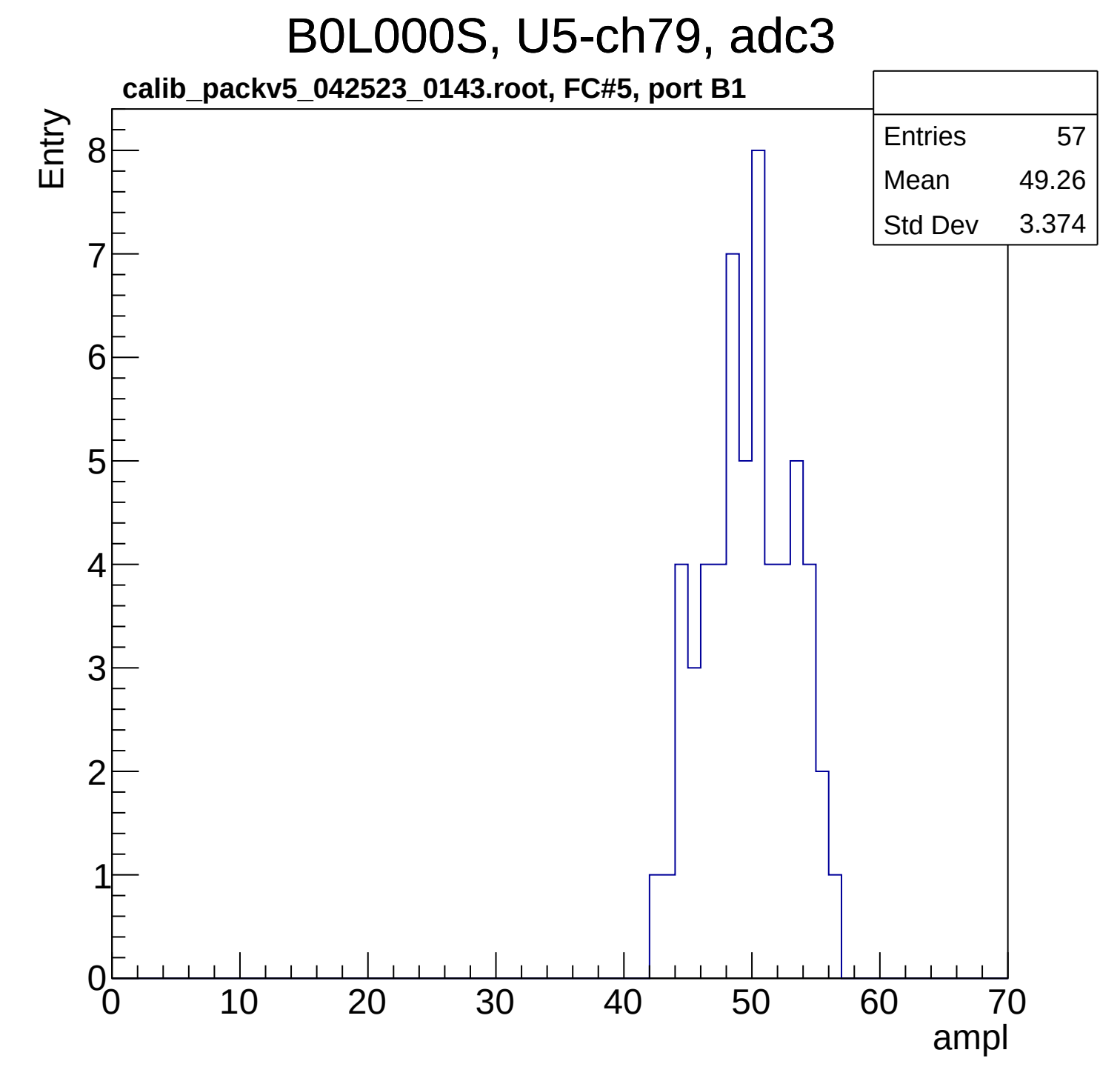

B0L000S, U5-ch78, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.11
Std Dev 1.197
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch78, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 5.5
Std Dev 9.526
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 89
Mean 29.94
Std Dev 4.984
Gaus mean : 30.8709
Gaus Width: 4.2243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 37.81
Std Dev 3.752
Gaus mean : 38.8997
Gaus Width: 4.1827
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 43.42
Std Dev 3.724
Gaus mean : 44.1142
Gaus Width: 3.7993
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.26
Std Dev 3.374
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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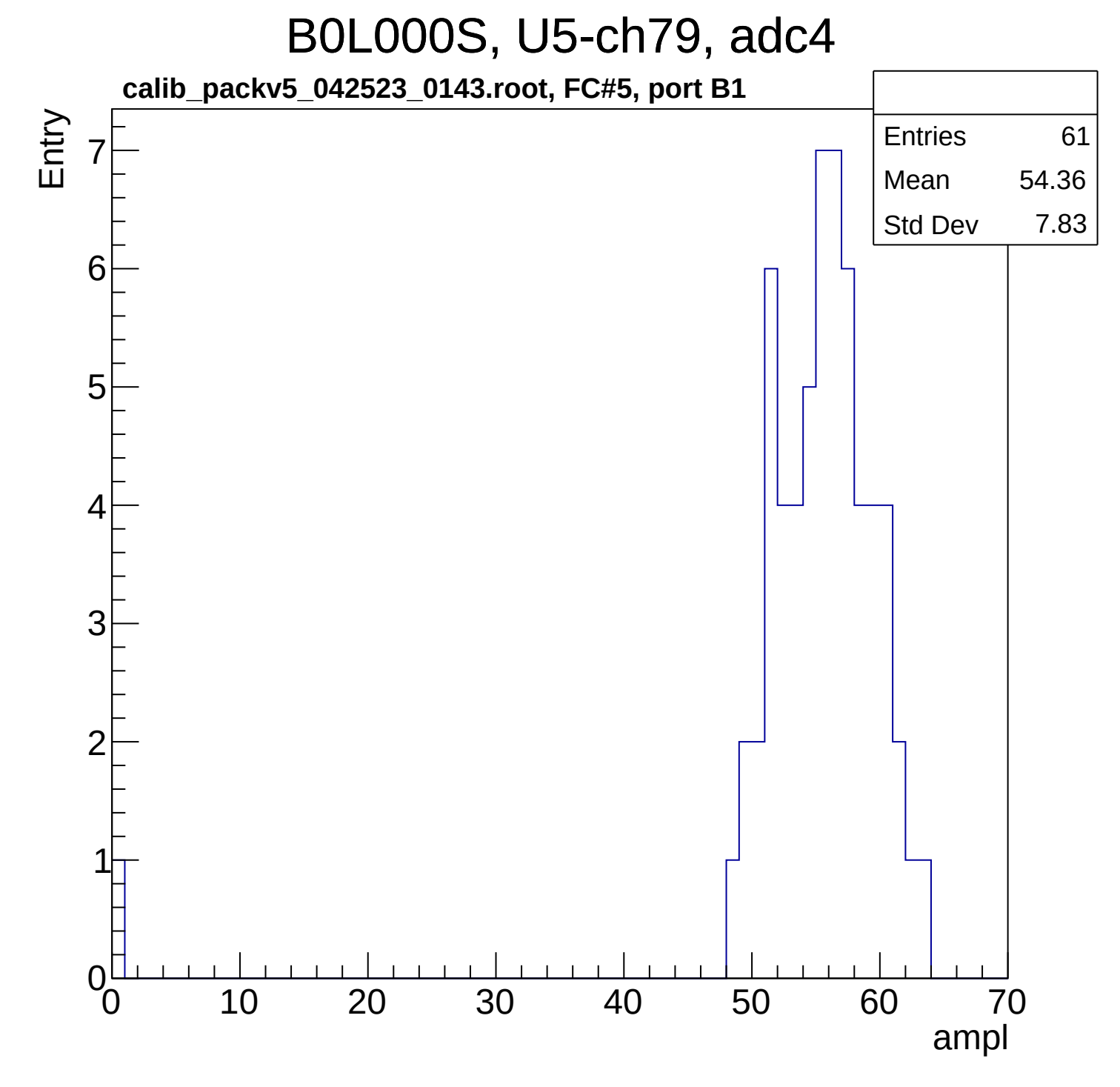
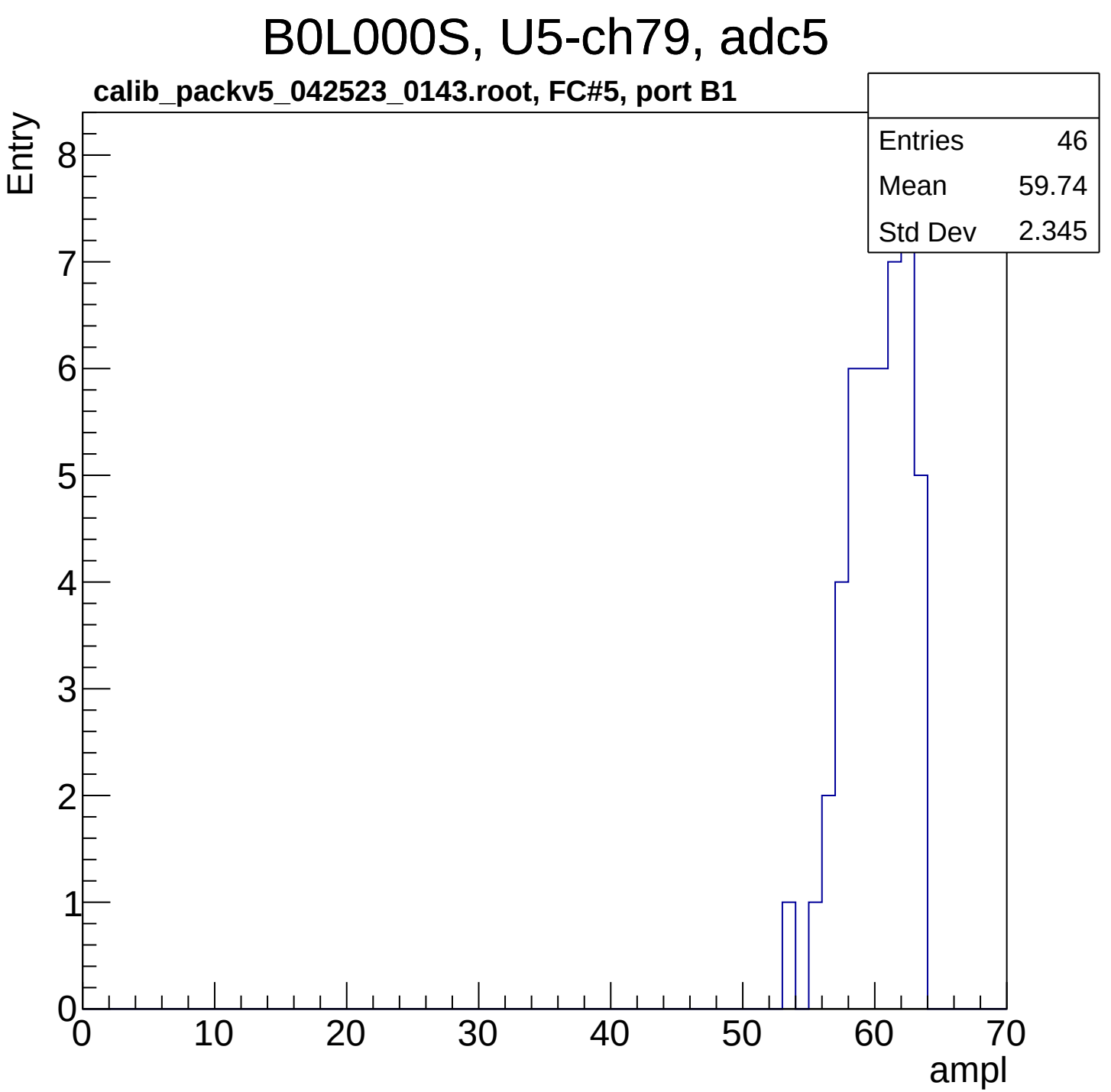

B0L000S, U5-ch79, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 54.36
Std Dev 7.83
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.74
Std Dev 2.345
Entry
ampl
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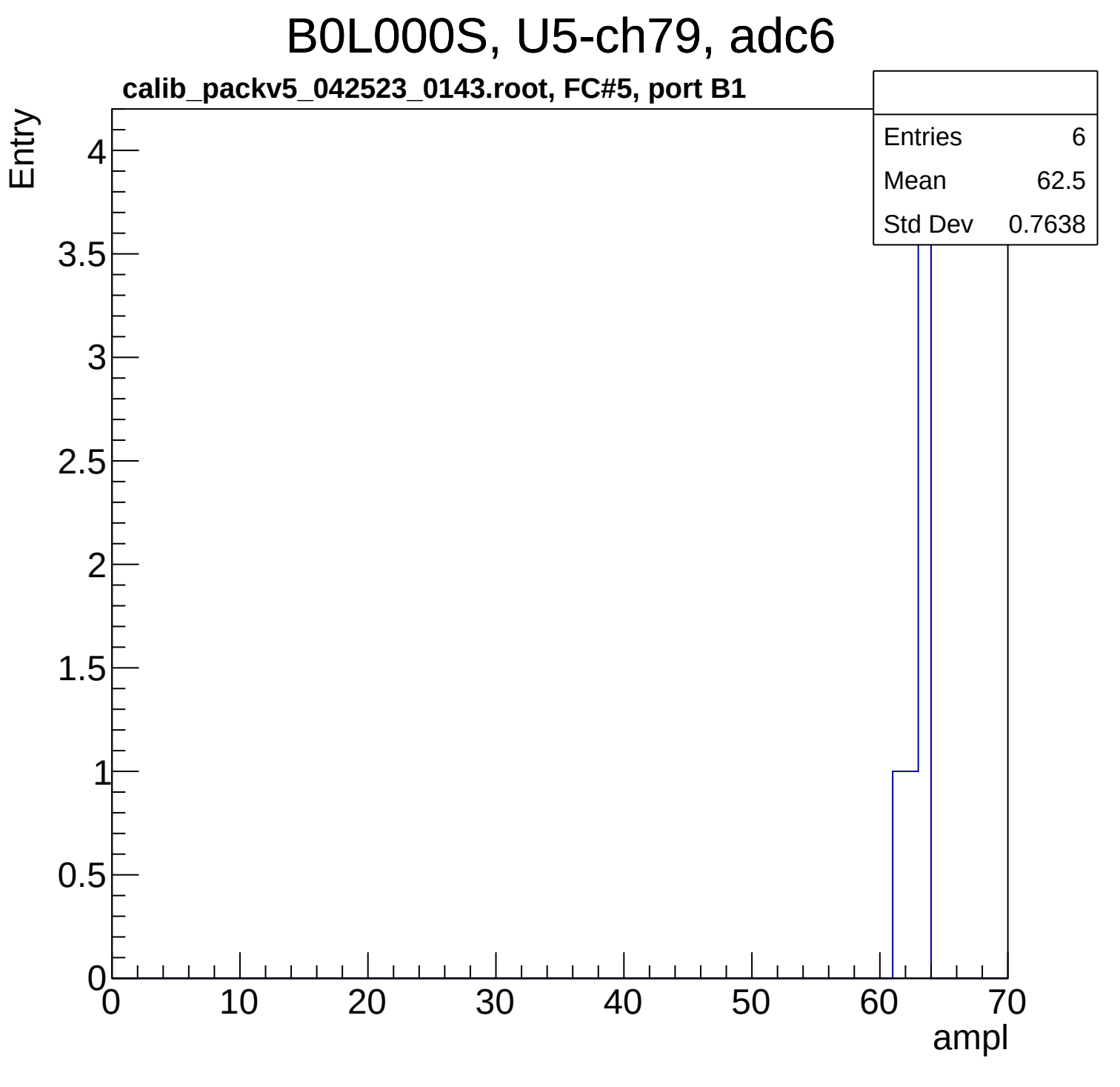
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch79, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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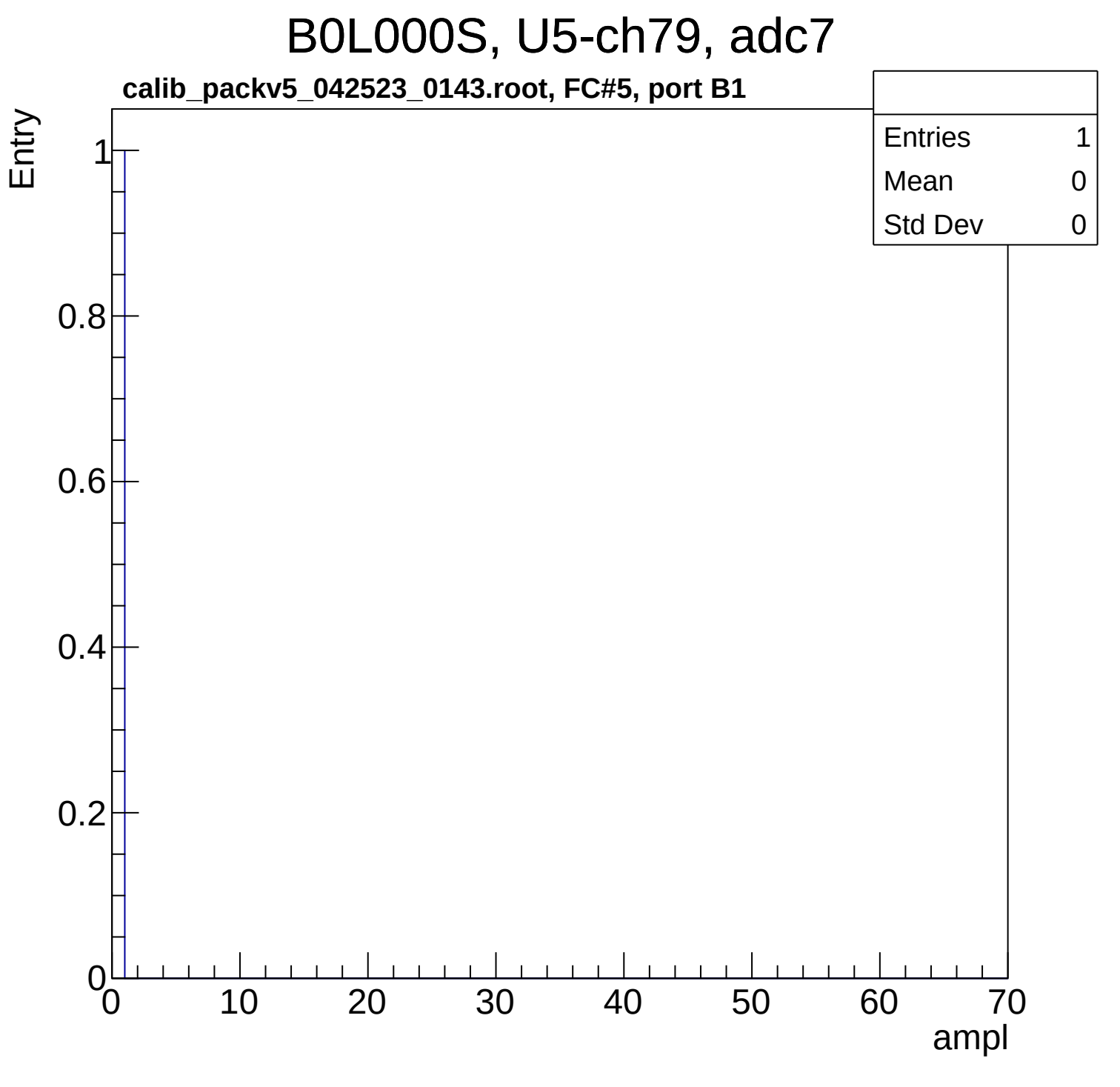
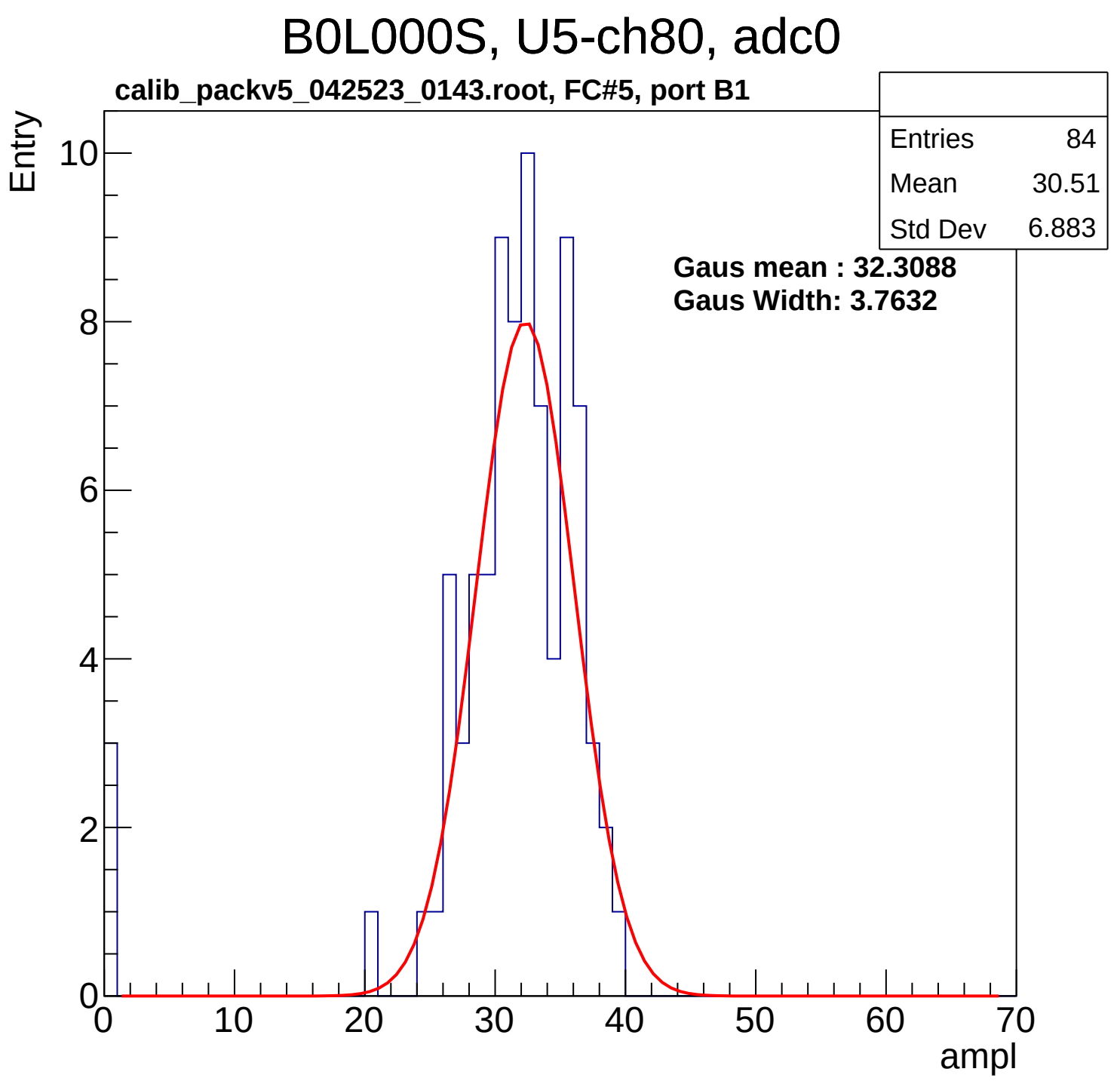
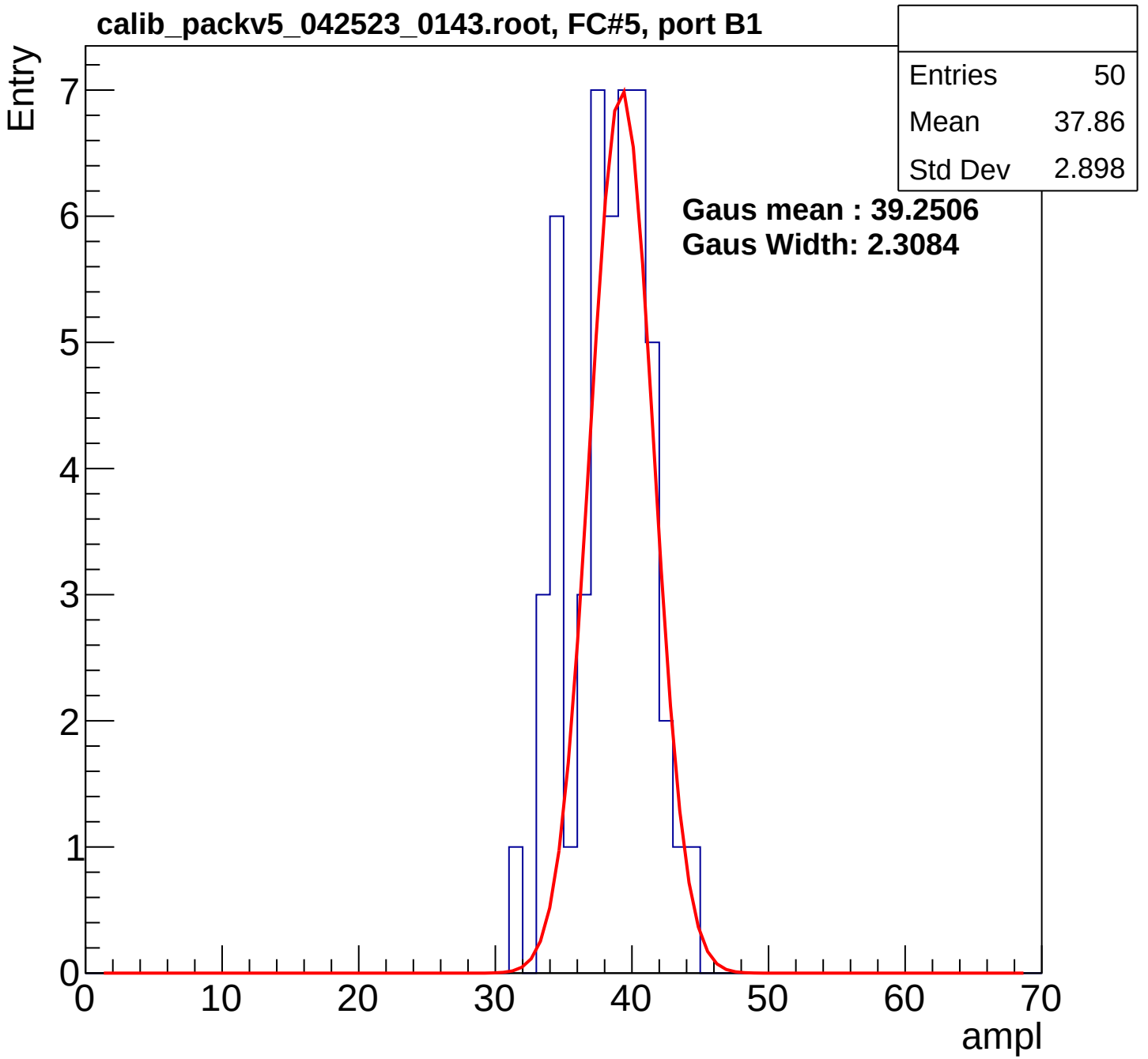
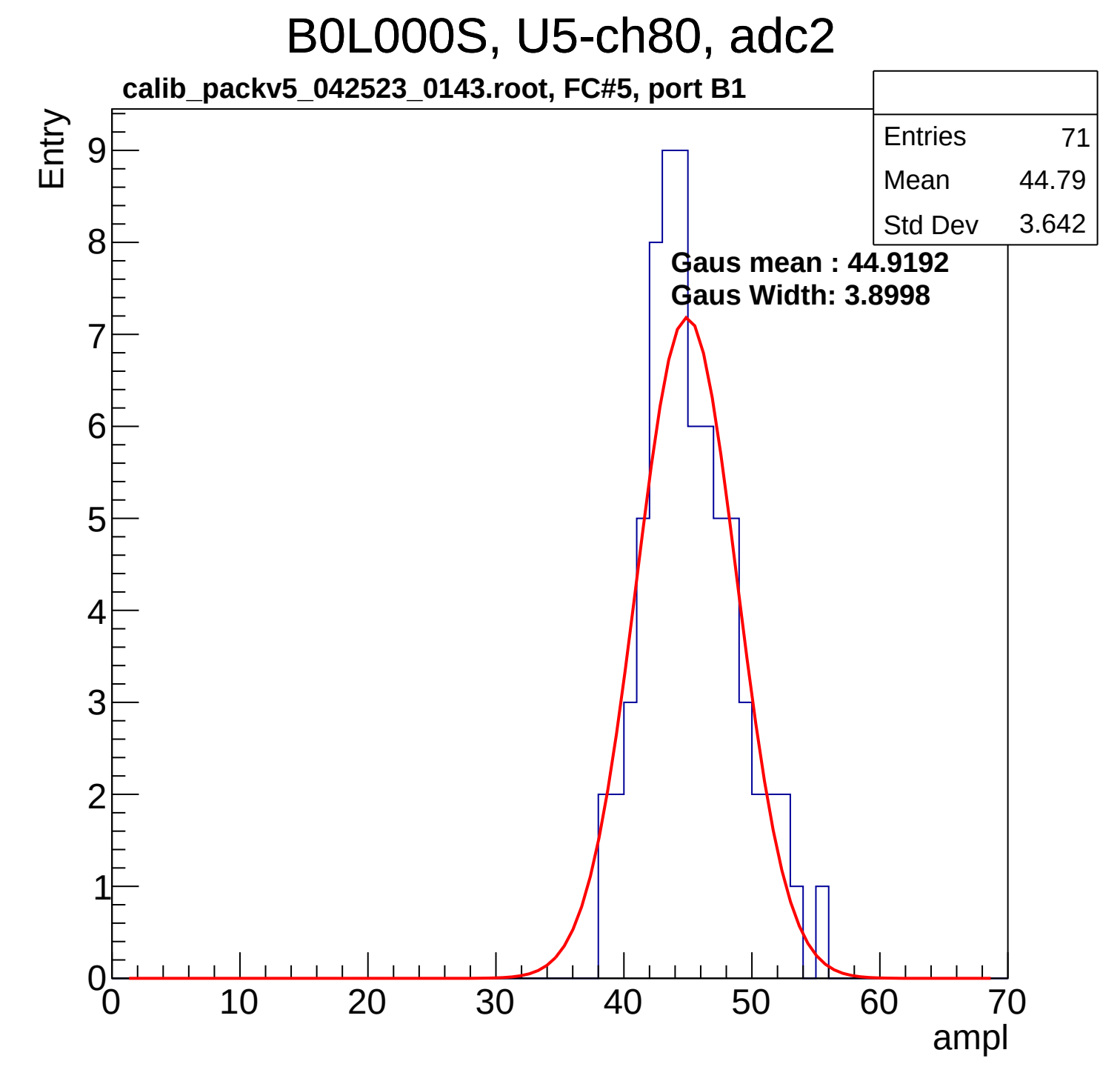
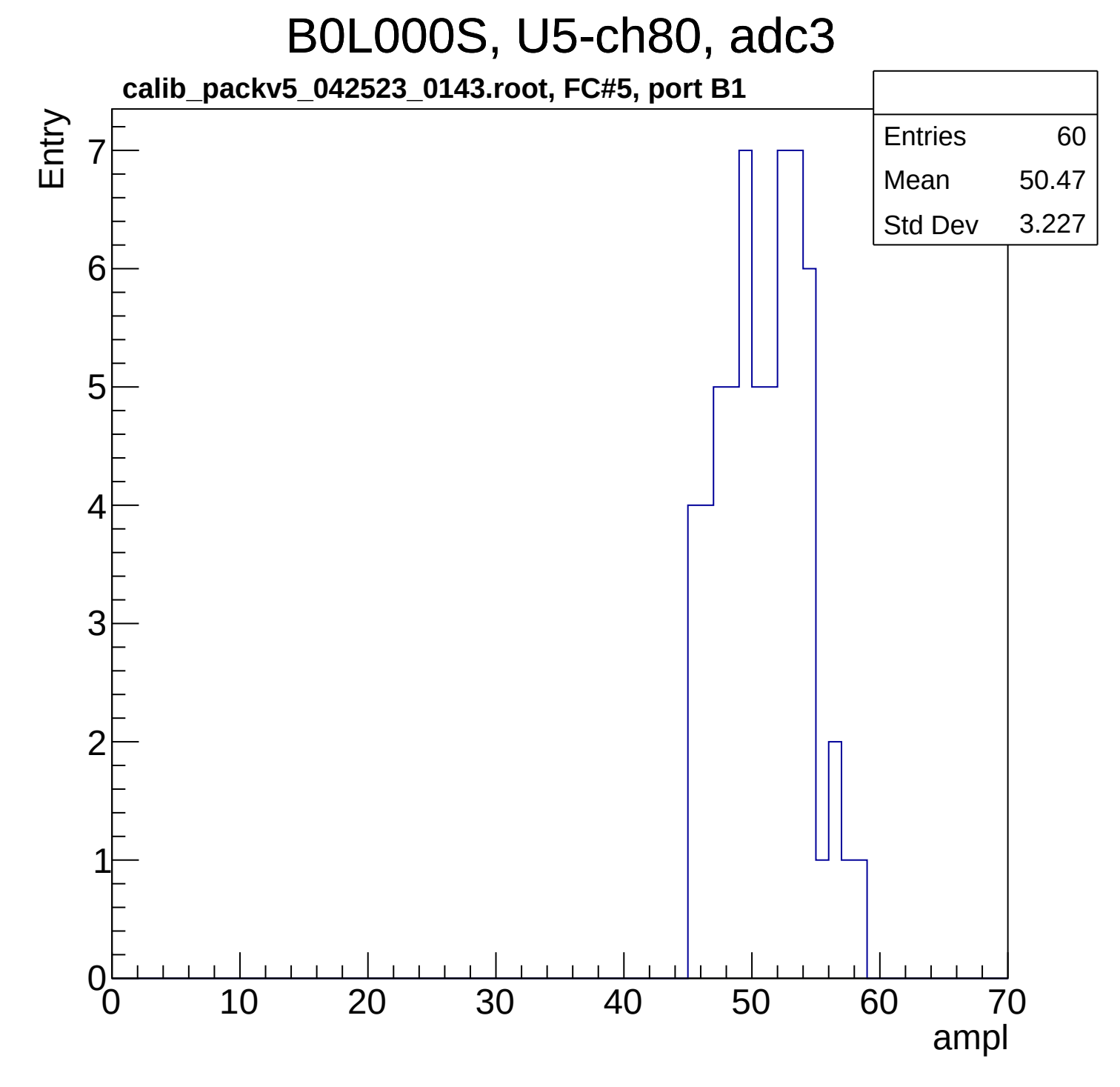
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

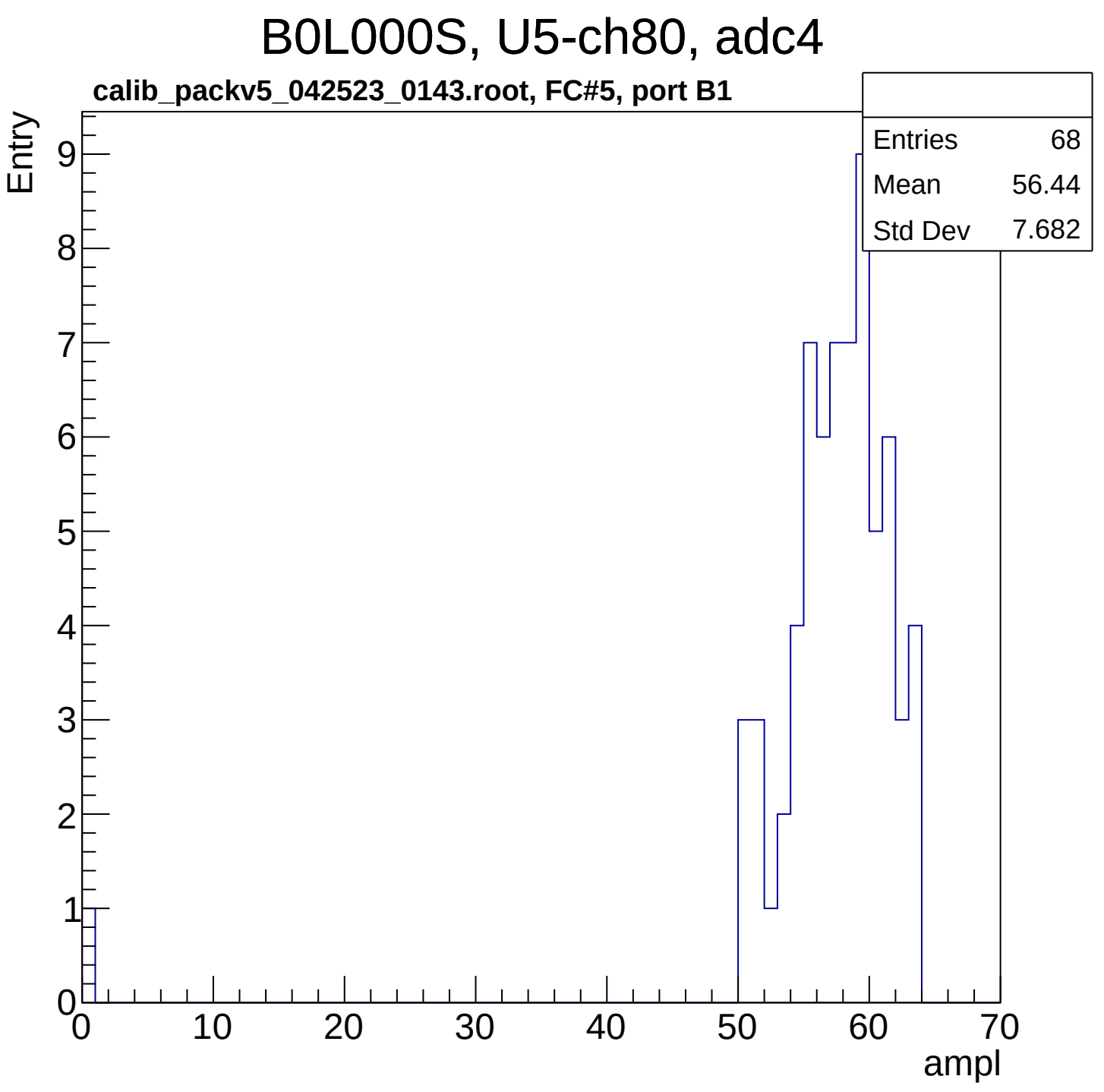

B0L000S, U5-ch79, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

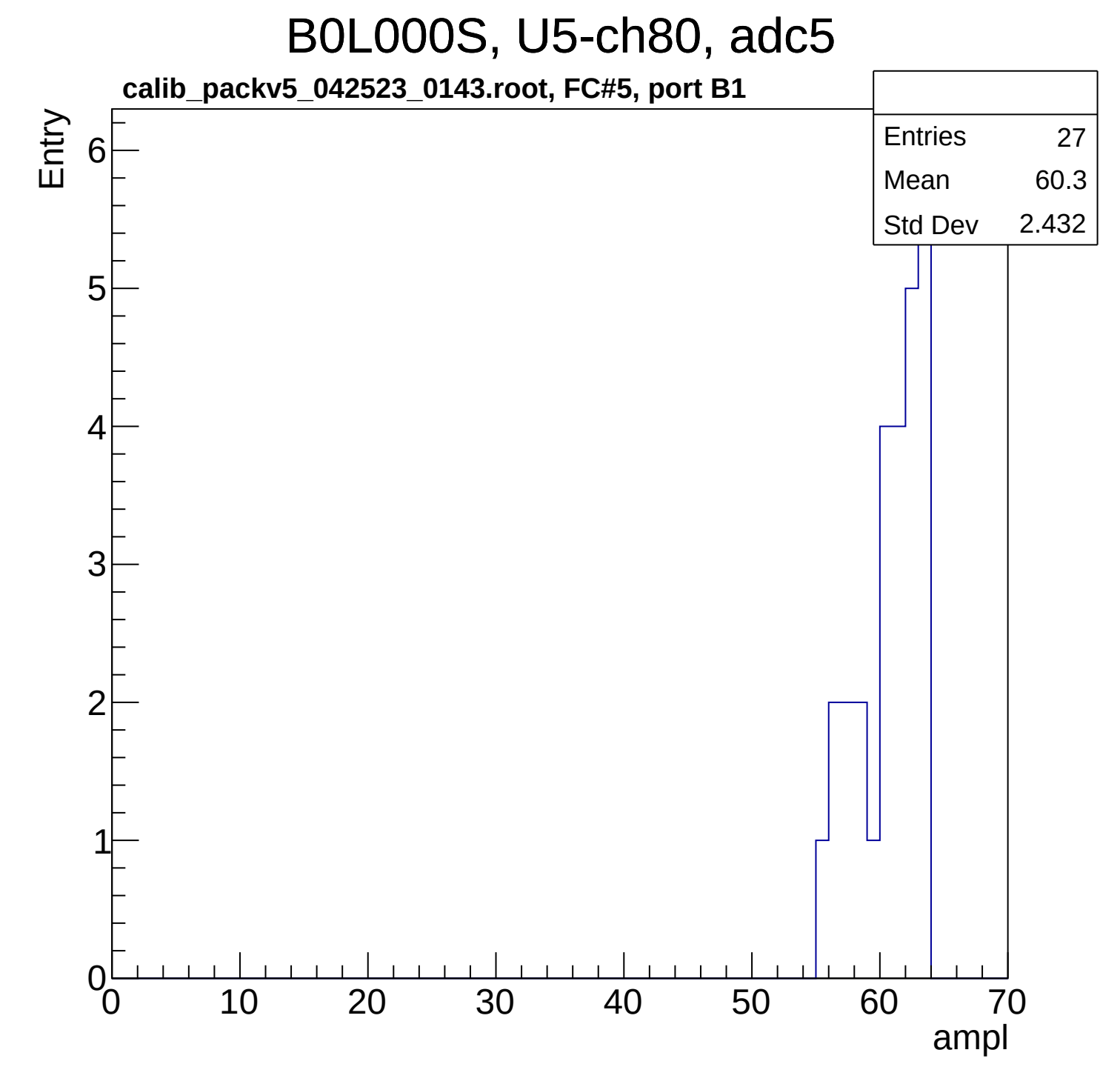

B0L000S, U5-ch80, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 84
Mean 30.51
Std Dev 6.883
Gaus mean : 32.3088
Gaus Width: 3.7632
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

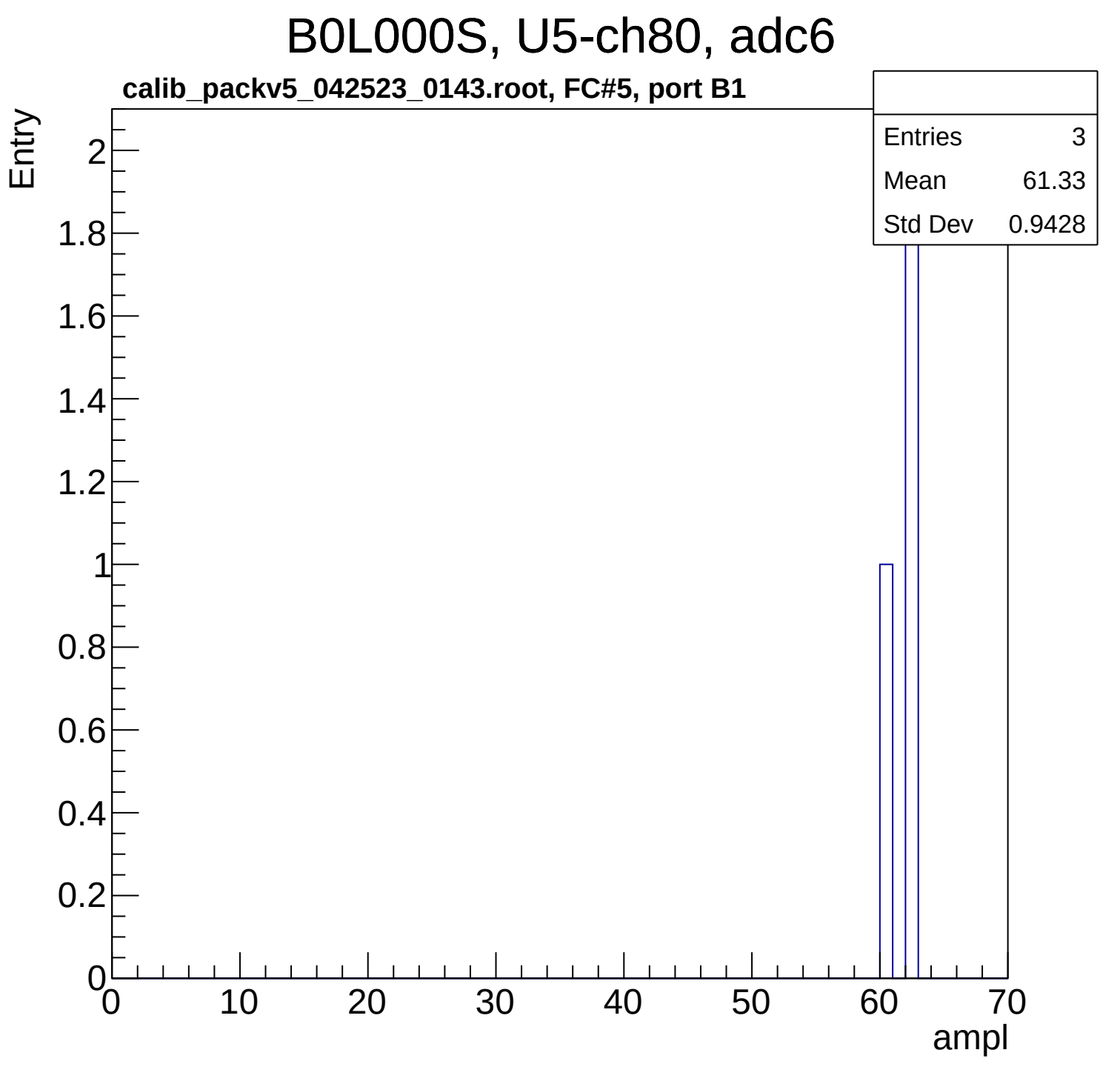

B0L000S, U5-ch80, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 37.86
Std Dev 2.898
Gaus mean : 39.2506
Gaus Width: 2.3084
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

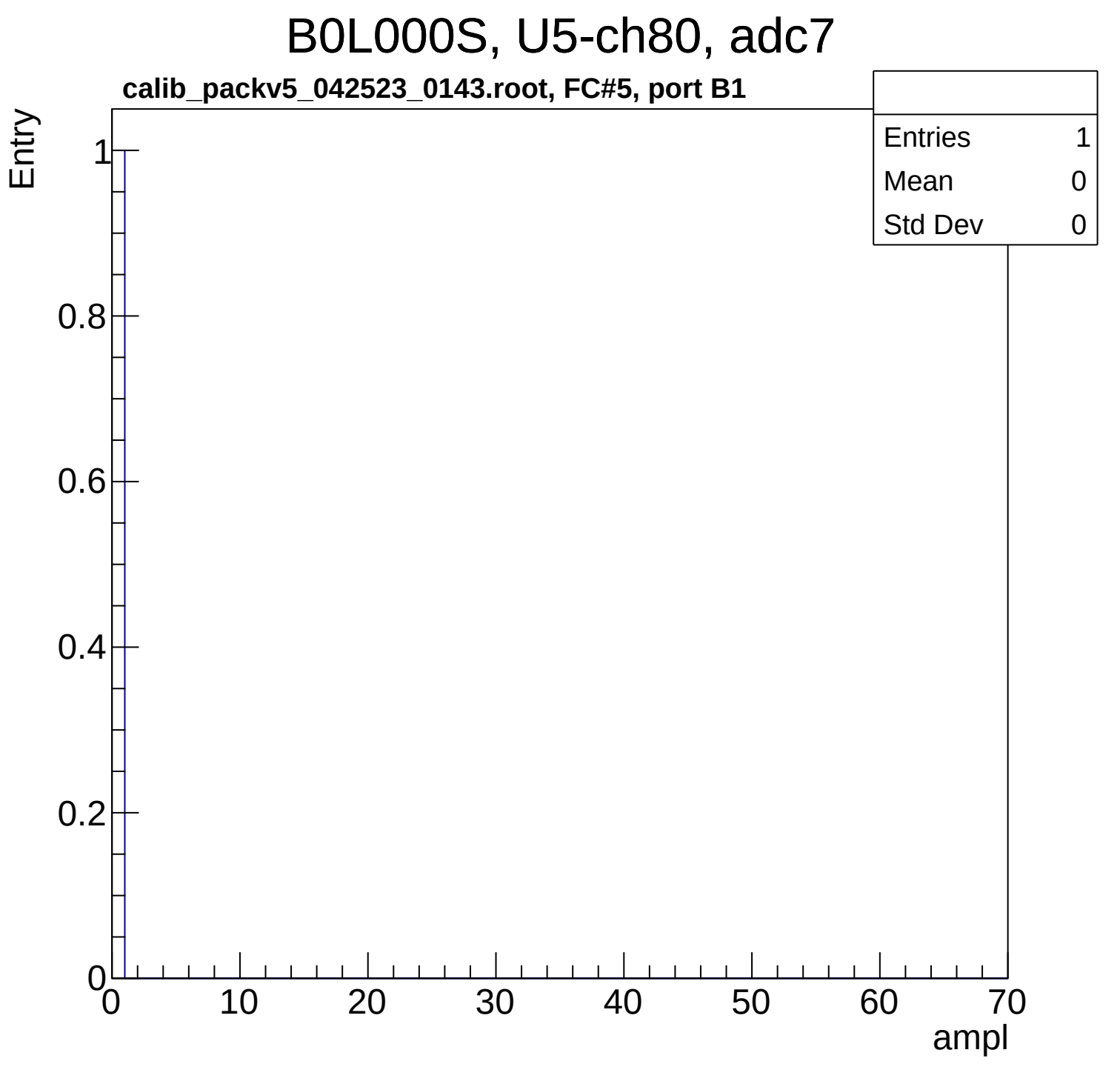

B0L000S, U5-ch80, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 44.79
Std Dev 3.642
Gaus mean : 44.9192
Gaus Width: 3.8998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch80, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 50.47
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch80, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 56.44
Std Dev 7.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch80, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 60.3
Std Dev 2.432
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch80, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch80, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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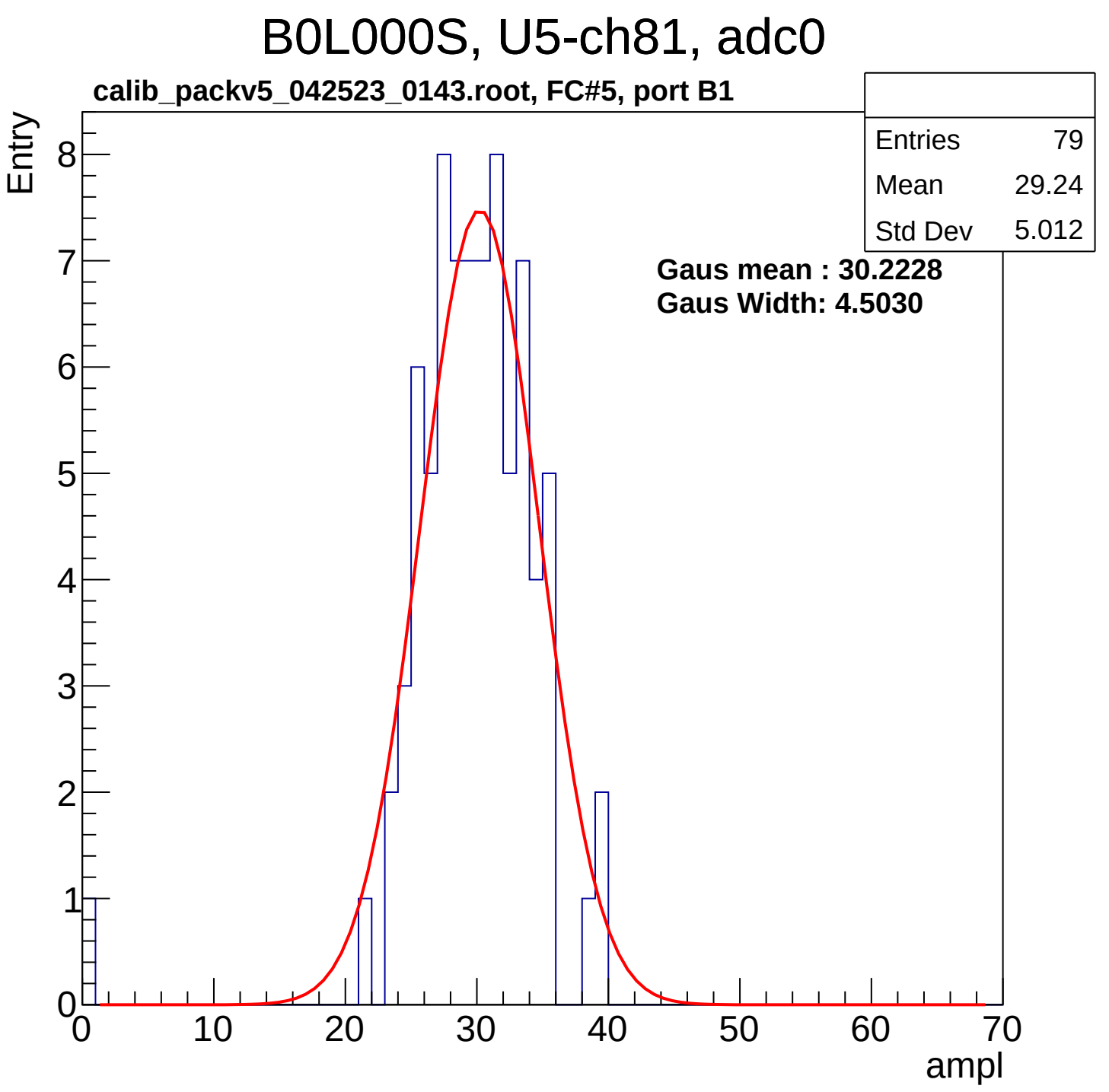
50
60
70
ampl


B0L000S, U5-ch81, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 29.24
Std Dev 5.012
Gaus mean : 30.2228
Gaus Width: 4.5030
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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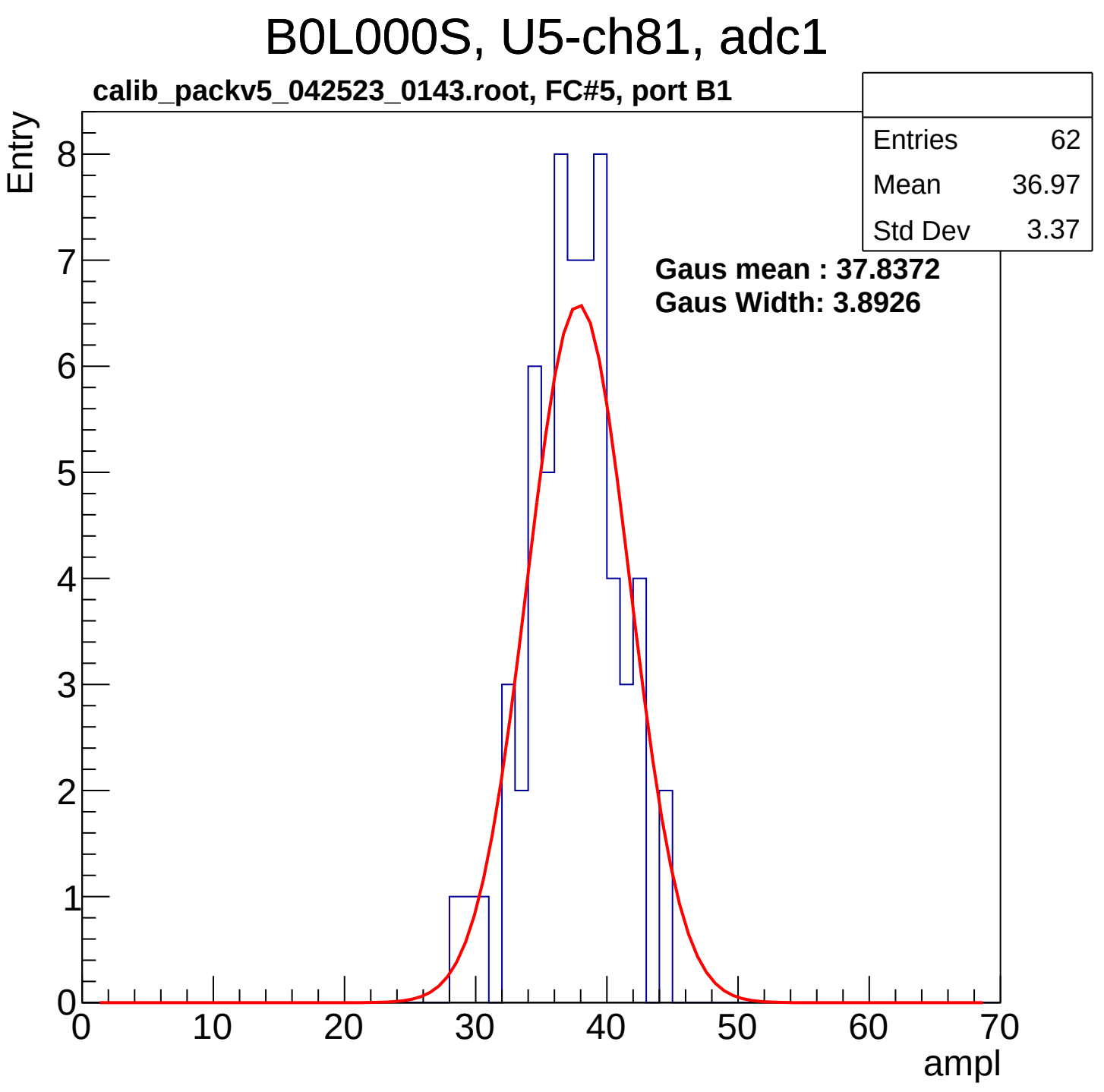
B0L000S, U5-ch81, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.97
Std Dev 3.37
Gaus mean : 37.8372
Gaus Width: 3.8926
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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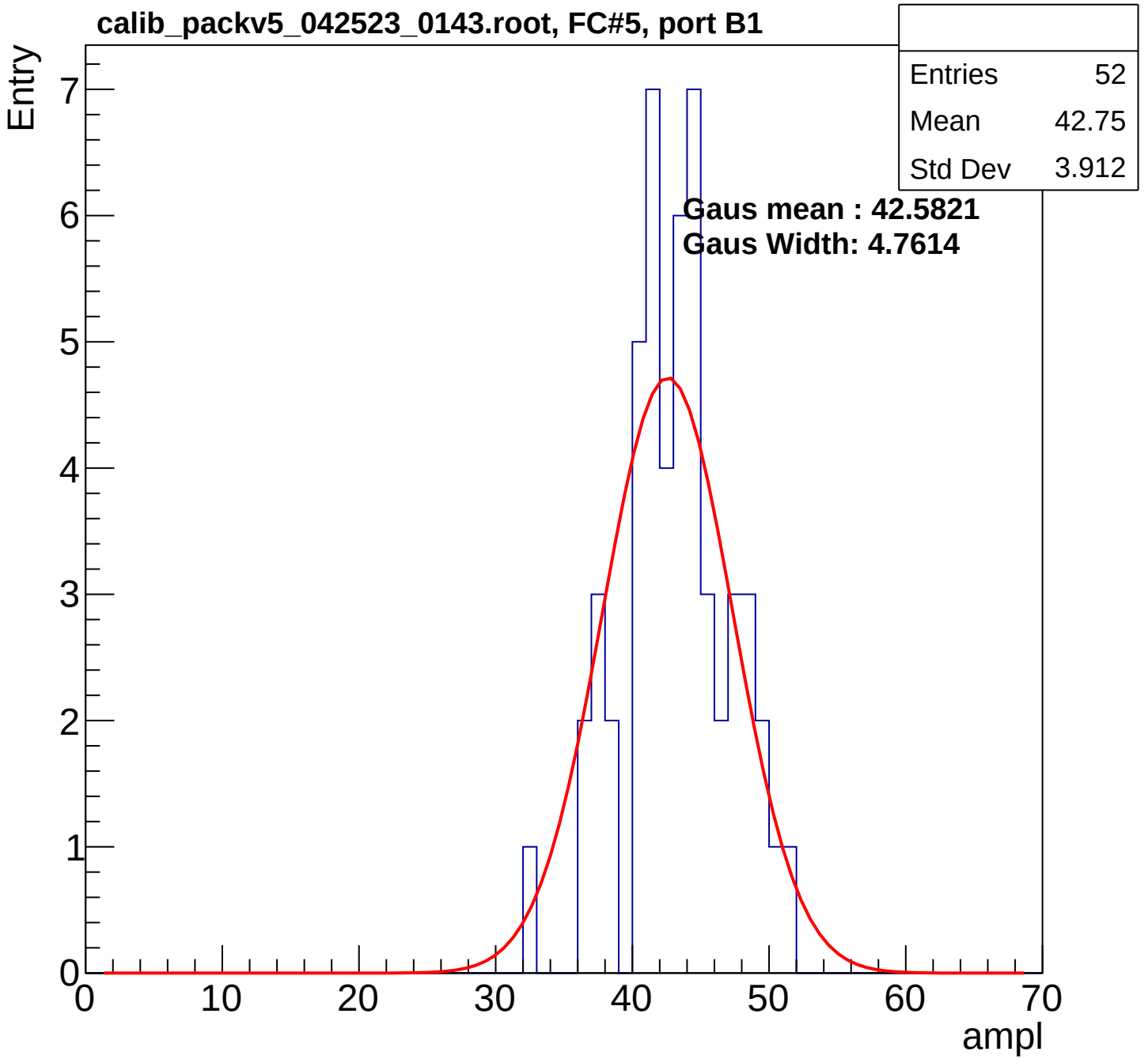
B0L000S, U5-ch81, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 42.75
Std Dev 3.912
Gaus mean : 42.5821
Gaus Width: 4.7614
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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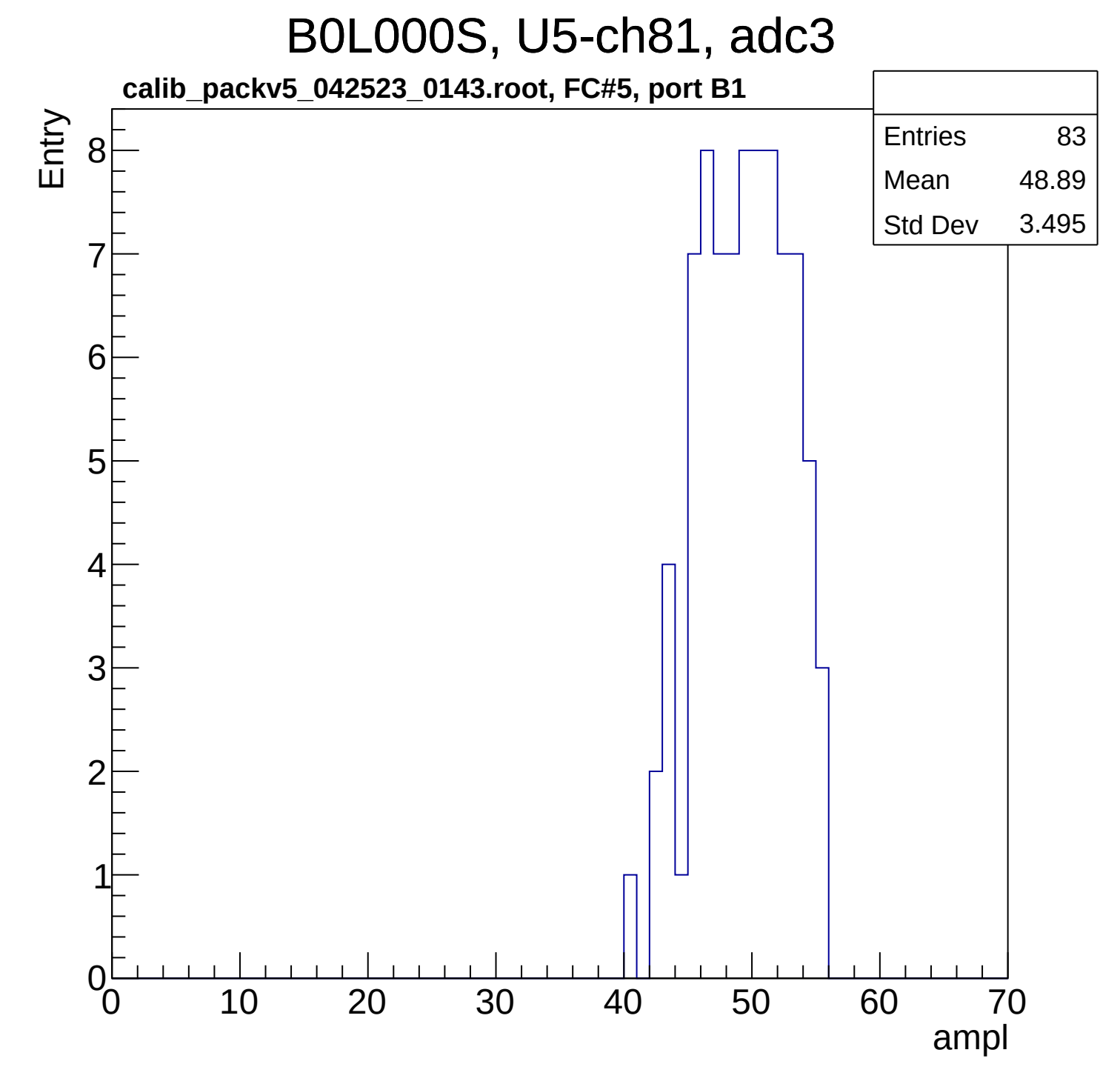
B0L000S, U5-ch81, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 48.89
Std Dev 3.495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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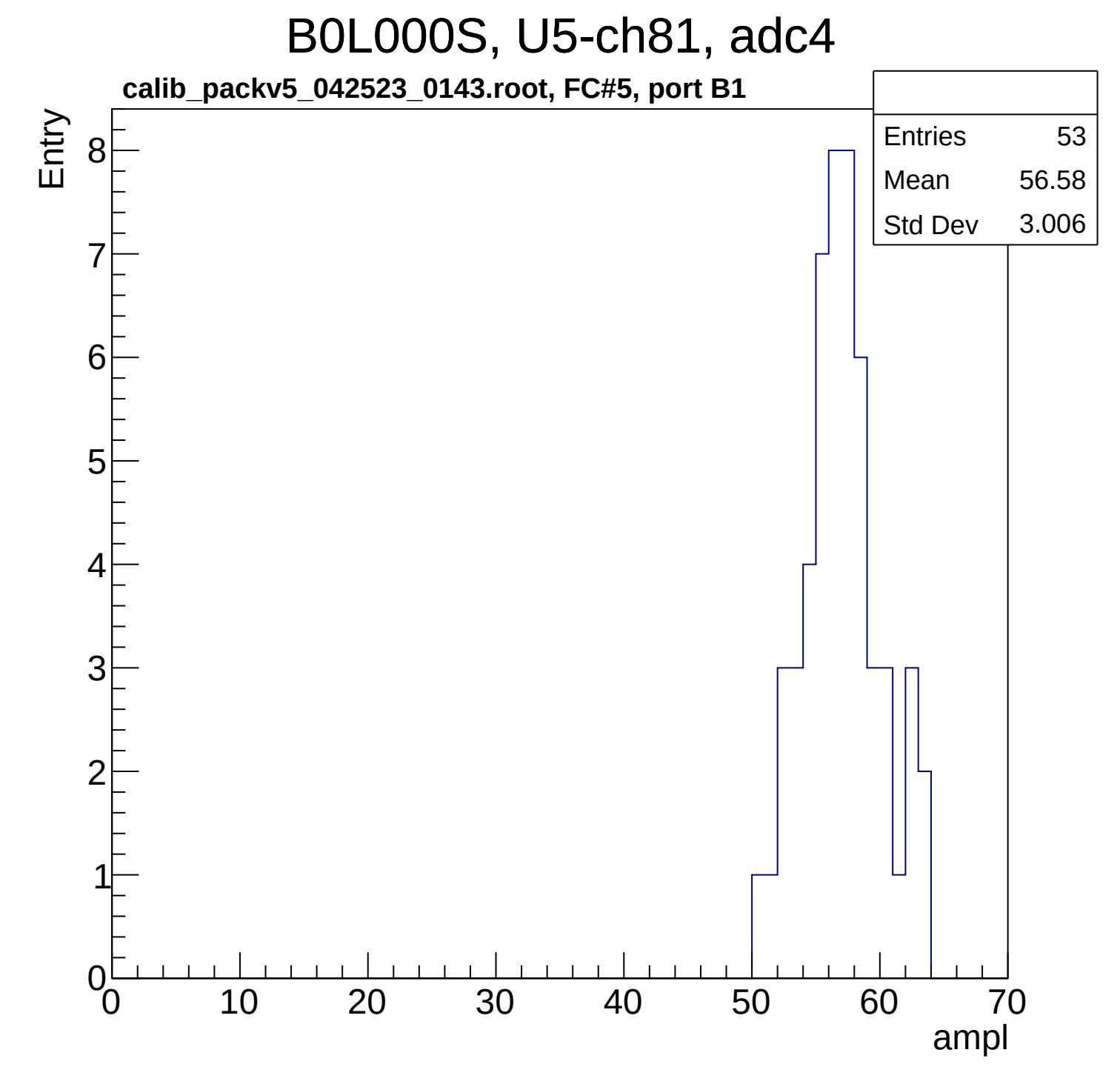
B0L000S, U5-ch81, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 56.58
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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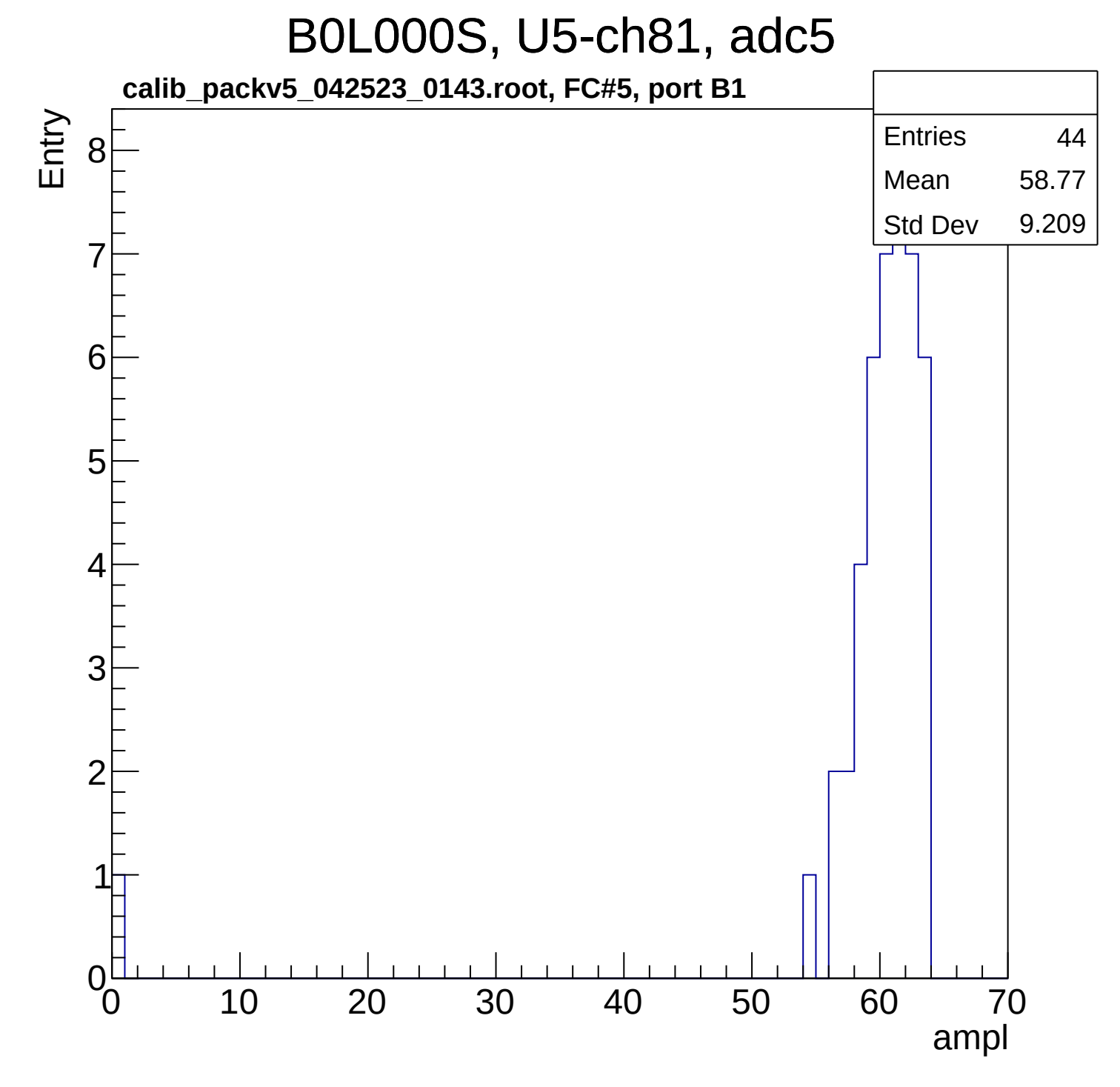
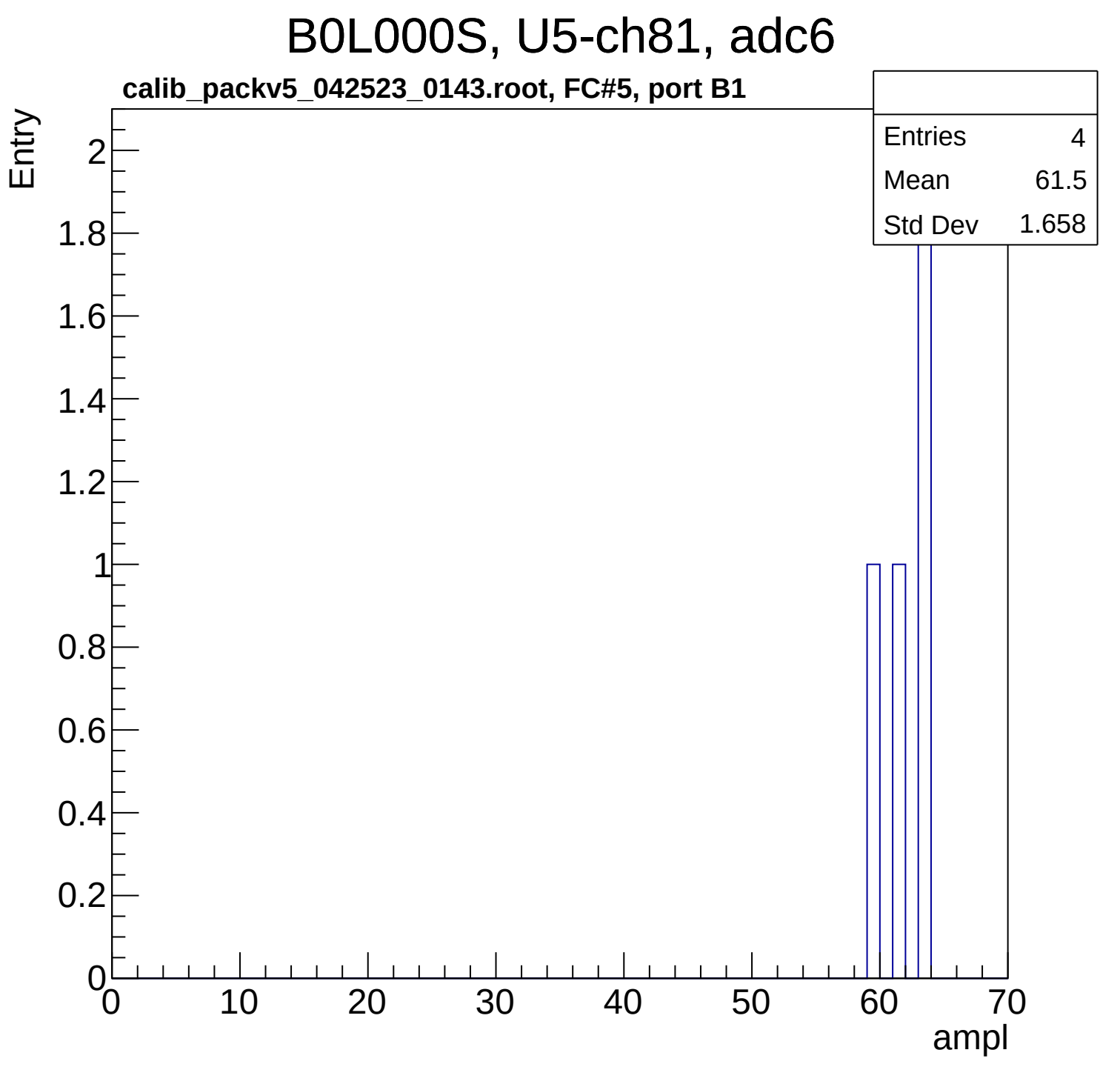
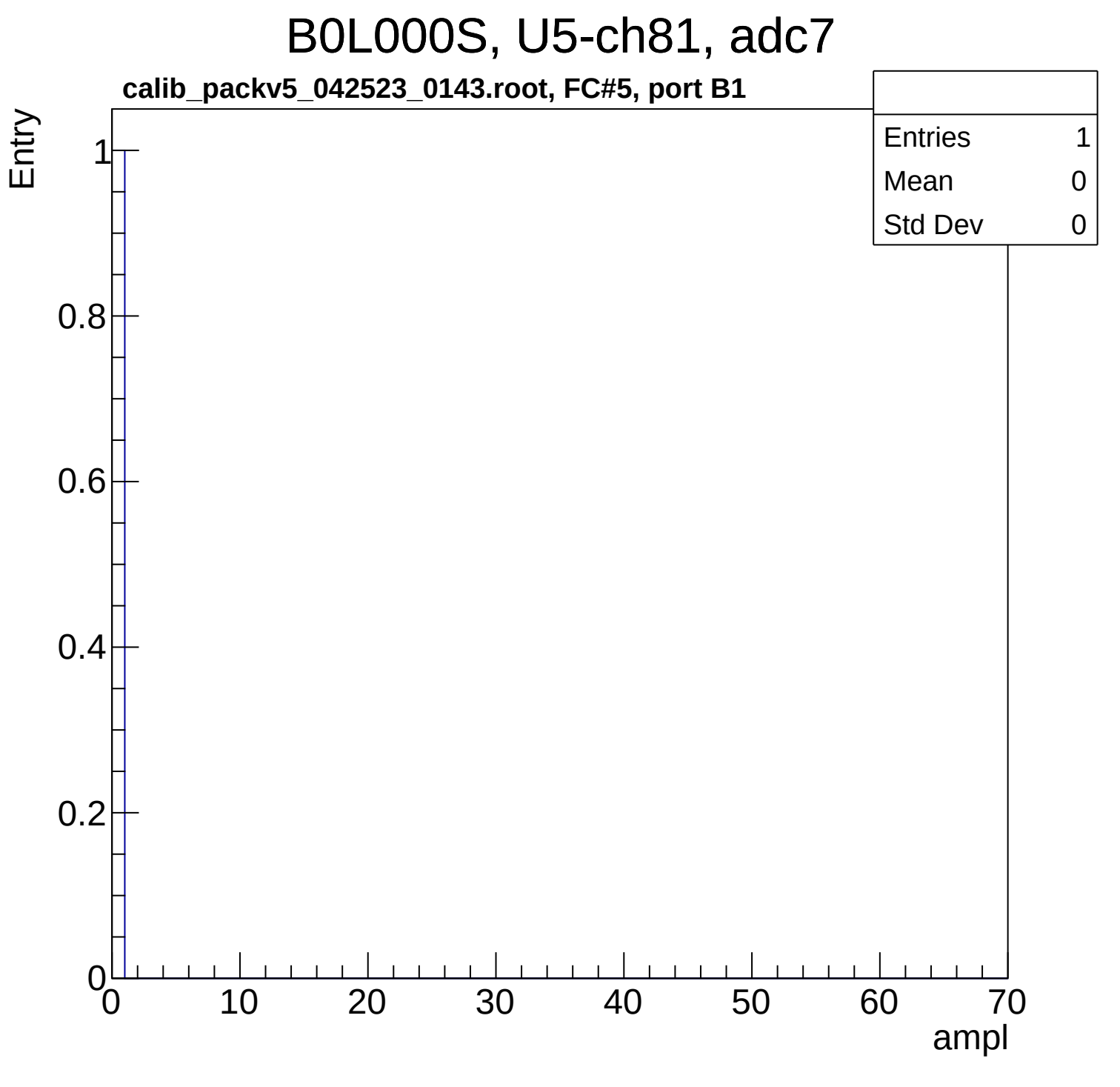
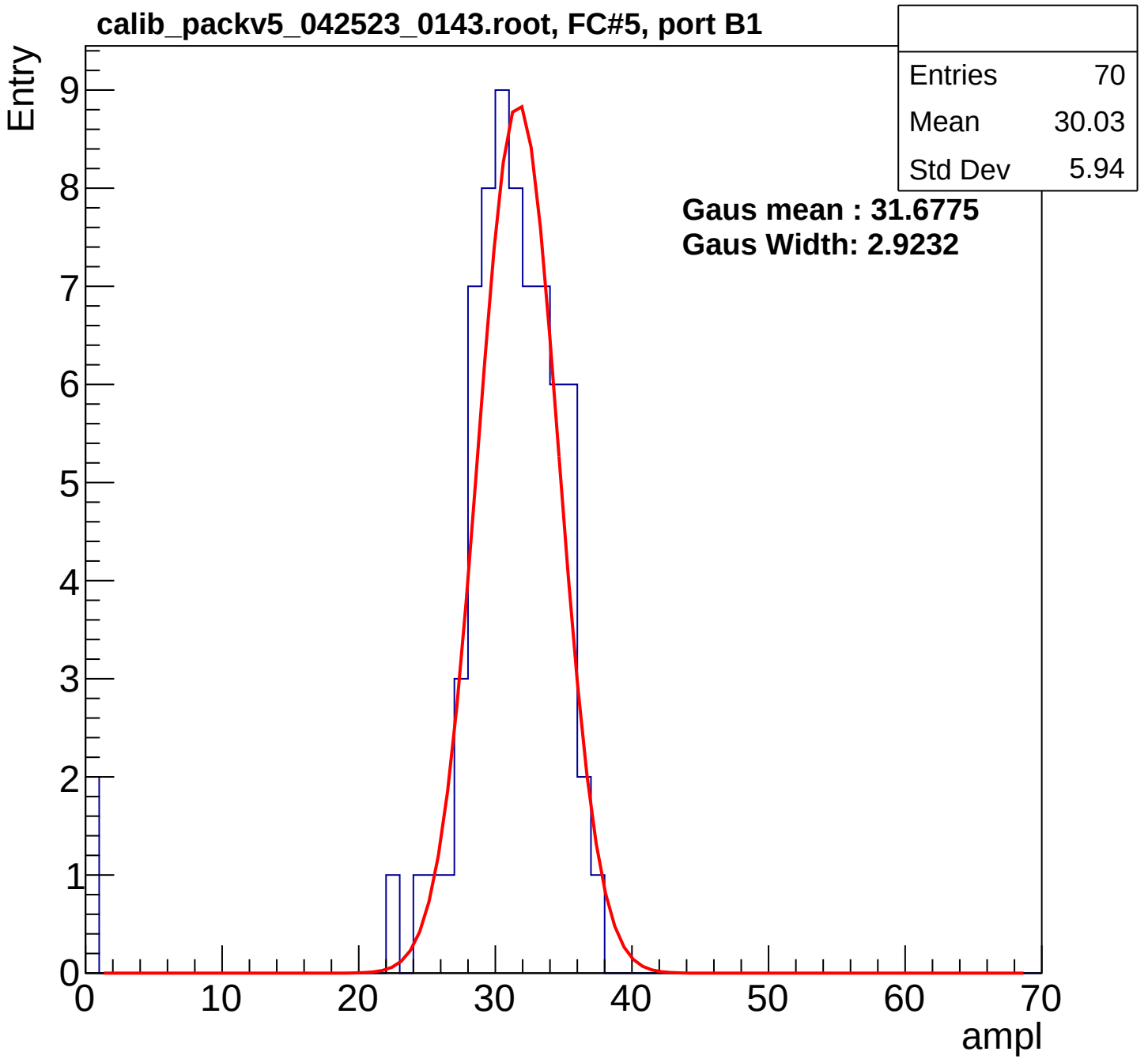
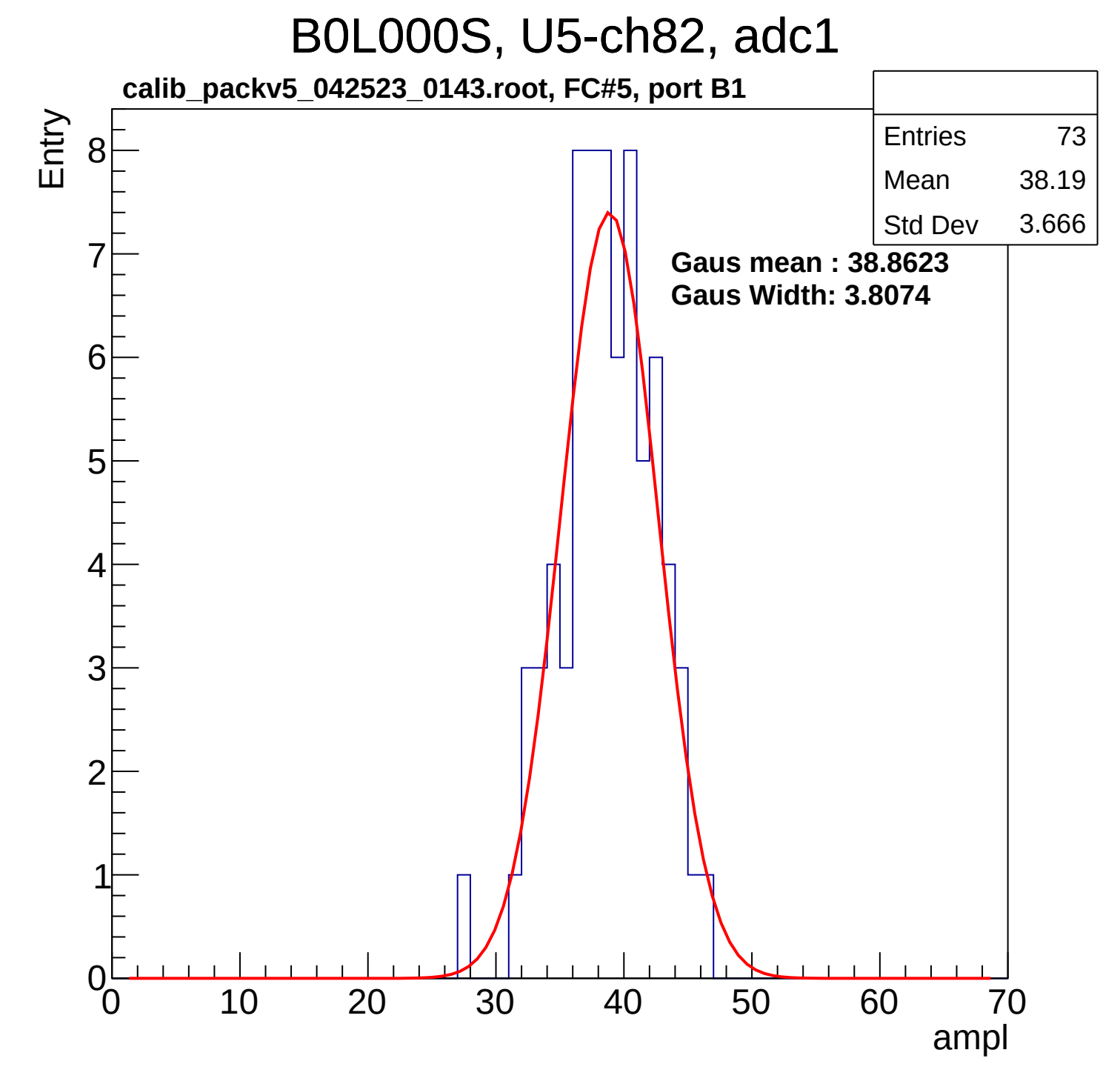

B0L000S, U5-ch81, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.77
Std Dev 9.209
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch81, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch81, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch82, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 30.03
Std Dev 5.94
Gaus mean : 31.6775
Gaus Width: 2.9232
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch82, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 38.19
Std Dev 3.666
Gaus mean : 38.8623
Gaus Width: 3.8074
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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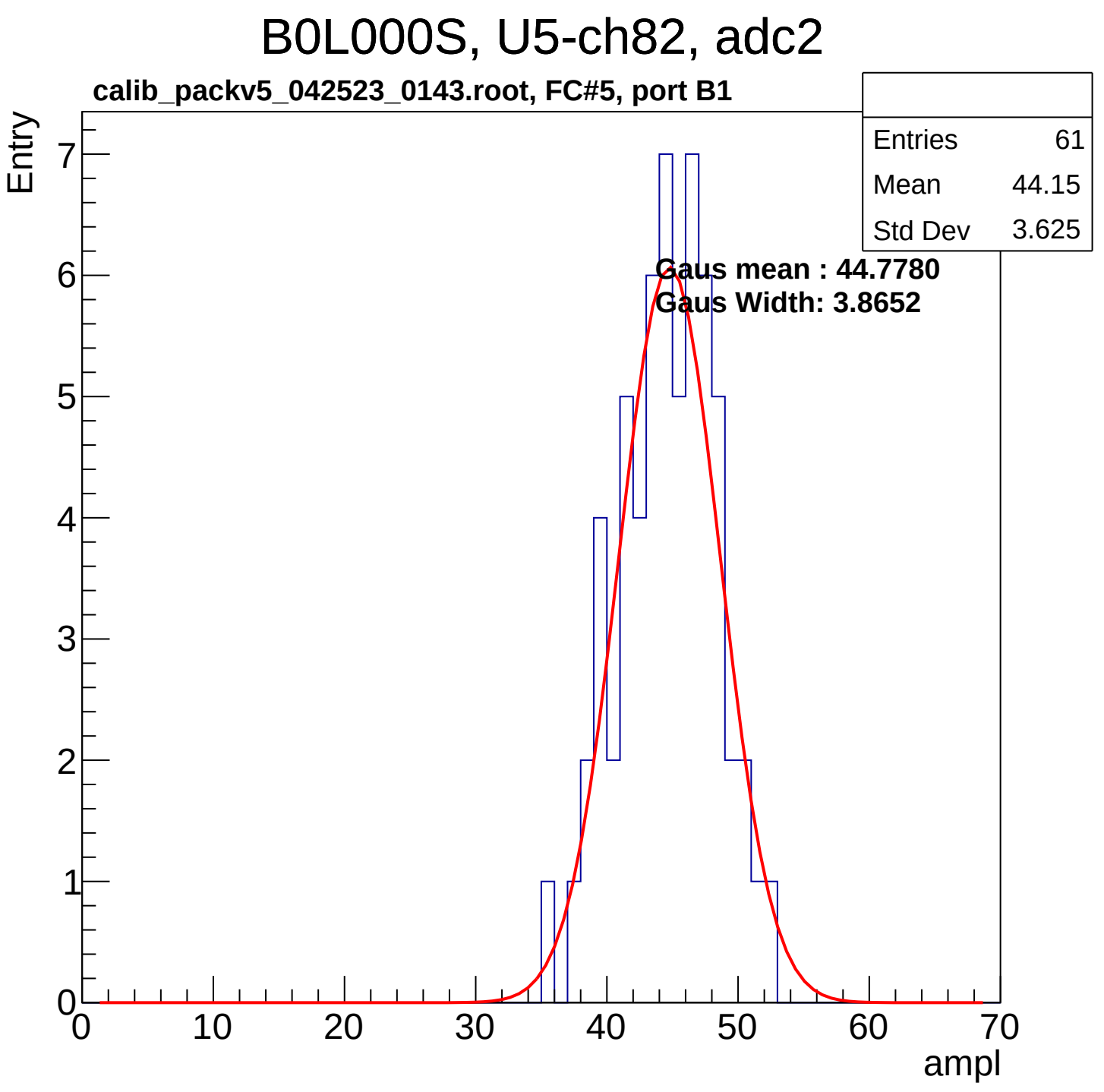
B0L000S, U5-ch82, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 44.15
Std Dev 3.625
Gaus mean : 44.7780
Gaus Width: 3.8652
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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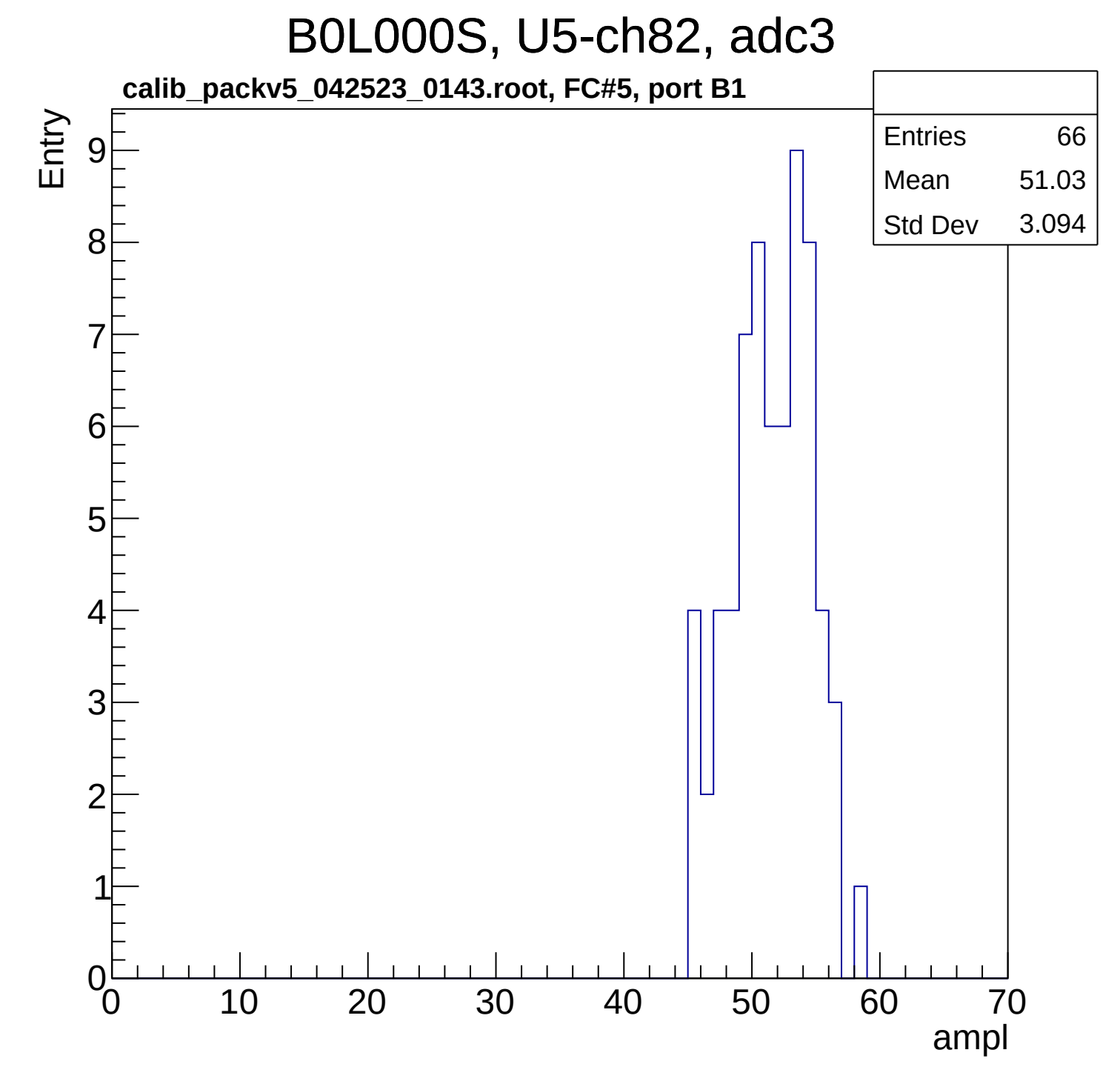
B0L000S, U5-ch82, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 51.03
Std Dev 3.094
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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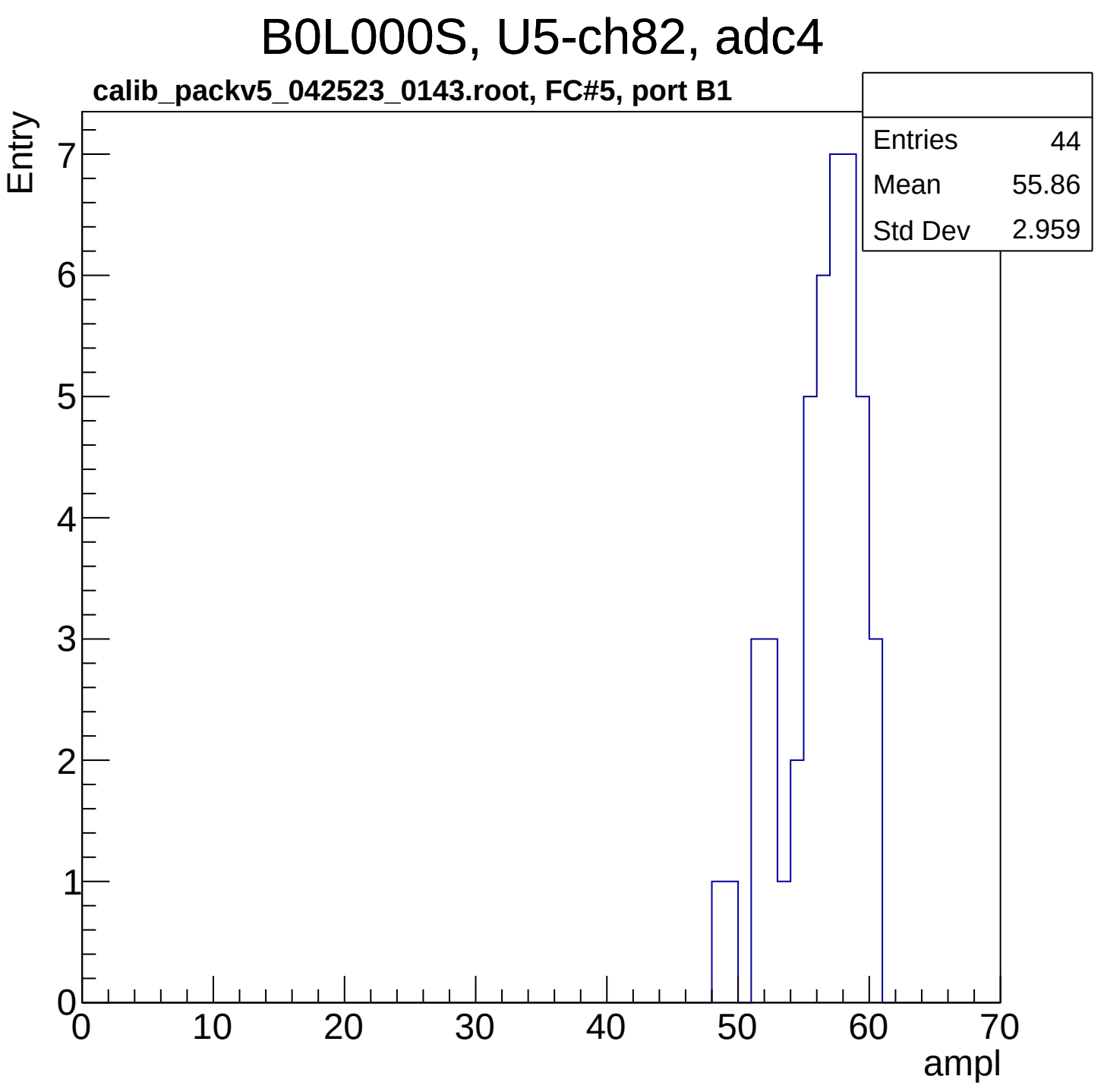
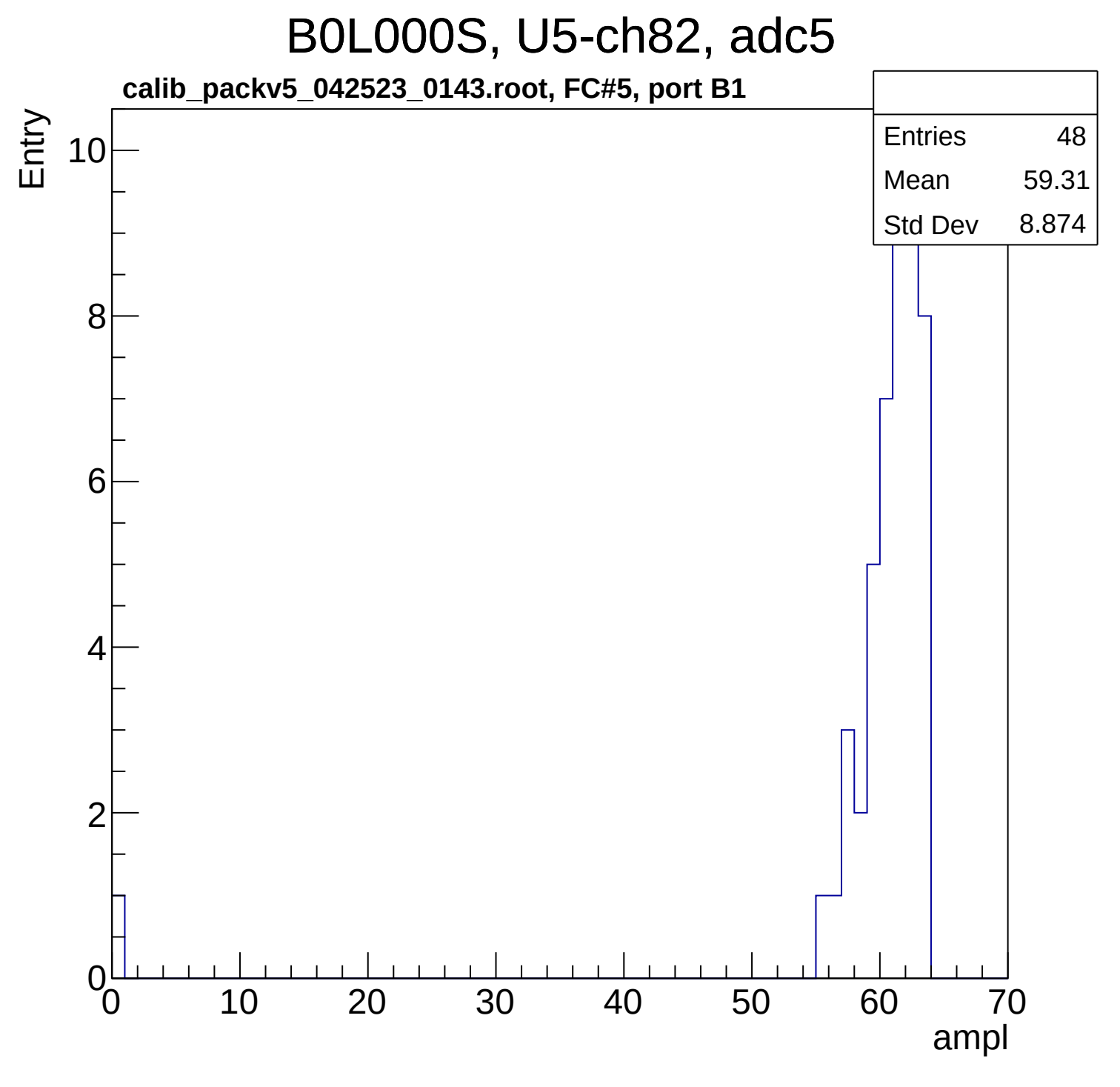
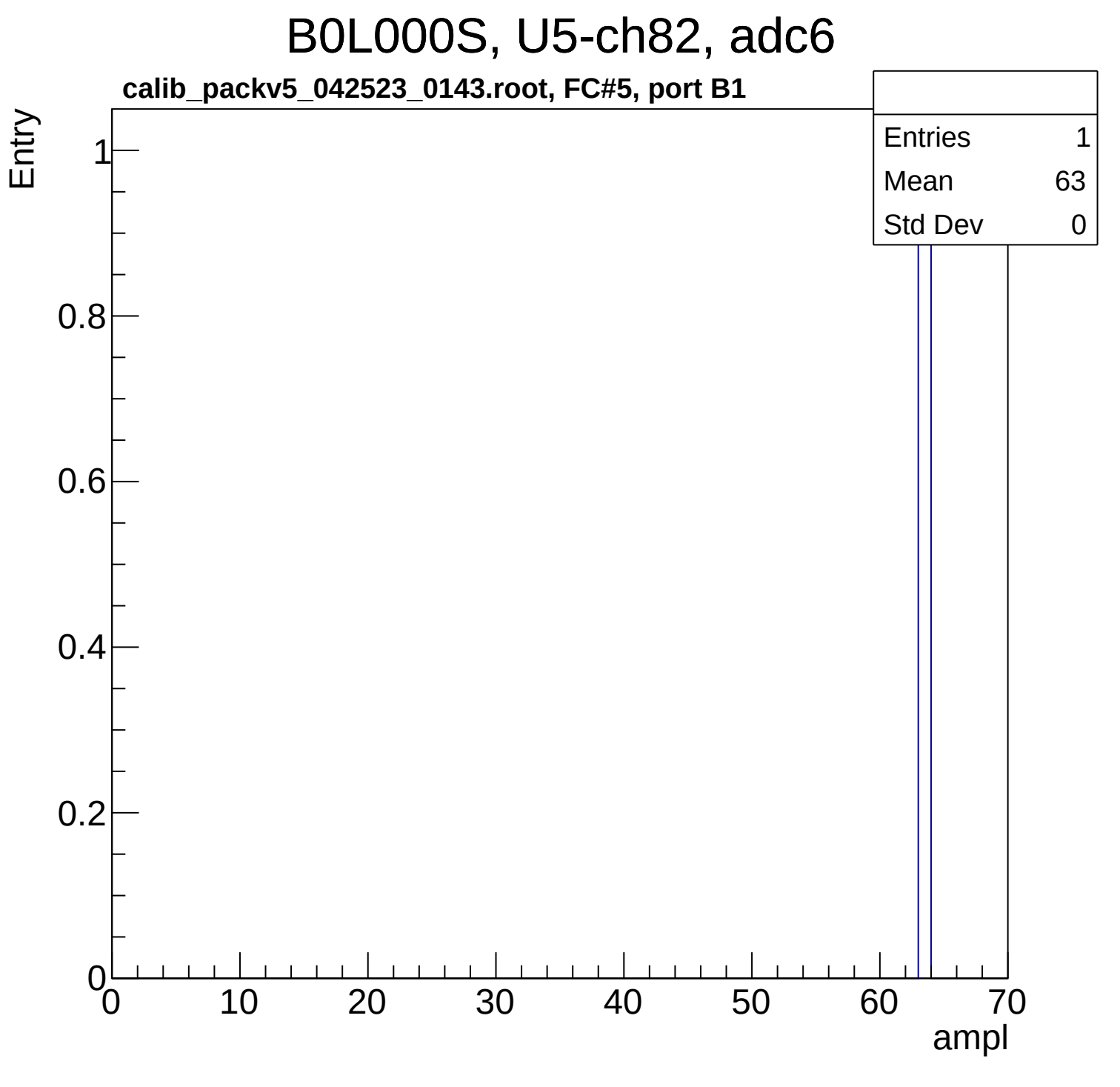
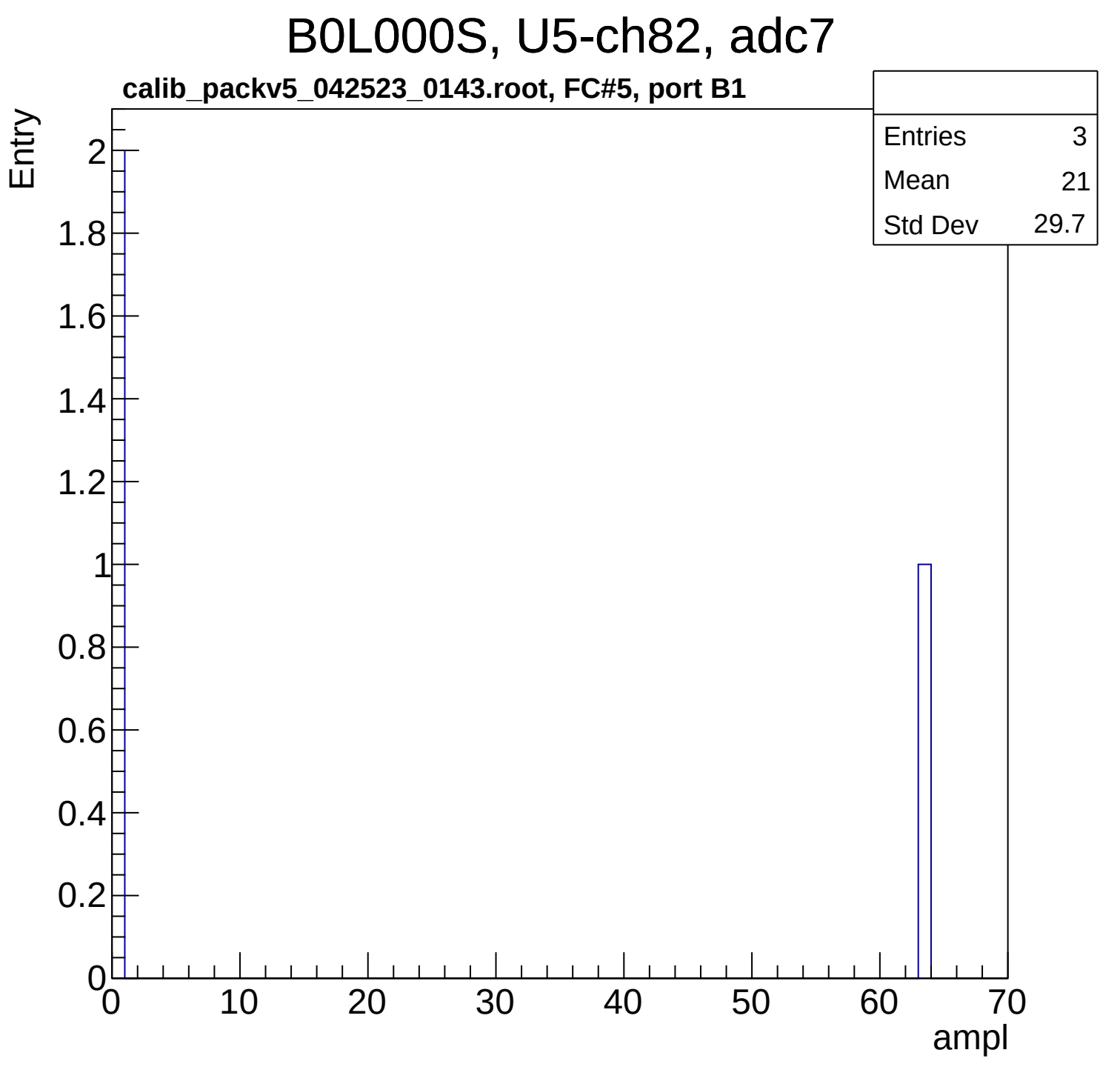
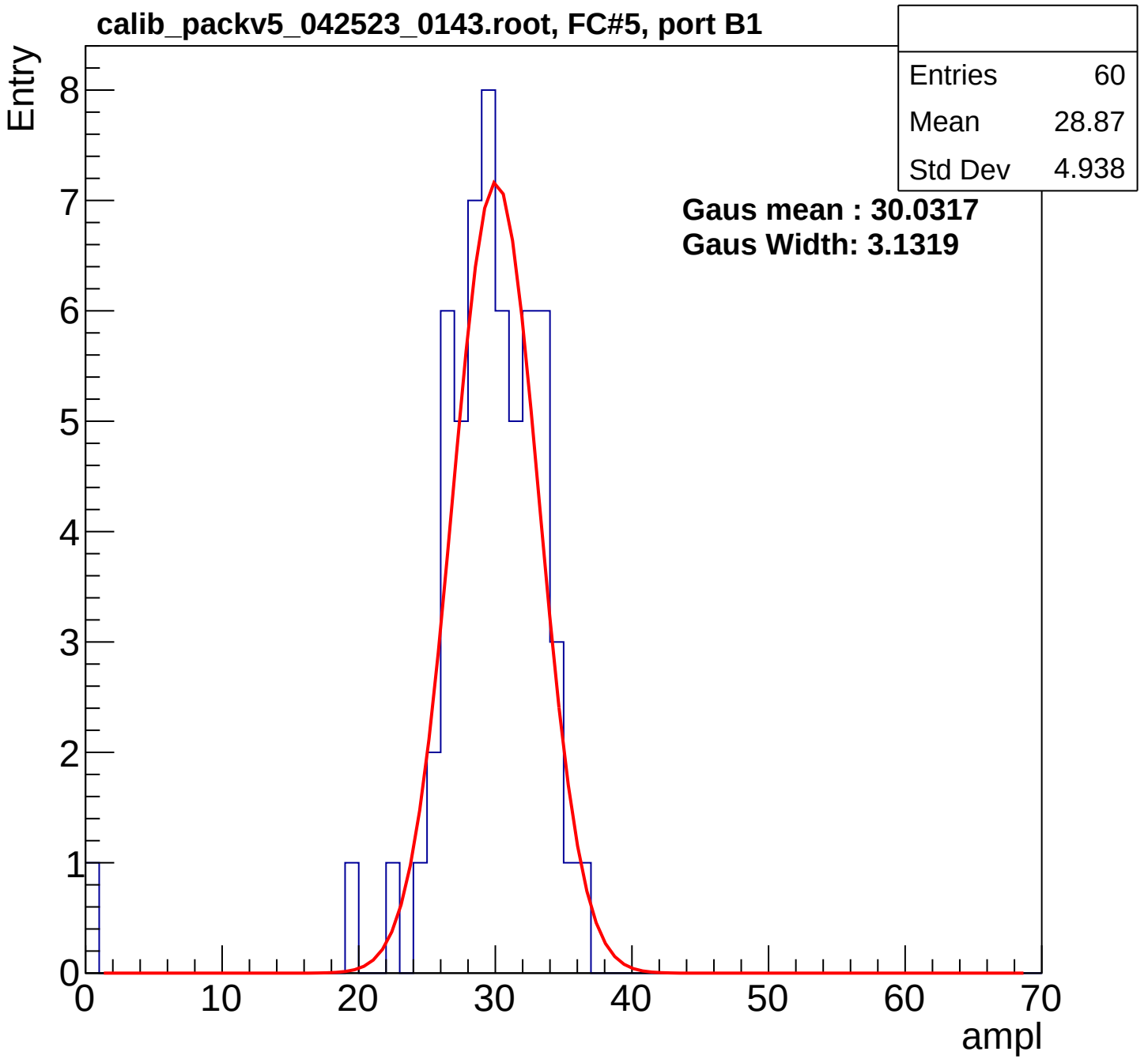

B0L000S, U5-ch82, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 55.86
Std Dev 2.959
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch82, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.31
Std Dev 8.874
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch82, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch82, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch83, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.87
Std Dev 4.938
Gaus mean : 30.0317
Gaus Width: 3.1319
Entry
ampl
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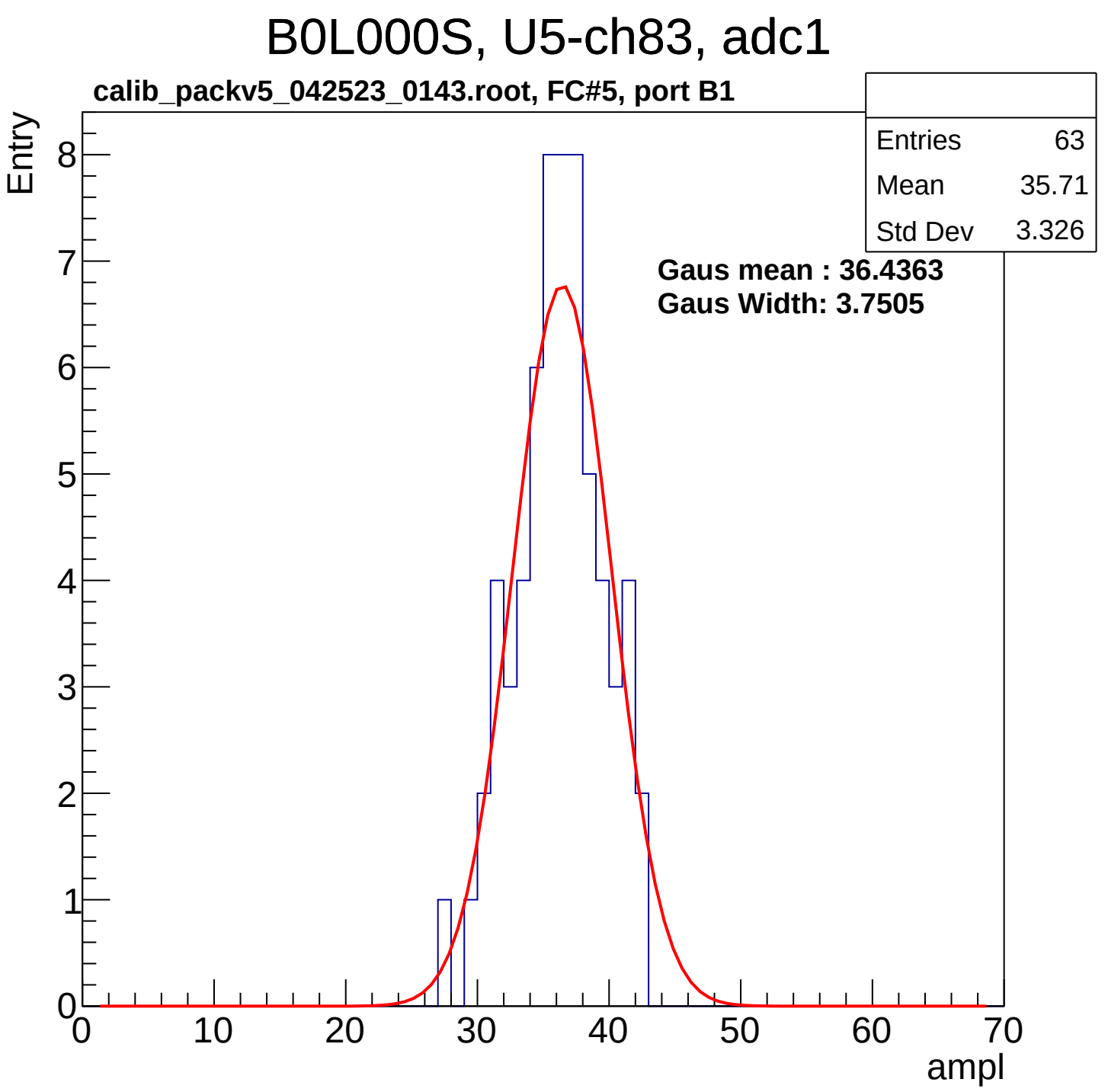
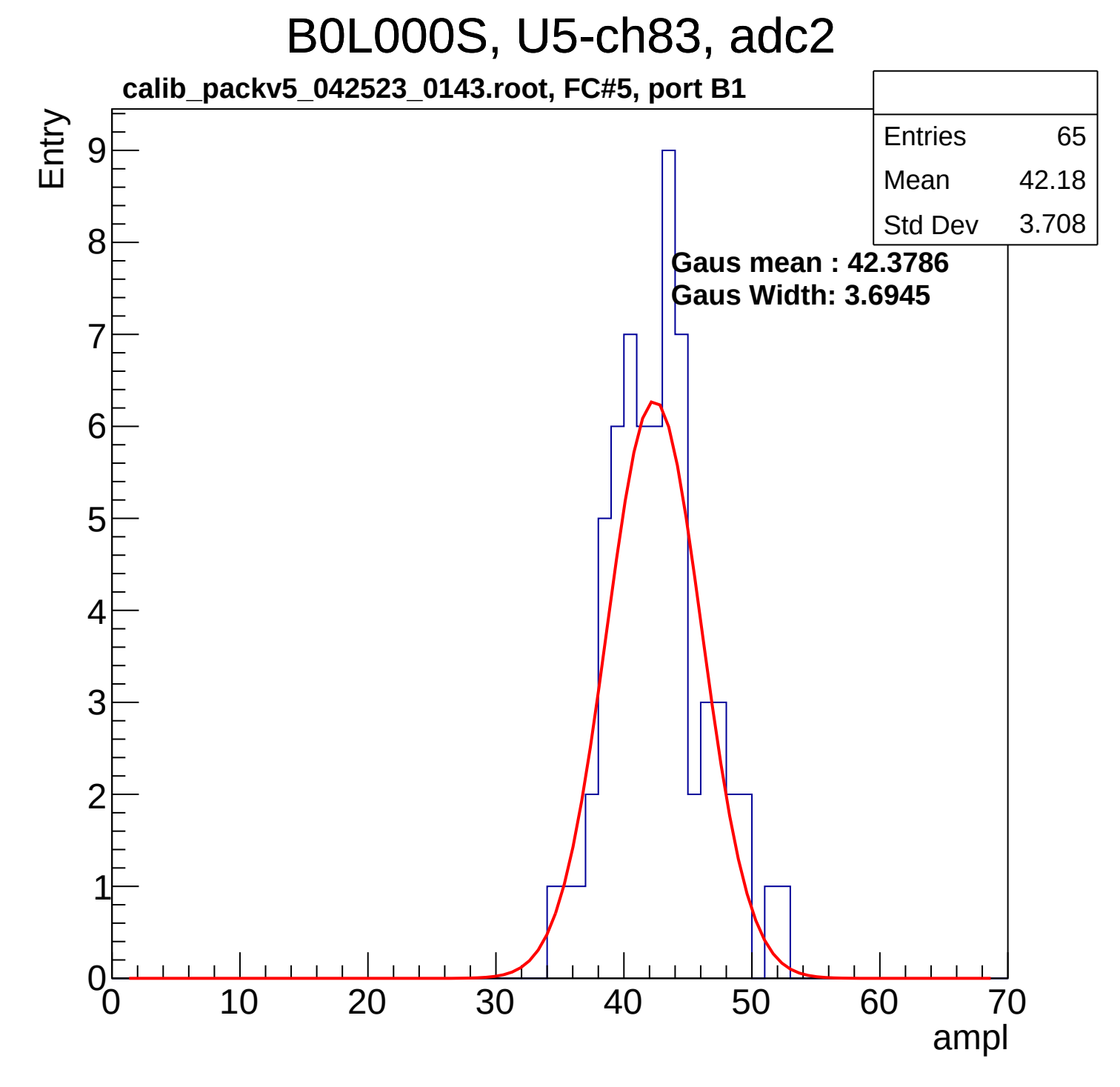
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch83, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.71
Std Dev 3.326
Gaus mean : 36.4363
Gaus Width: 3.7505
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch83, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 42.18
Std Dev 3.708
Gaus mean : 42.3786
Gaus Width: 3.6945
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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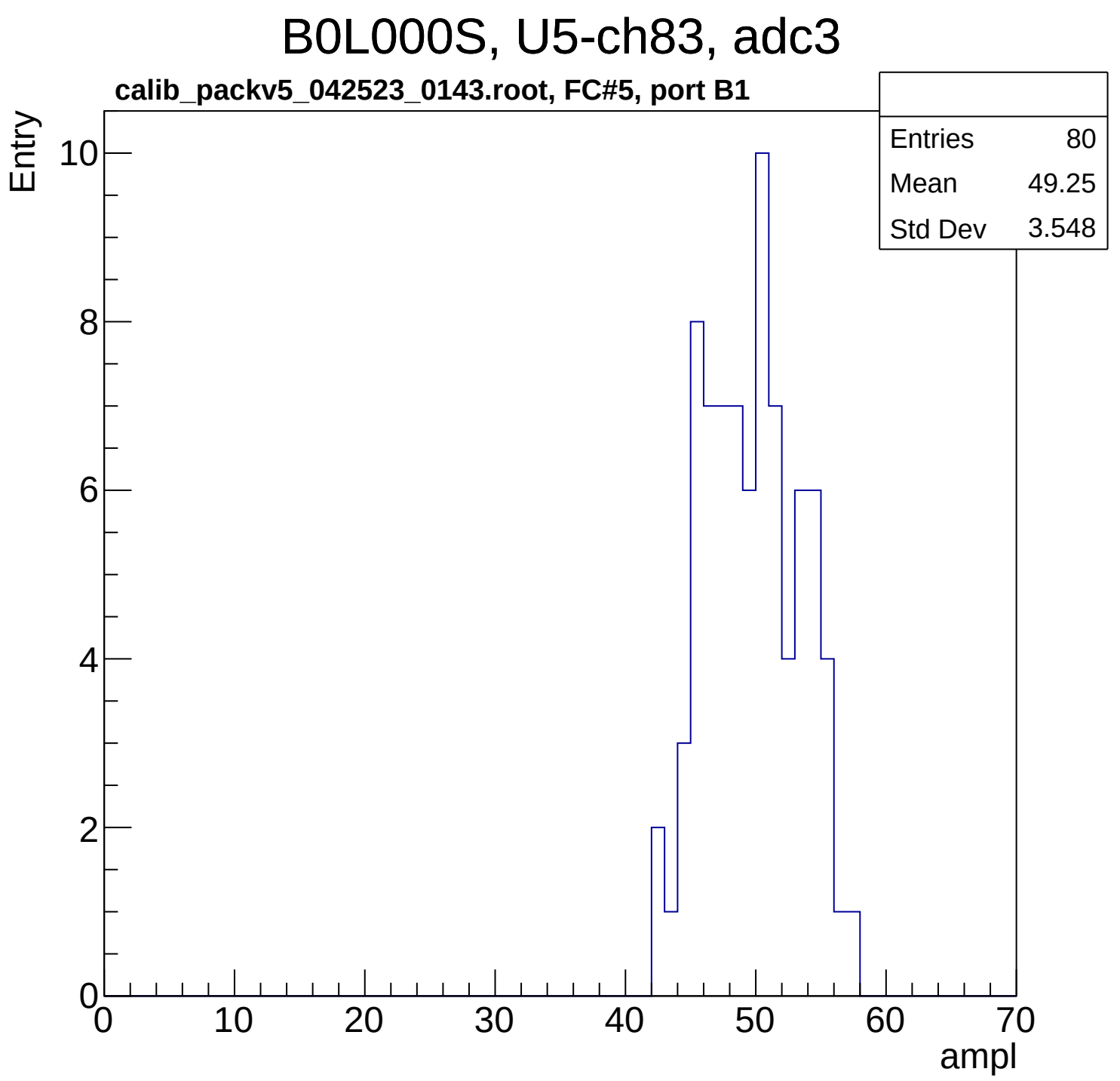
B0L000S, U5-ch83, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 49.25
Std Dev 3.548
Entry
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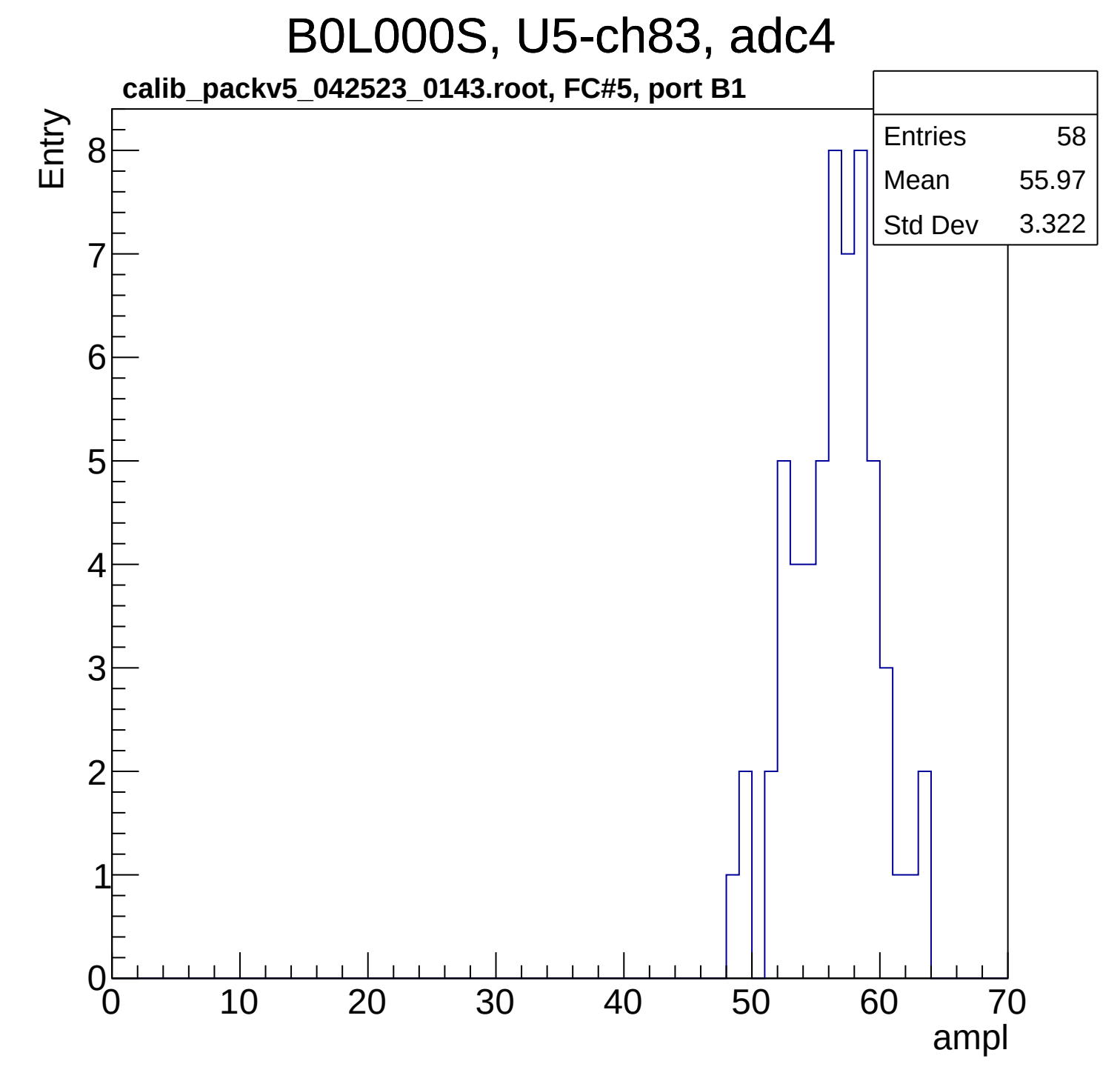
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch83, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 55.97
Std Dev 3.322
Entry
ampl
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2
3
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5
6
7
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0
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60
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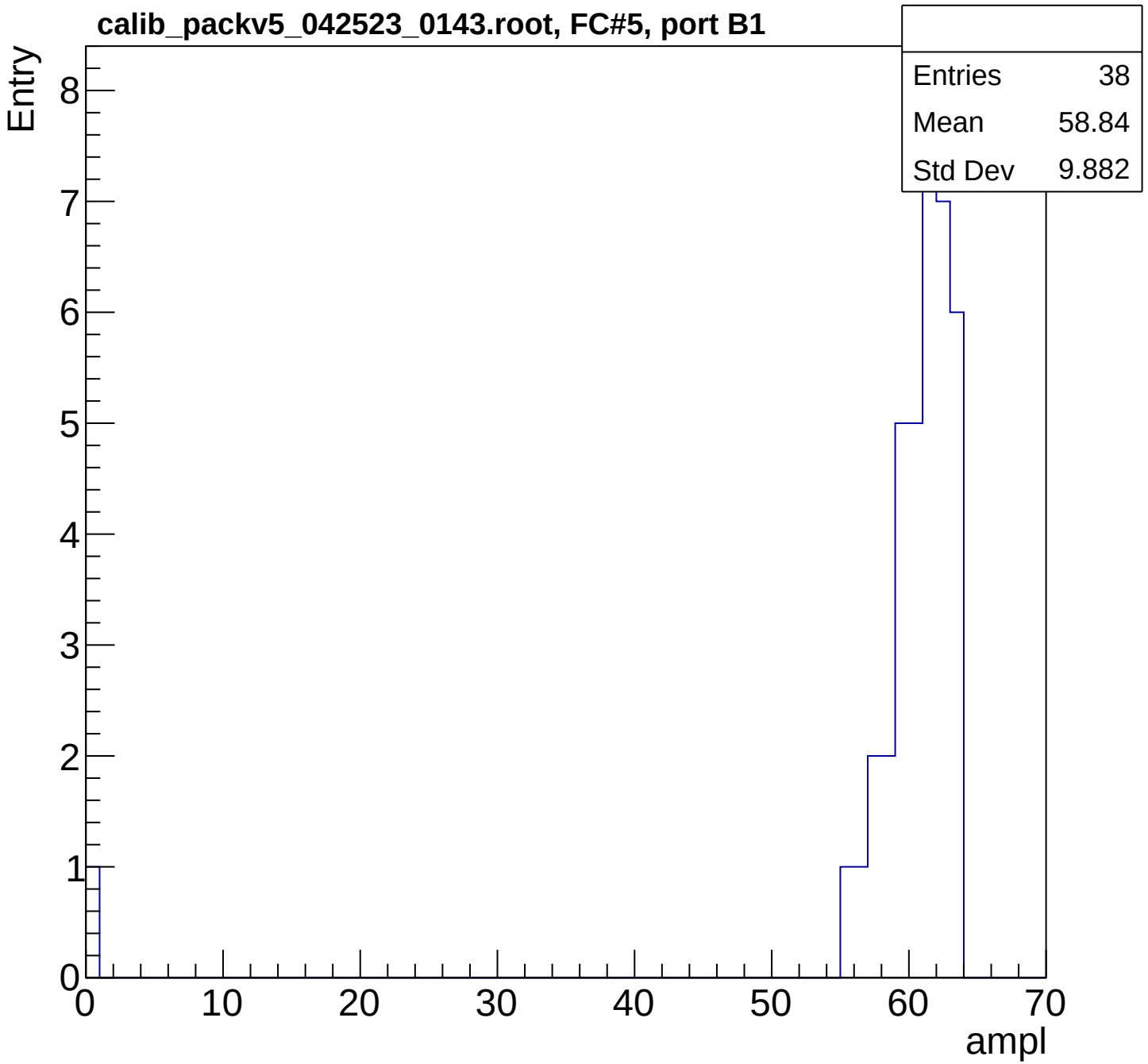
B0L000S, U5-ch83, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.84
Std Dev 9.882
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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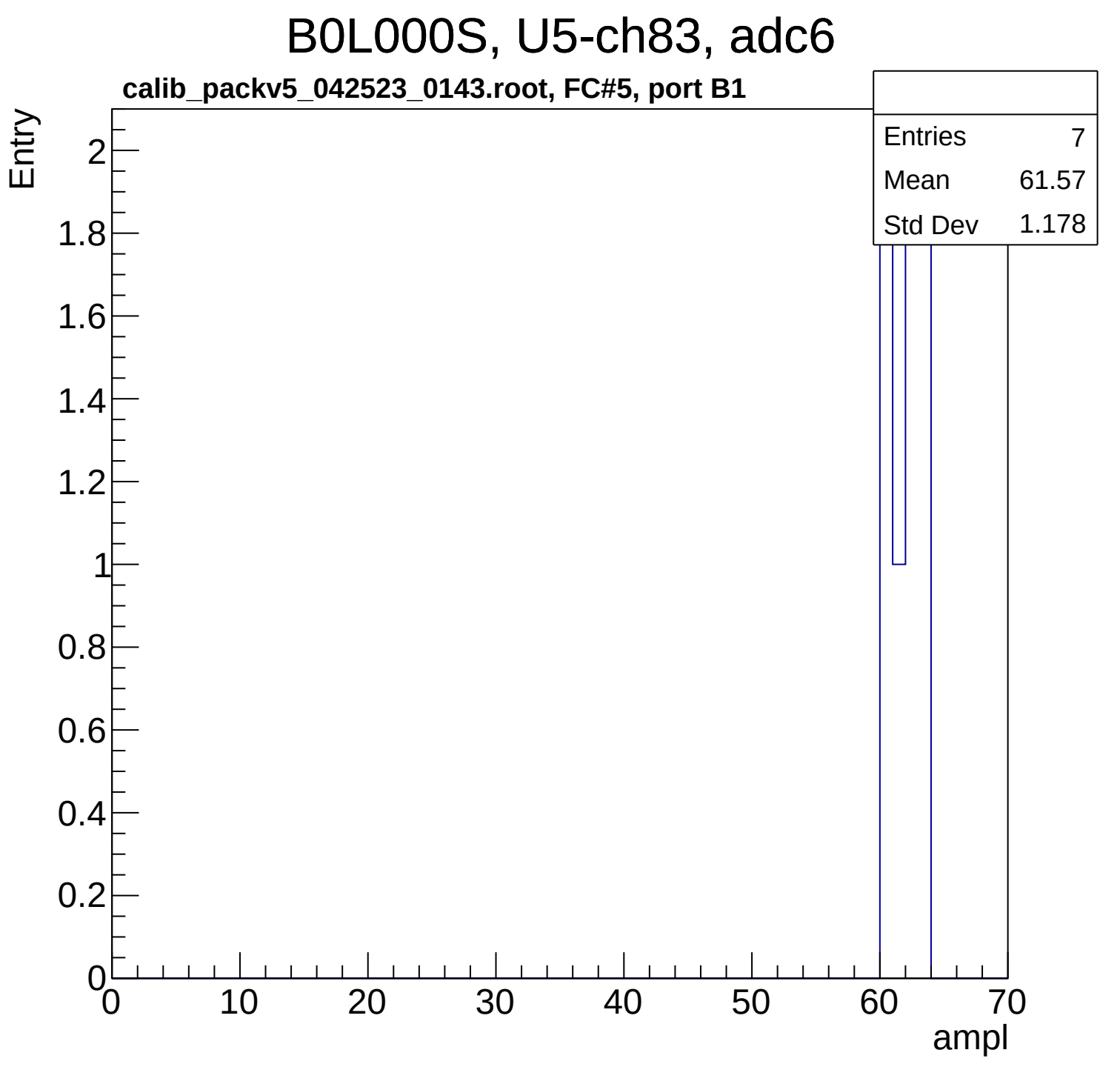
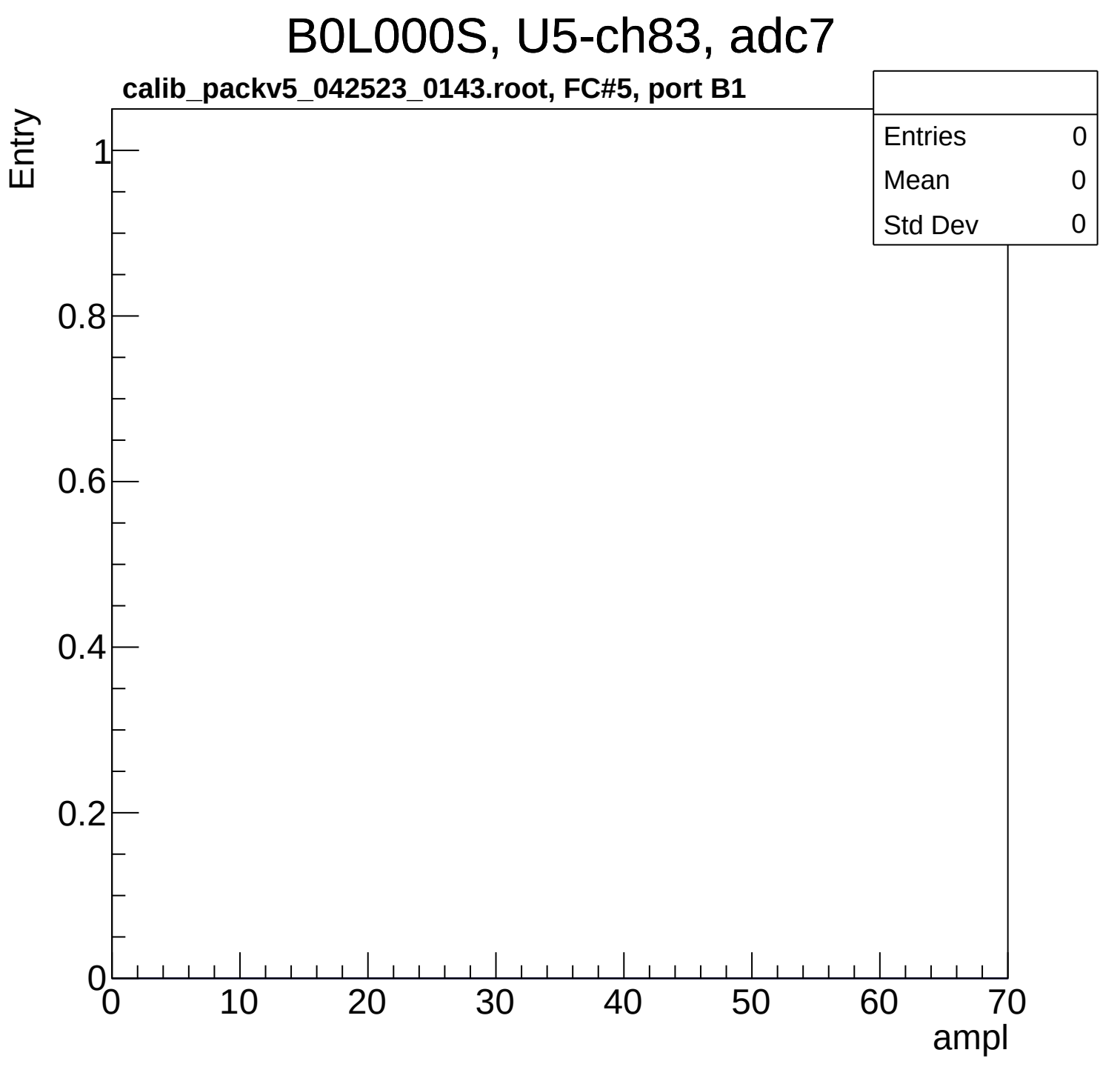

B0L000S, U5-ch83, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.57
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch83, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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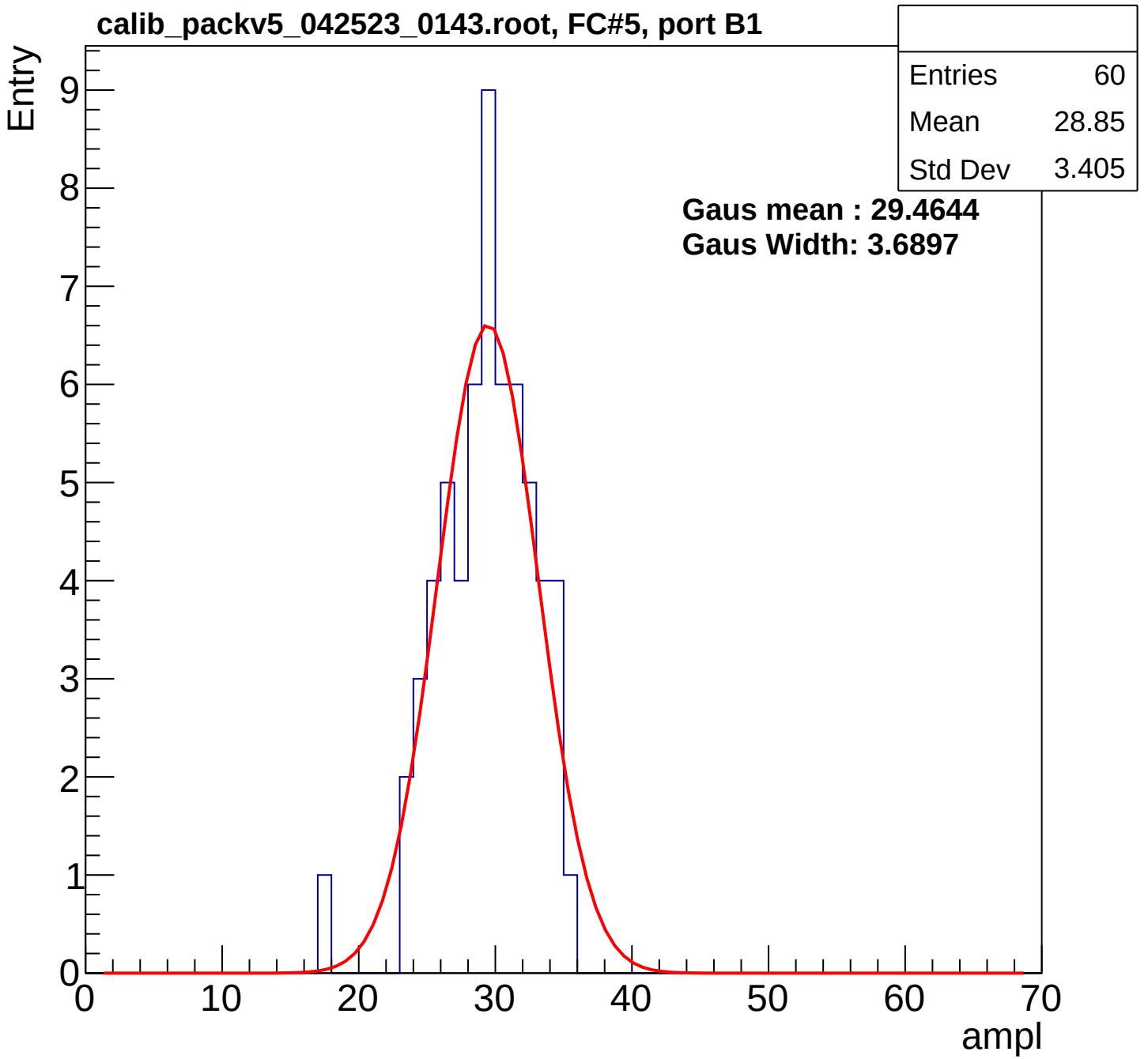
50
60
70
ampl


B0L000S, U5-ch84, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 28.85
Std Dev 3.405
Gaus mean : 29.4644
Gaus Width: 3.6897
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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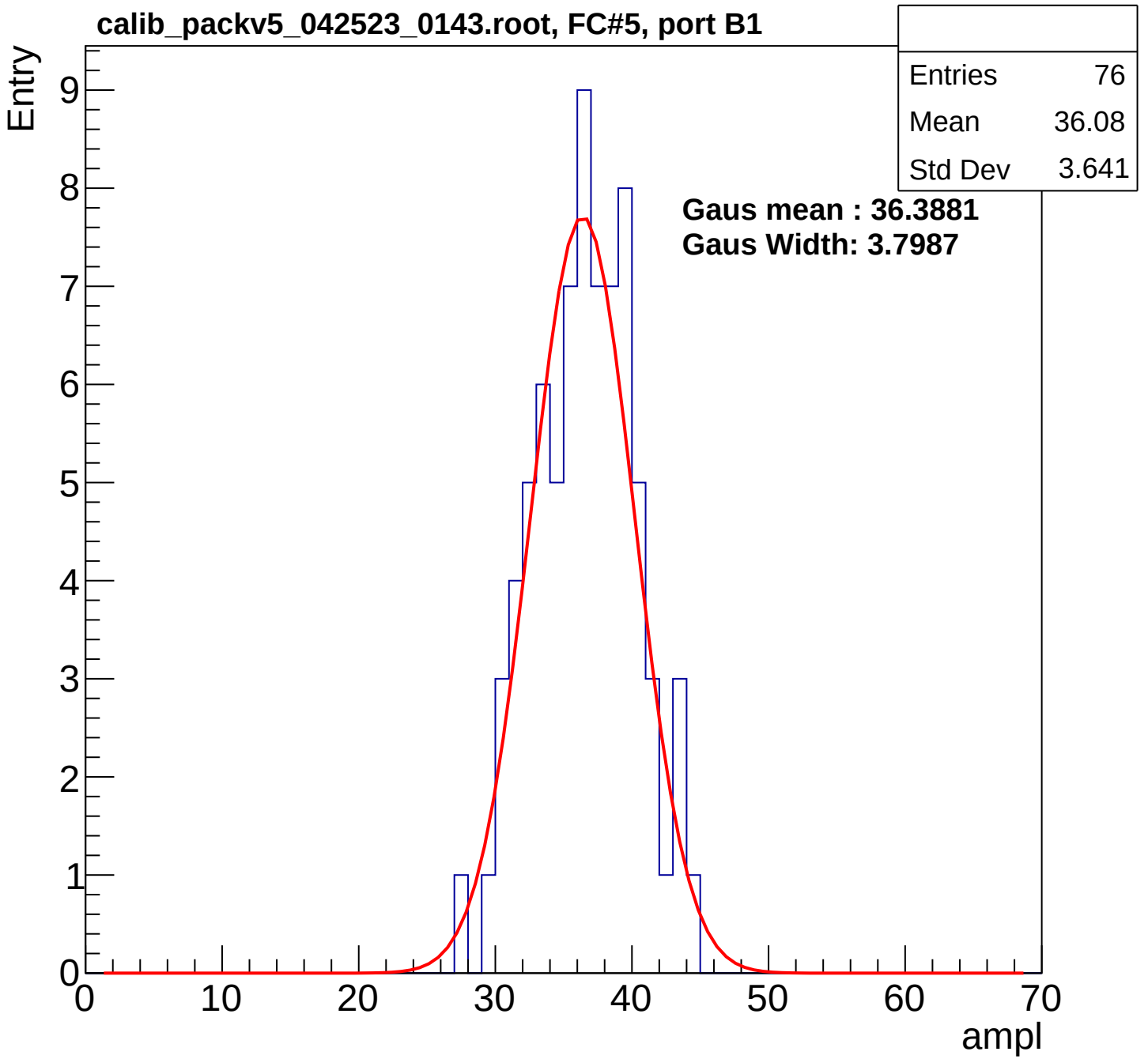
60
70


B0L000S, U5-ch84, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.08
Std Dev 3.641
Gaus mean : 36.3881
Gaus Width: 3.7987
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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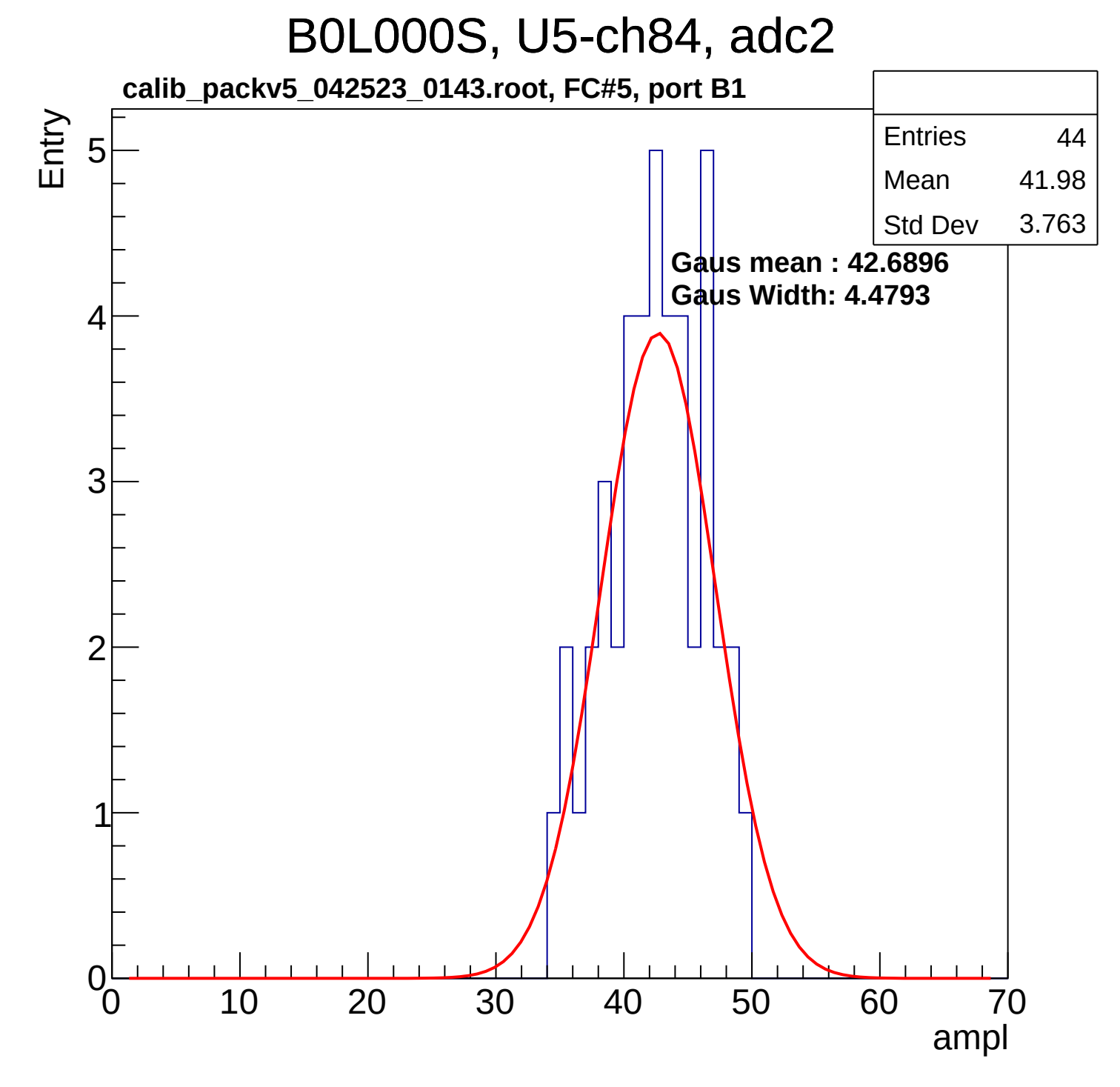
B0L000S, U5-ch84, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 41.98
Std Dev 3.763
Gaus mean : 42.6896
Gaus Width: 4.4793
Entry
ampl
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2
3
4
5
0
10
20
30
40
50
60
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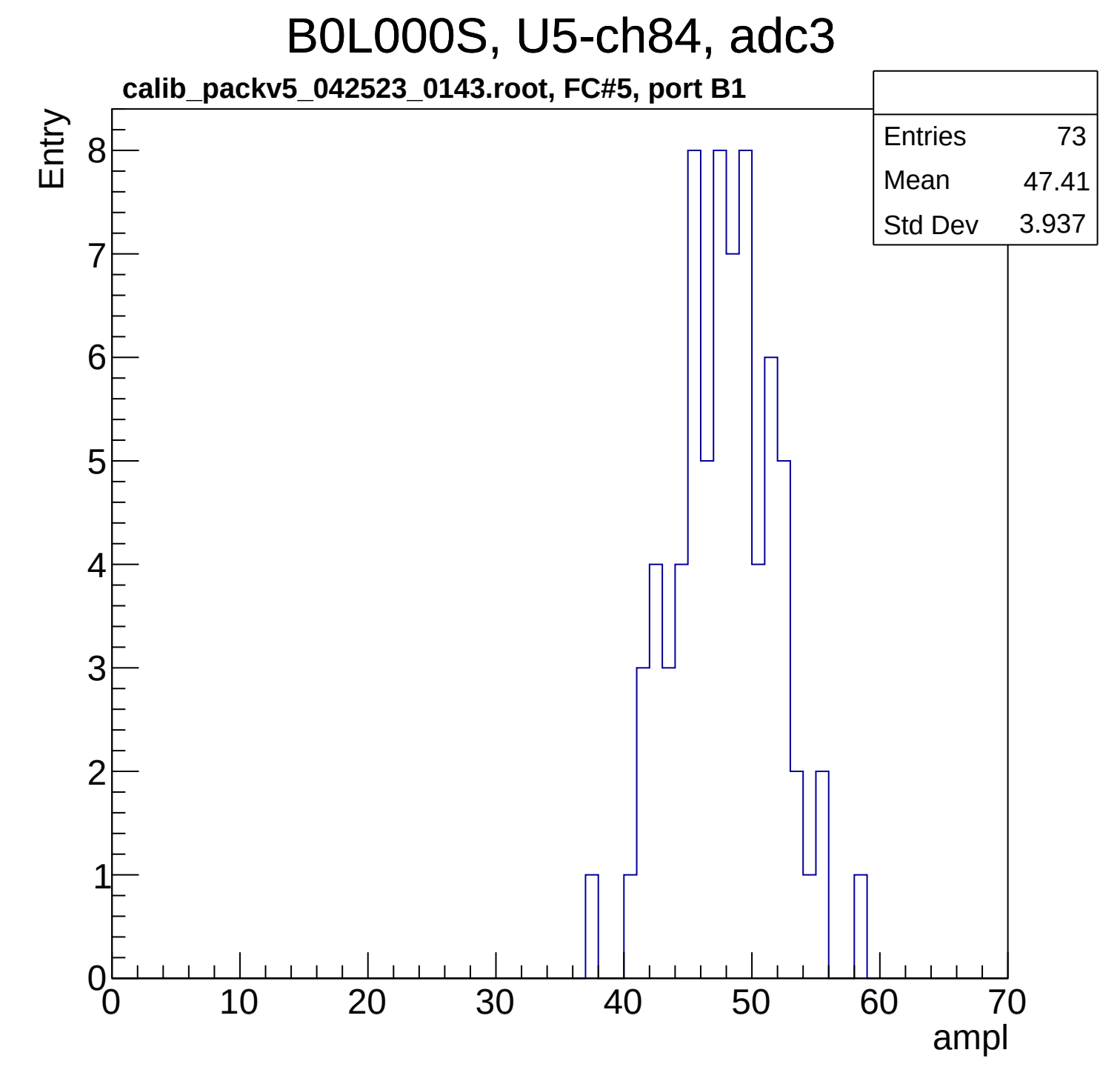
B0L000S, U5-ch84, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 47.41
Std Dev 3.937
Entry
ampl
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2
3
4
5
6
7
8
0
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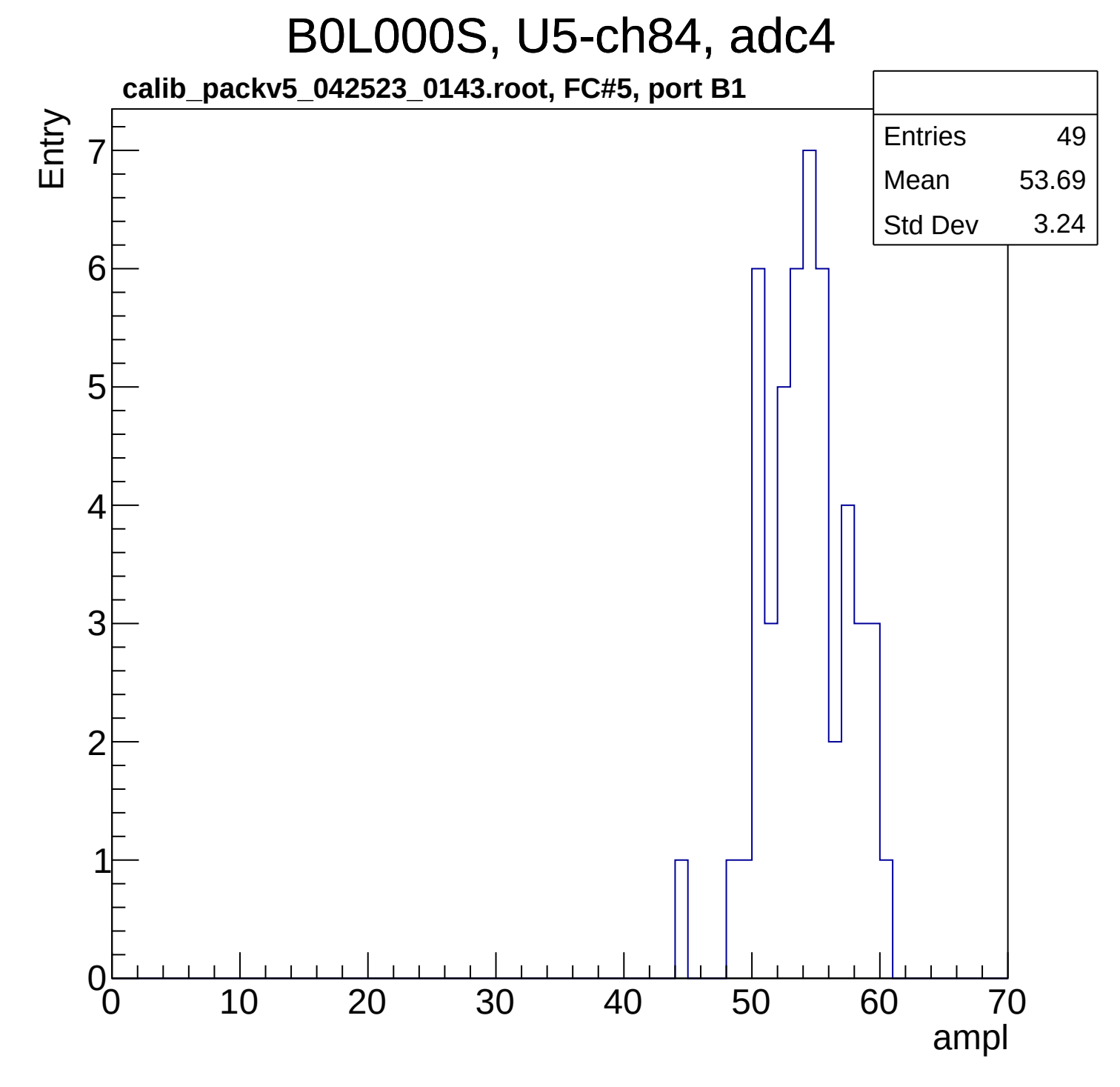
B0L000S, U5-ch84, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 53.69
Std Dev 3.24
Entry
ampl
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40
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60
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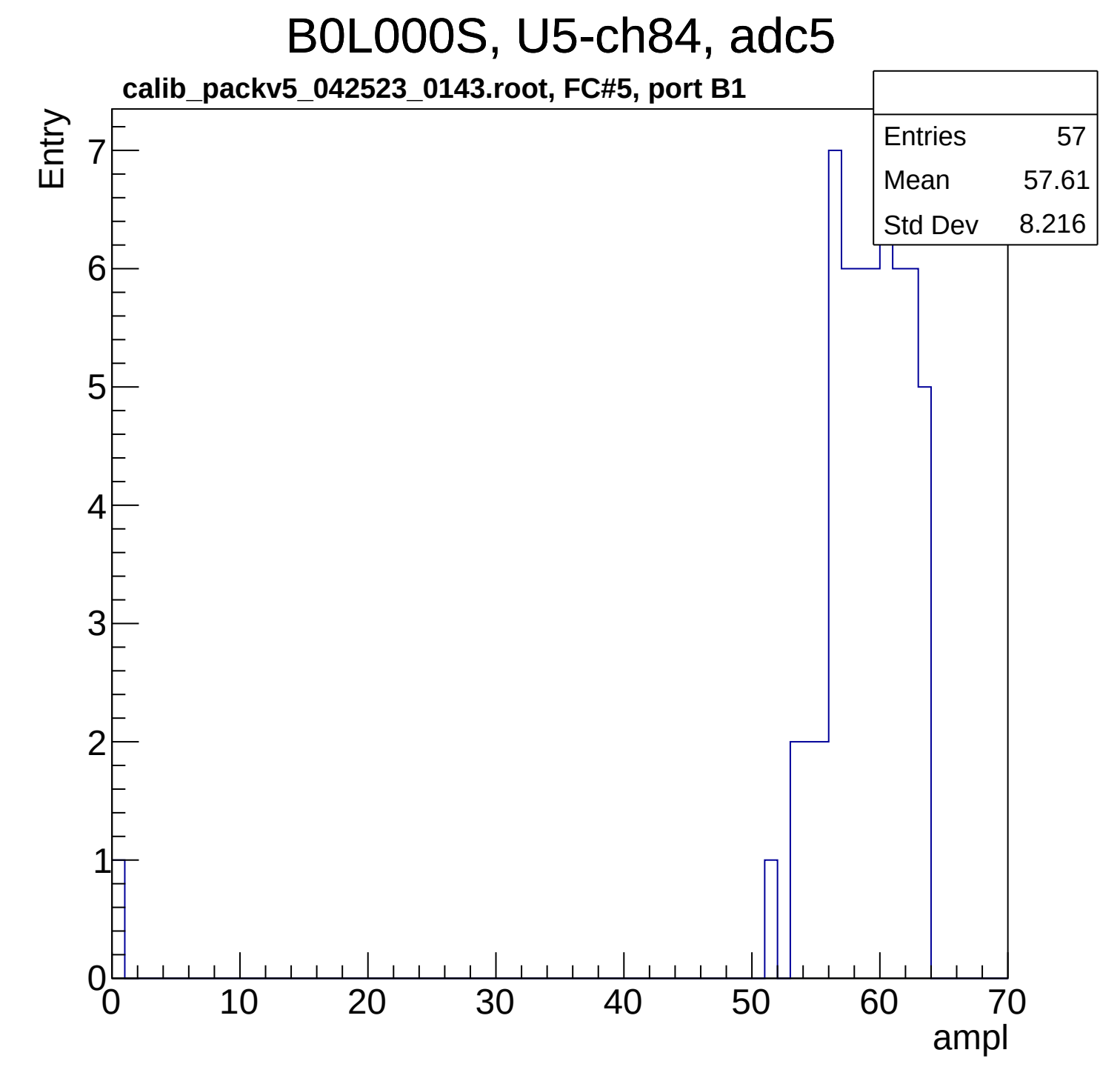
B0L000S, U5-ch84, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 57.61
Std Dev 8.216
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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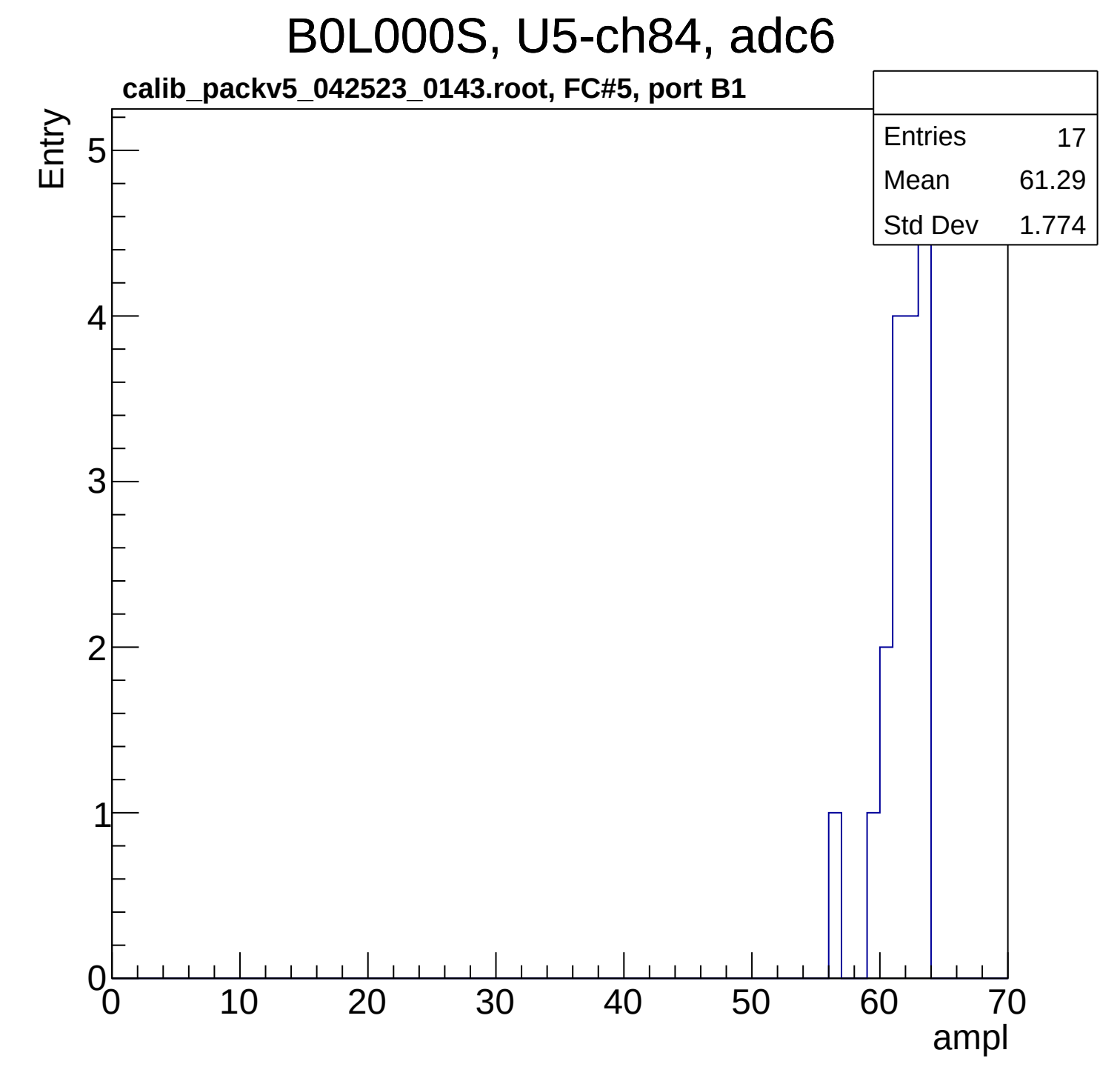
40
50
60
70


B0L000S, U5-ch84, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 61.29
Std Dev 1.774
Entry
ampl
0
1
2
3
4
5
0
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40
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60
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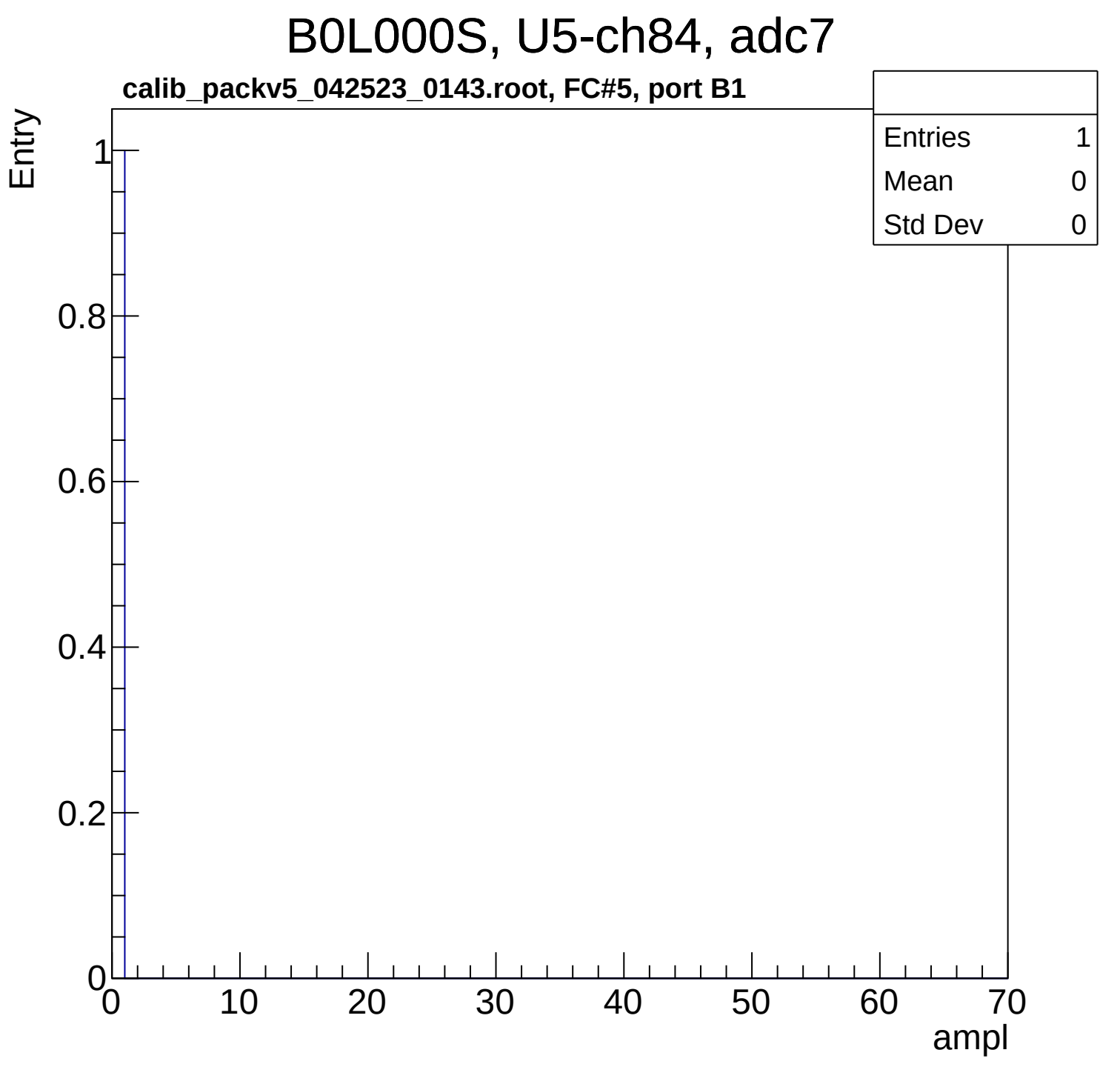
B0L000S, U5-ch84, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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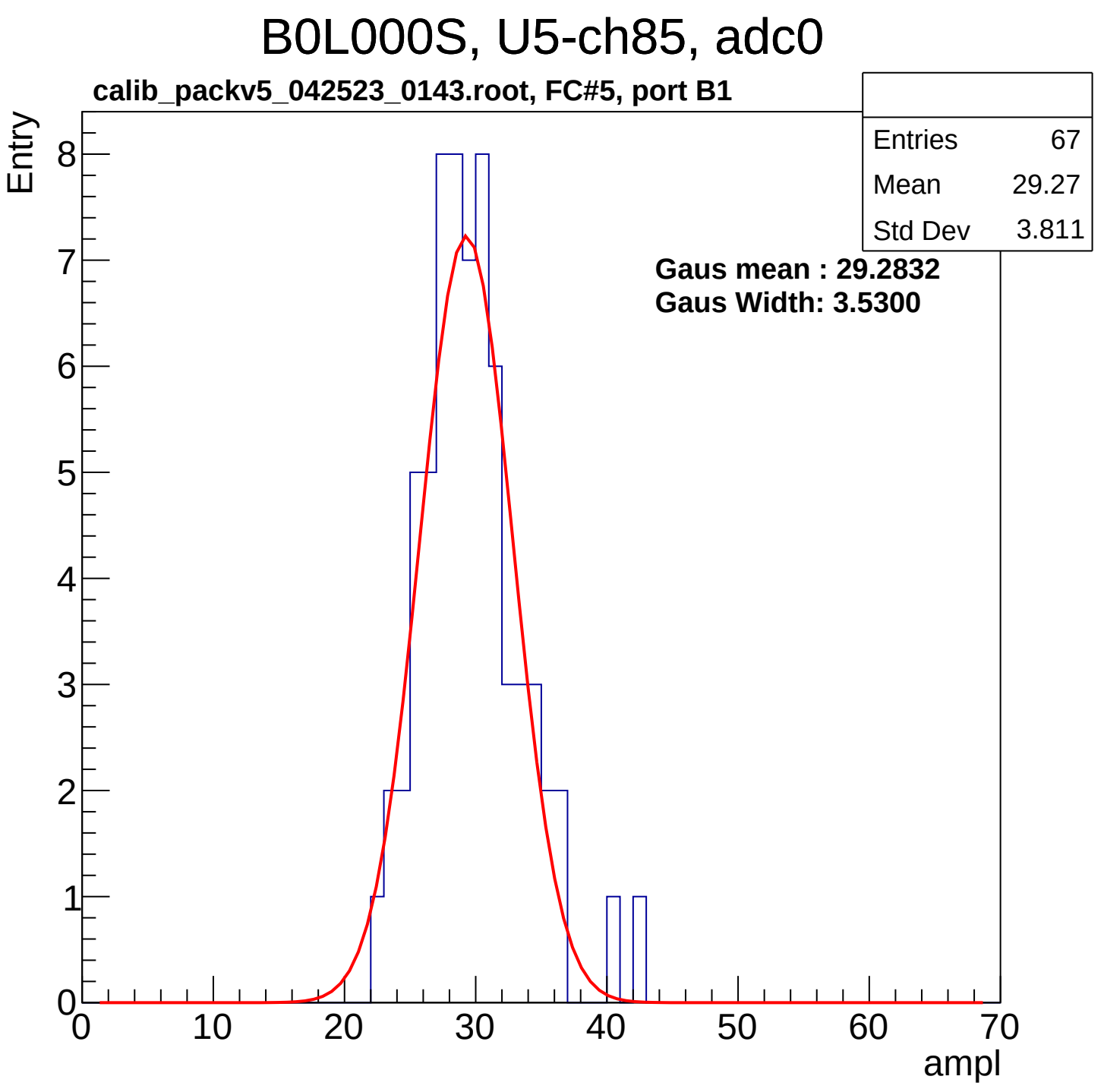
40
50
60
70
ampl


B0L000S, U5-ch85, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 29.27
Std Dev 3.811
Gaus mean : 29.2832
Gaus Width: 3.5300
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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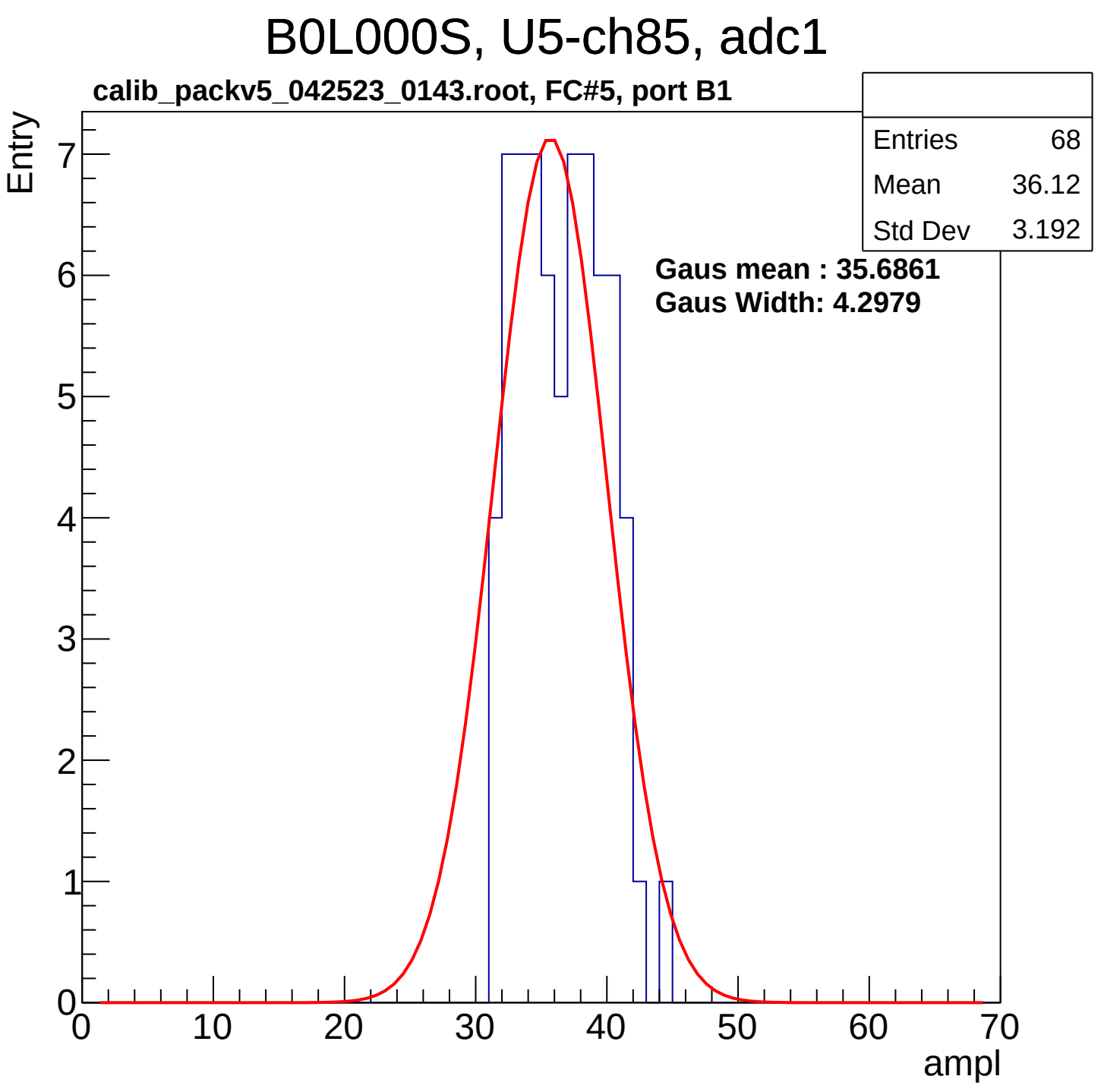
B0L000S, U5-ch85, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 36.12
Std Dev 3.192
Gaus mean : 35.6861
Gaus Width: 4.2979
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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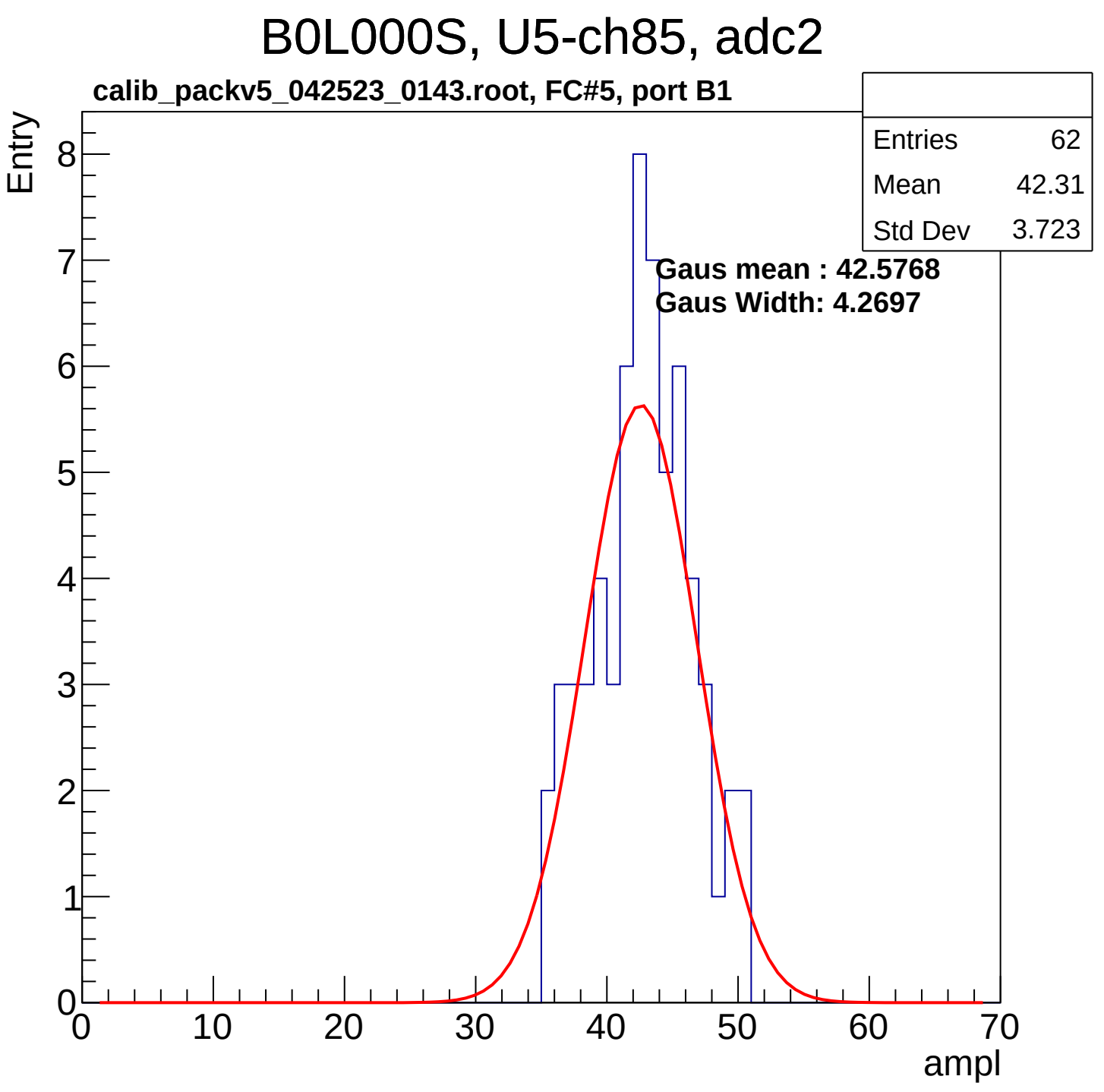
B0L000S, U5-ch85, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 42.31
Std Dev 3.723
Gaus mean : 42.5768
Gaus Width: 4.2697
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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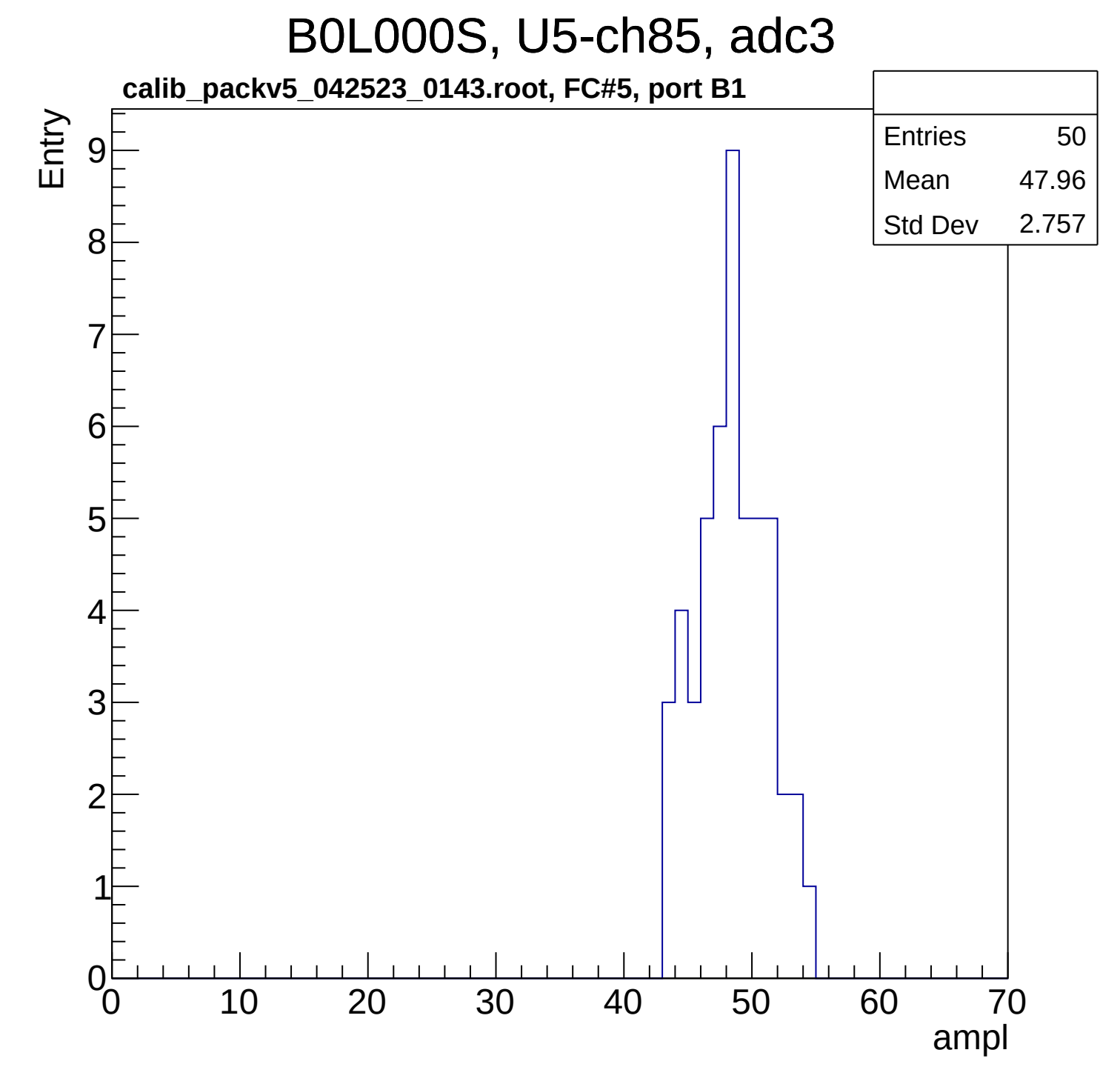
B0L000S, U5-ch85, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 47.96
Std Dev 2.757
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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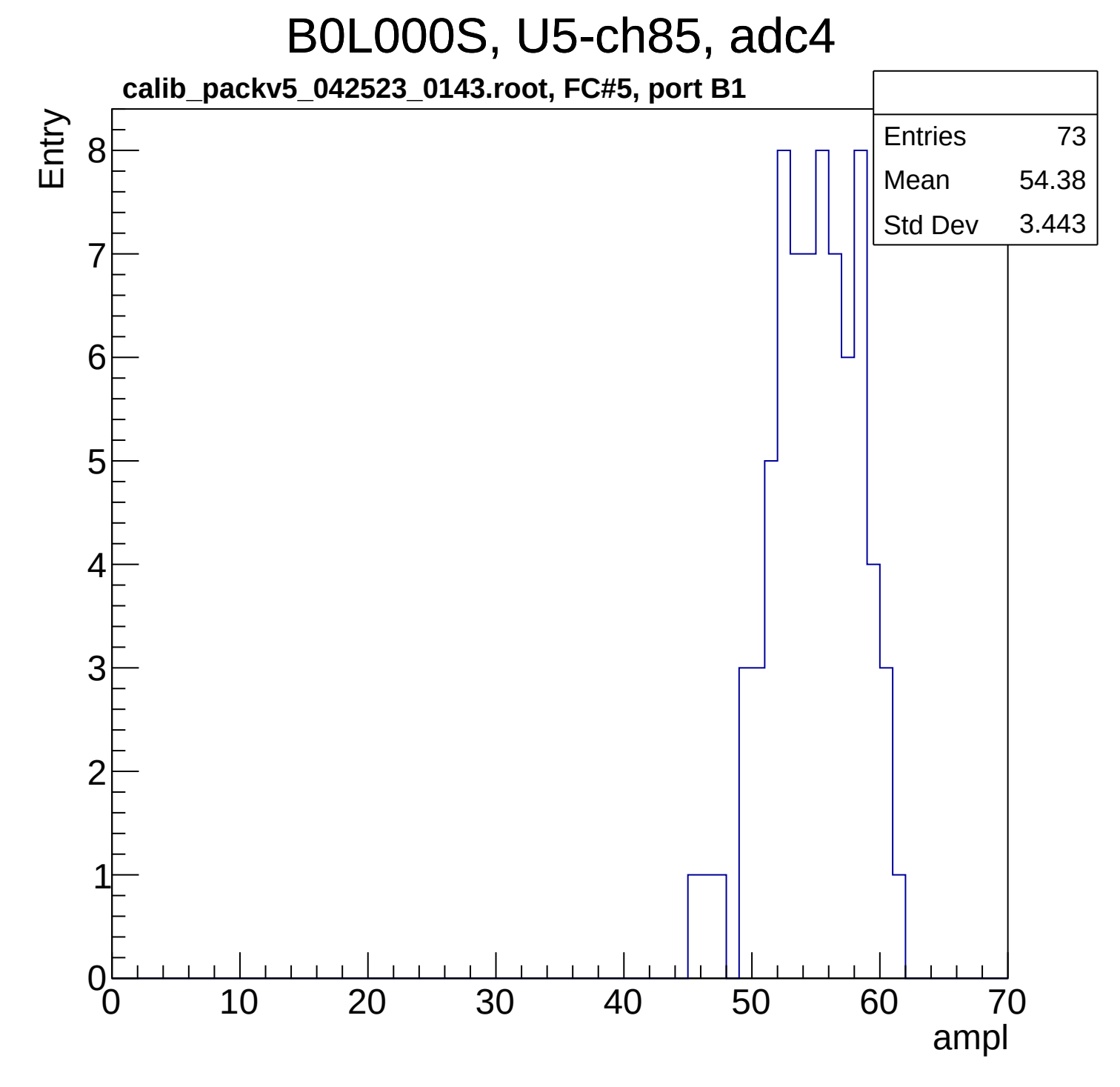
B0L000S, U5-ch85, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 54.38
Std Dev 3.443
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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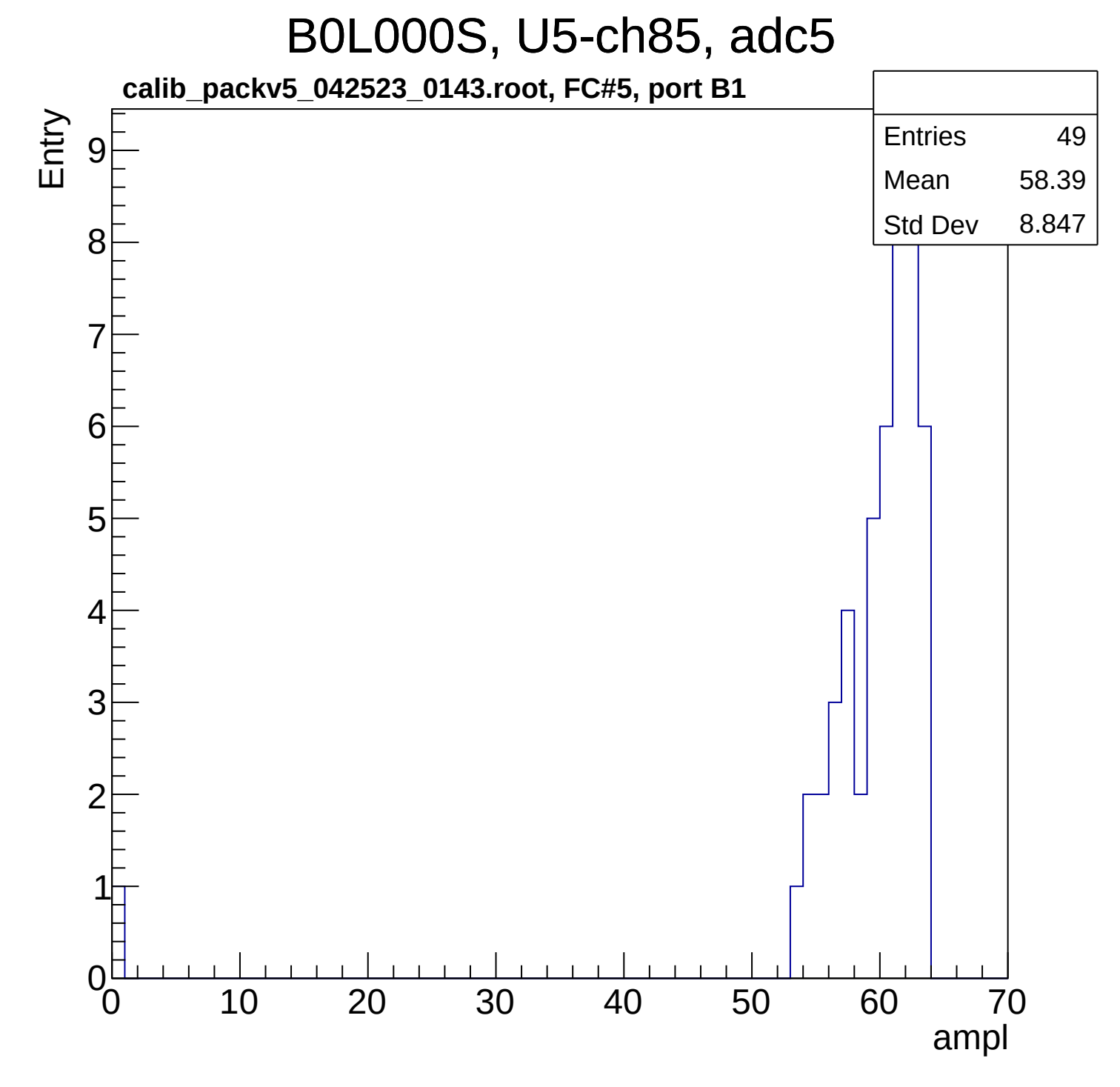
B0L000S, U5-ch85, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.39
Std Dev 8.847
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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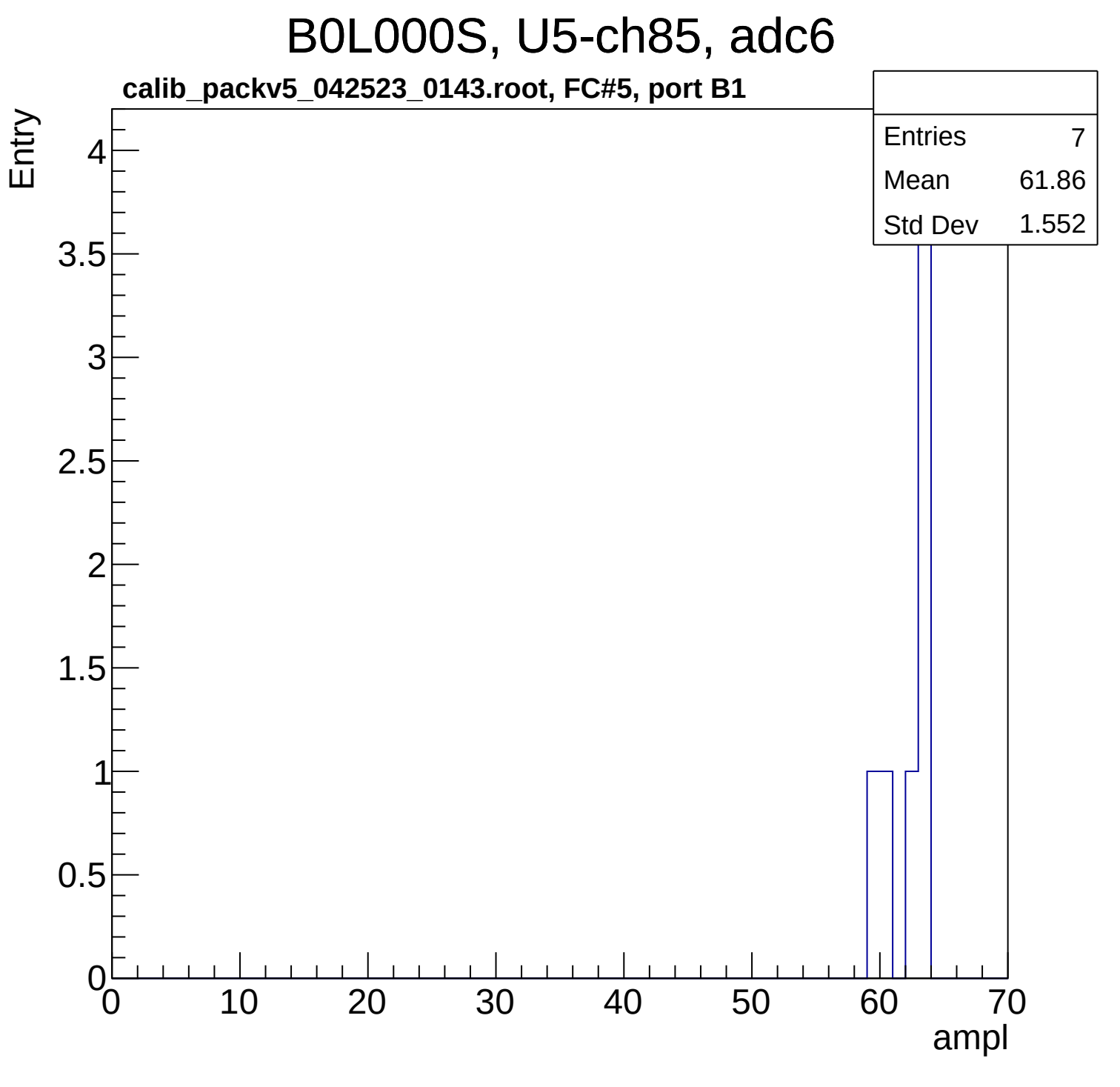
B0L000S, U5-ch85, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.86
Std Dev 1.552
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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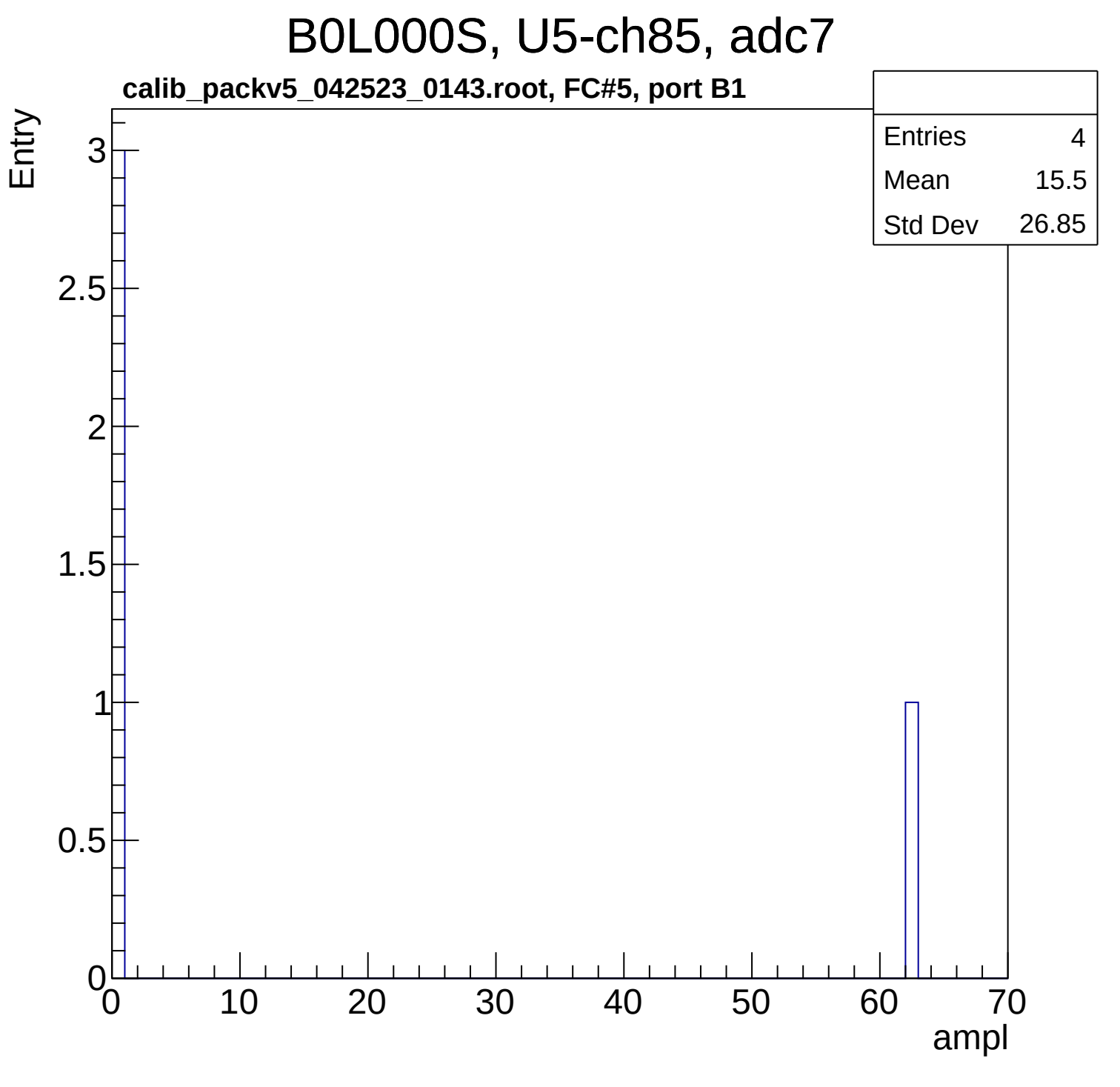
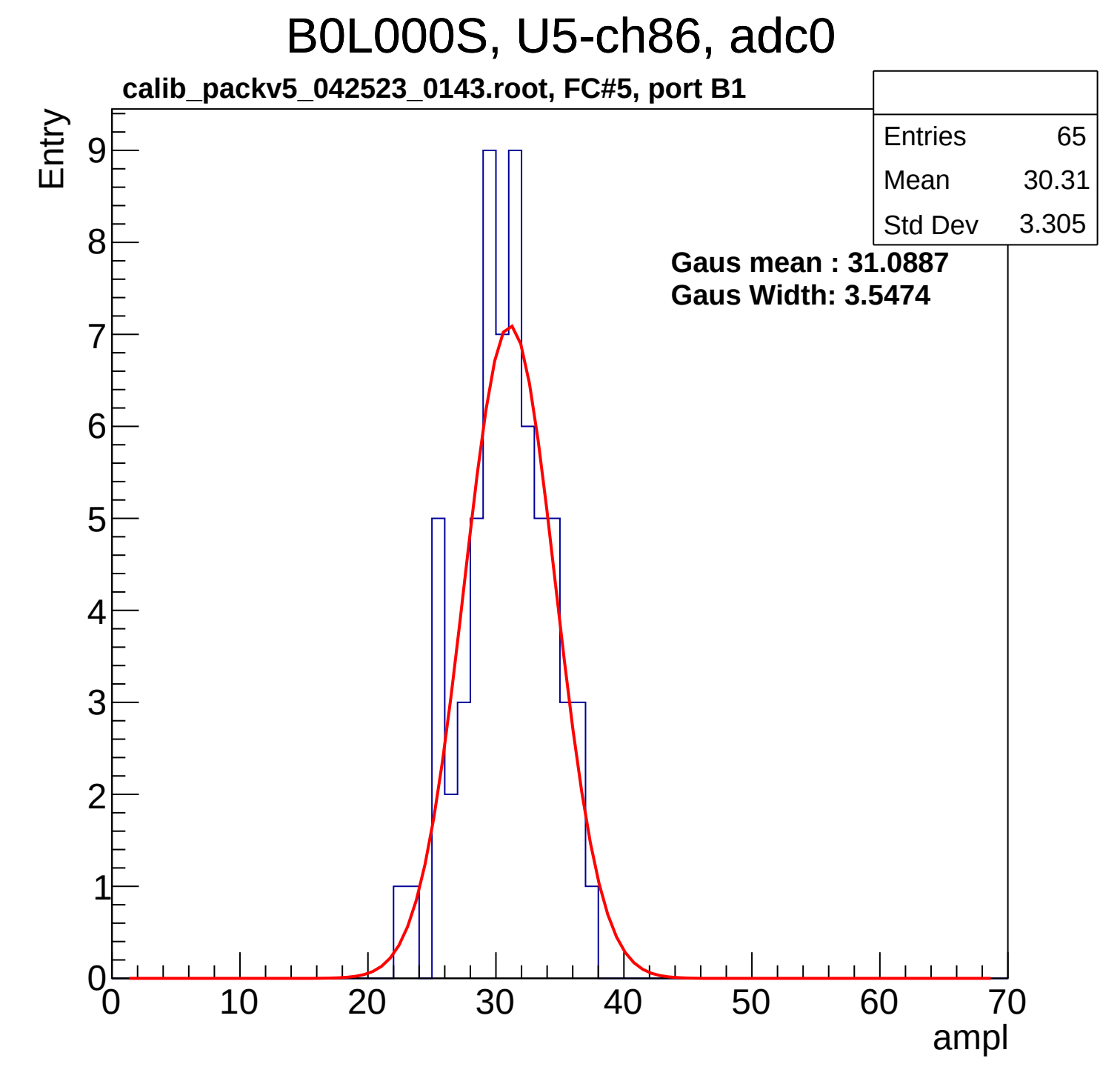

B0L000S, U5-ch85, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 15.5
Std Dev 26.85
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch86, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 30.31
Std Dev 3.305
Gaus mean : 31.0887
Gaus Width: 3.5474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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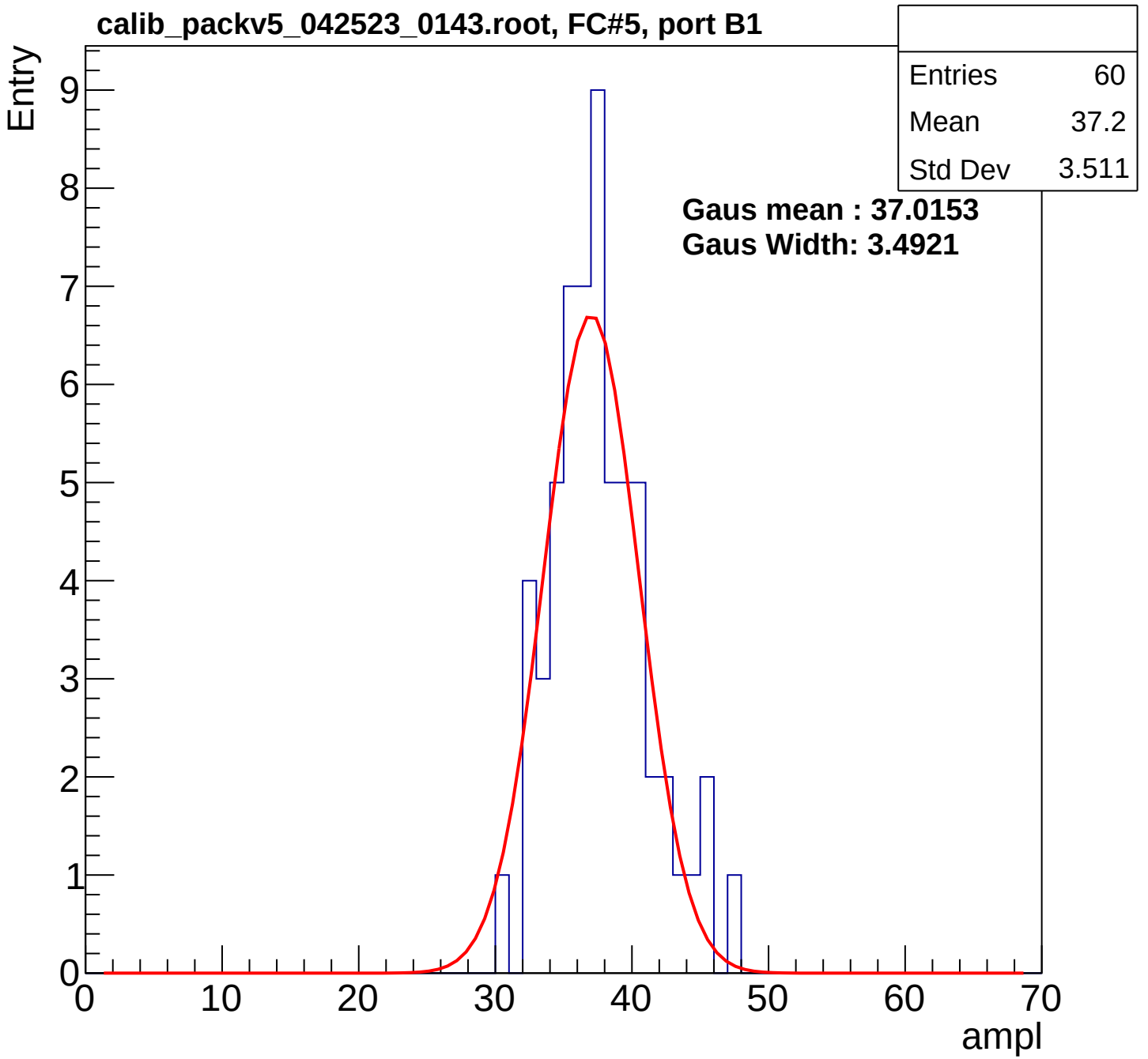
B0L000S, U5-ch86, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 37.2
Std Dev 3.511
Gaus mean : 37.0153
Gaus Width: 3.4921
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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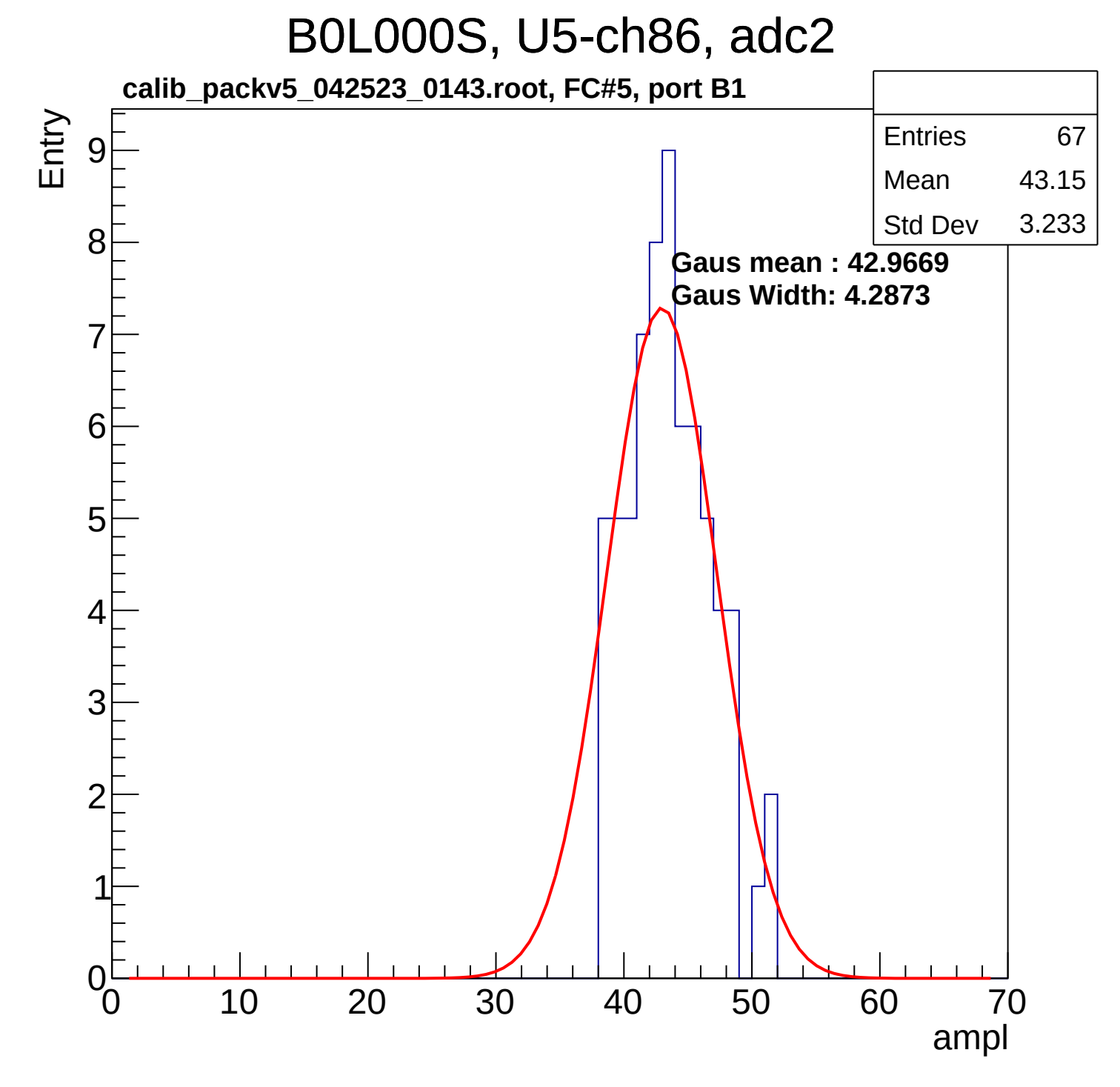
B0L000S, U5-ch86, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 43.15
Std Dev 3.233
Gaus mean : 42.9669
Gaus Width: 4.2873
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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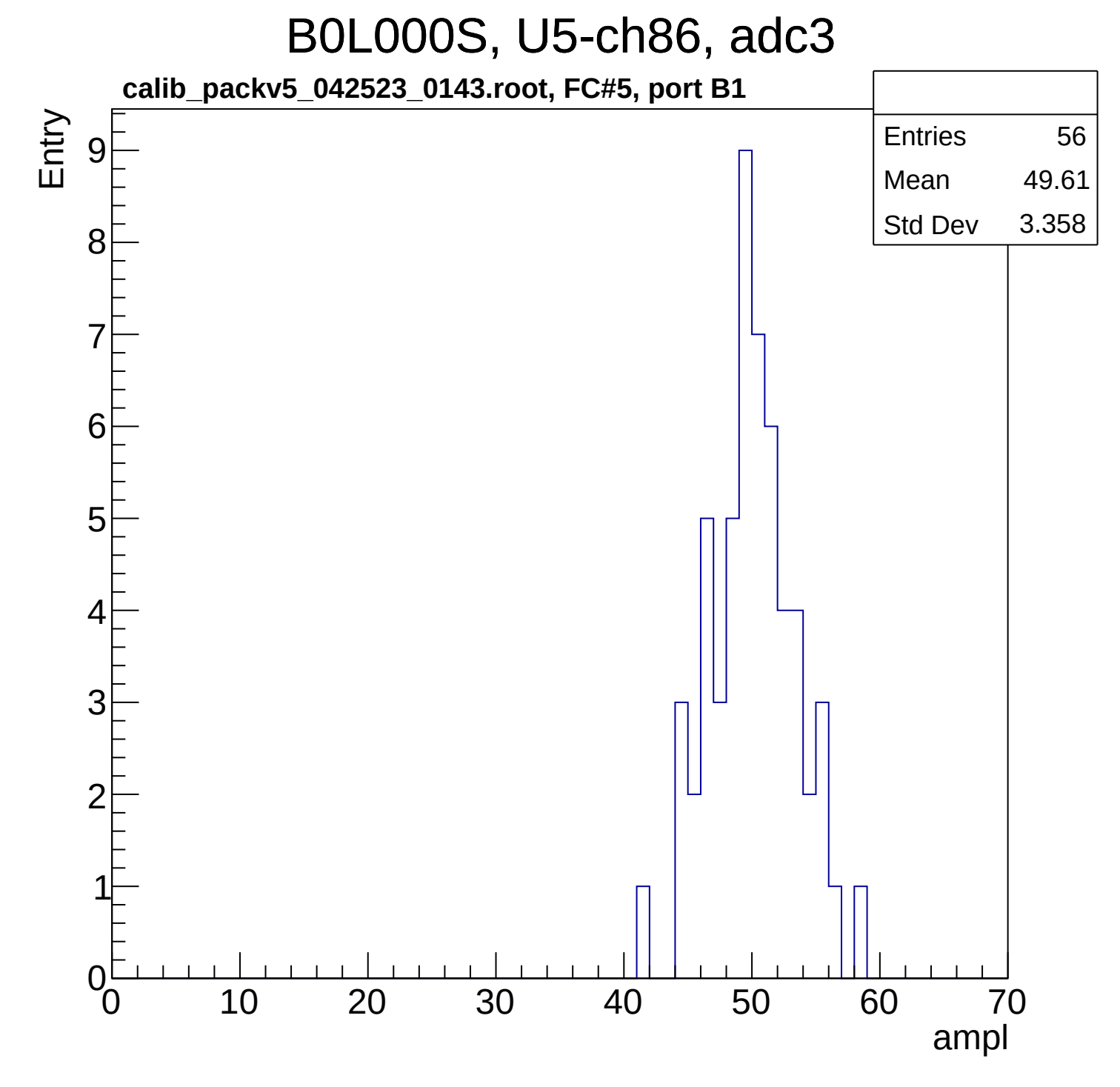
B0L000S, U5-ch86, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 49.61
Std Dev 3.358
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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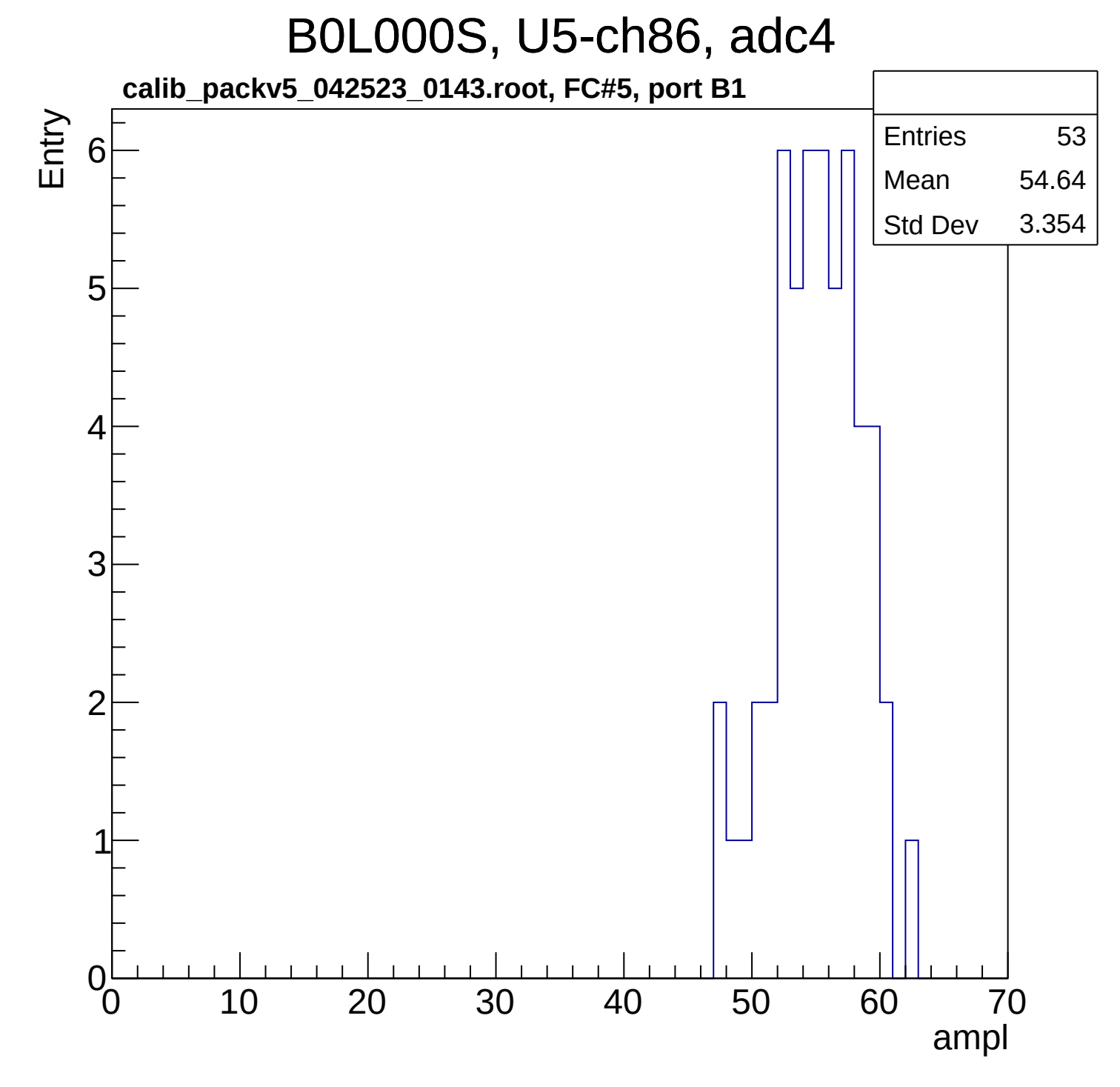
B0L000S, U5-ch86, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 54.64
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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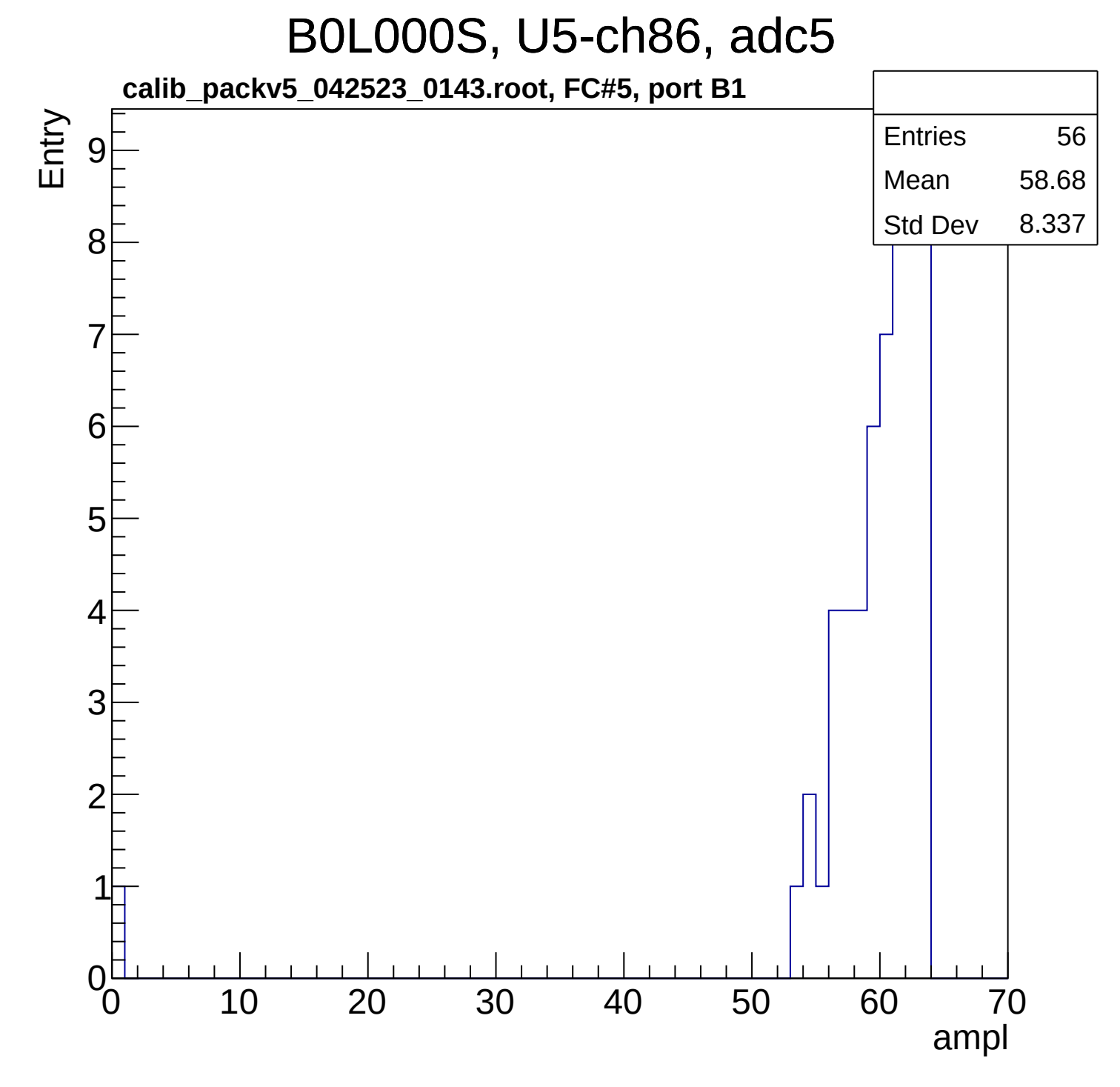
B0L000S, U5-ch86, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 58.68
Std Dev 8.337
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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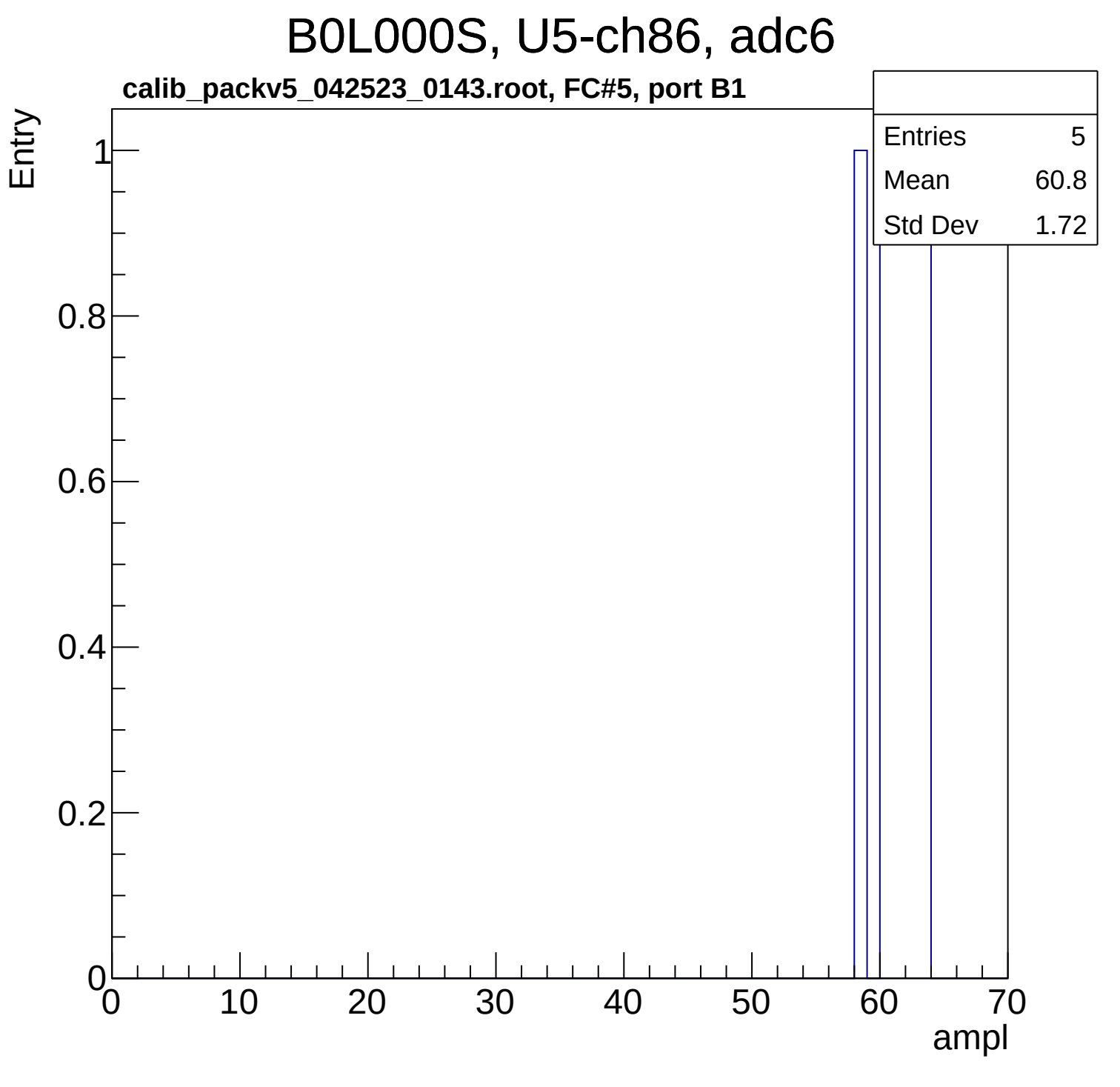
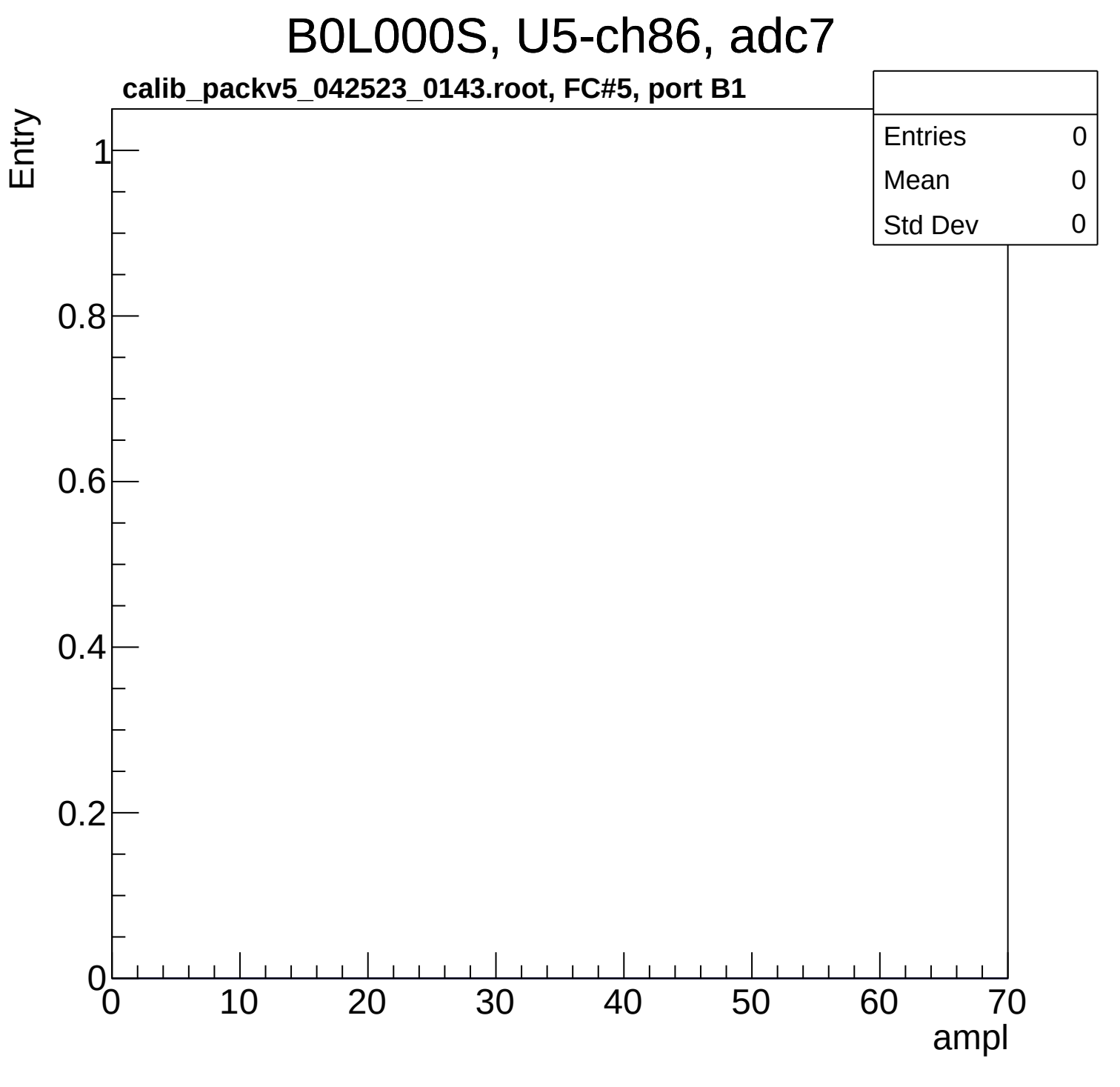
60
70


B0L000S, U5-ch86, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 60.8
Std Dev 1.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch86, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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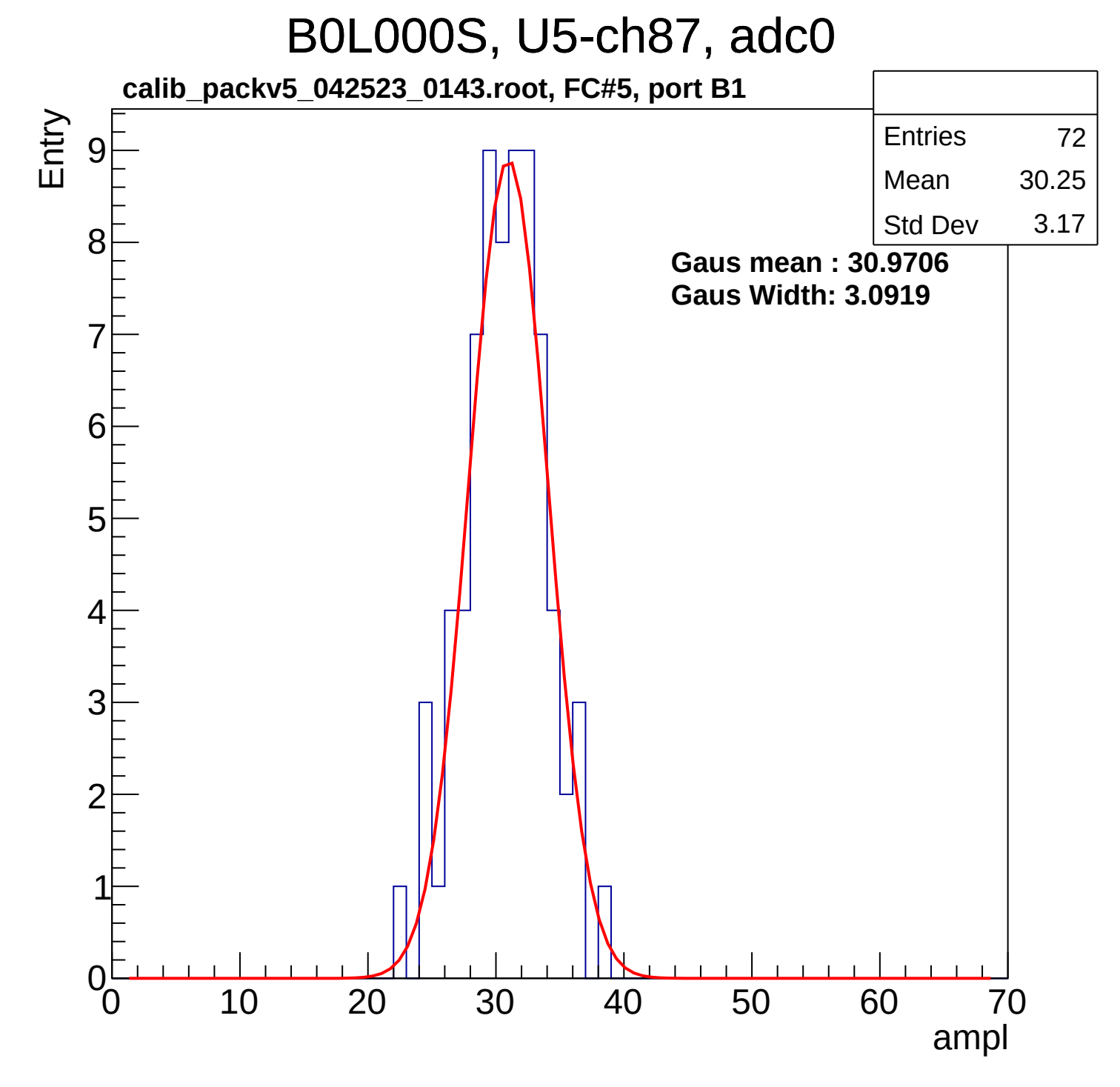
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch87, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.25
Std Dev 3.17
Gaus mean : 30.9706
Gaus Width: 3.0919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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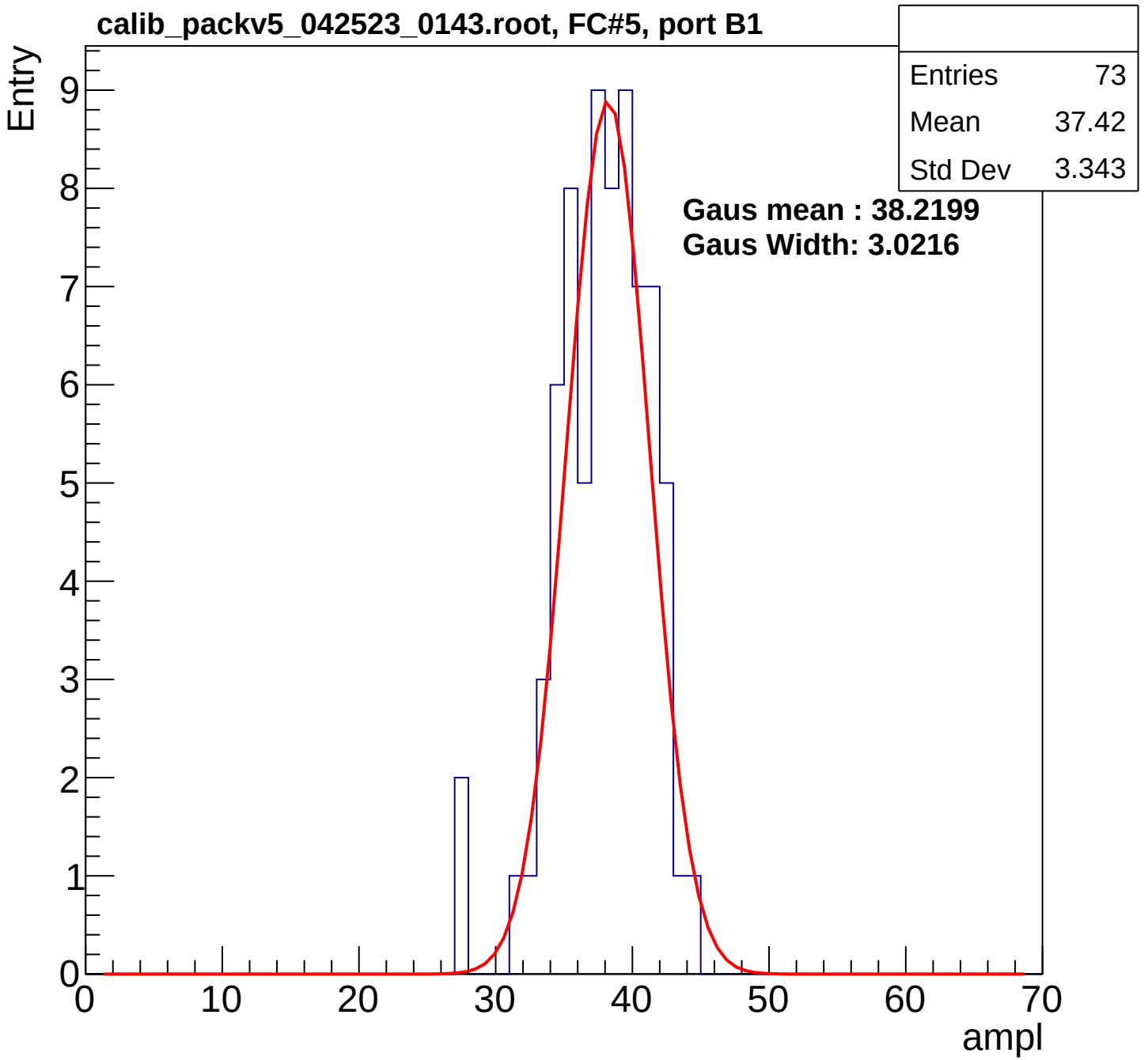
20
30
40
50
60
70


B0L000S, U5-ch87, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 37.42
Std Dev 3.343
Gaus mean : 38.2199
Gaus Width: 3.0216
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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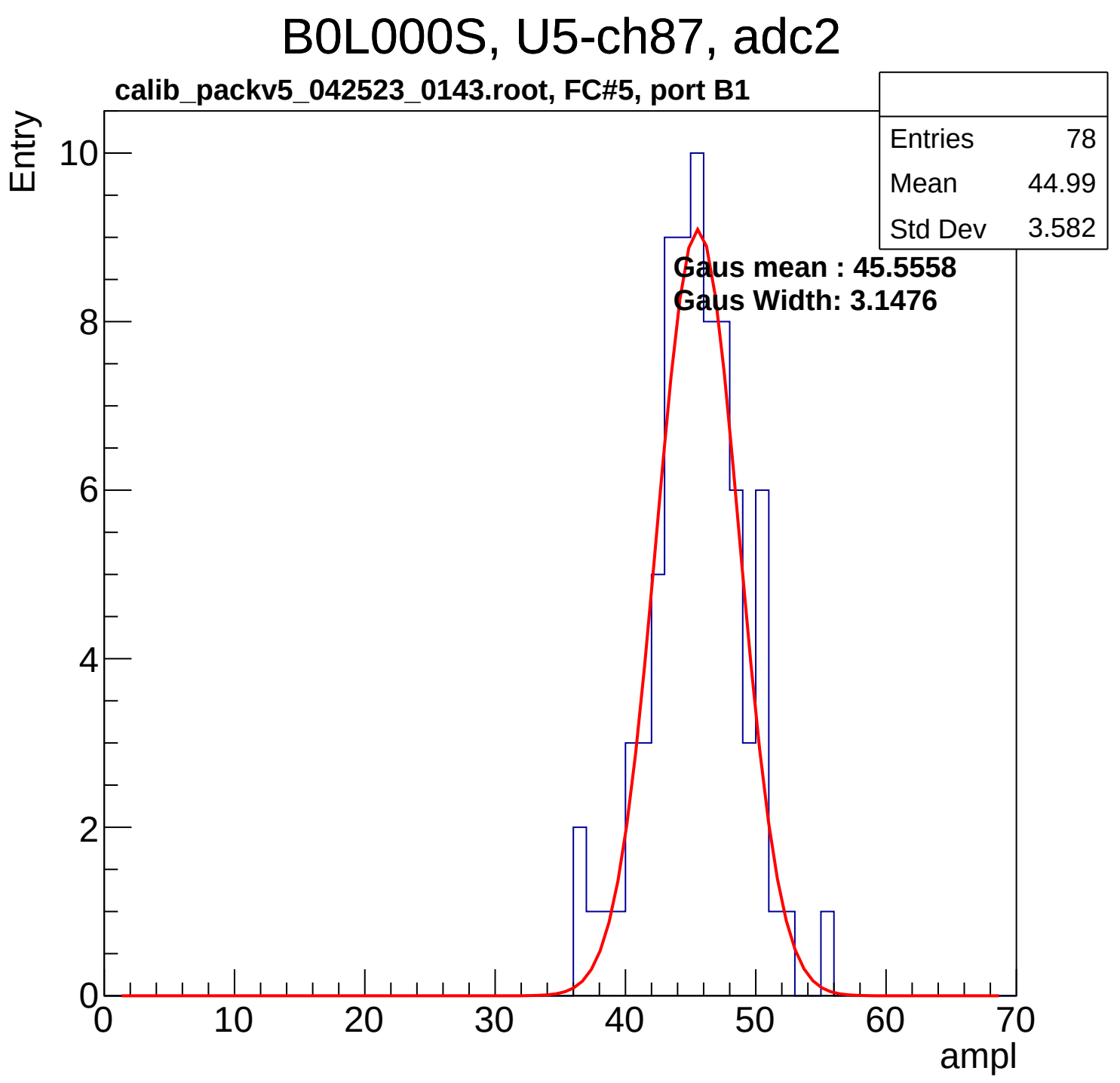
B0L000S, U5-ch87, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 44.99
Std Dev 3.582
Gaus mean : 45.5558
Gaus Width: 3.1476
Entry
ampl
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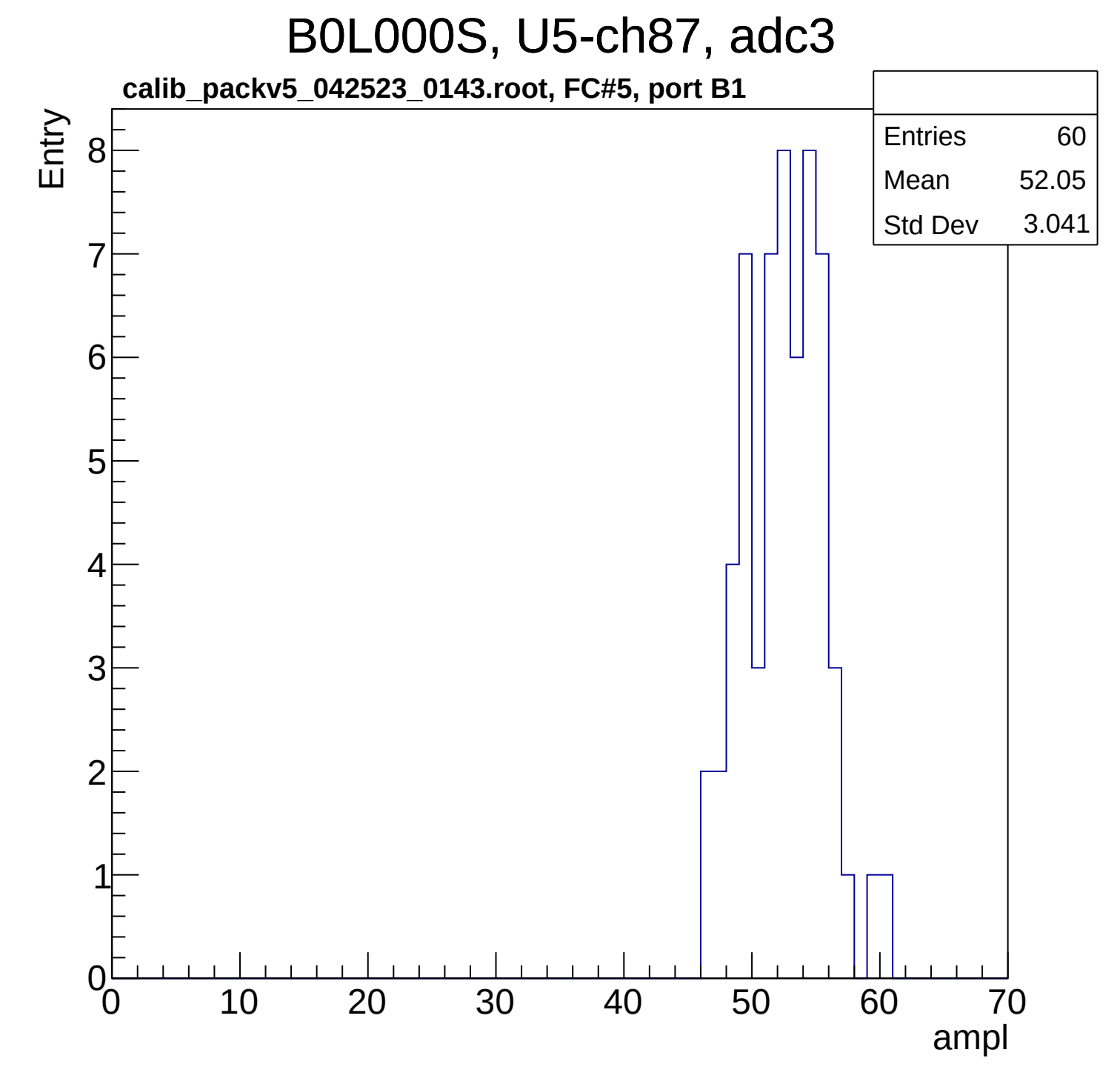
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch87, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 52.05
Std Dev 3.041
Entry
ampl
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2
3
4
5
6
7
8
0
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40
50
60
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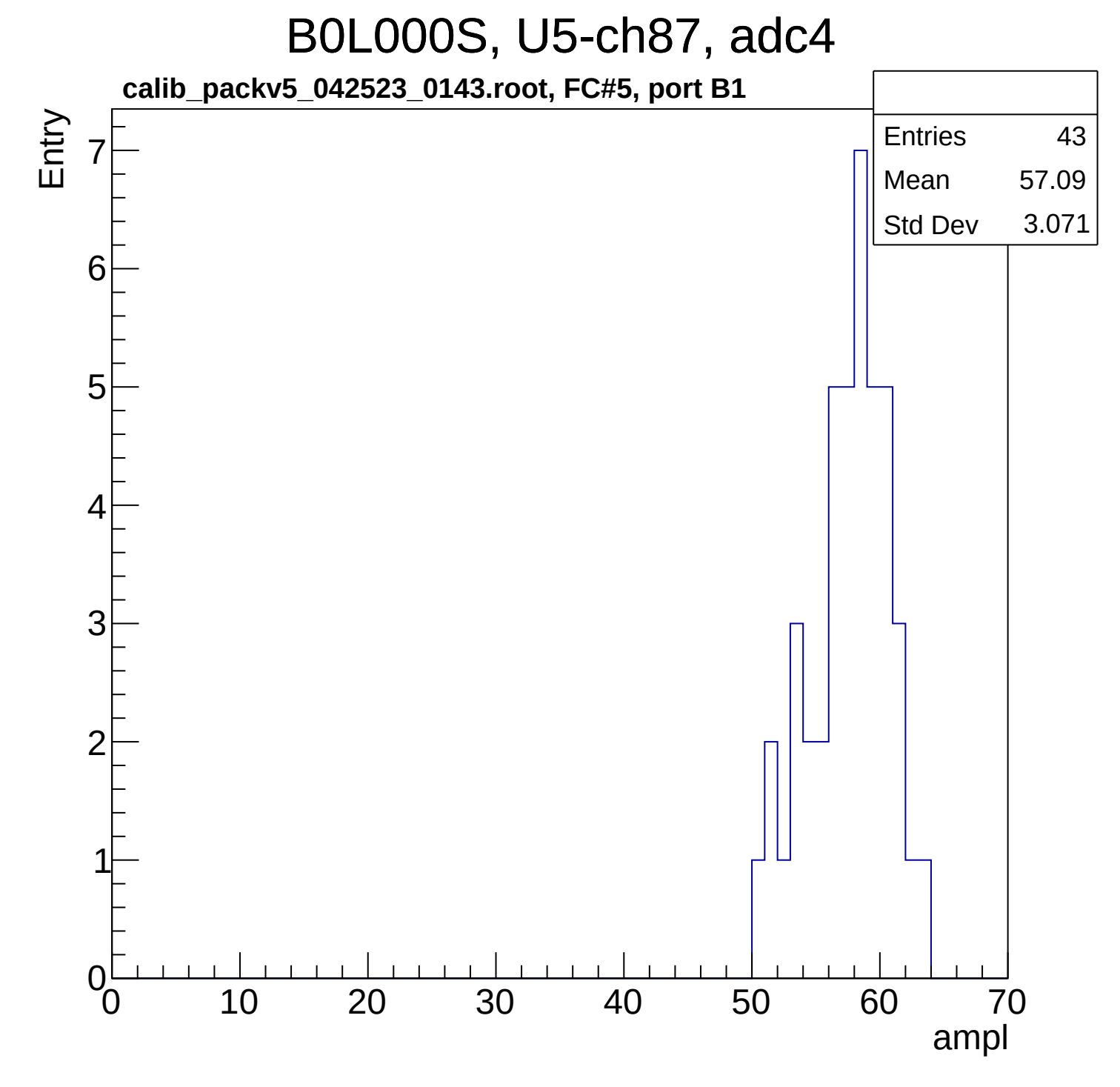
B0L000S, U5-ch87, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 57.09
Std Dev 3.071
Entry
ampl
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2
3
4
5
6
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0
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60
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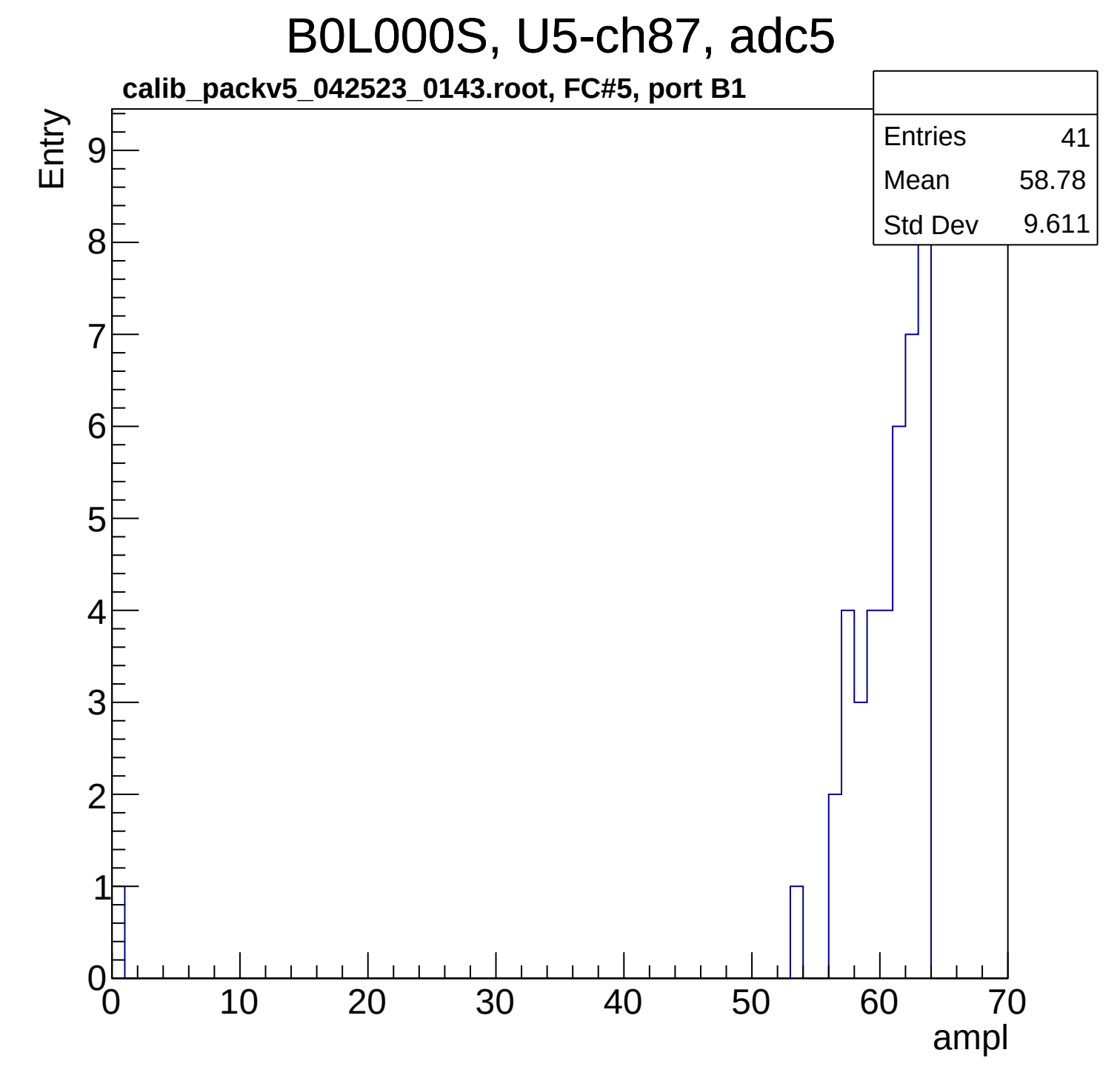
B0L000S, U5-ch87, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.78
Std Dev 9.611
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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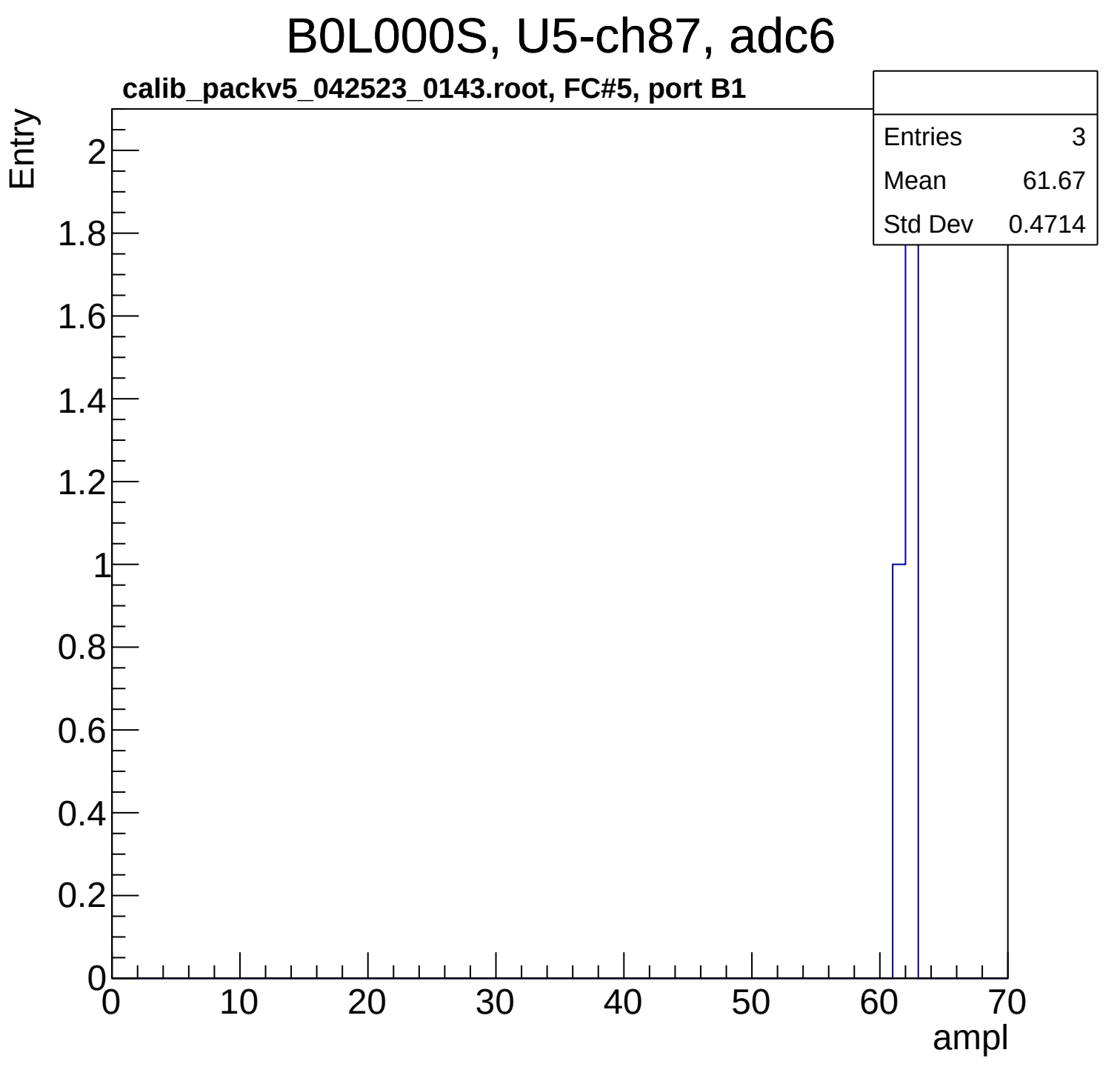
B0L000S, U5-ch87, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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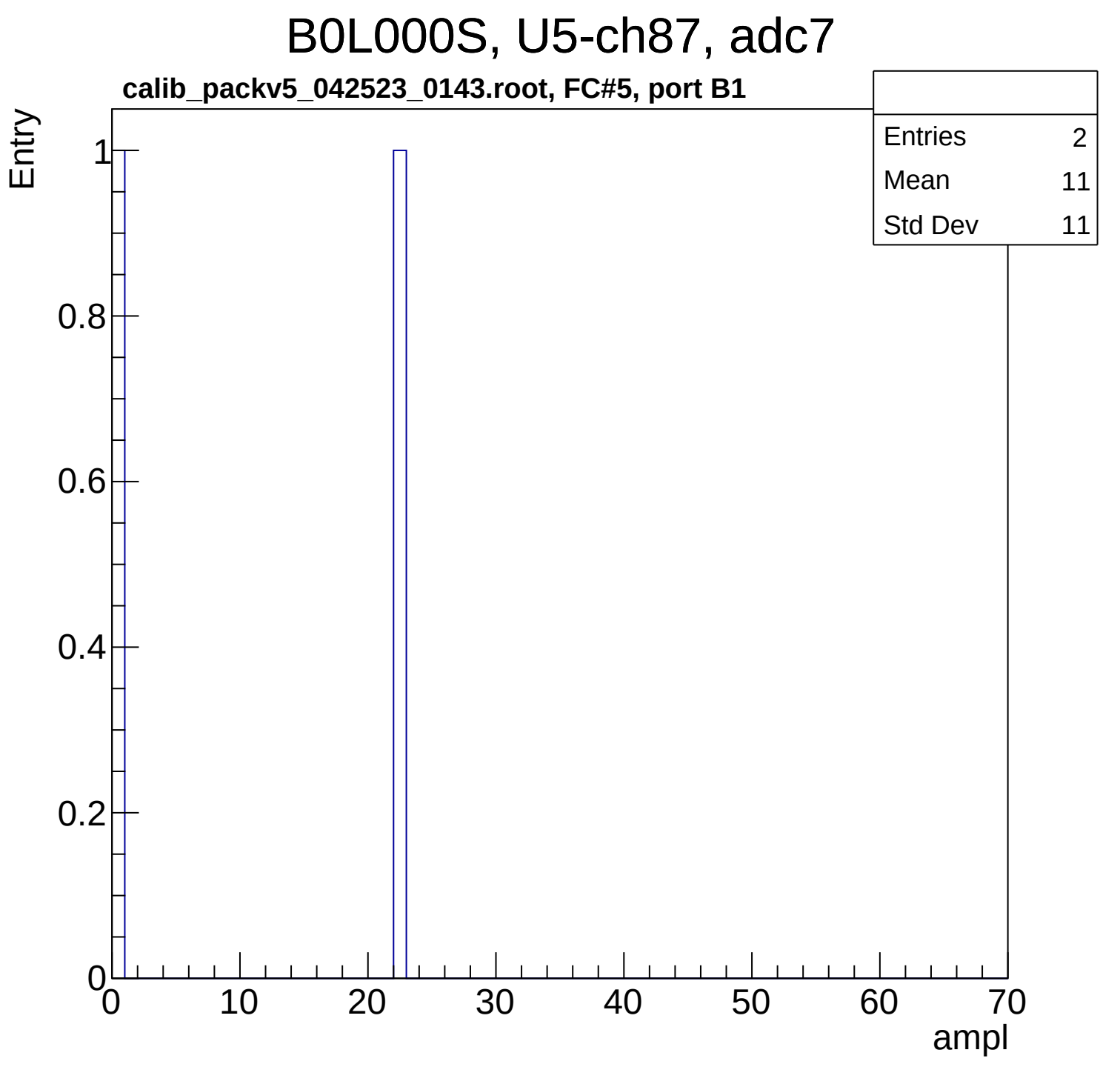
B0L000S, U5-ch87, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 11
Std Dev 11
Entry
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0.6
0.4
0.2
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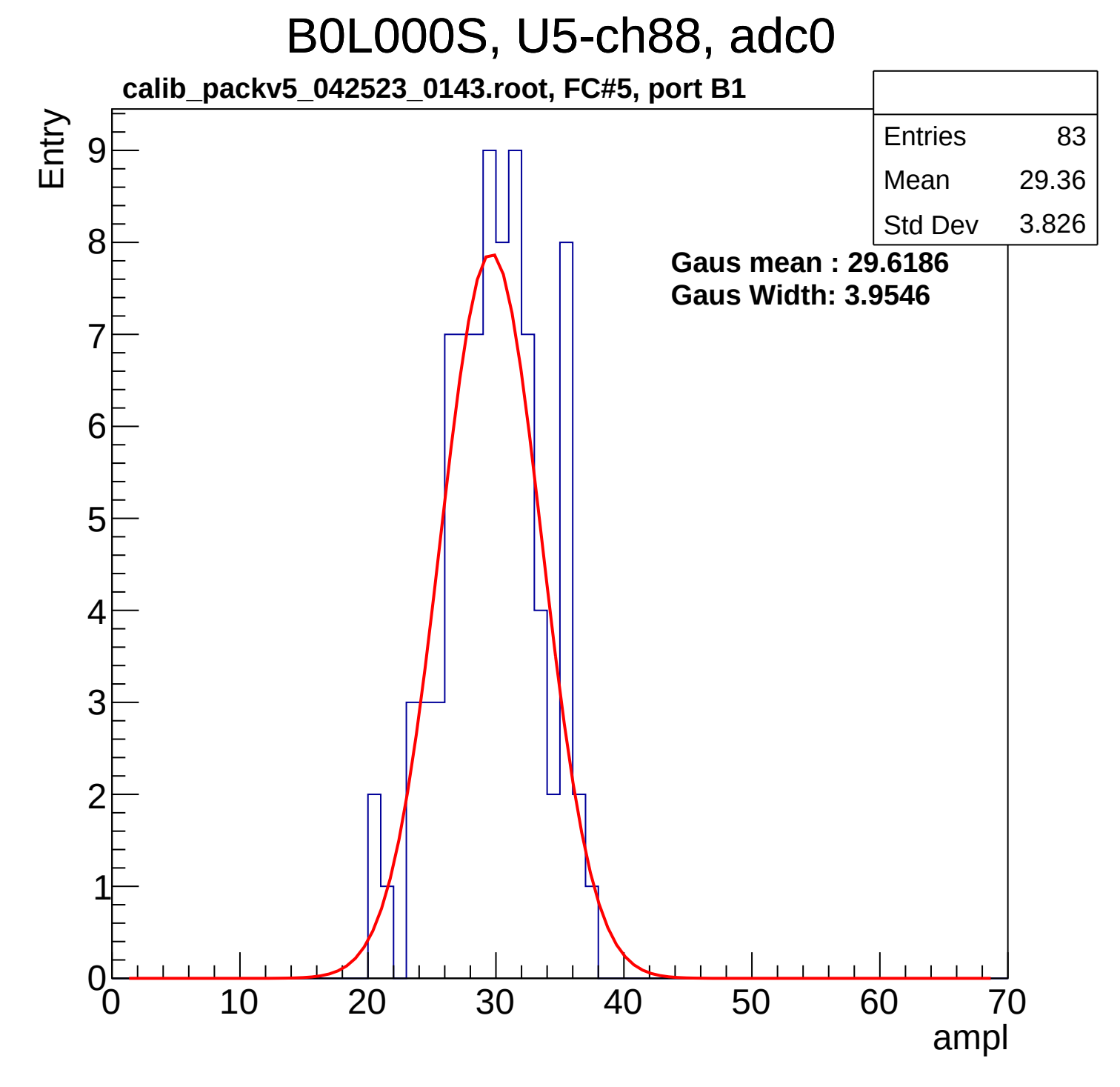
50
60
70
ampl


B0L000S, U5-ch88, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 83
Mean 29.36
Std Dev 3.826
Gaus mean : 29.6186
Gaus Width: 3.9546
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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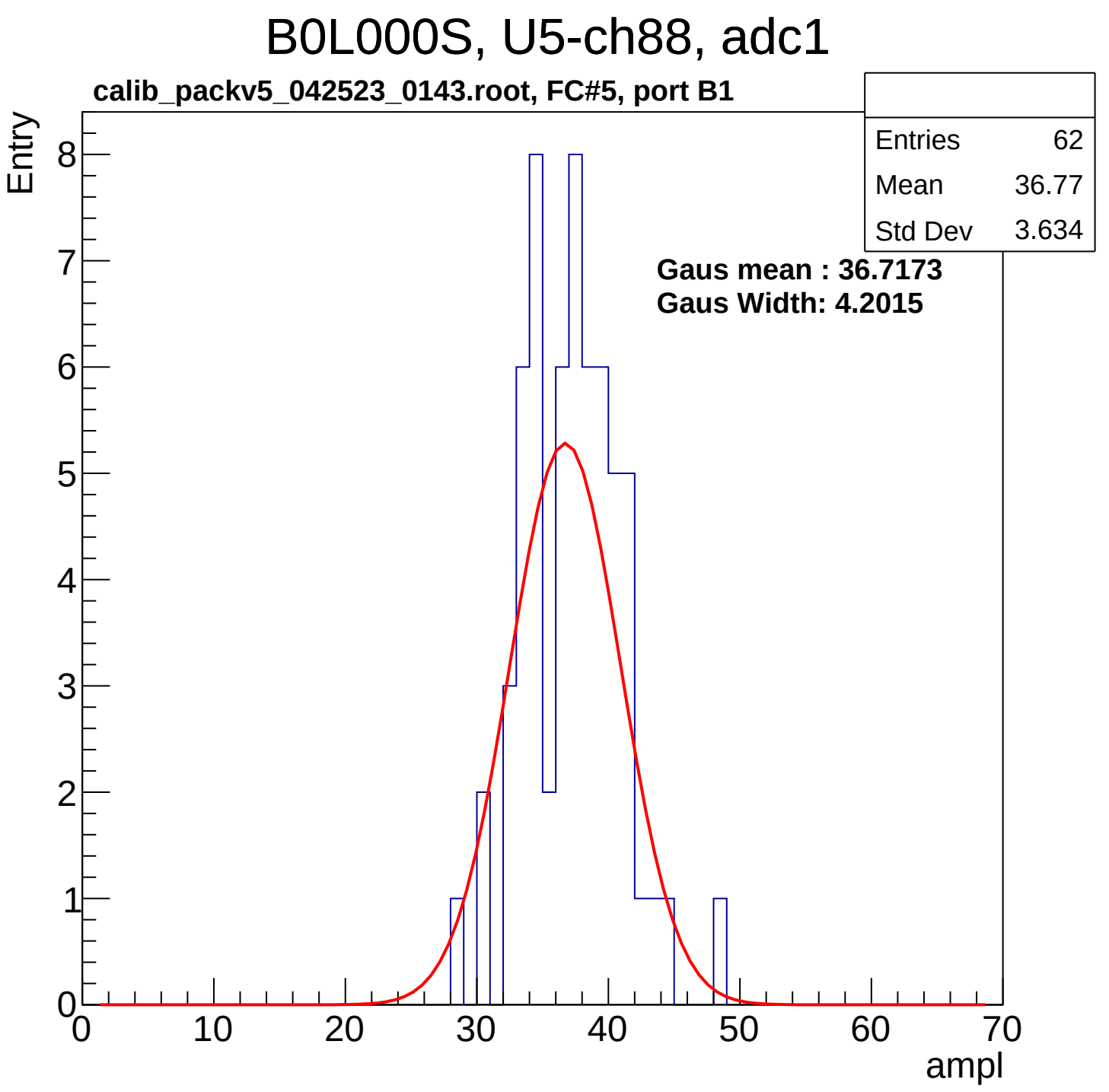
B0L000S, U5-ch88, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 36.77
Std Dev 3.634
Gaus mean : 36.7173
Gaus Width: 4.2015
Entry
ampl
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2
3
4
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7
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0
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60
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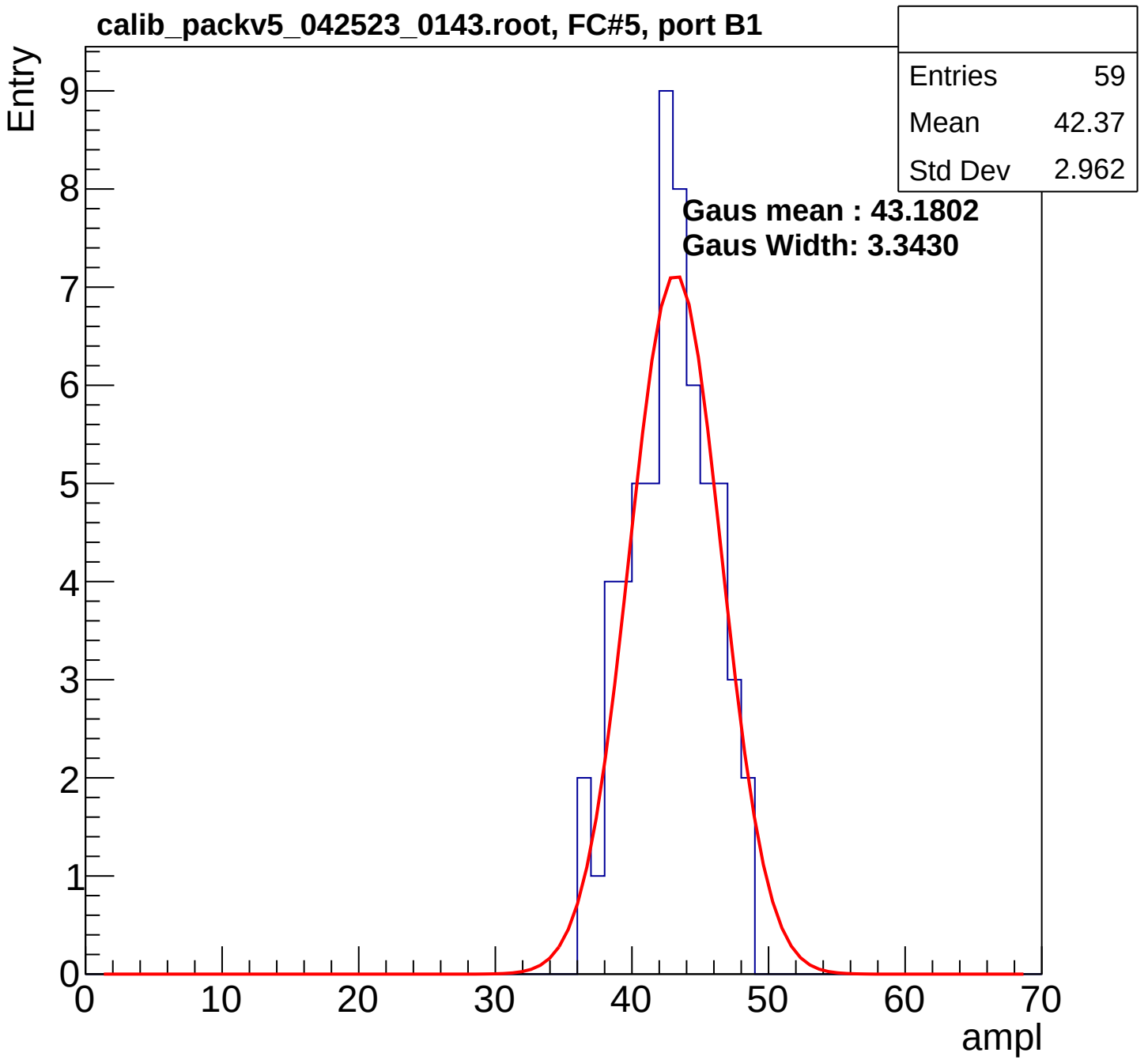
B0L000S, U5-ch88, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.37
Std Dev 2.962
Gaus mean : 43.1802
Gaus Width: 3.3430
Entry
ampl
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2
3
4
5
6
7
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0
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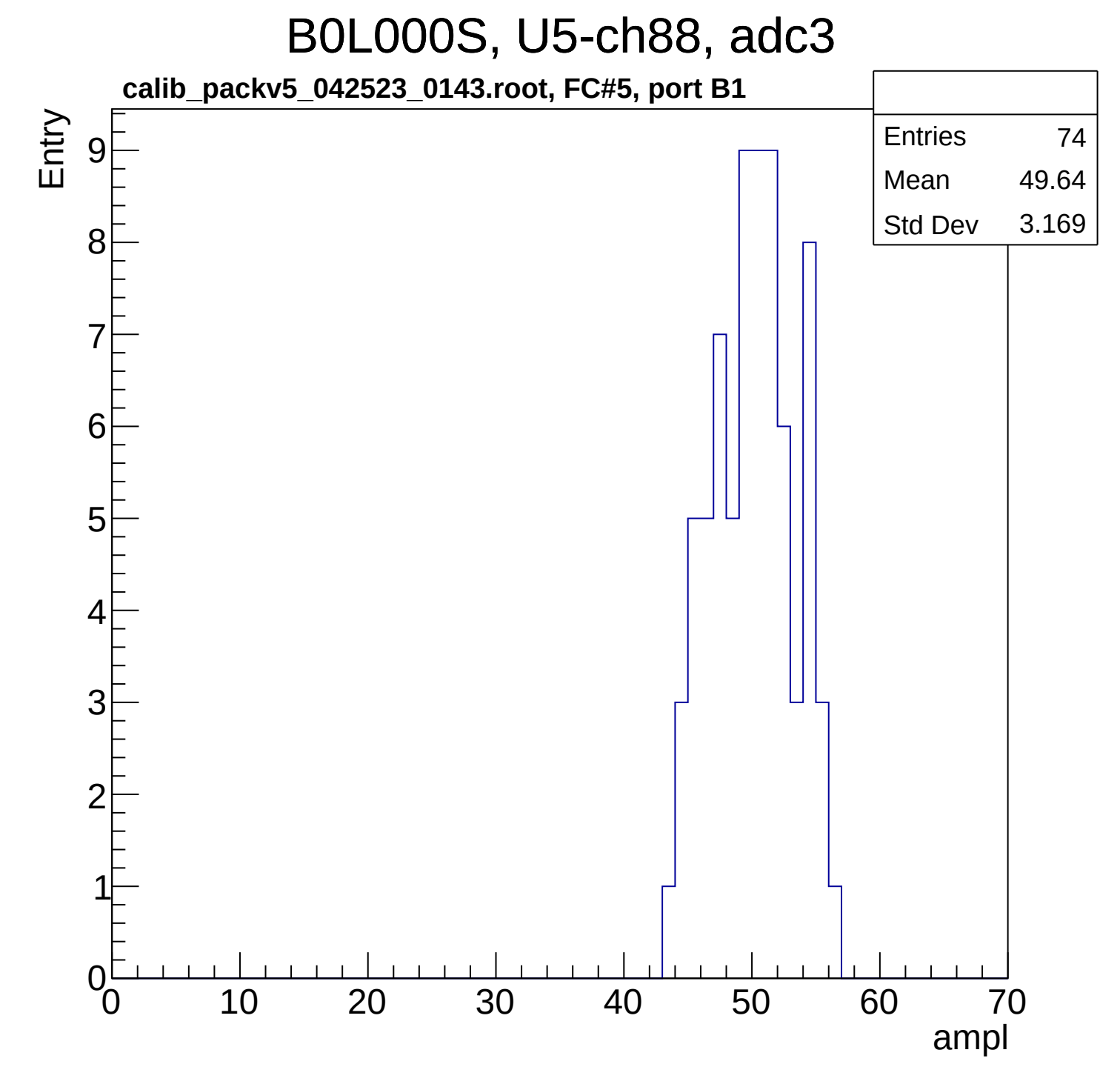
B0L000S, U5-ch88, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 49.64
Std Dev 3.169
Entry
ampl
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6
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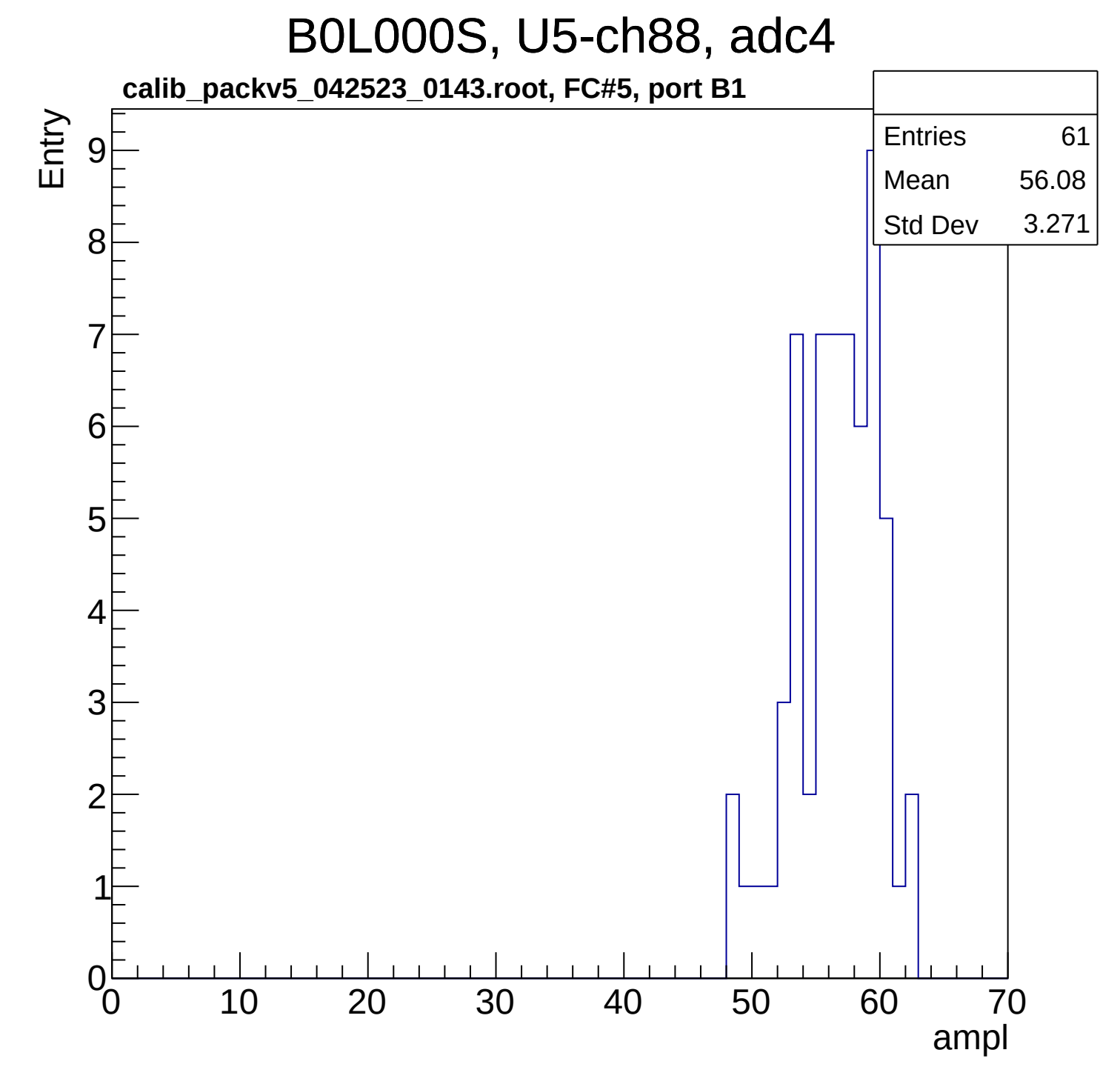
B0L000S, U5-ch88, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 56.08
Std Dev 3.271
Entry
ampl
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6
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0
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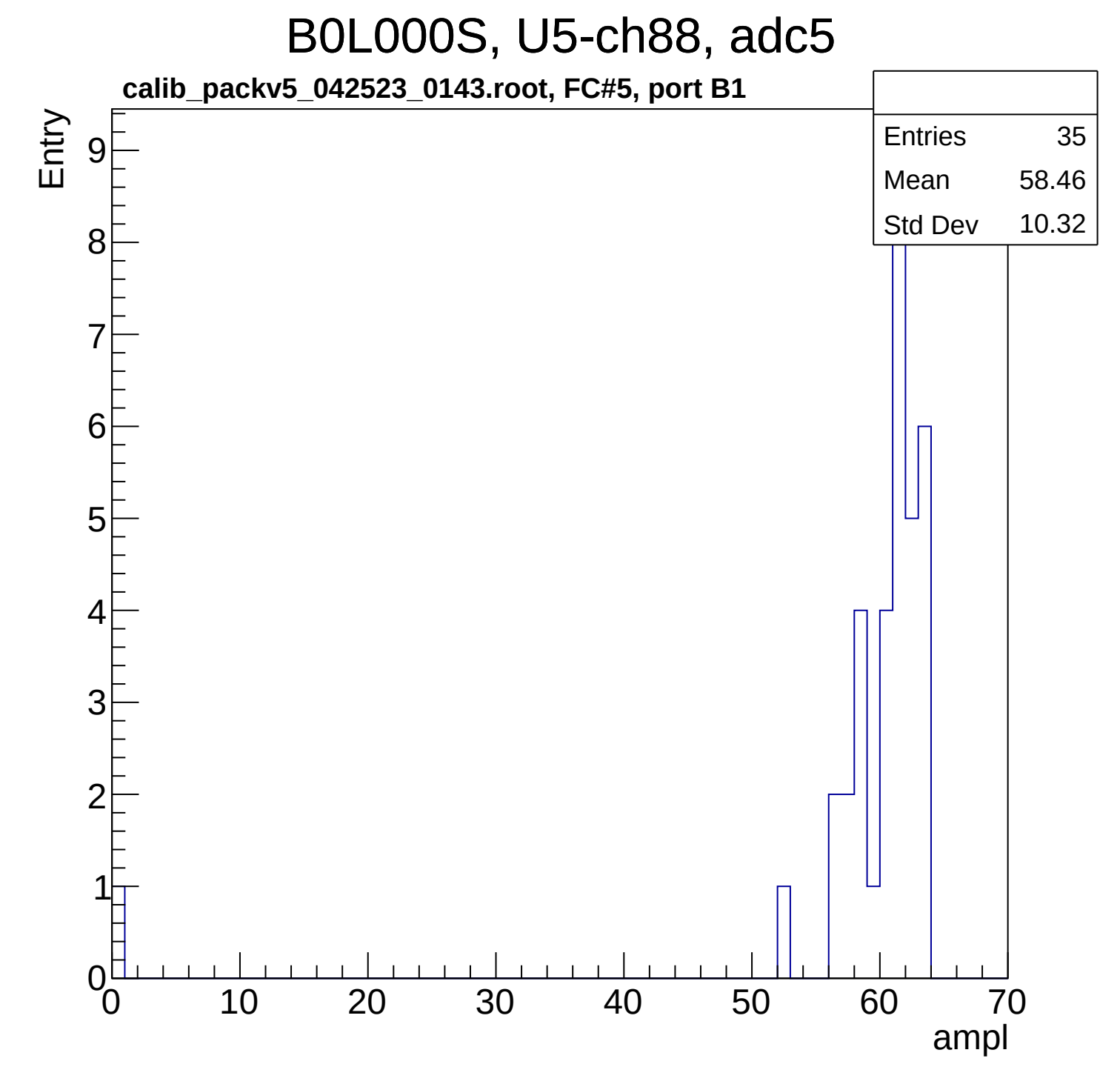
B0L000S, U5-ch88, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.46
Std Dev 10.32
Entry
ampl
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3
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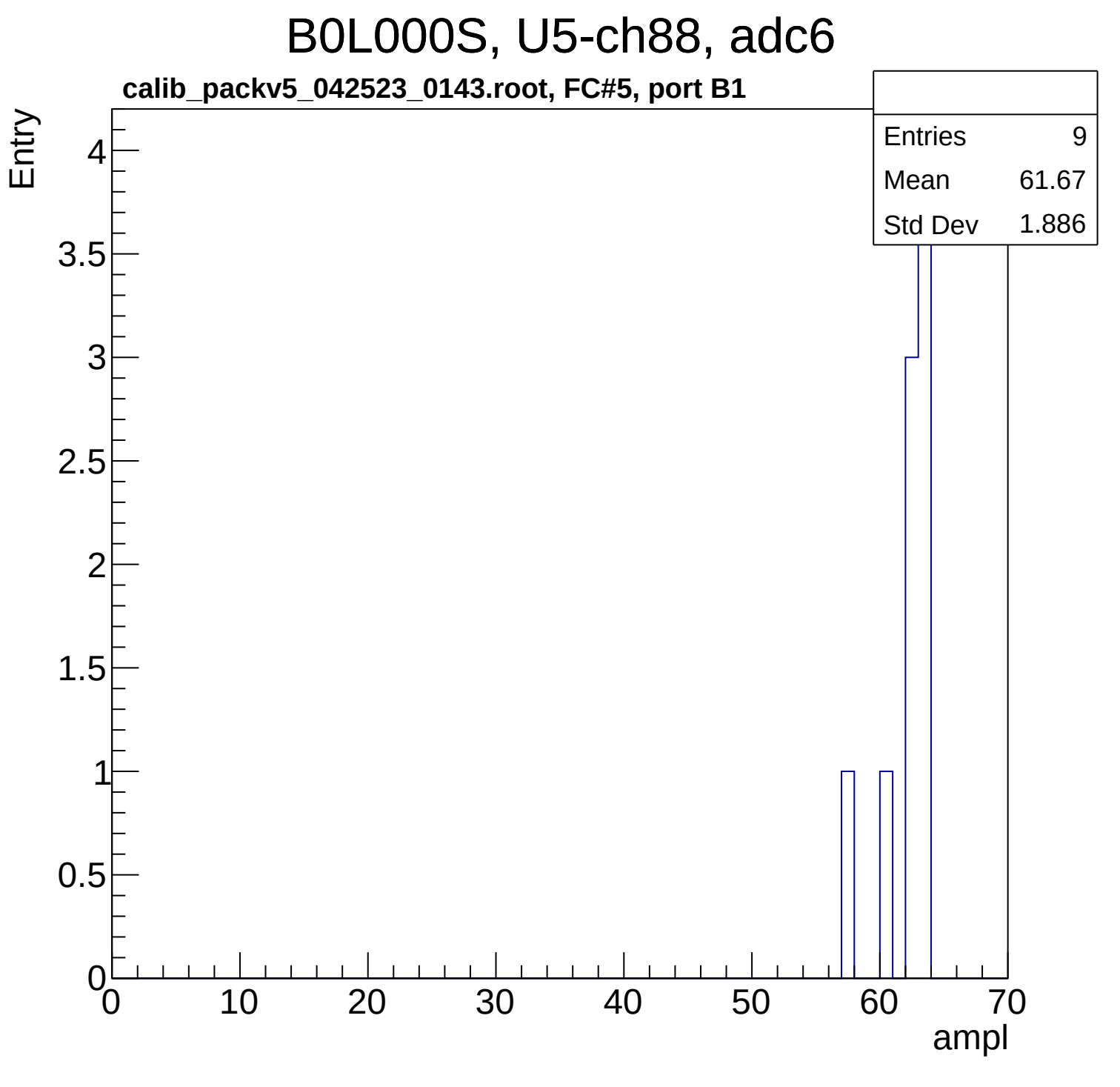
B0L000S, U5-ch88, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.67
Std Dev 1.886
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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60
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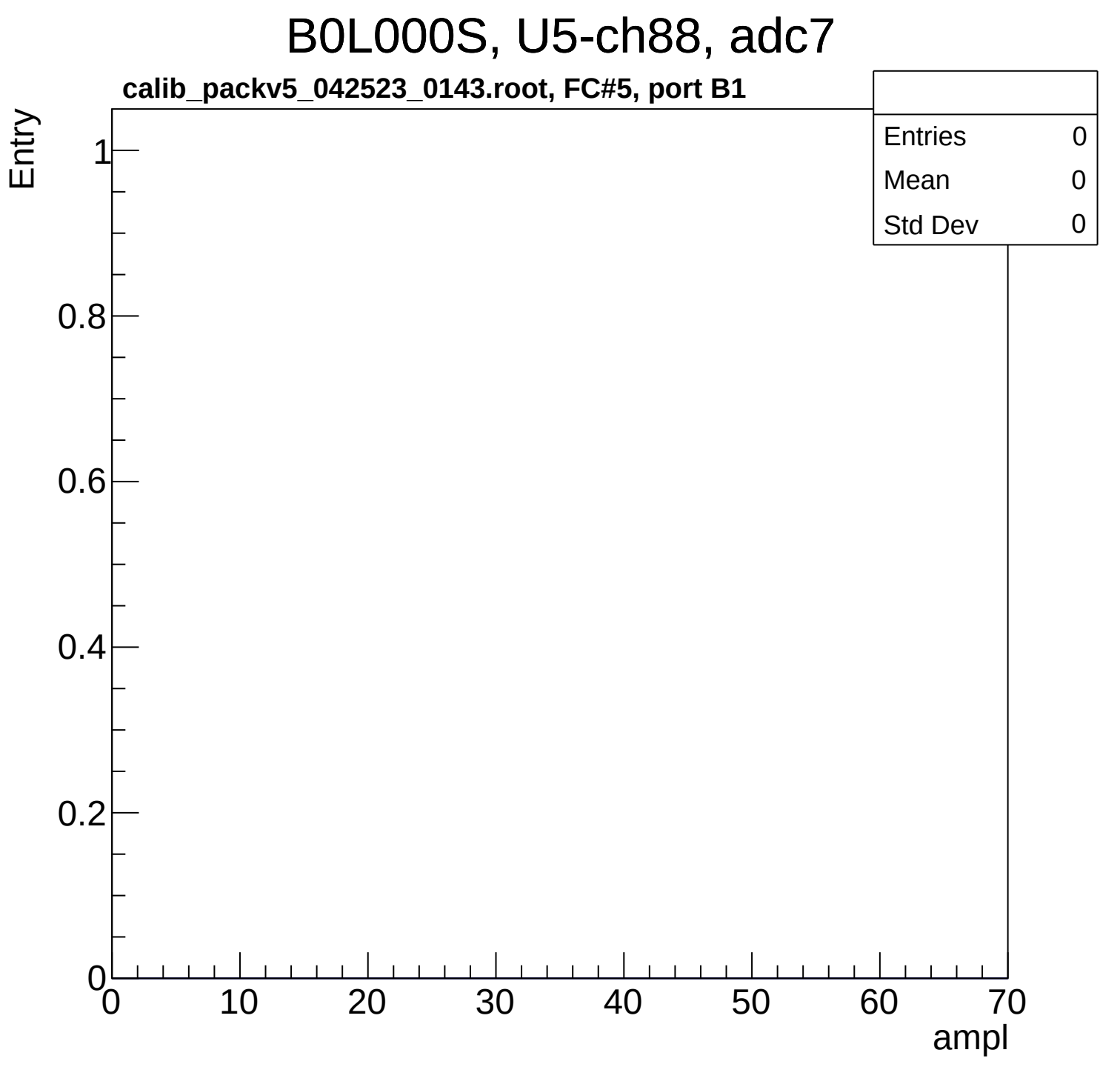
B0L000S, U5-ch88, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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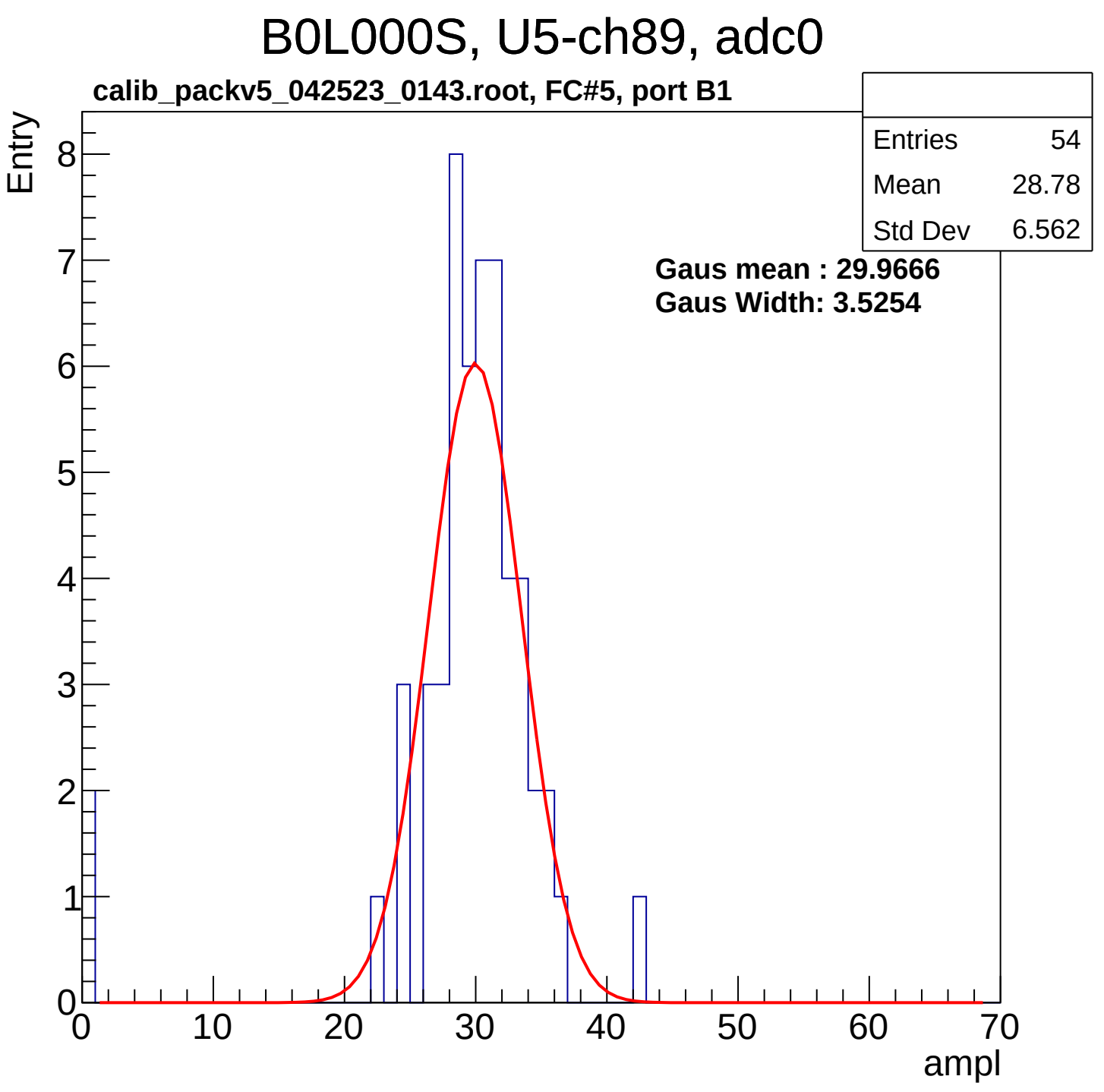
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch89, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 28.78
Std Dev 6.562
Gaus mean : 29.9666
Gaus Width: 3.5254
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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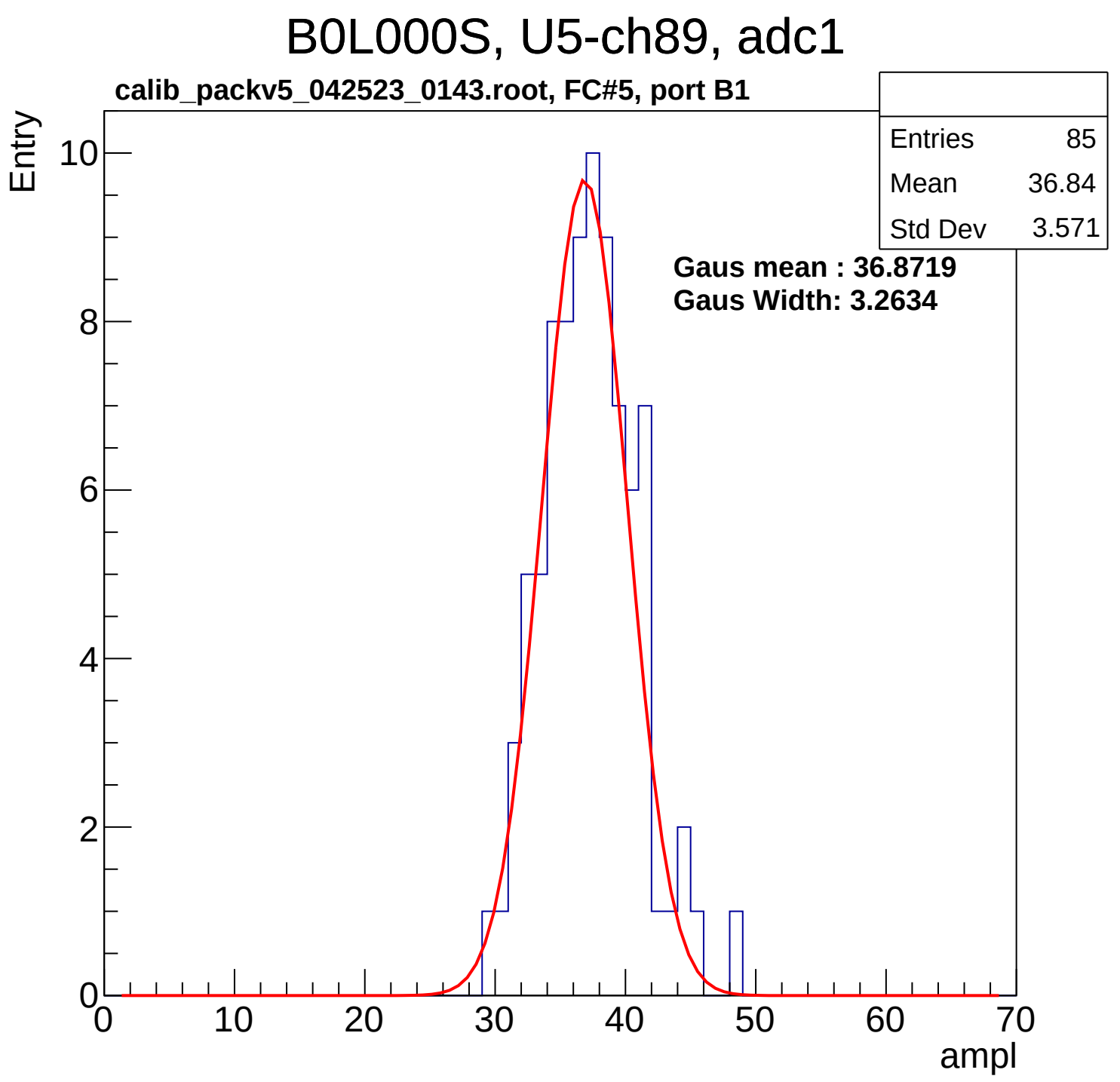
B0L000S, U5-ch89, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 36.84
Std Dev 3.571
Gaus mean : 36.8719
Gaus Width: 3.2634
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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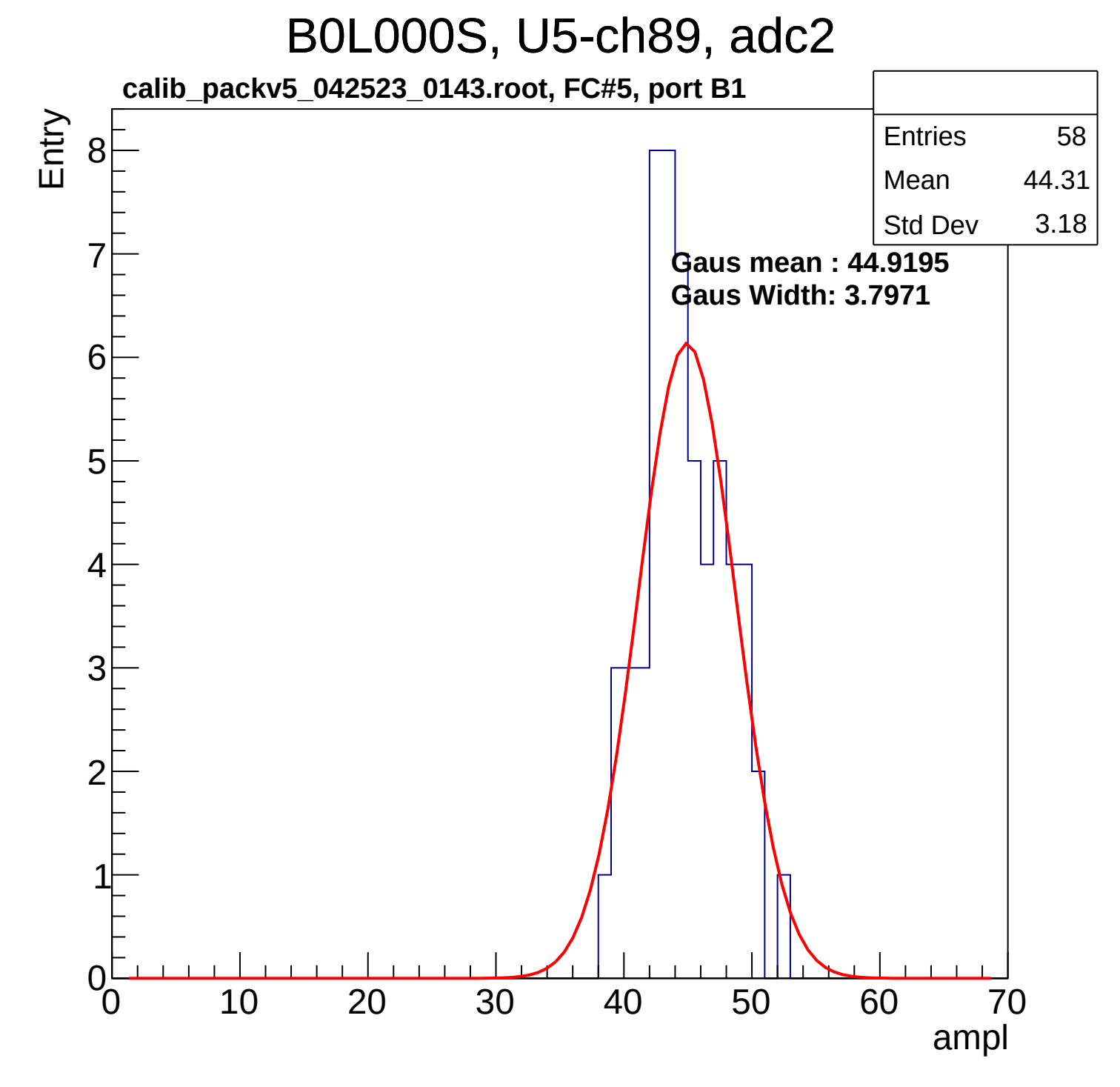
B0L000S, U5-ch89, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 44.31
Std Dev 3.18
Gaus mean : 44.9195
Gaus Width: 3.7971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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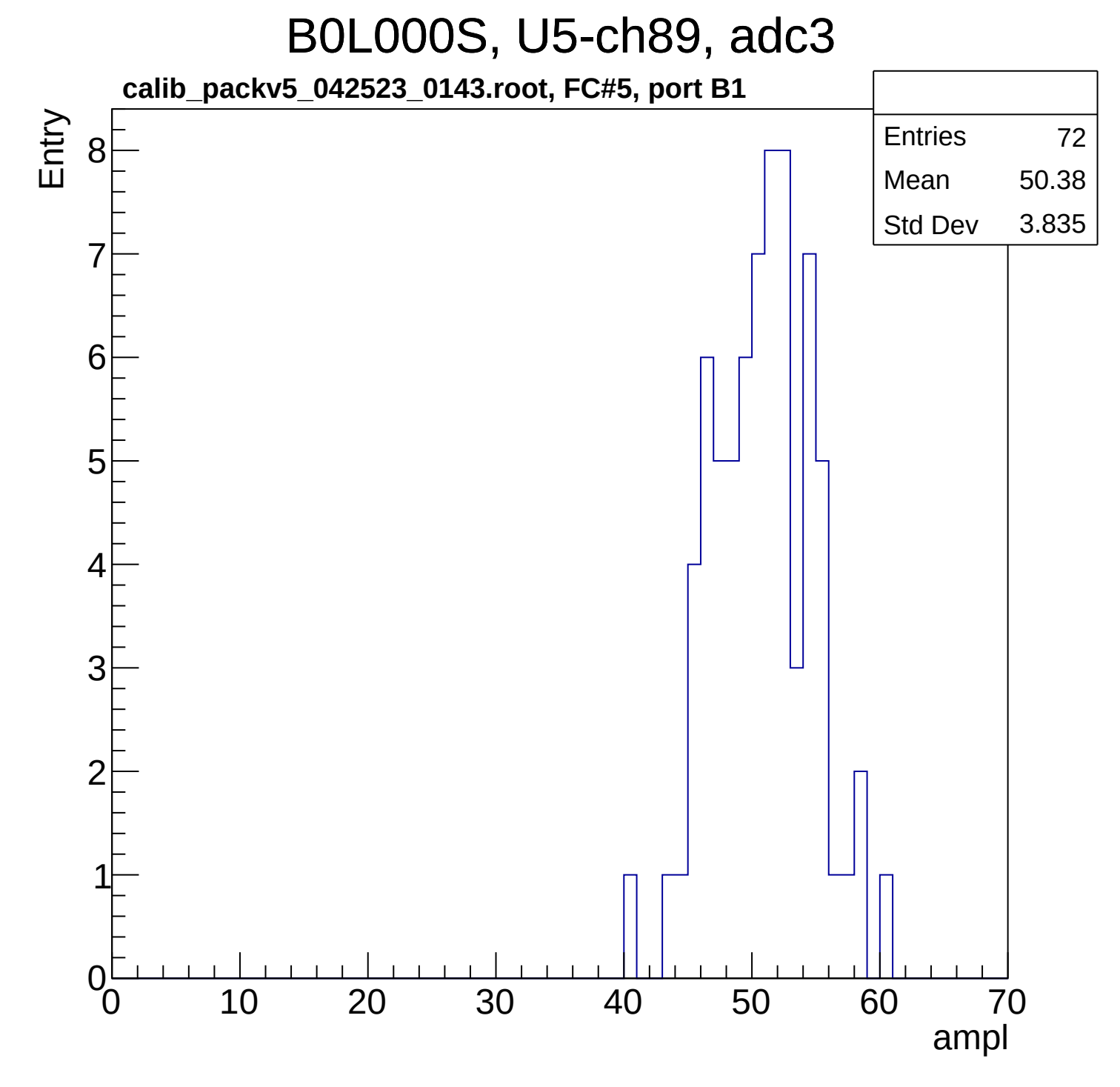
B0L000S, U5-ch89, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 50.38
Std Dev 3.835
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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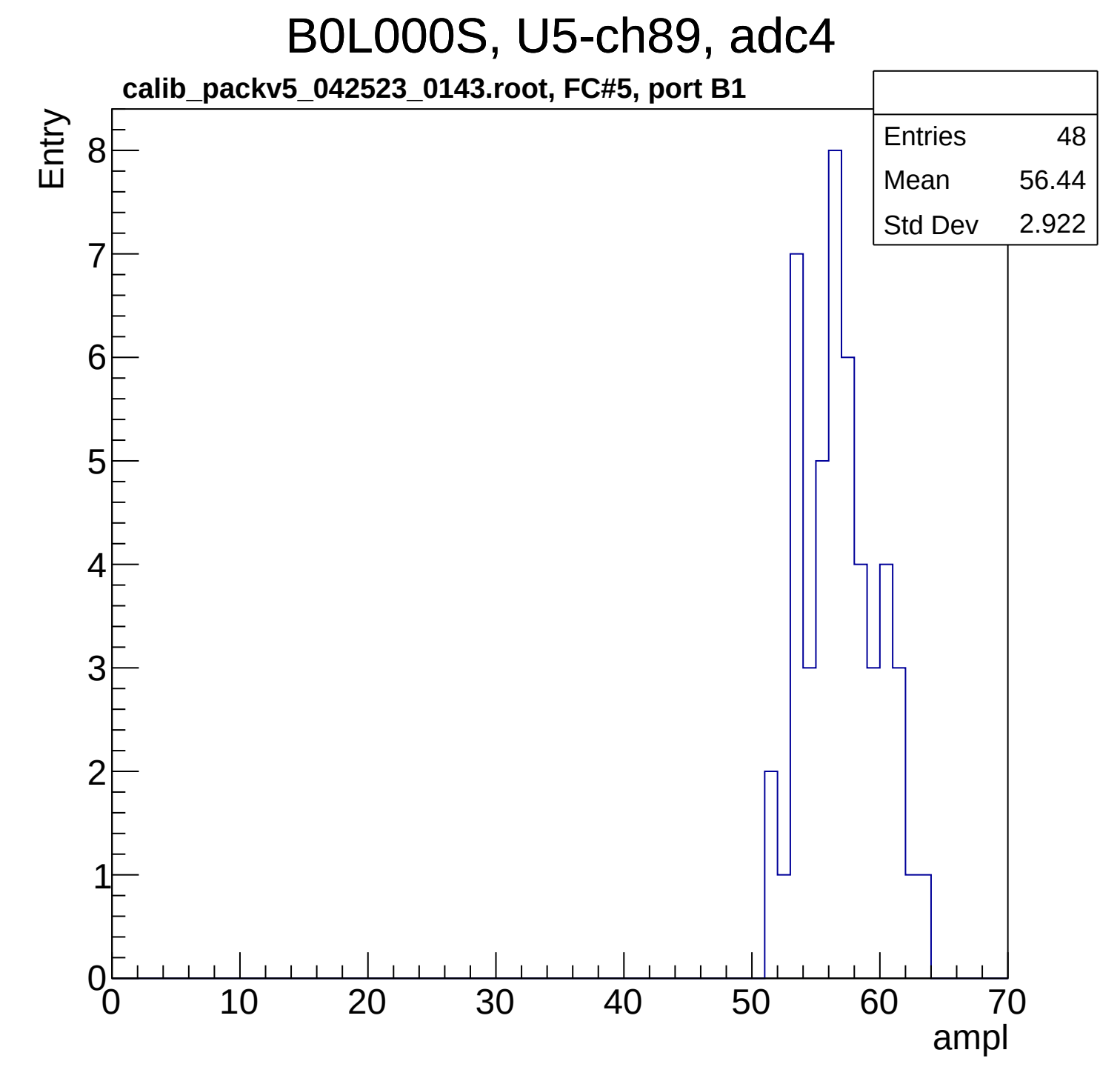
B0L000S, U5-ch89, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.44
Std Dev 2.922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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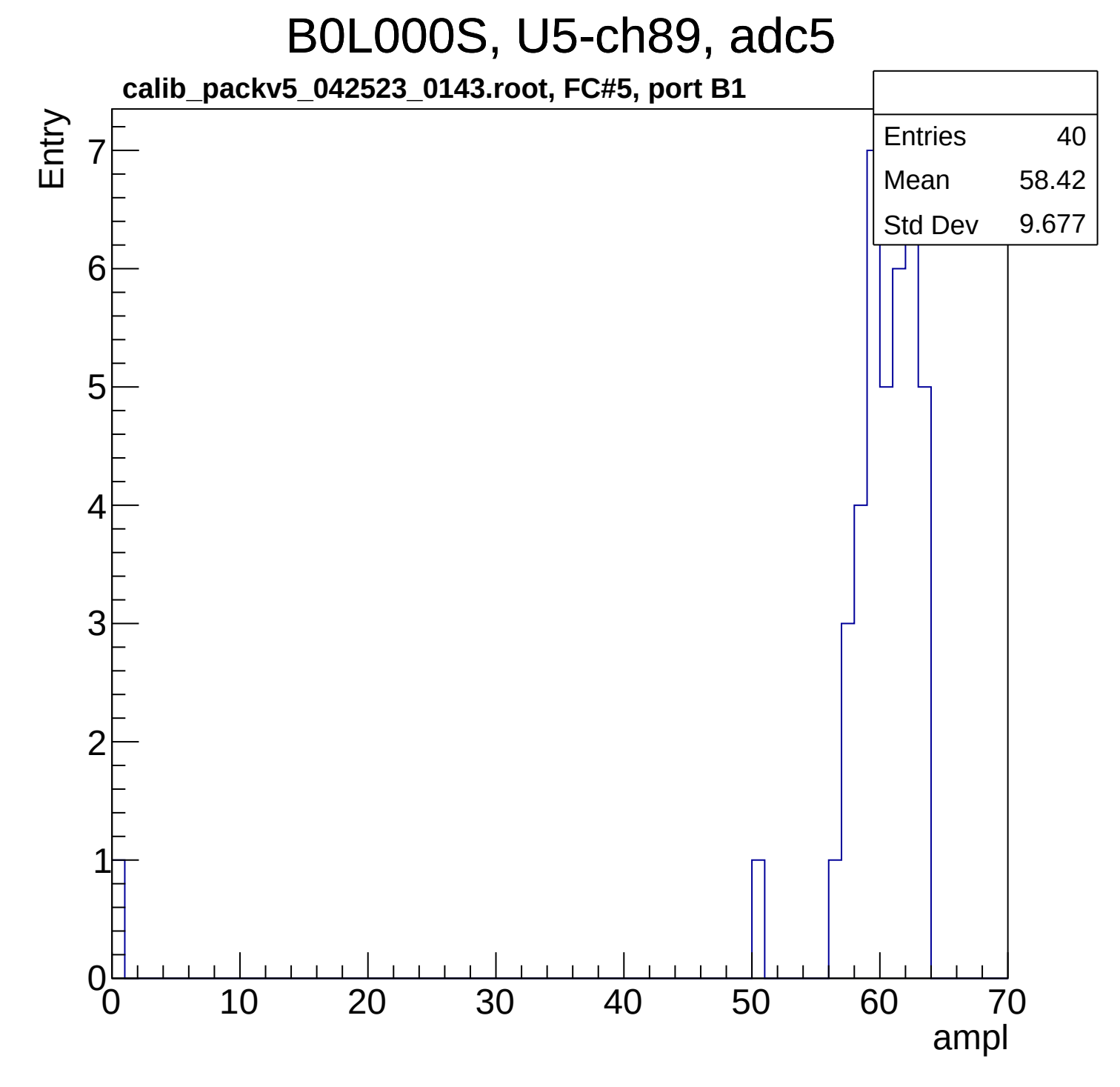
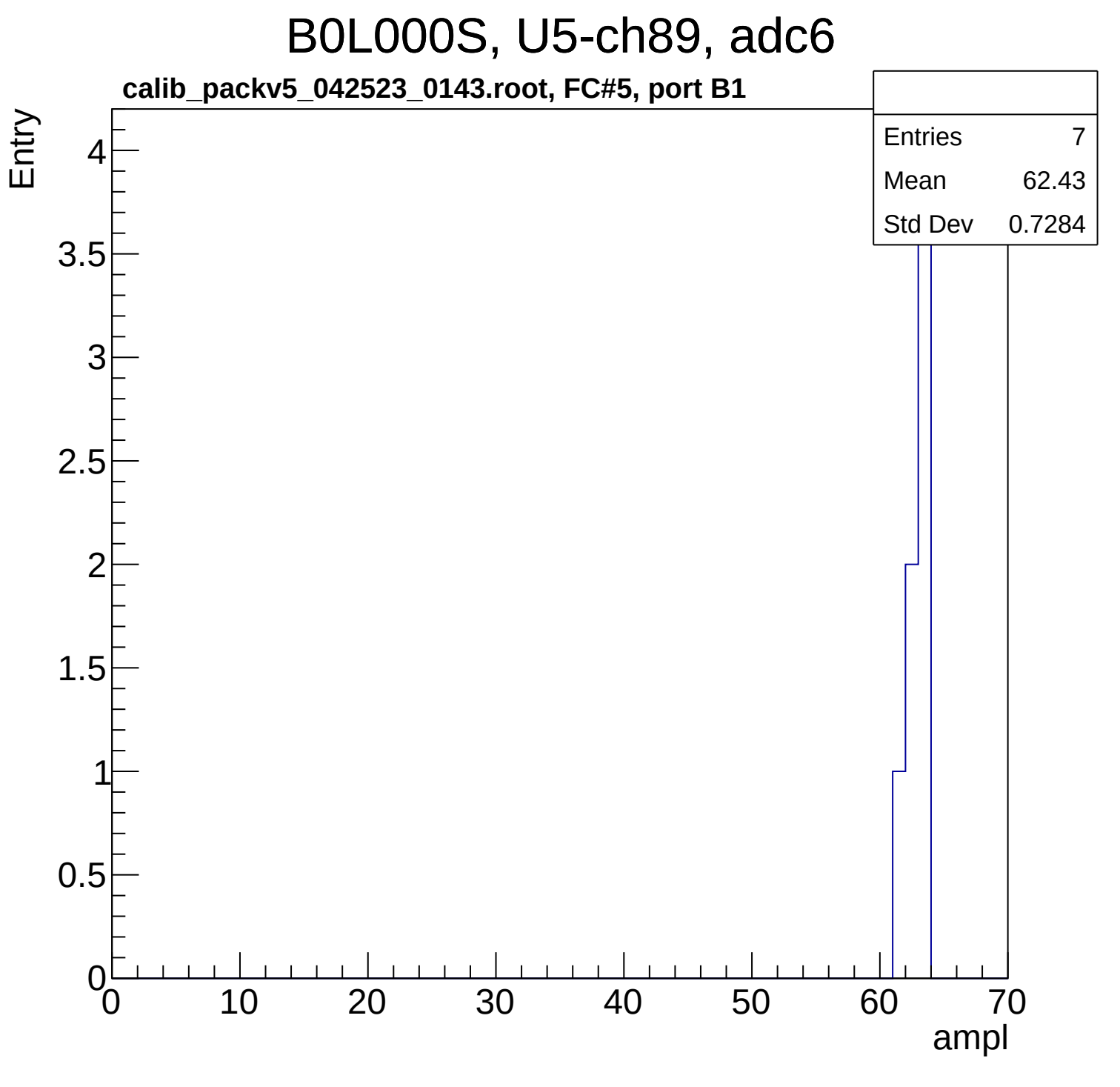

B0L000S, U5-ch89, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.42
Std Dev 9.677
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch89, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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60
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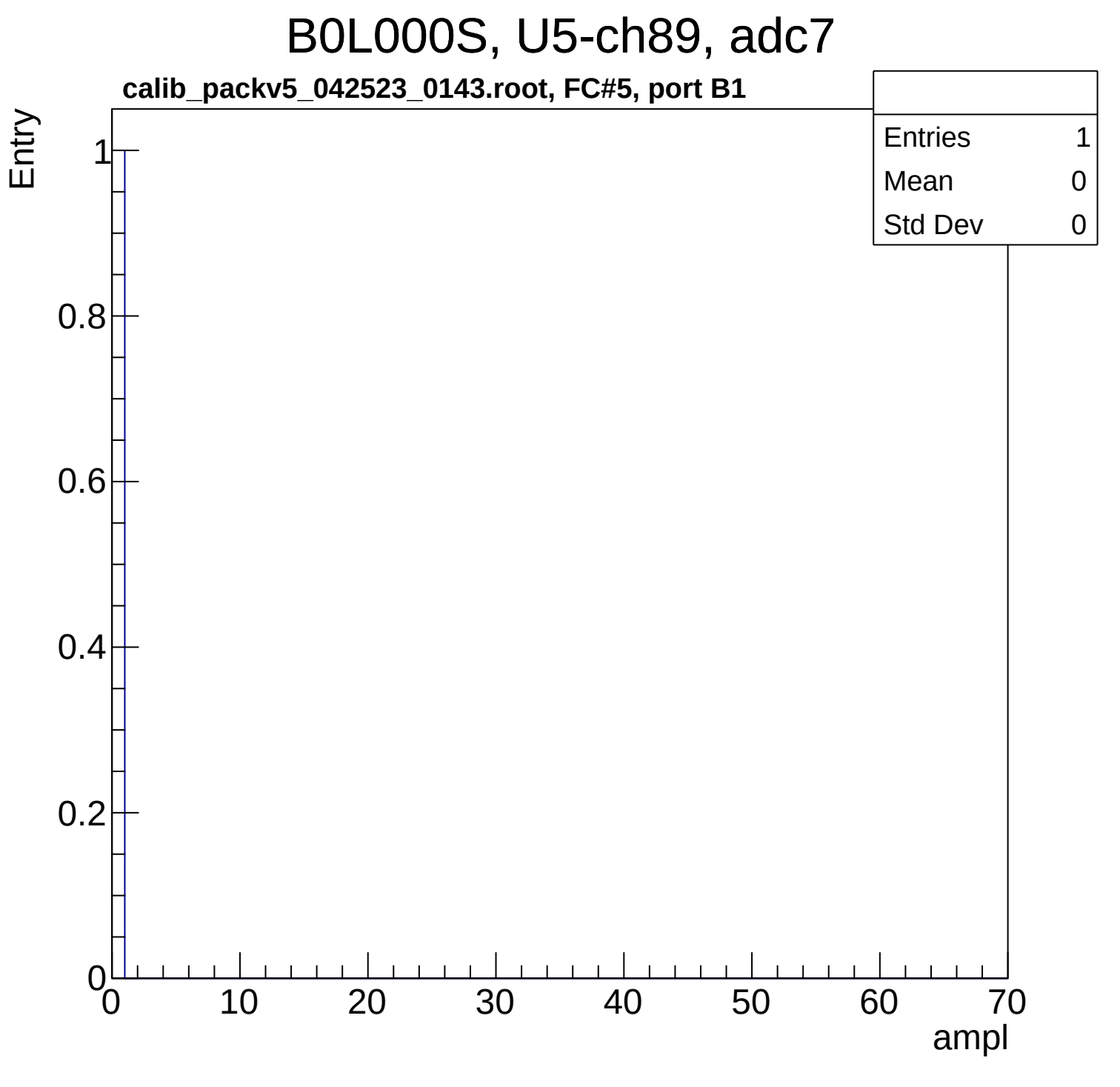
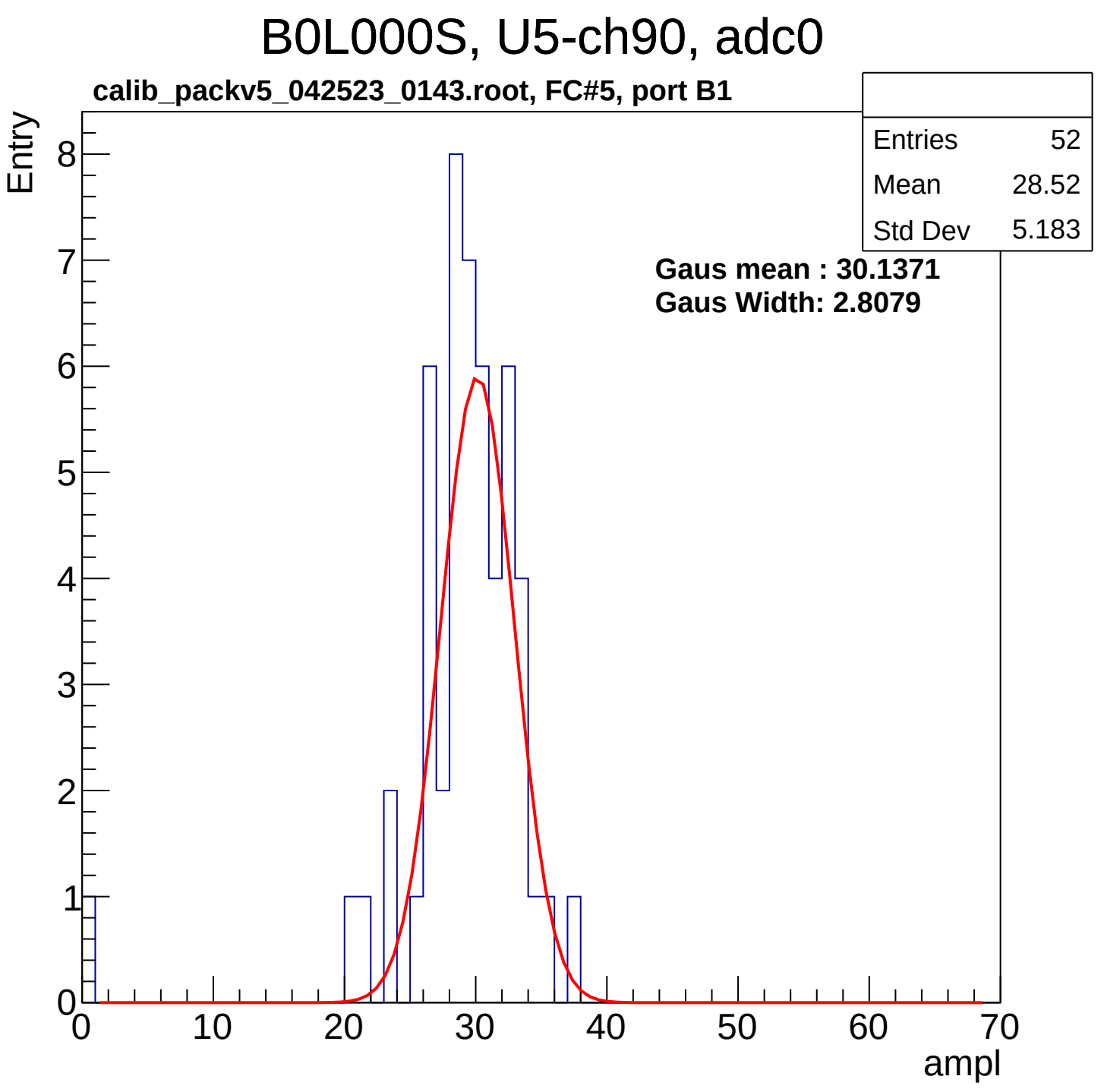
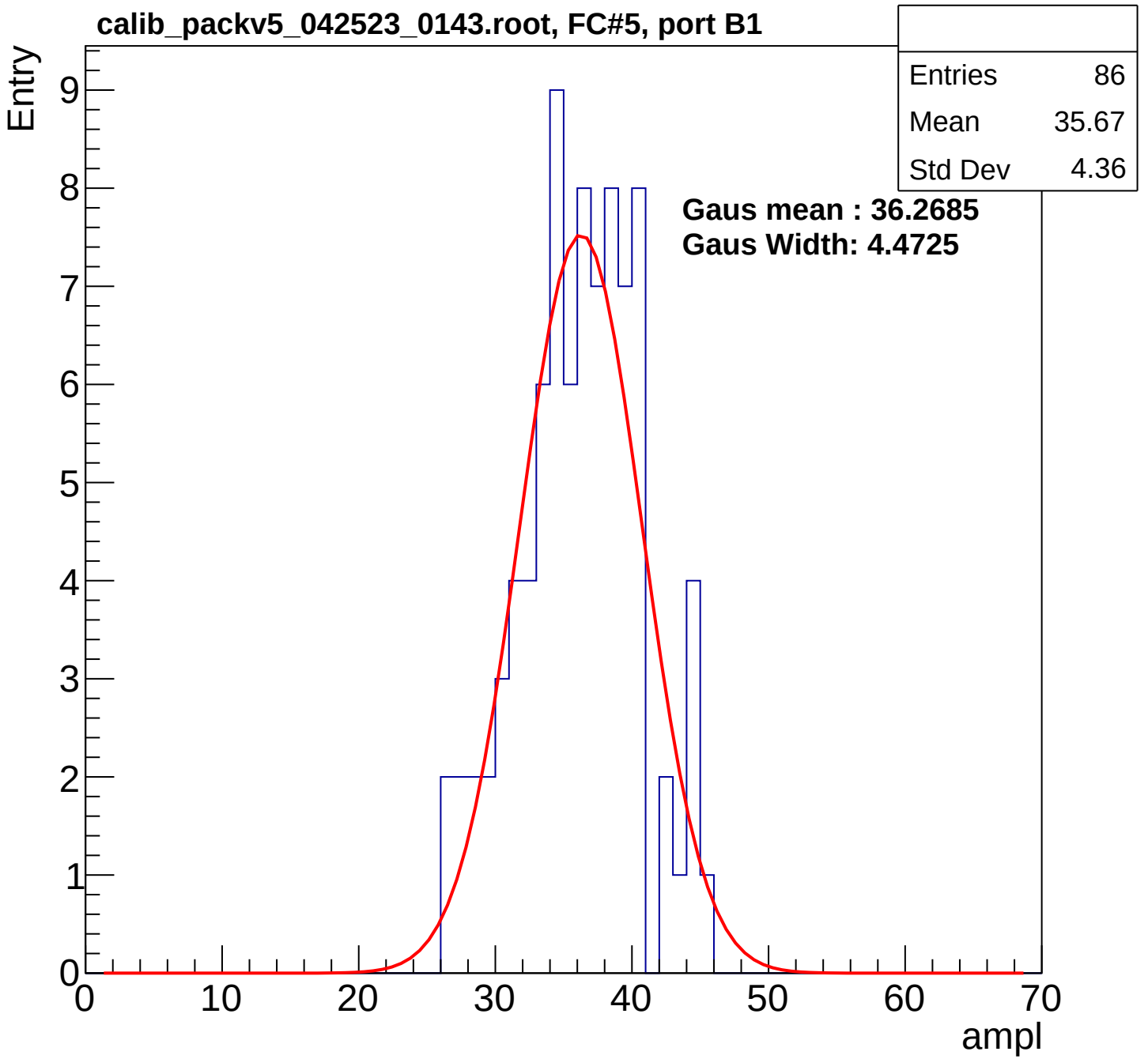

B0L000S, U5-ch89, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch90, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 28.52
Std Dev 5.183
Gaus mean : 30.1371
Gaus Width: 2.8079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch90, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 86
Mean 35.67
Std Dev 4.36
Gaus mean : 36.2685
Gaus Width: 4.4725
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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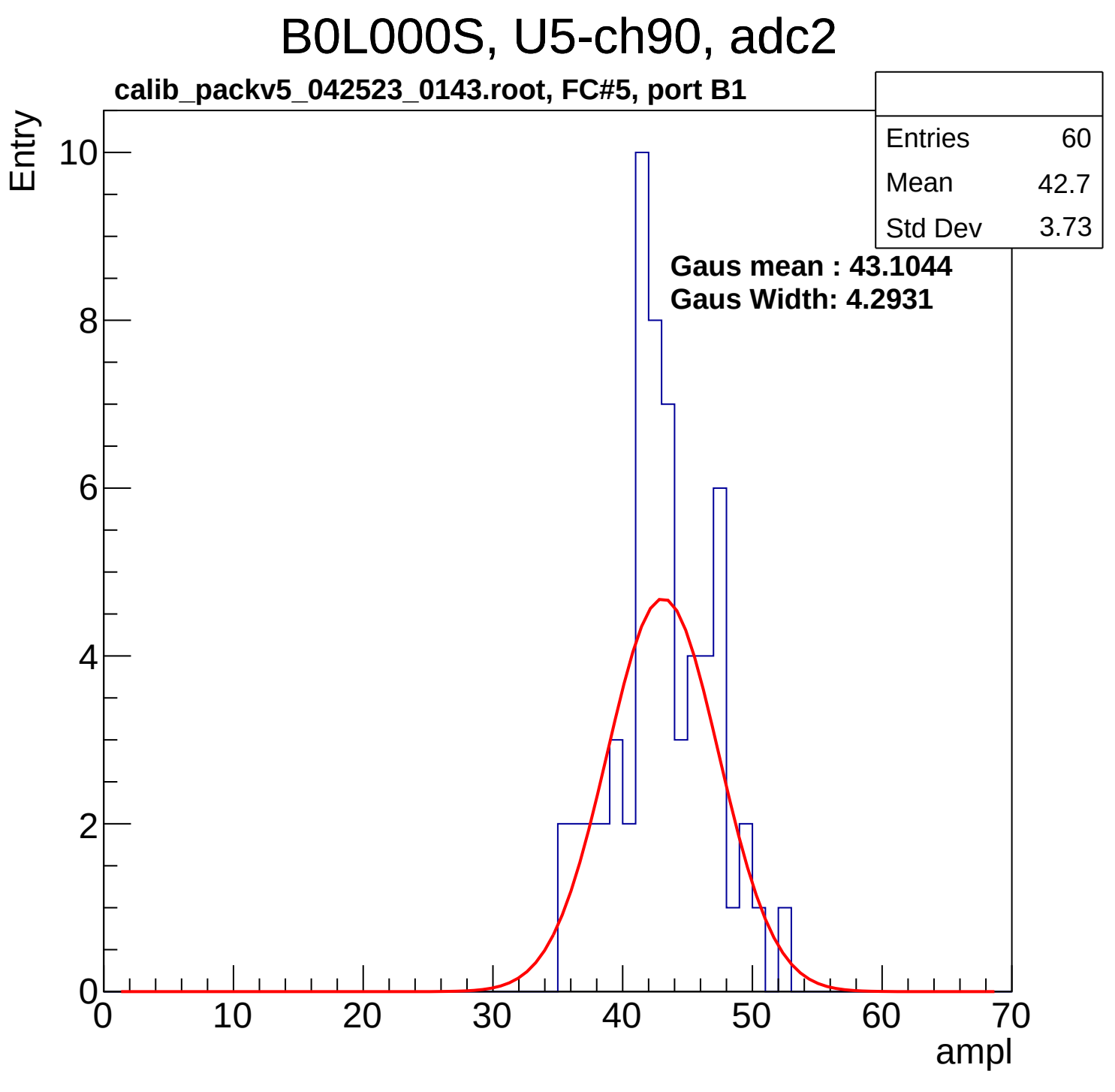
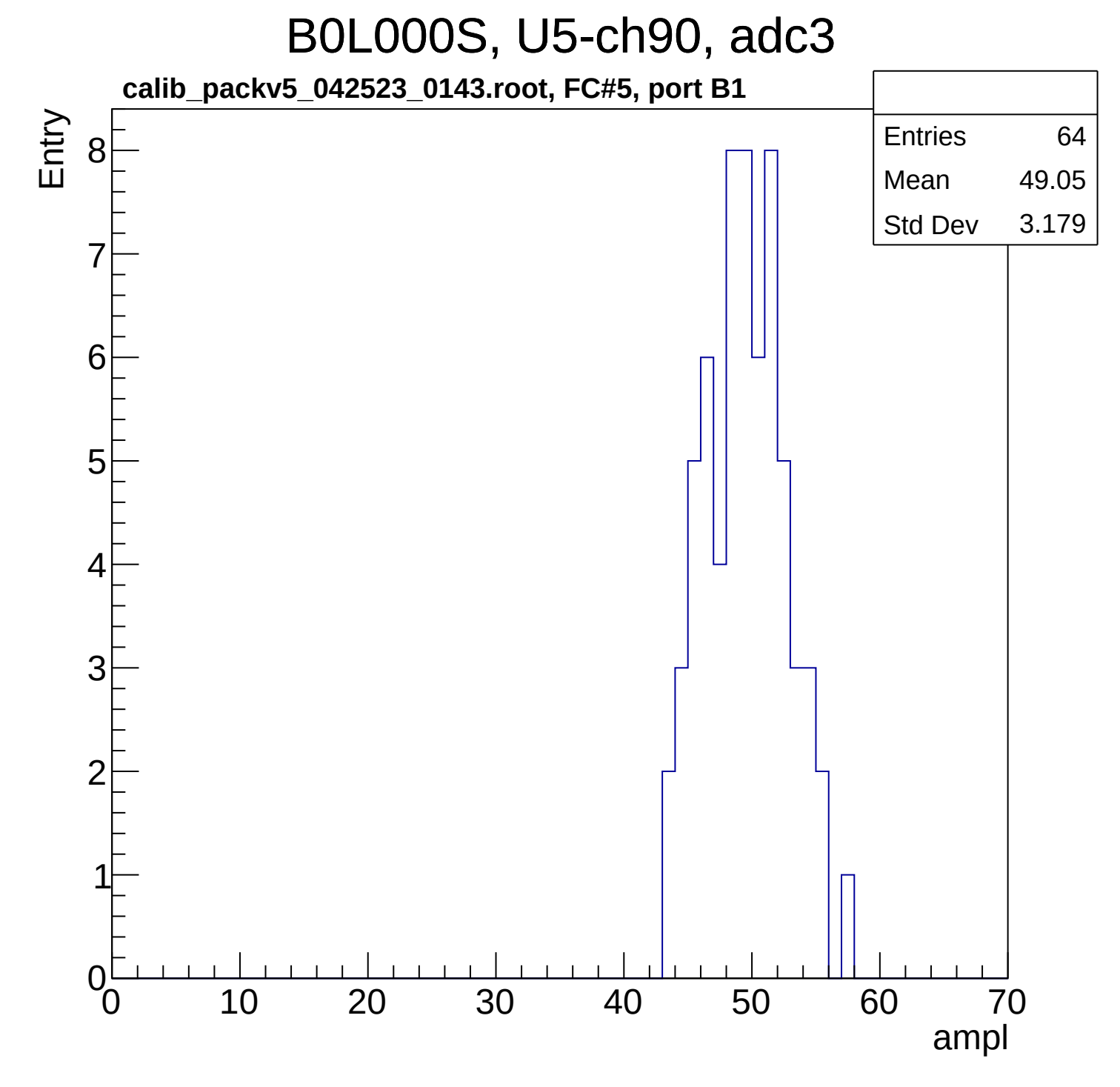

B0L000S, U5-ch90, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 42.7
Std Dev 3.73
Gaus mean : 43.1044
Gaus Width: 4.2931
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch90, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.05
Std Dev 3.179
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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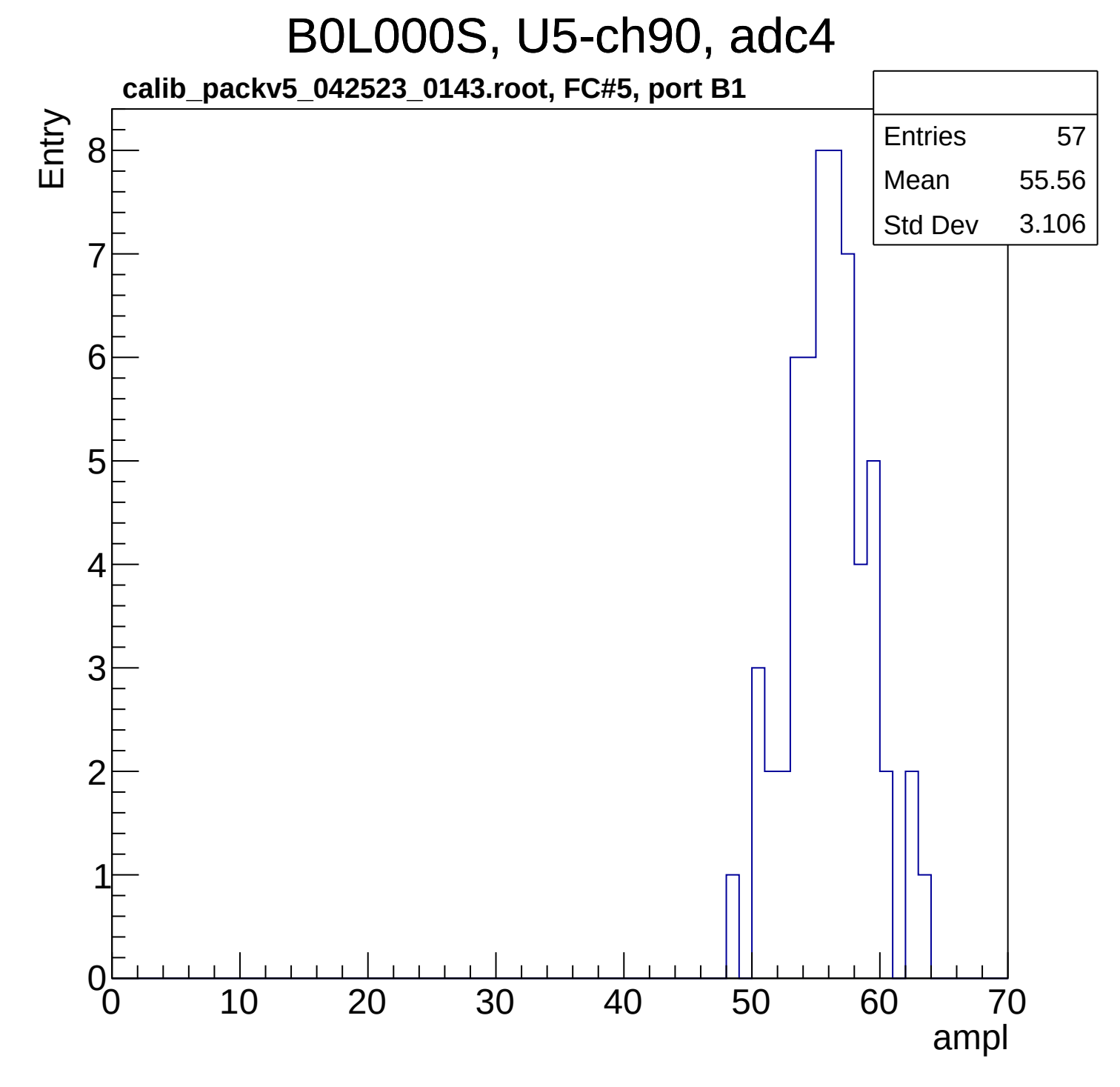
B0L000S, U5-ch90, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 55.56
Std Dev 3.106
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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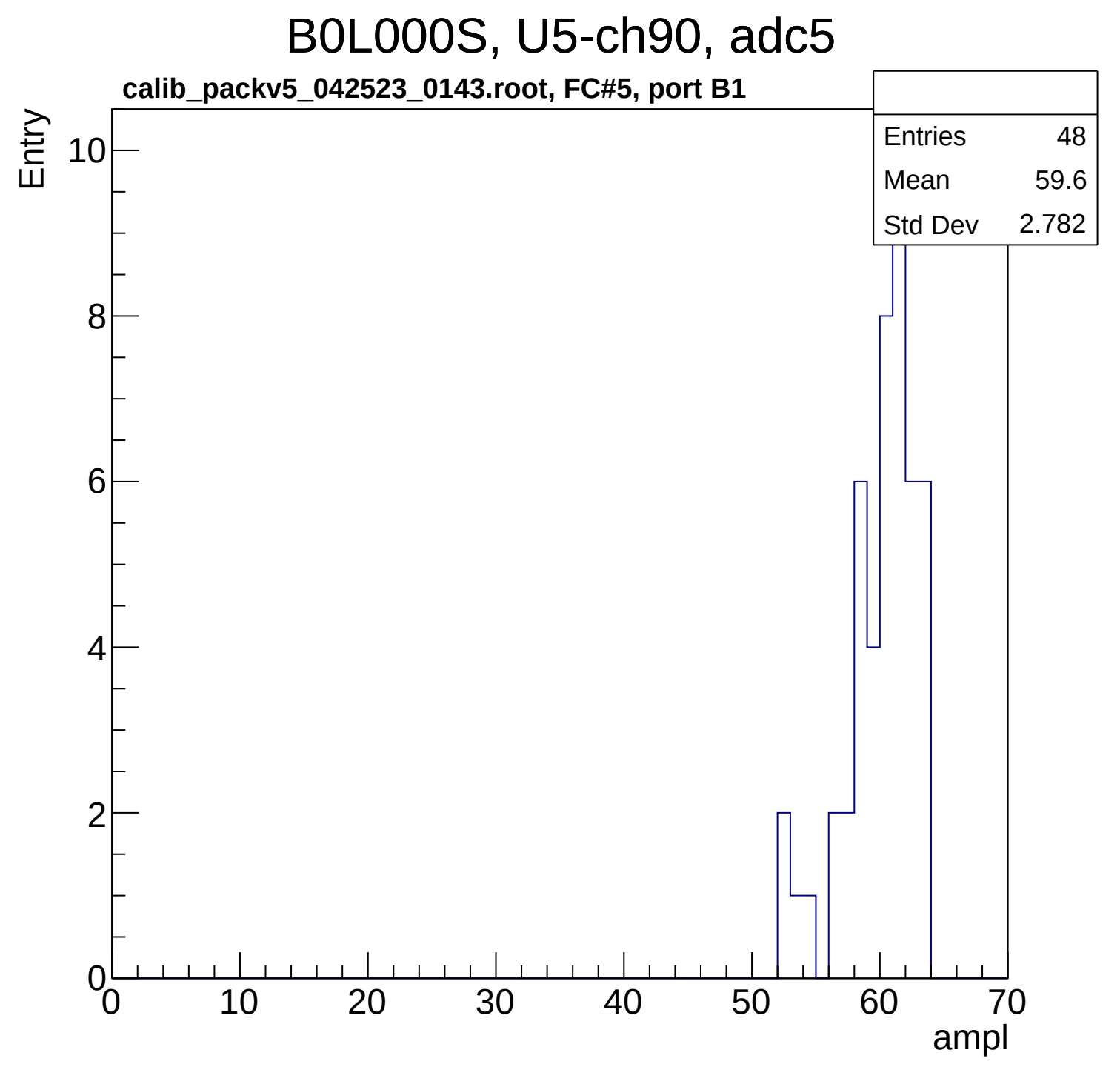
B0L000S, U5-ch90, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 59.6
Std Dev 2.782
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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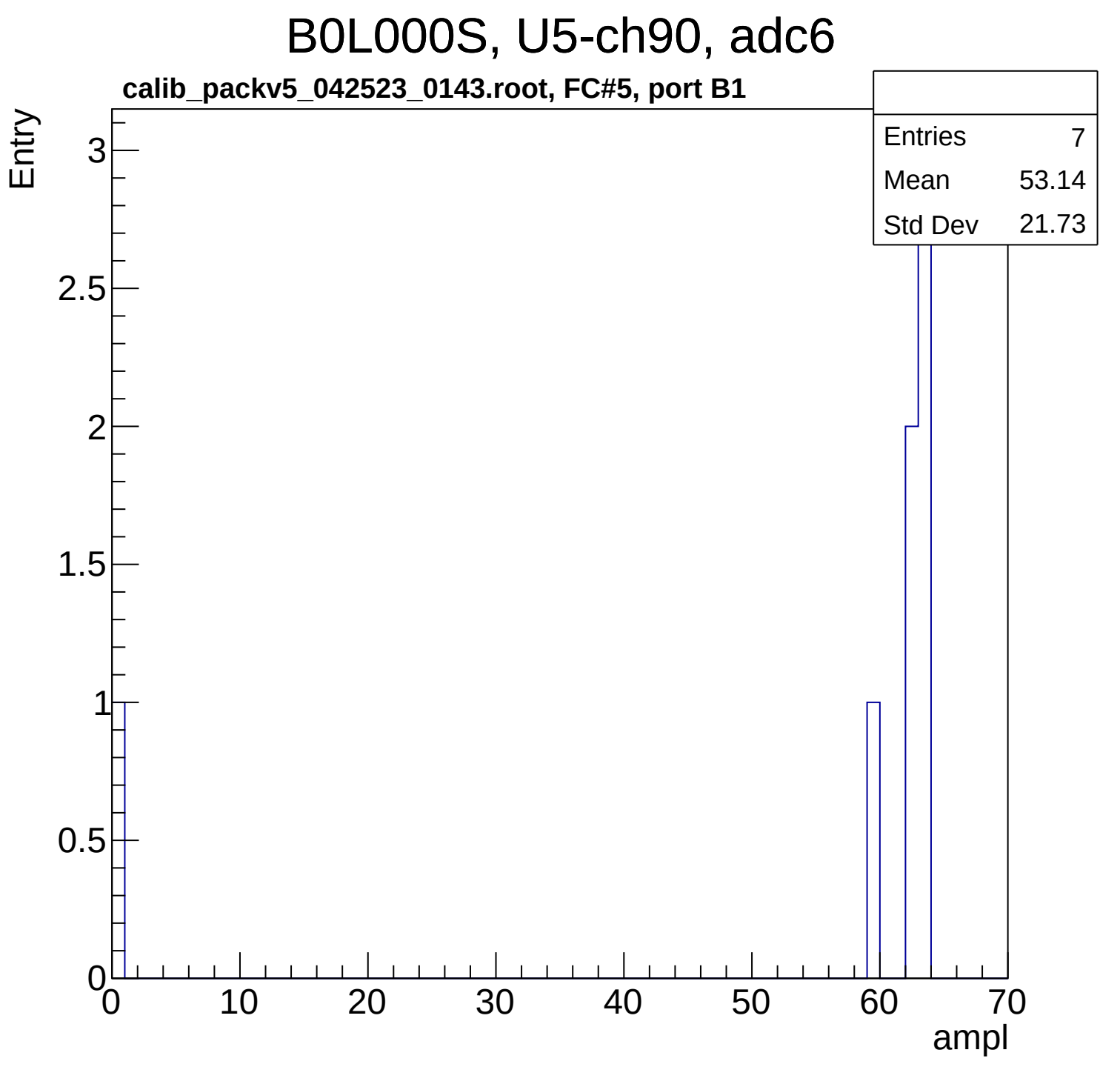
B0L000S, U5-ch90, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 53.14
Std Dev 21.73
Entry
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1
1.5
2
2.5
3
0
10
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30
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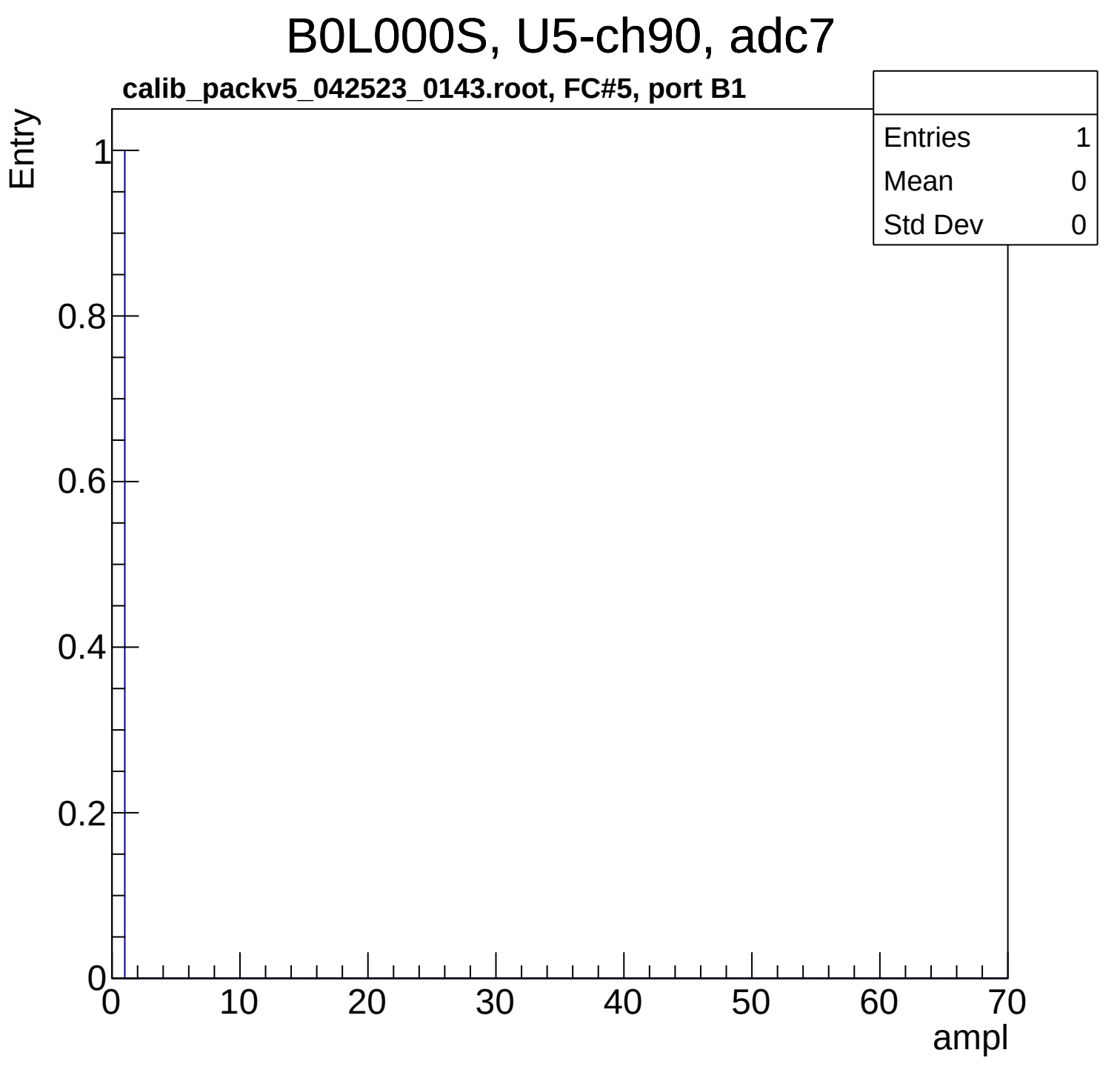
50
60
70
ampl


B0L000S, U5-ch90, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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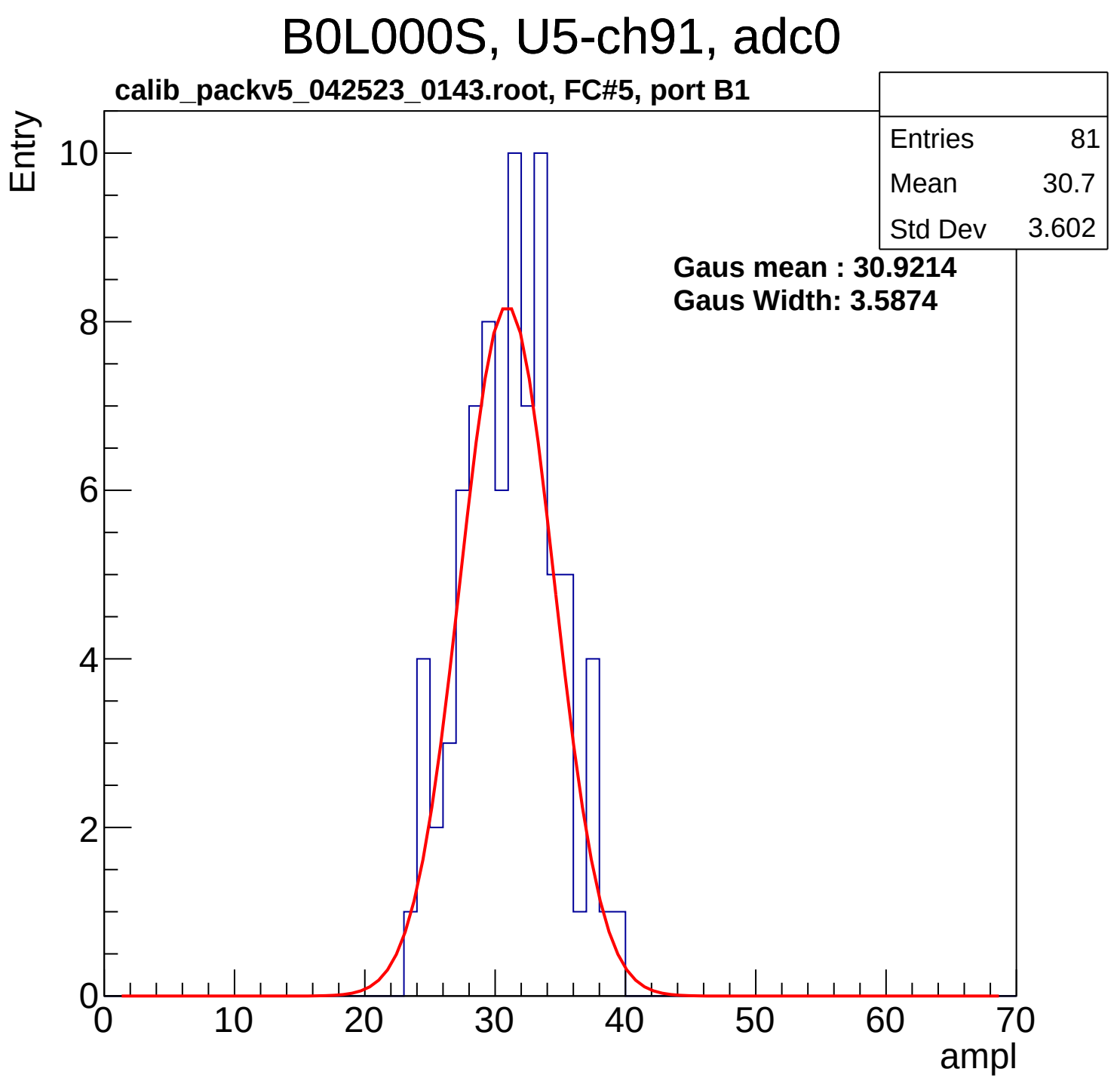
50
60
70
ampl


B0L000S, U5-ch91, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 30.7
Std Dev 3.602
Gaus mean : 30.9214
Gaus Width: 3.5874
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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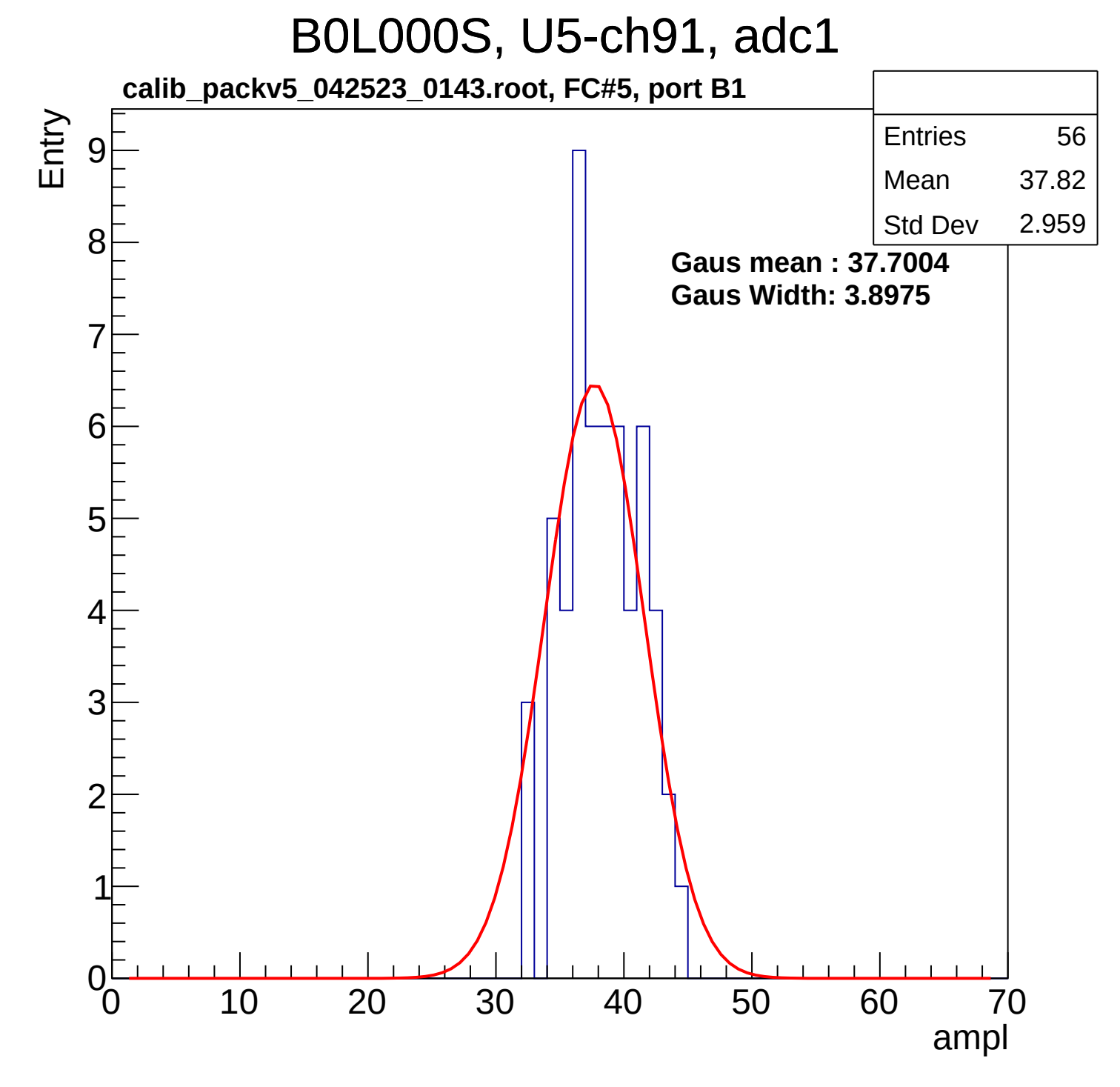
B0L000S, U5-ch91, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.82
Std Dev 2.959
Gaus mean : 37.7004
Gaus Width: 3.8975
Entry
ampl
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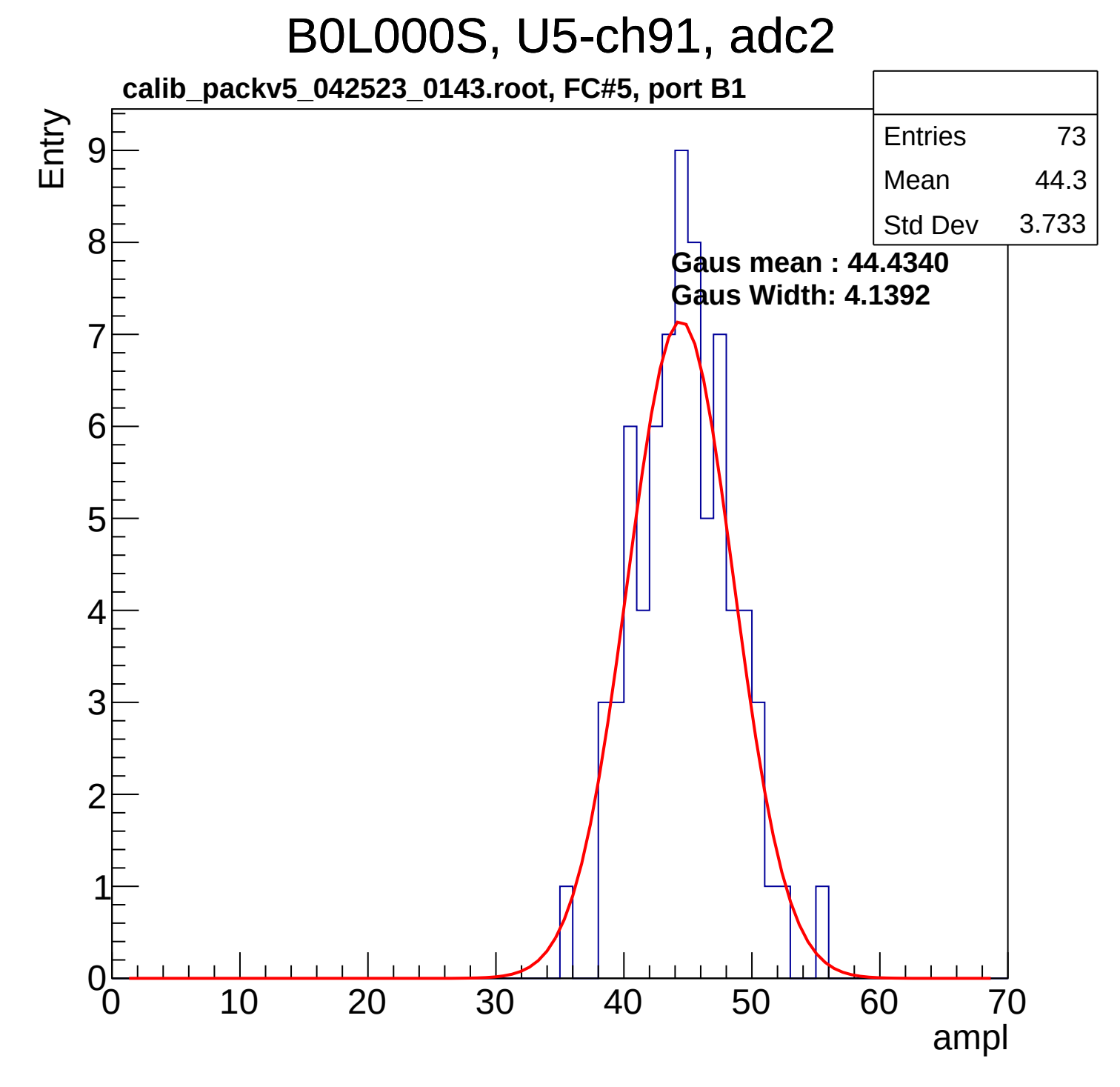
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L000S, U5-ch91, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.3
Std Dev 3.733
Gaus mean : 44.4340
Gaus Width: 4.1392
Entry
ampl
0
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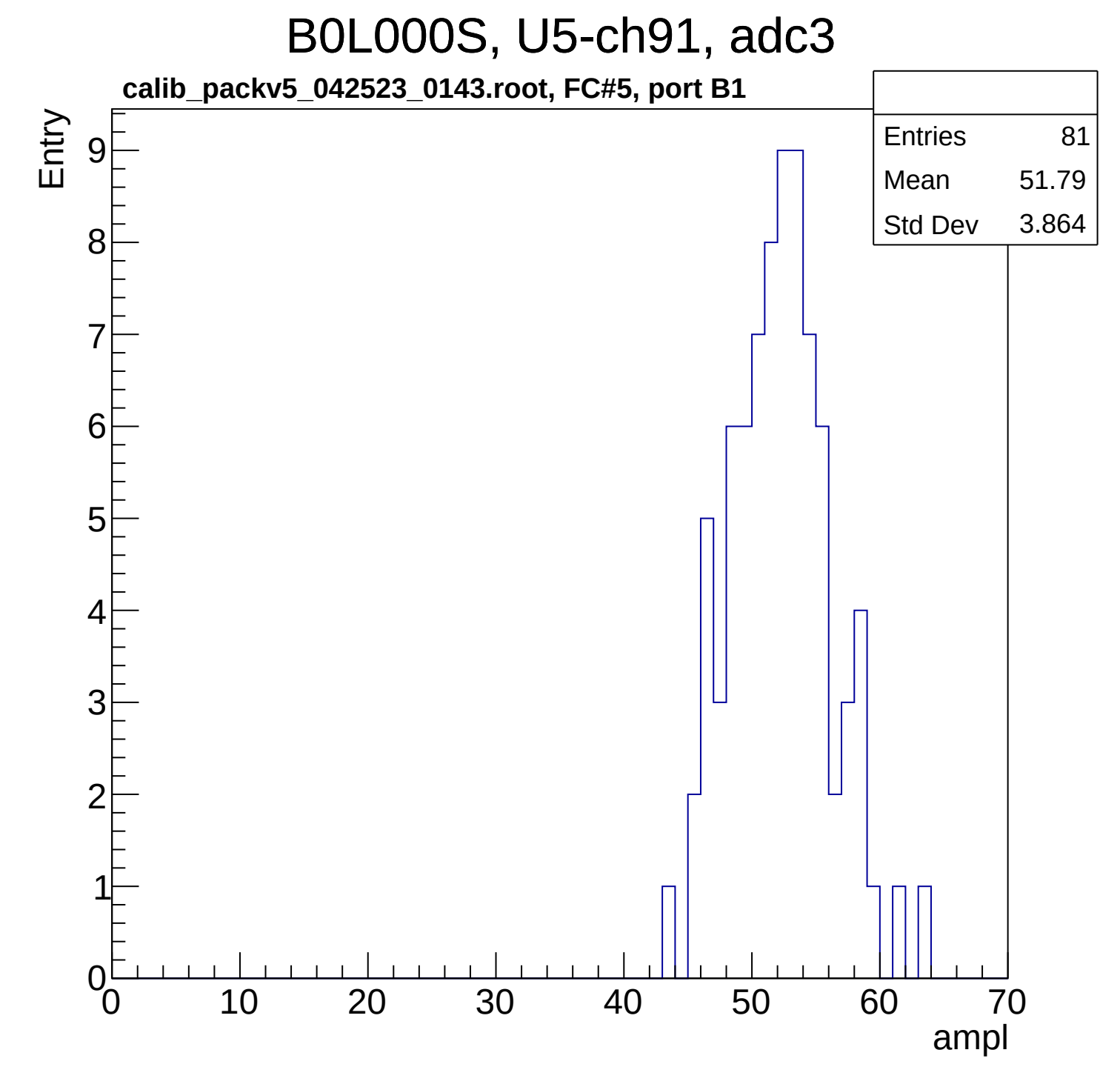
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch91, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 51.79
Std Dev 3.864
Entry
ampl
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4
5
6
7
8
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0
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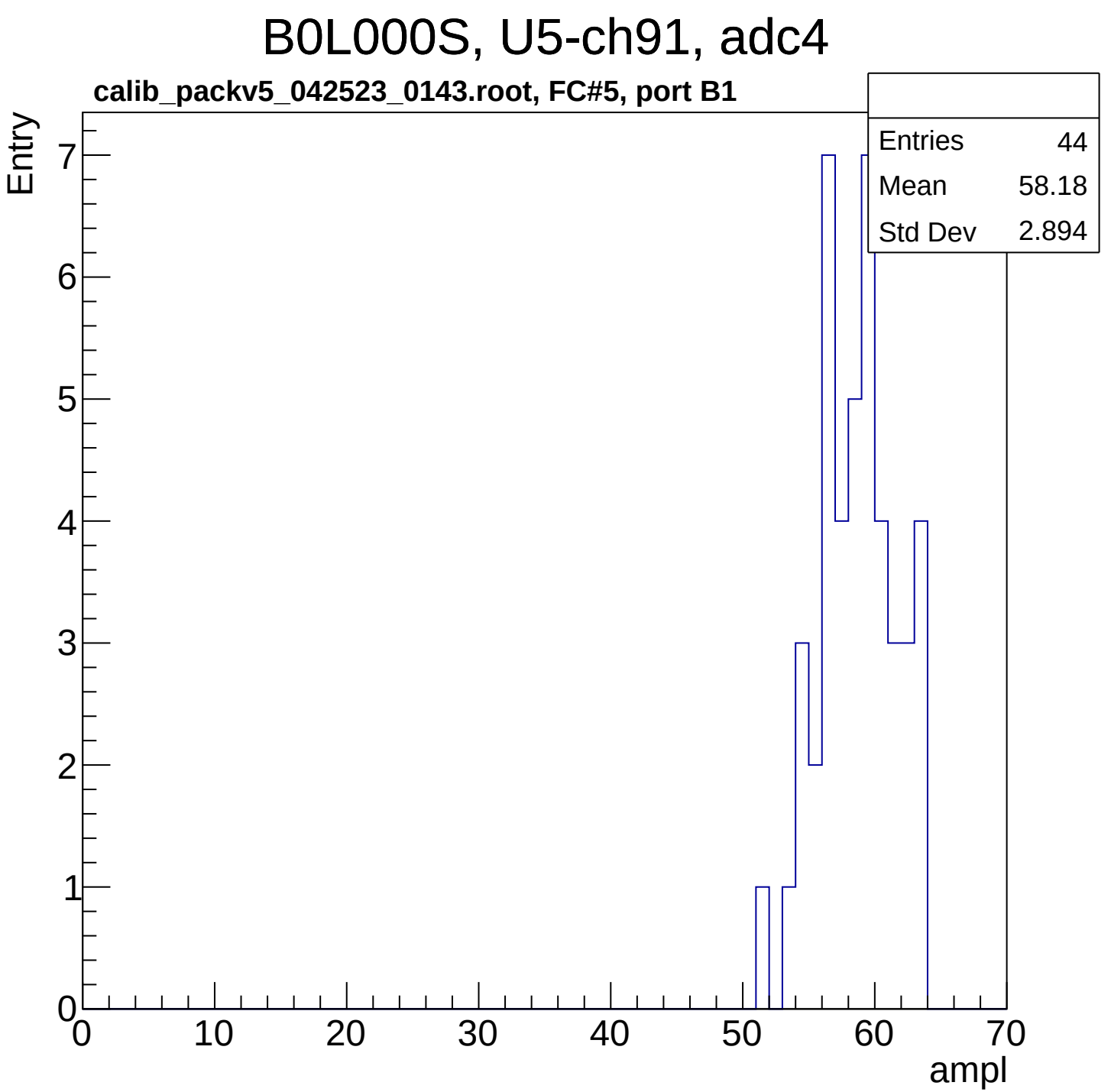
B0L000S, U5-ch91, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 58.18
Std Dev 2.894
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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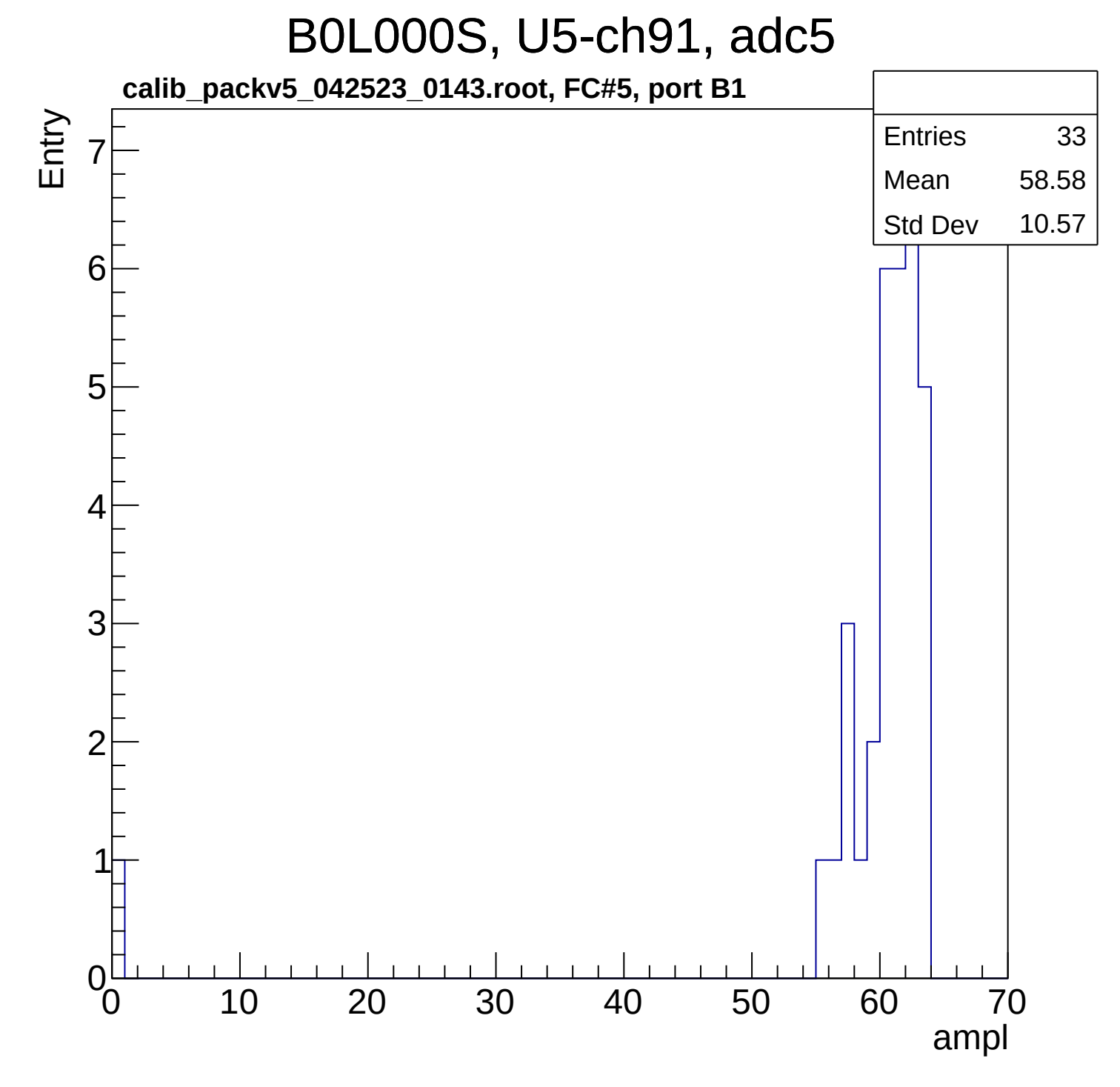
B0L000S, U5-ch91, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 33
Mean 58.58
Std Dev 10.57
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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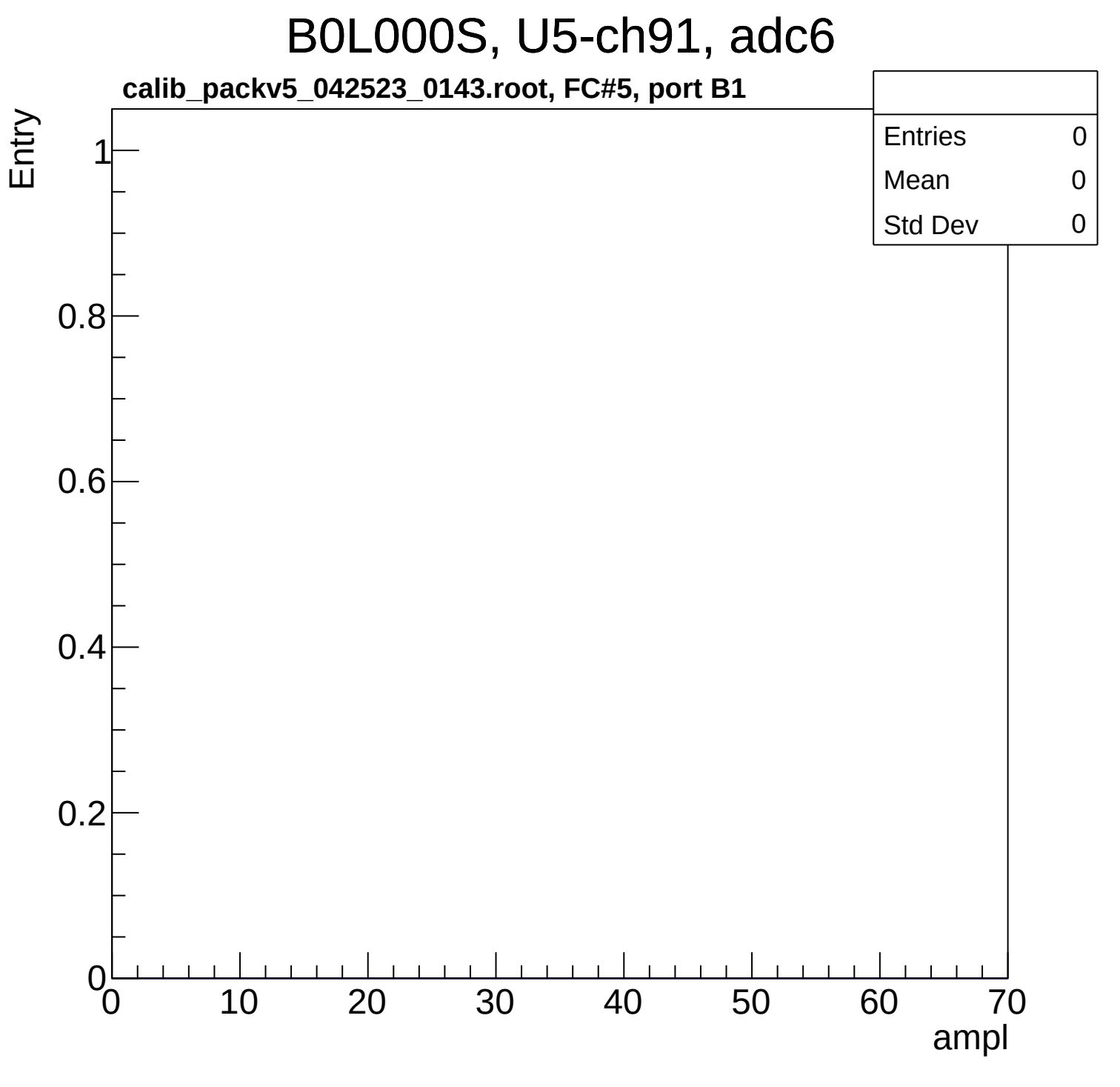
B0L000S, U5-ch91, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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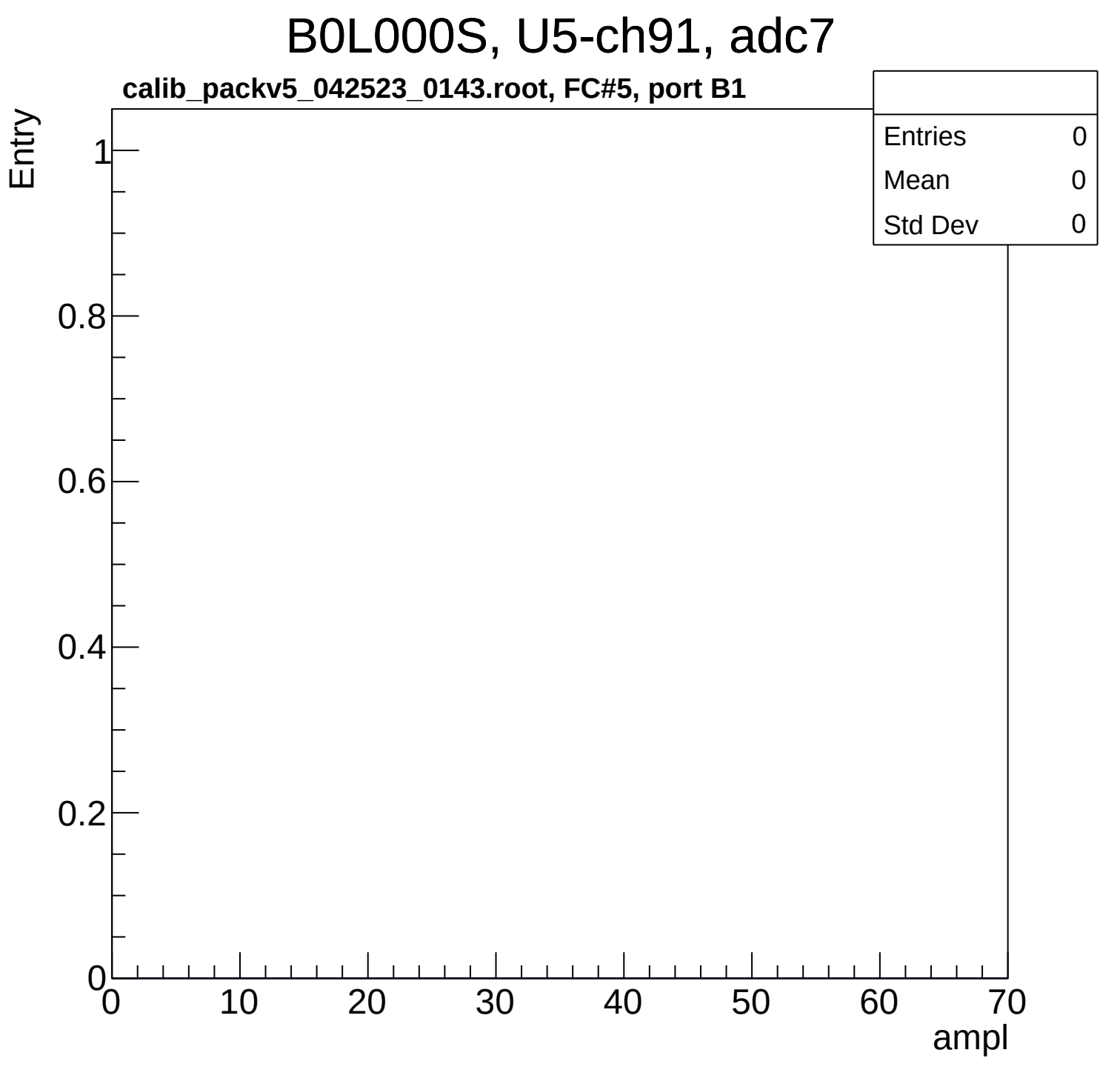
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch91, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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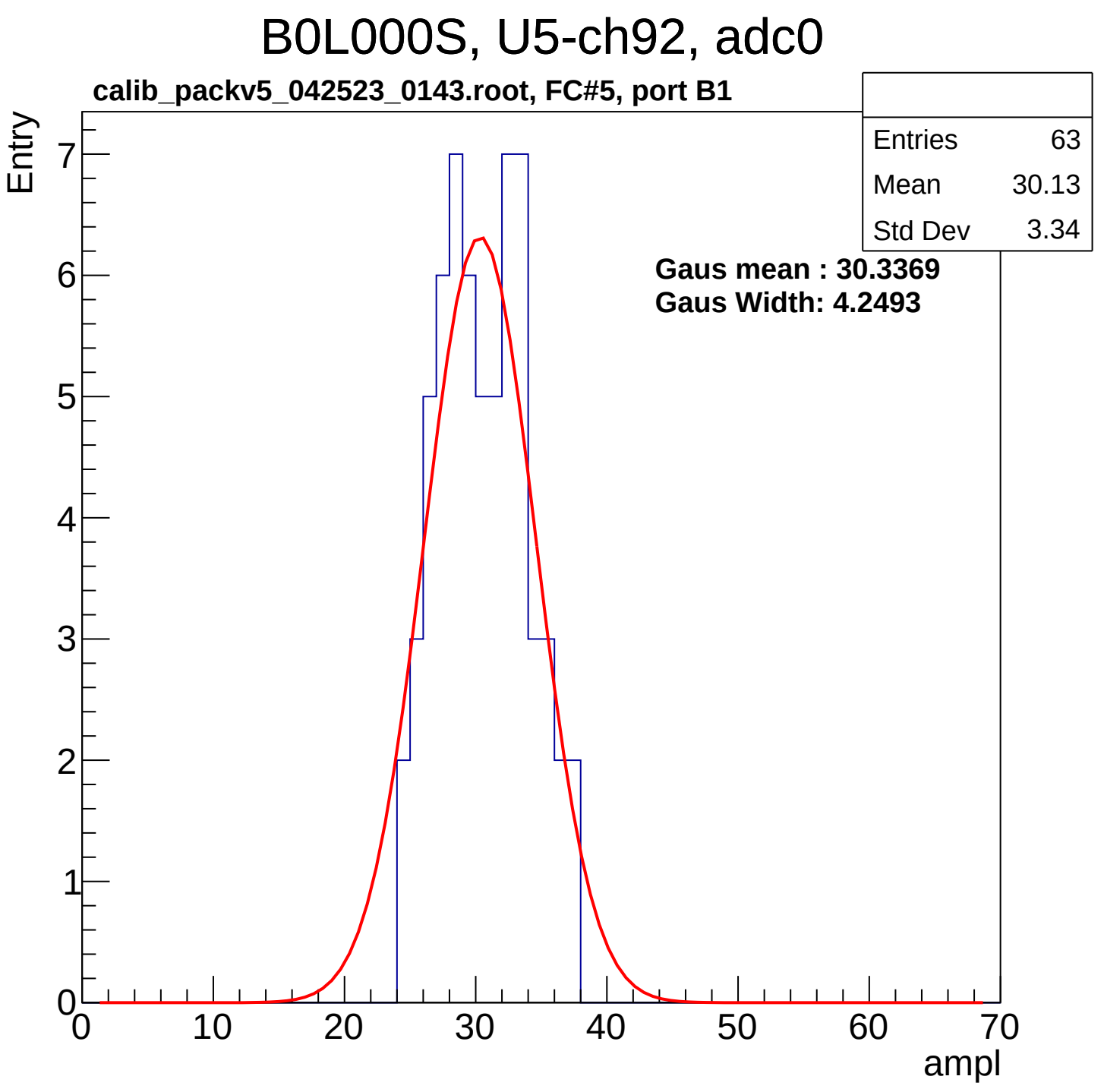
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch92, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 30.13
Std Dev 3.34
Gaus mean : 30.3369
Gaus Width: 4.2493
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
30
40
50
60
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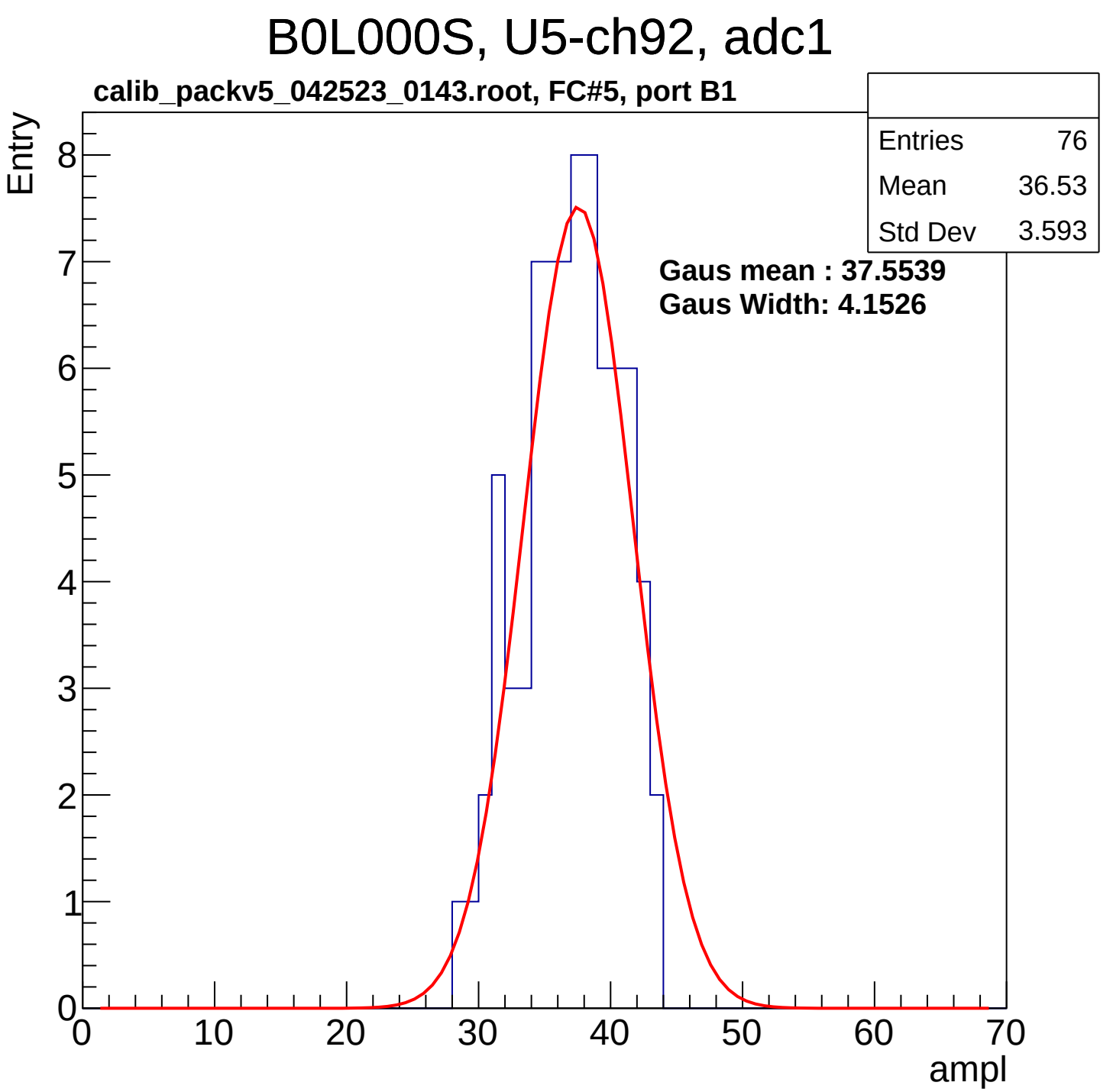
B0L000S, U5-ch92, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 36.53
Std Dev 3.593
Gaus mean : 37.5539
Gaus Width: 4.1526
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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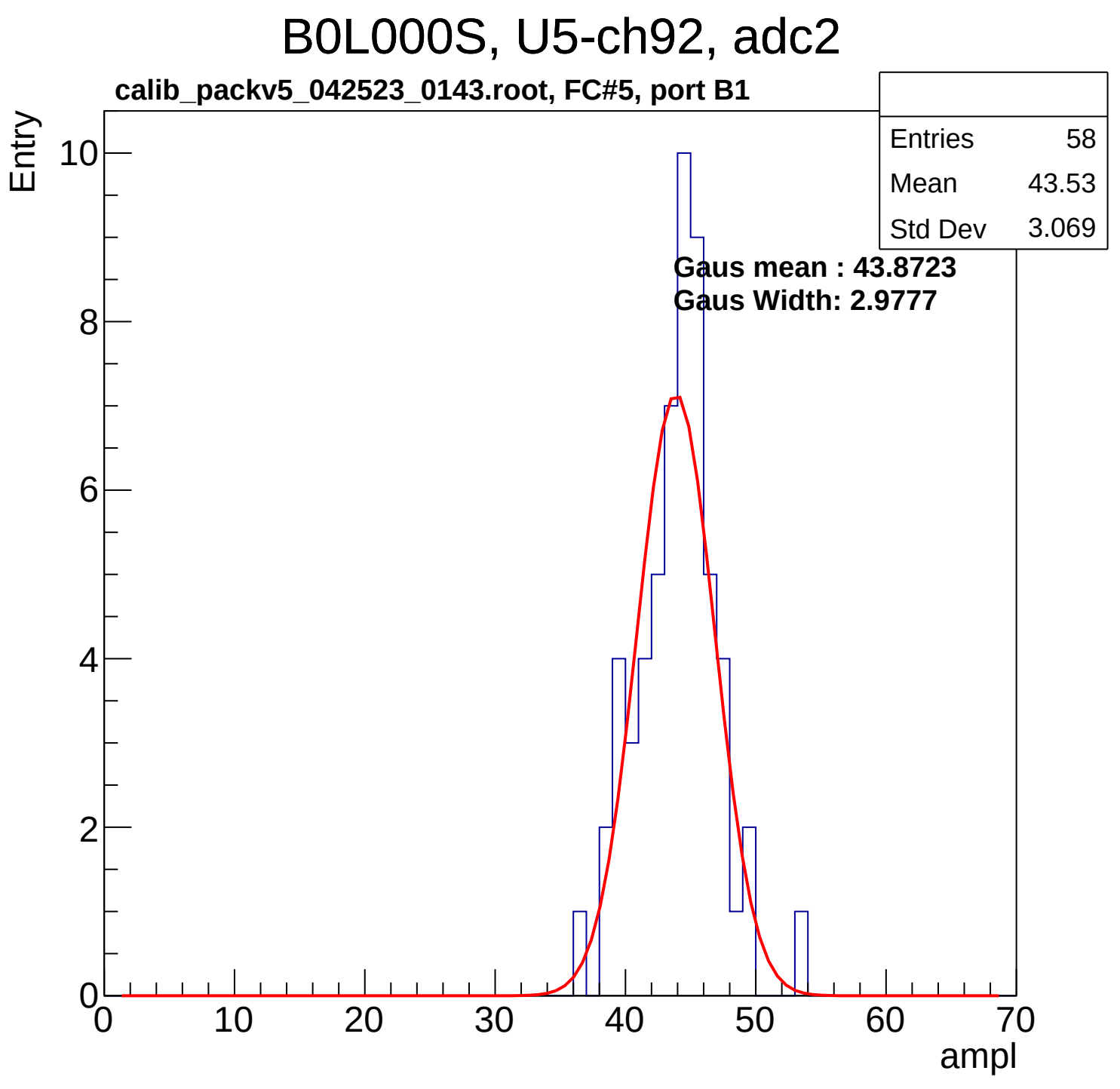
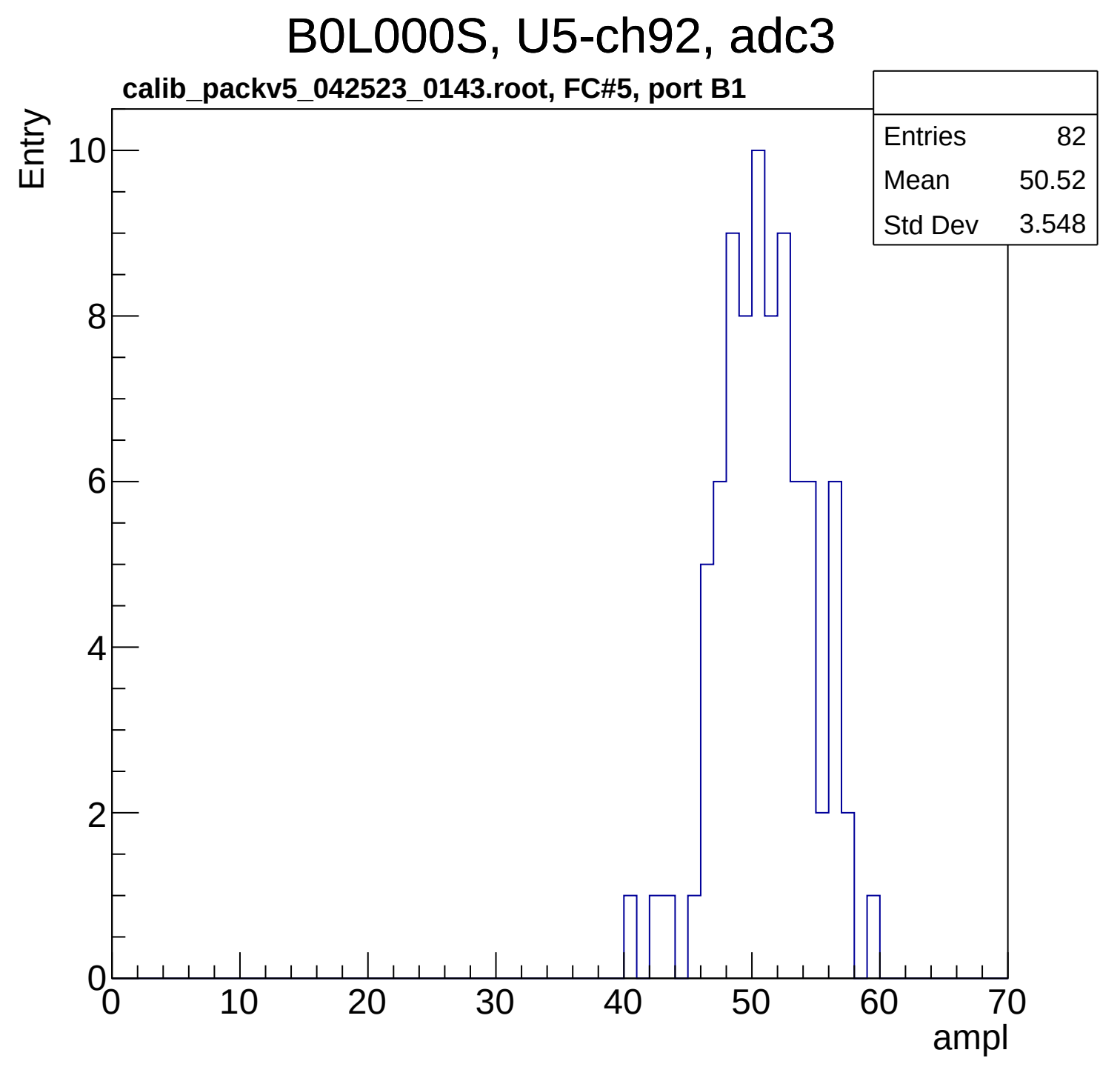

B0L000S, U5-ch92, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 43.53
Std Dev 3.069
Gaus mean : 43.8723
Gaus Width: 2.9777
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch92, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 50.52
Std Dev 3.548
Entry
ampl
0
2
4
6
8
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0
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60
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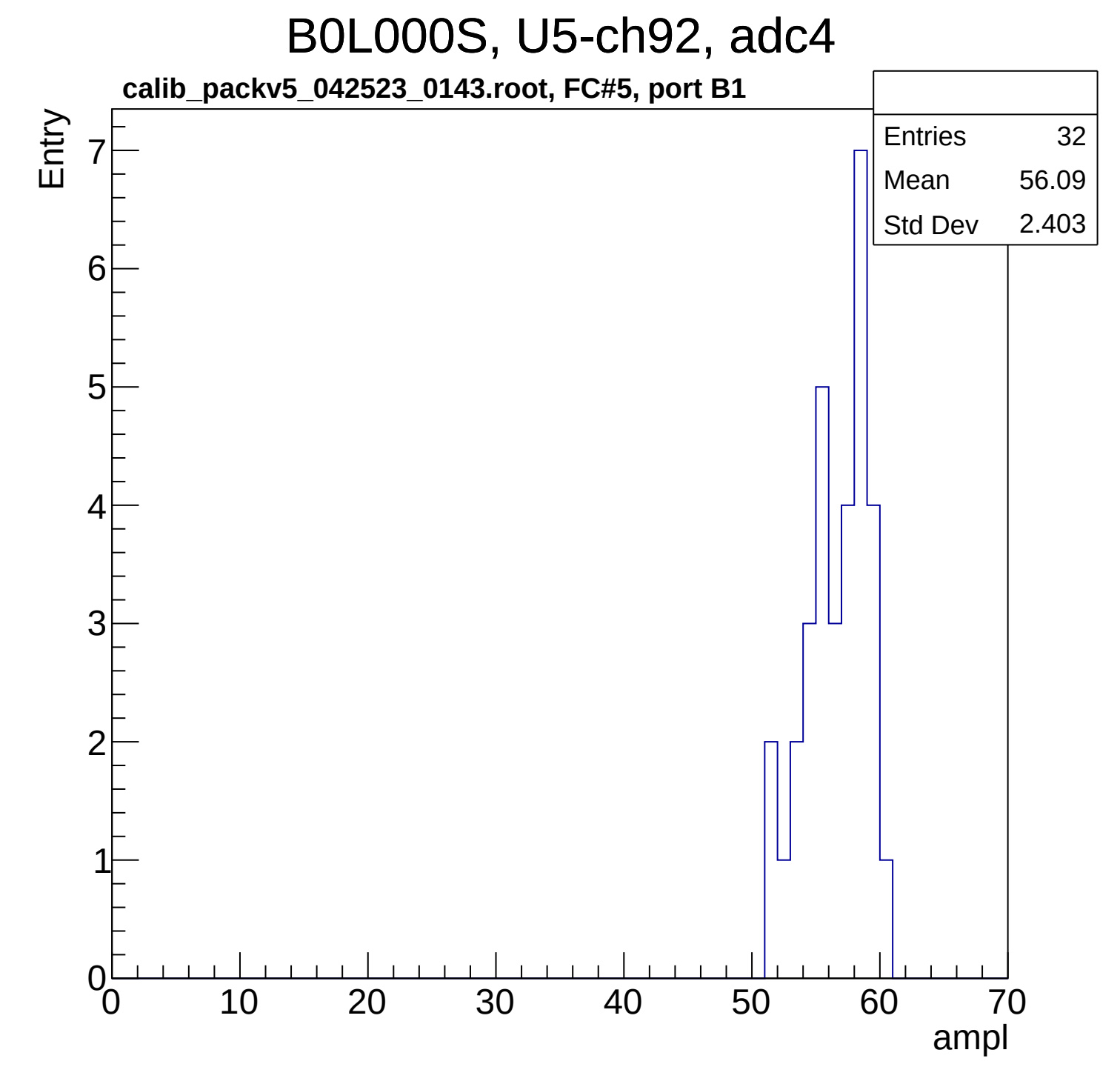
B0L000S, U5-ch92, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 32
Mean 56.09
Std Dev 2.403
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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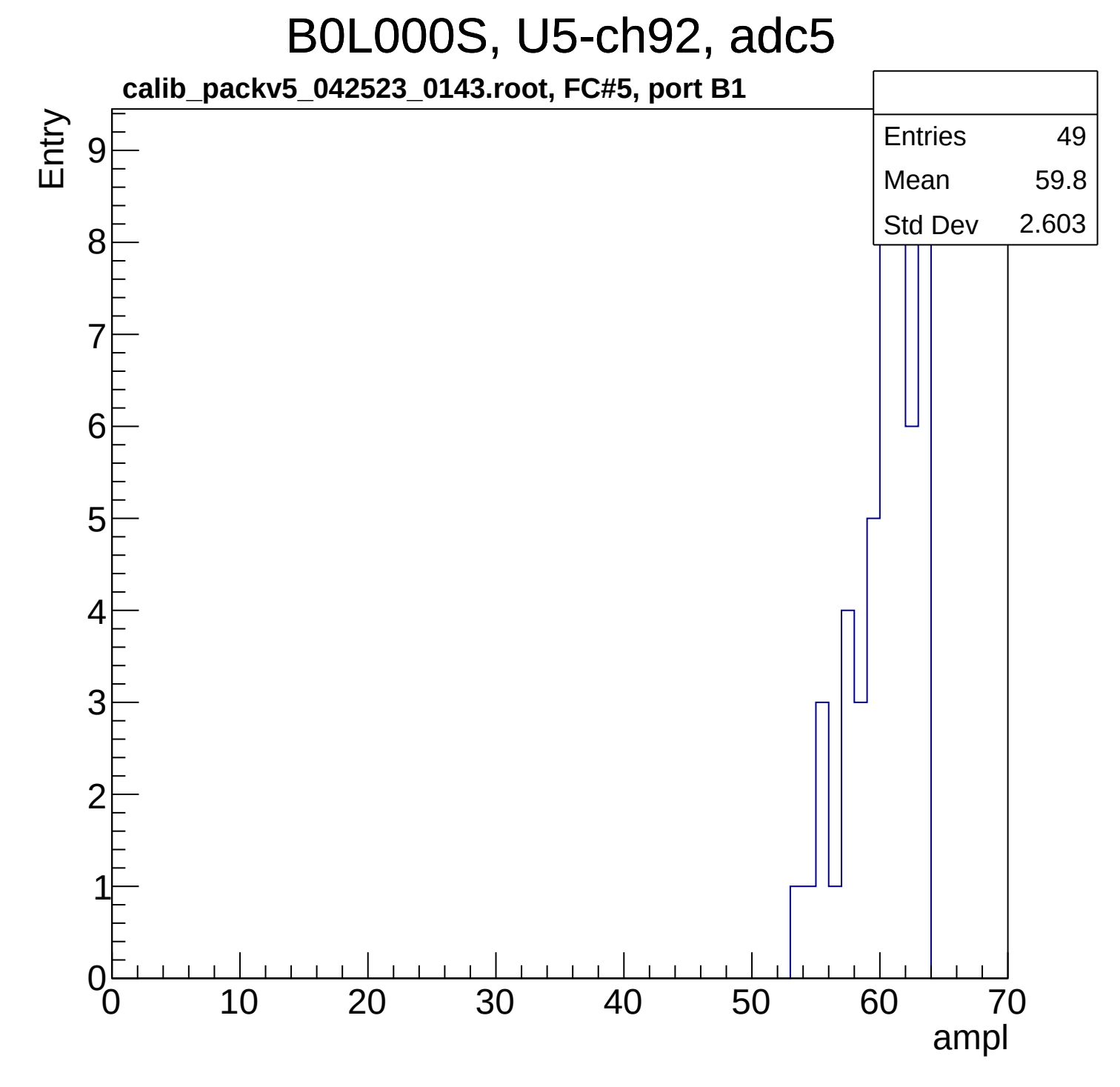
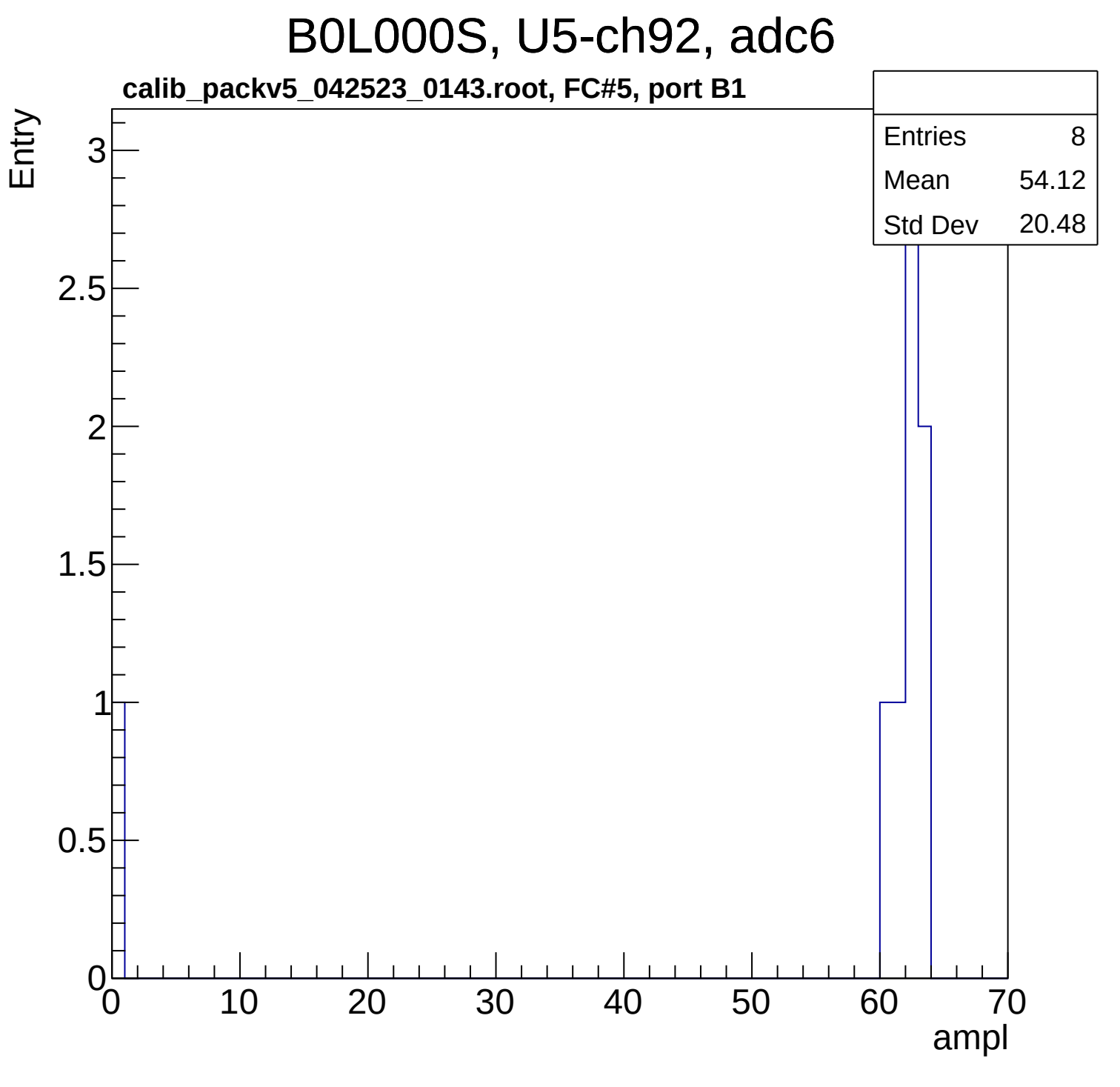
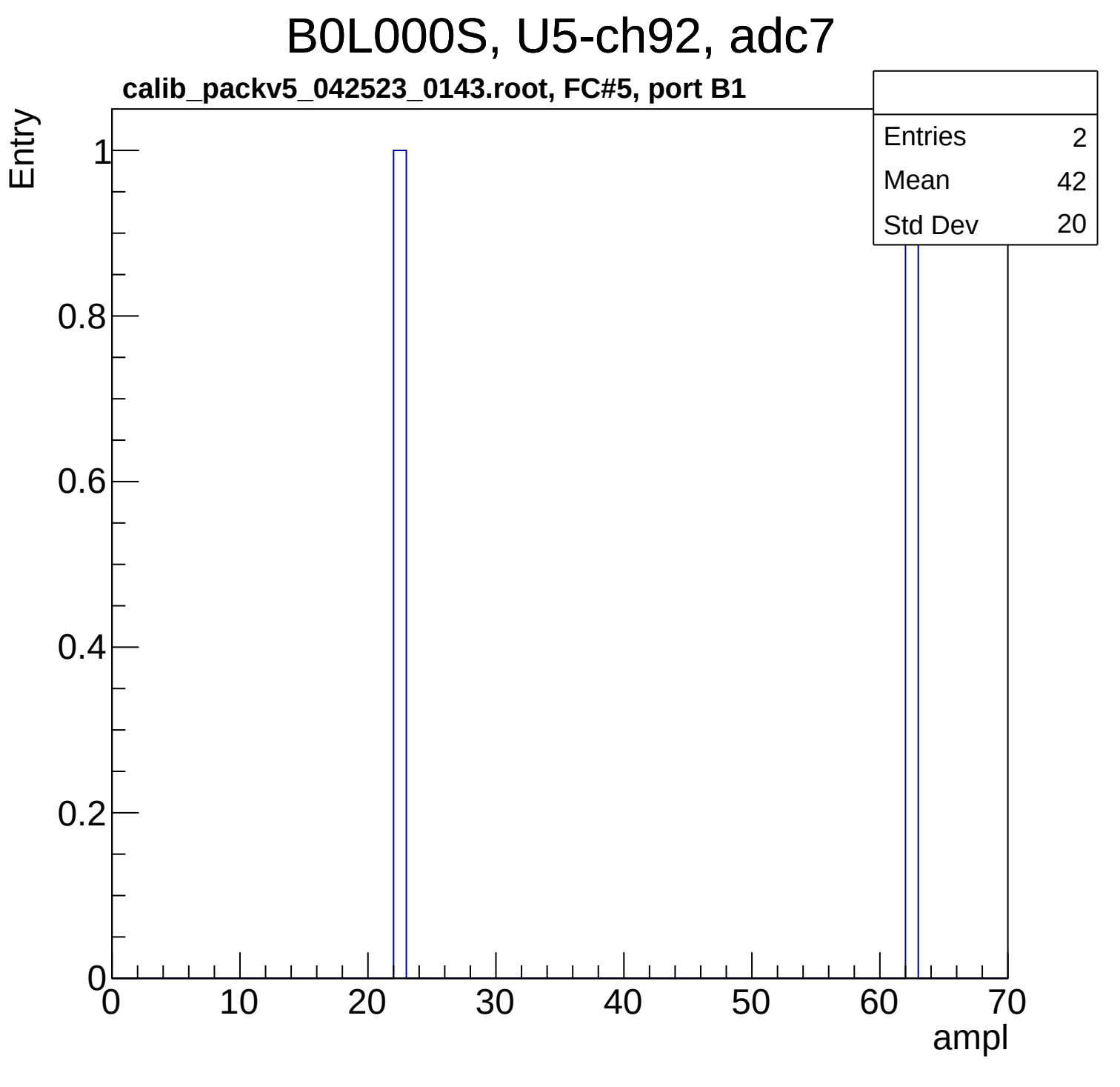
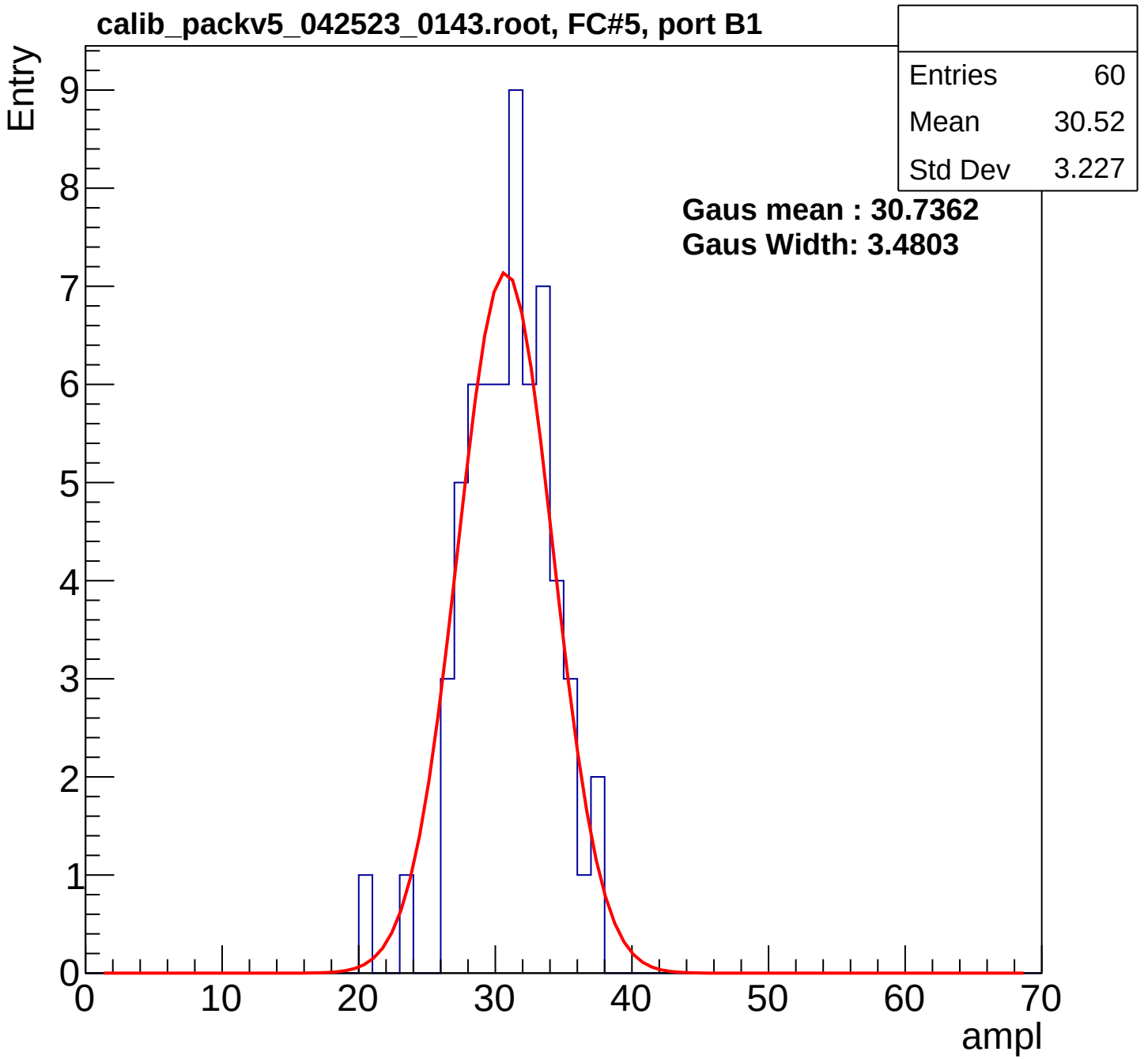
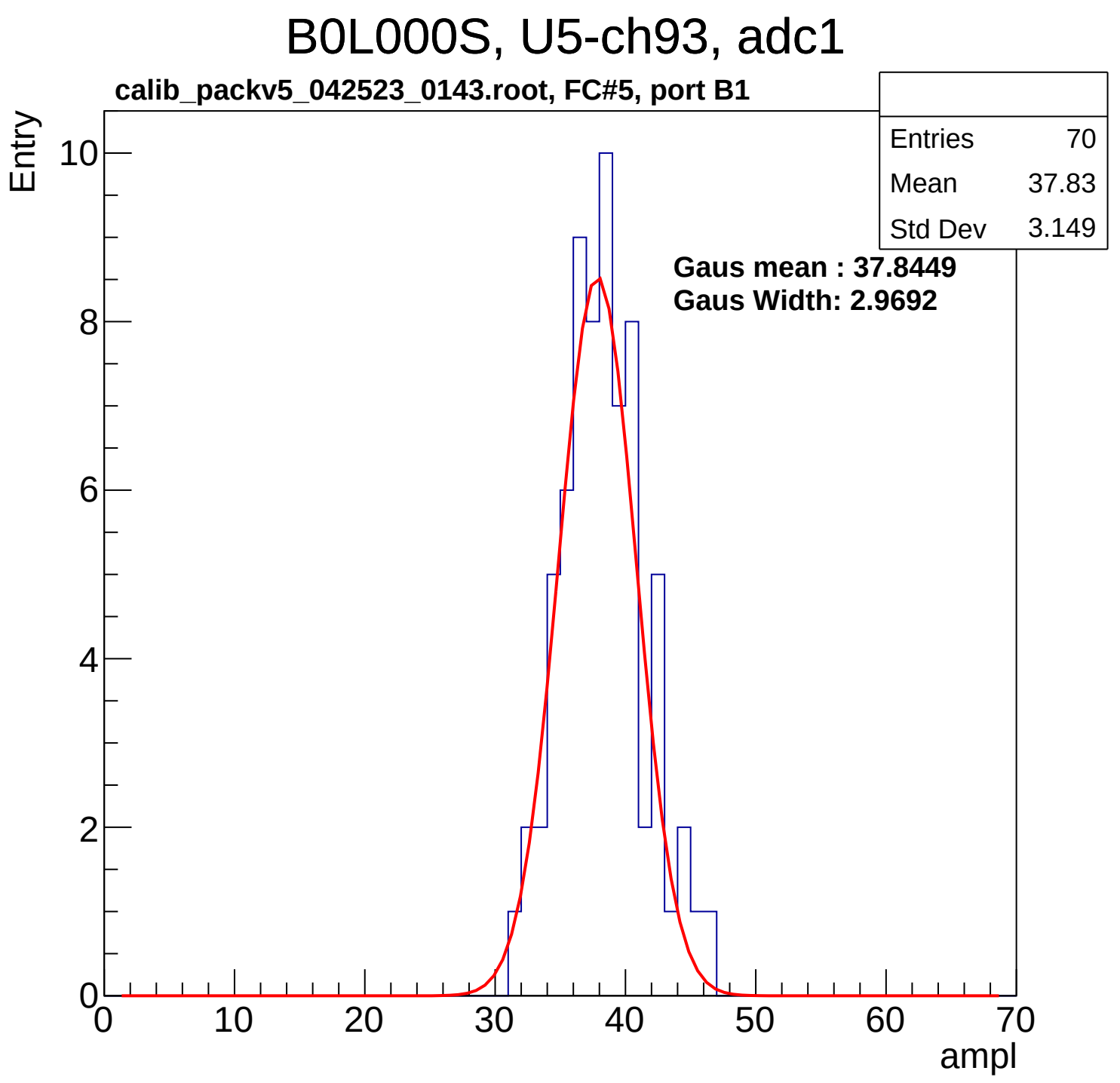
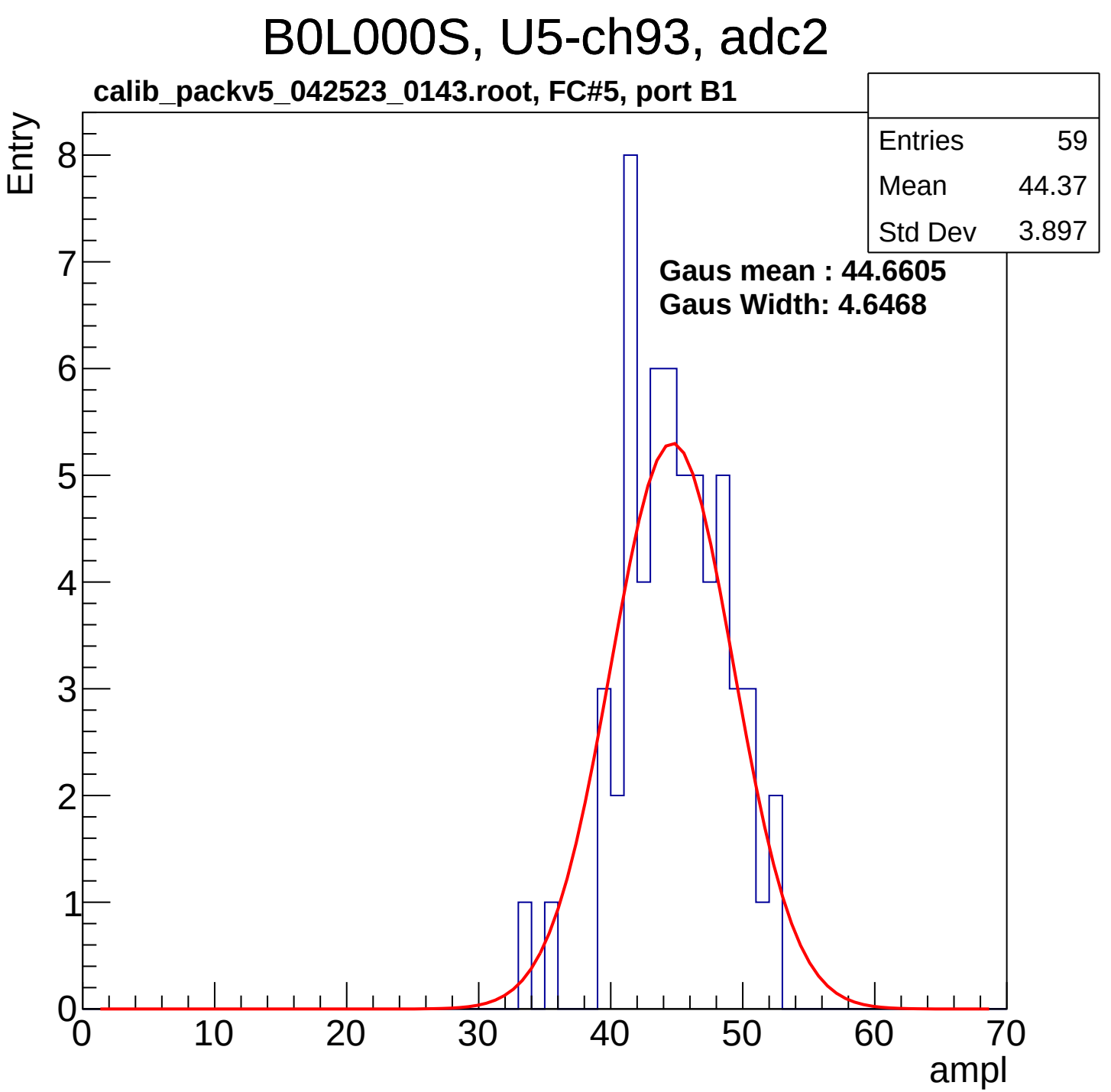

B0L000S, U5-ch92, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 59.8
Std Dev 2.603
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

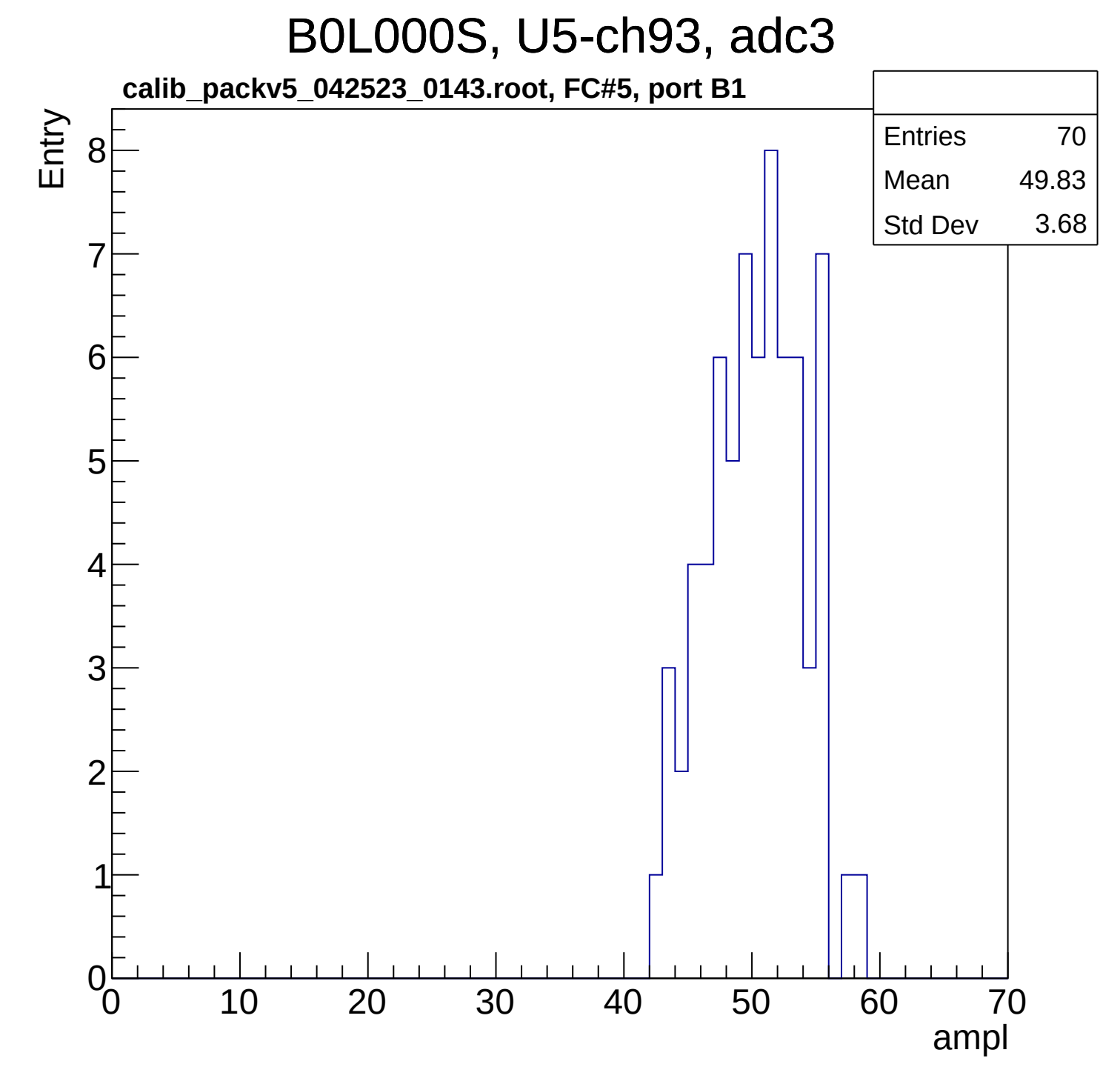

B0L000S, U5-ch92, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.12
Std Dev 20.48
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

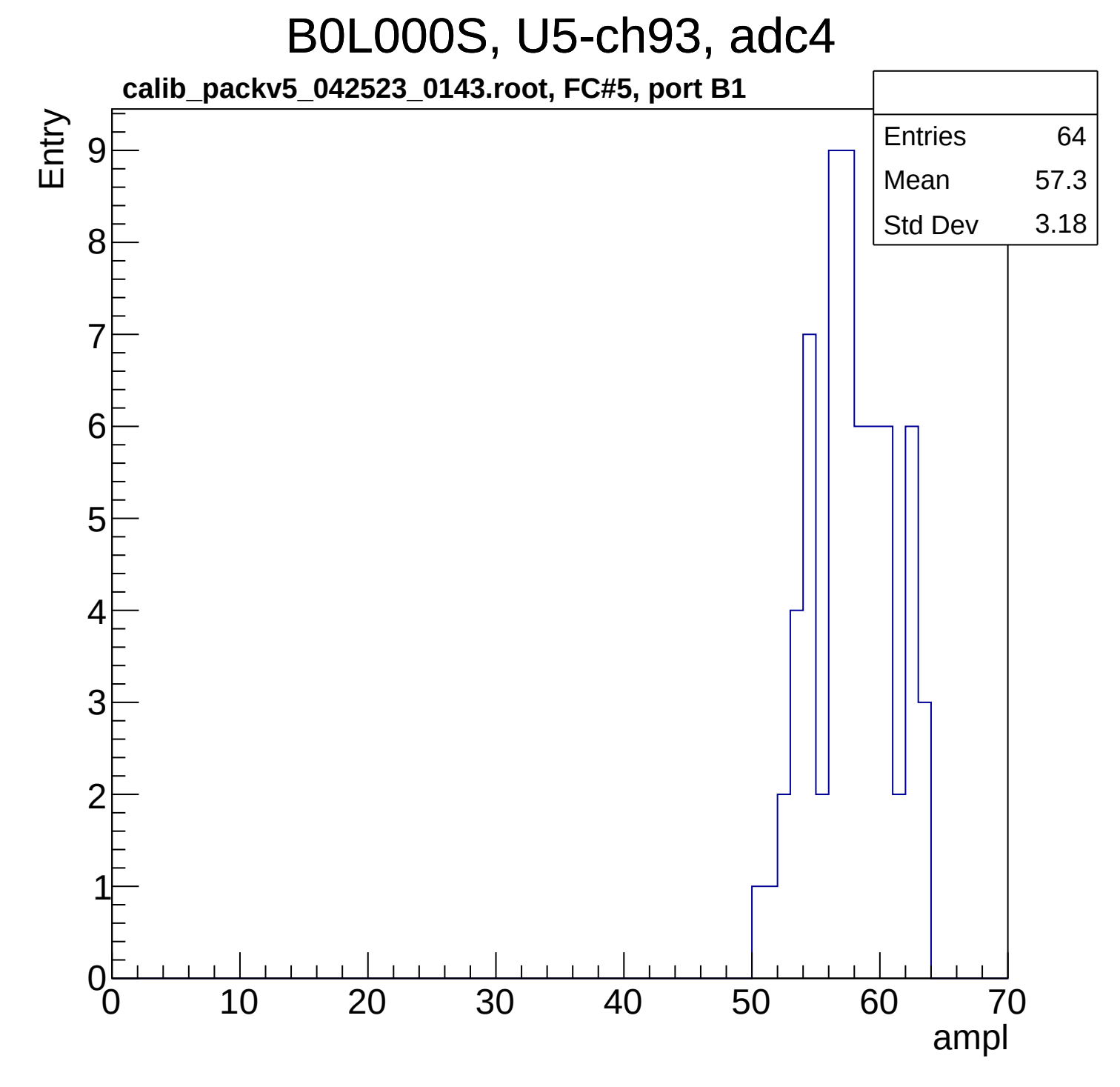

B0L000S, U5-ch92, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 42
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

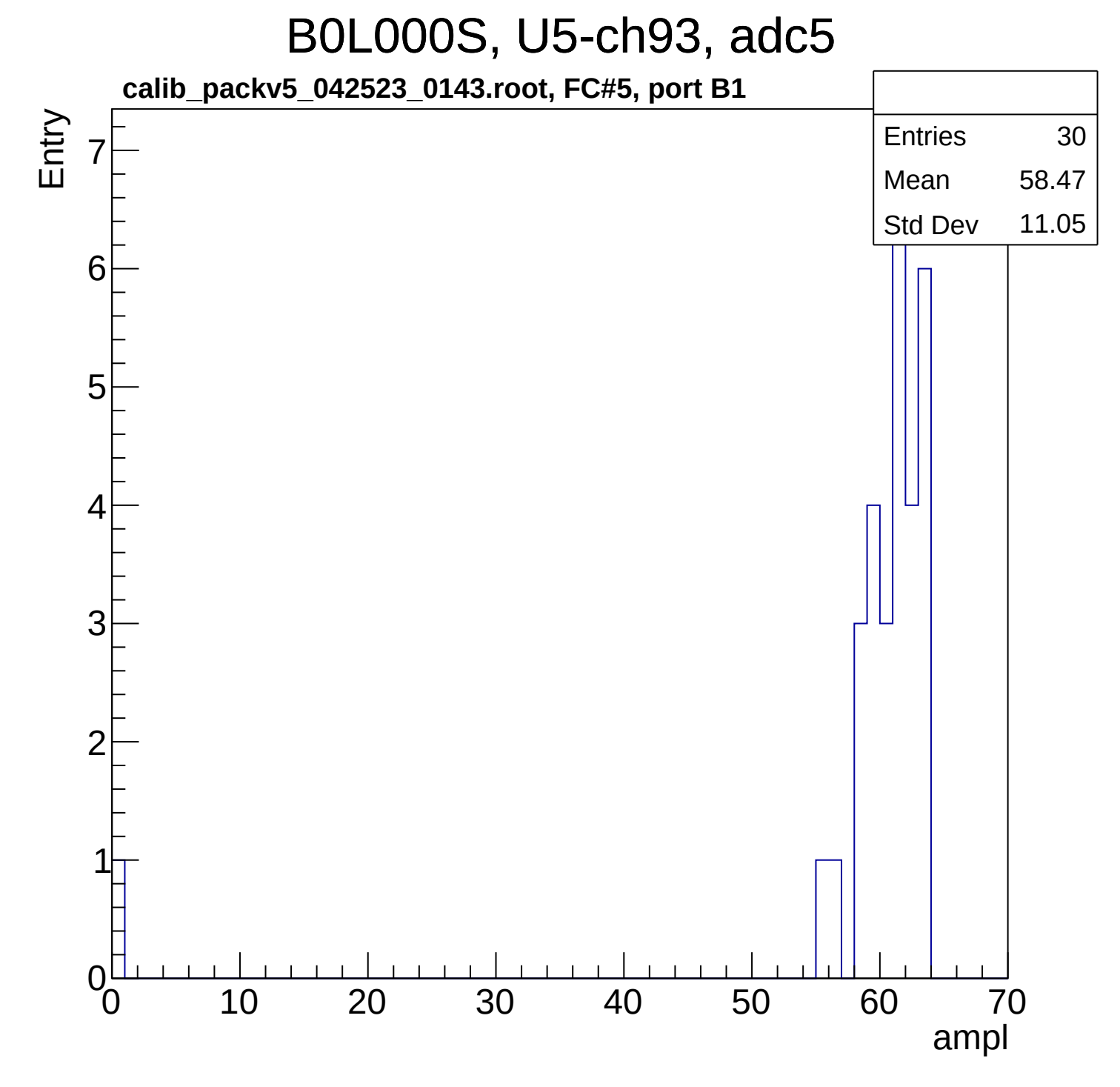

B0L000S, U5-ch93, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.52
Std Dev 3.227
Gaus mean : 30.7362
Gaus Width: 3.4803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch93, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 37.83
Std Dev 3.149
Gaus mean : 37.8449
Gaus Width: 2.9692
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch93, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 44.37
Std Dev 3.897
Gaus mean : 44.6605
Gaus Width: 4.6468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch93, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 49.83
Std Dev 3.68
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch93, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 57.3
Std Dev 3.18
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch93, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.47
Std Dev 11.05
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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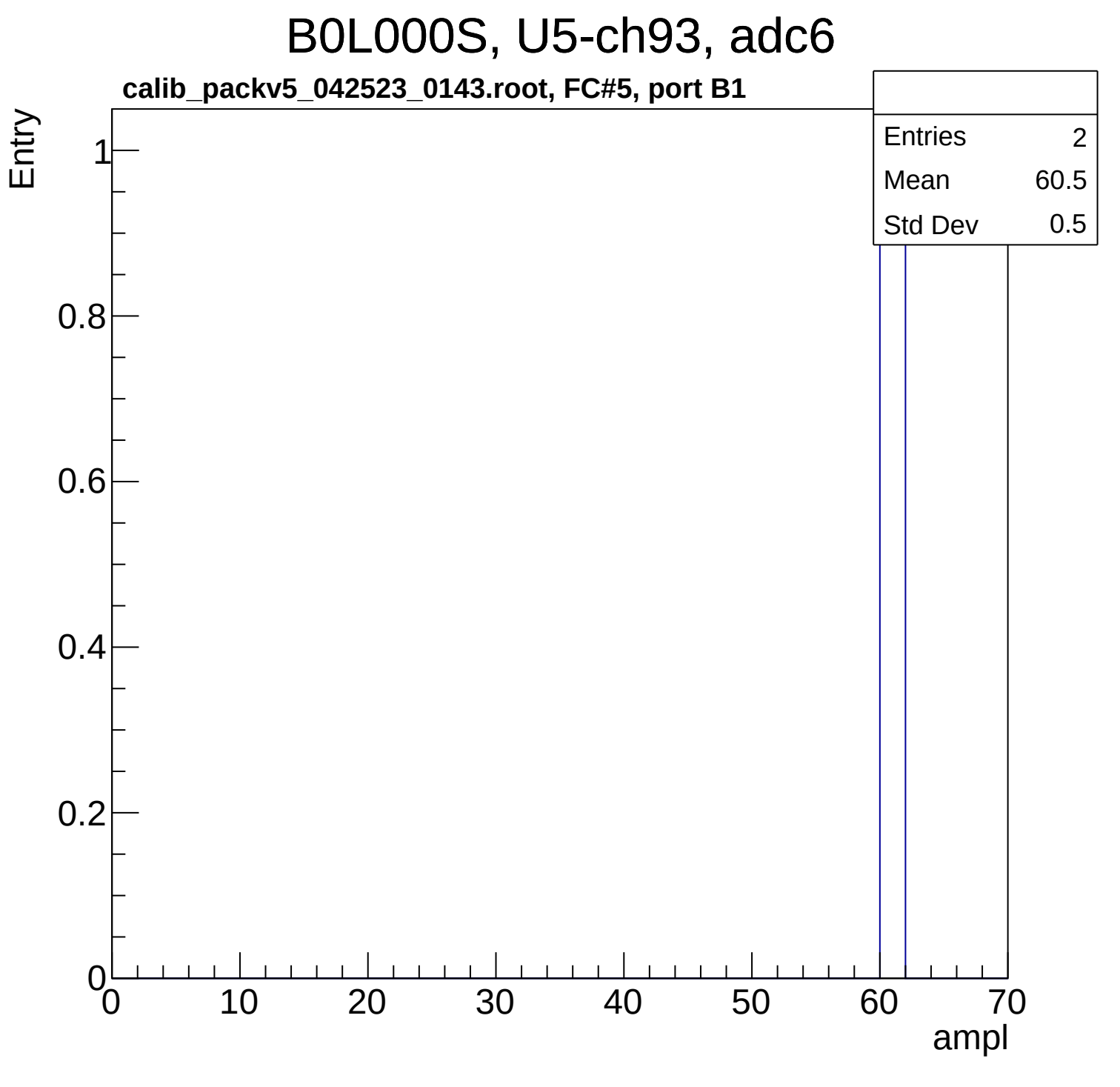
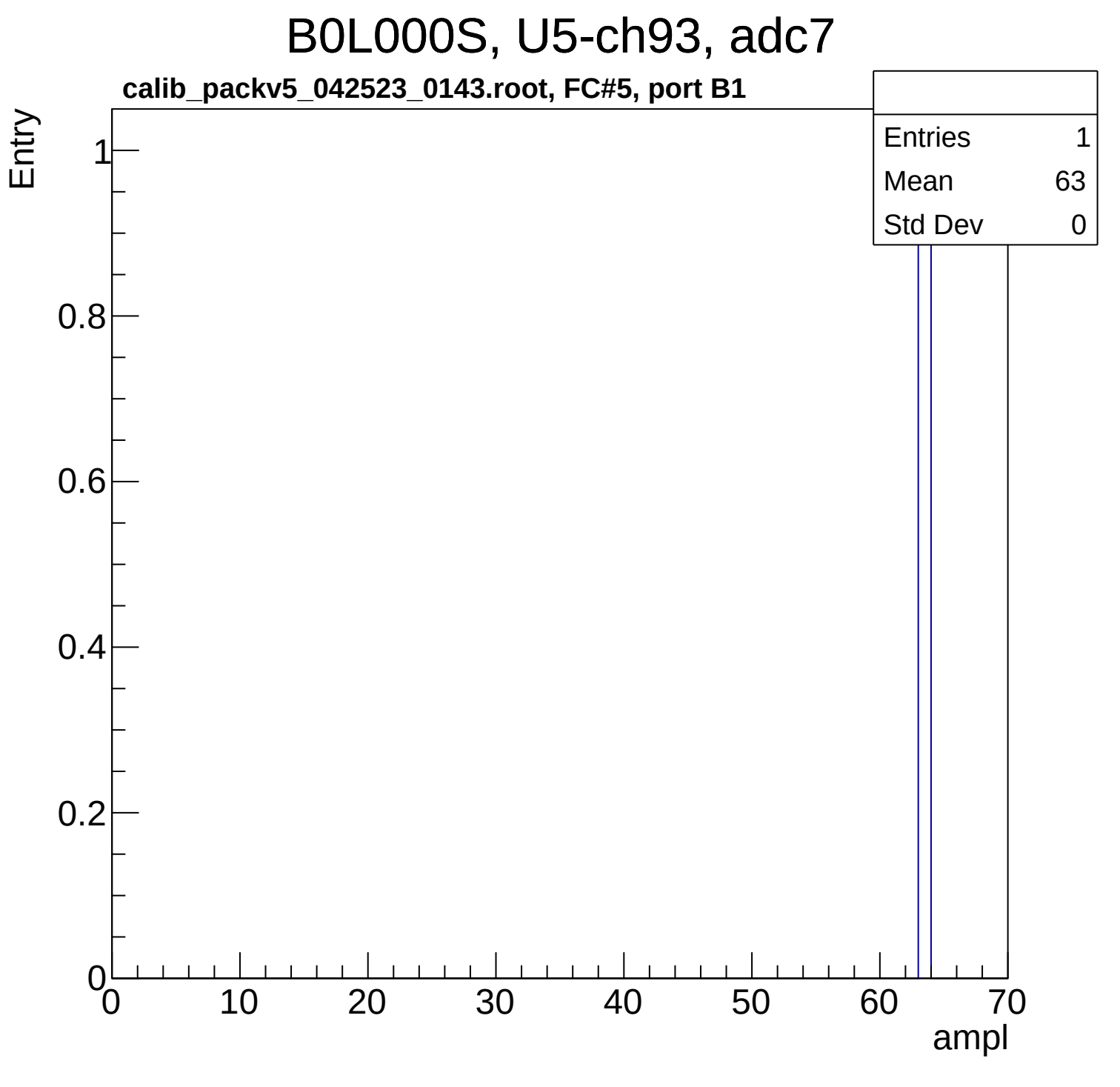
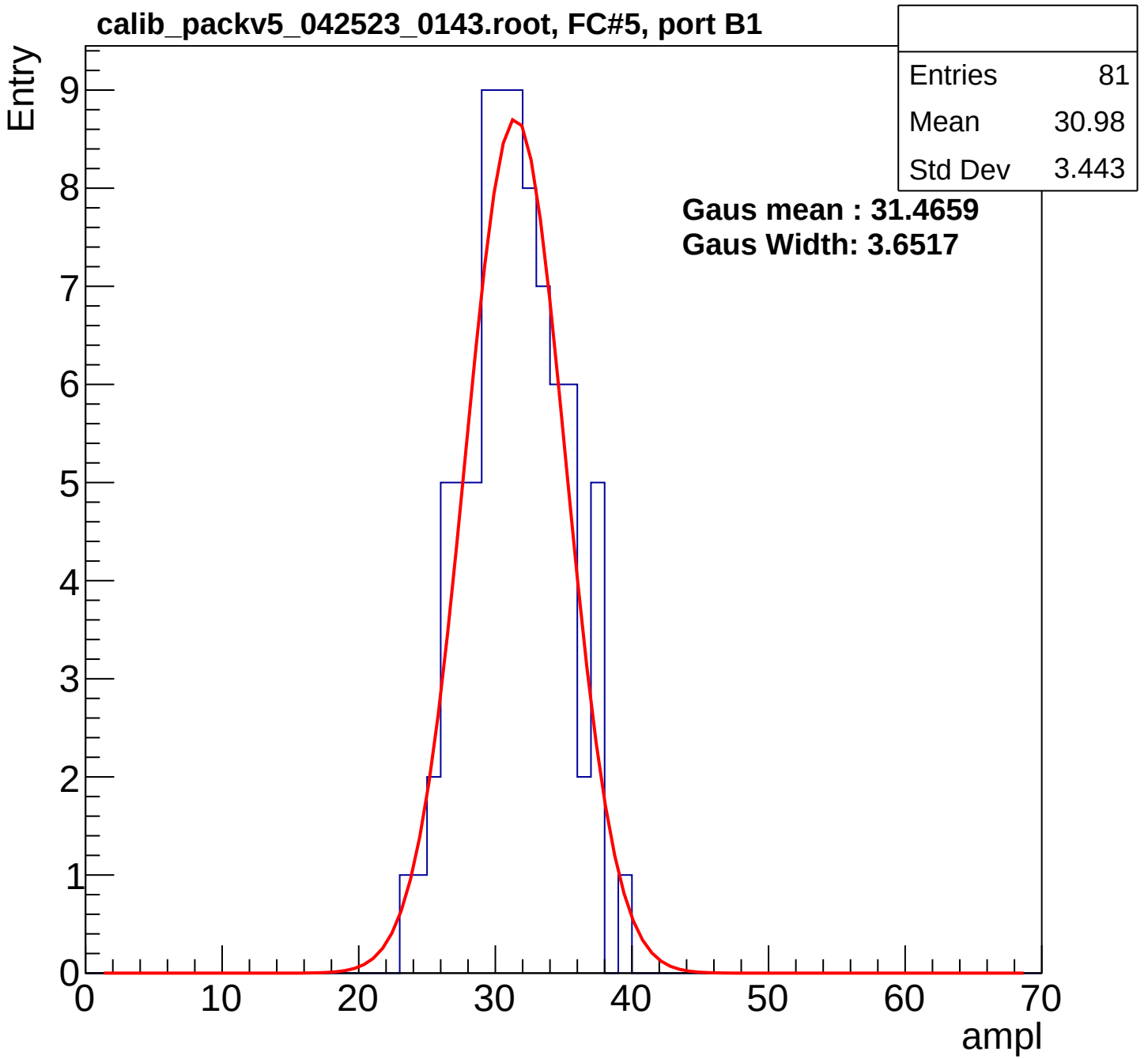
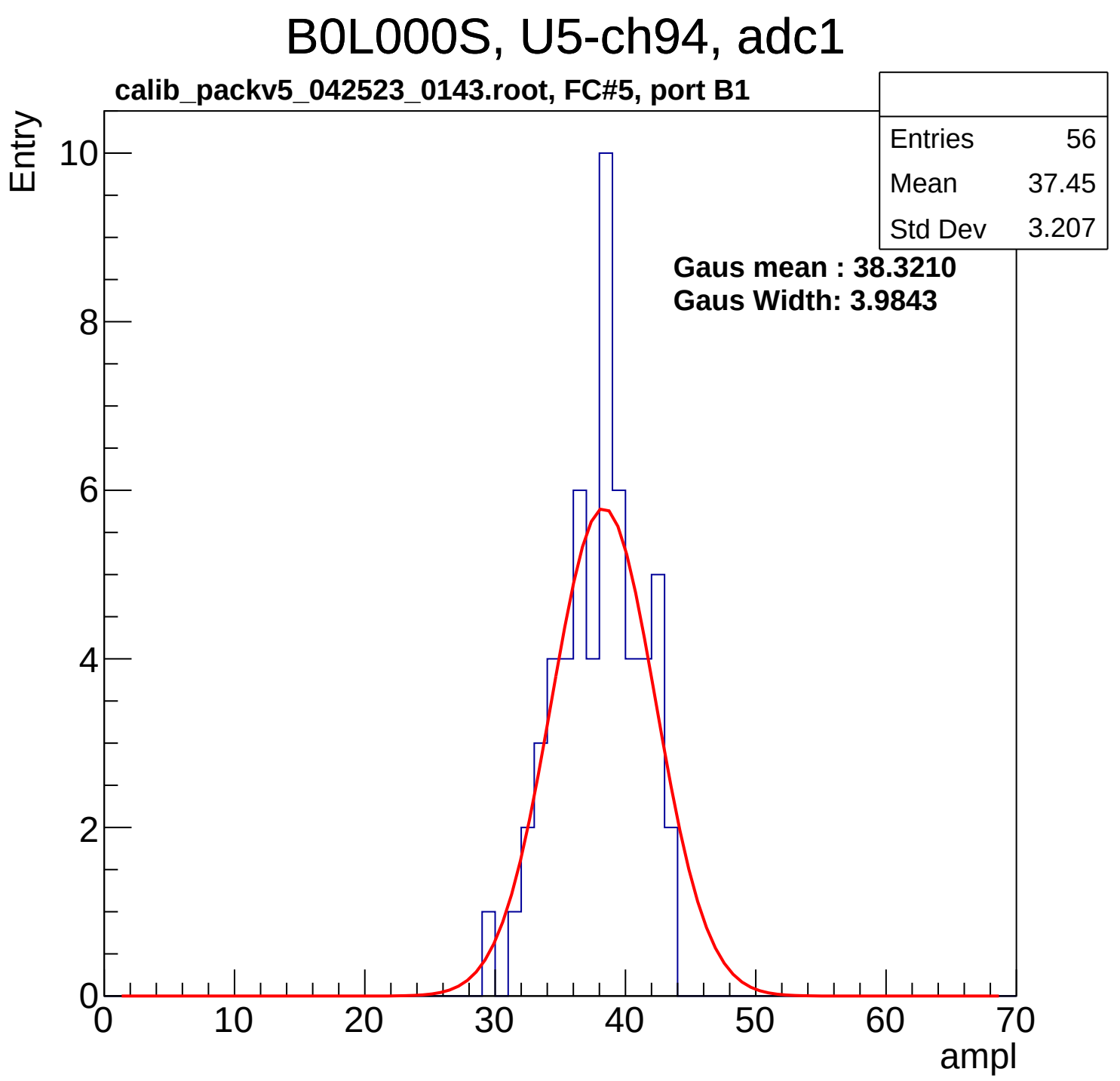
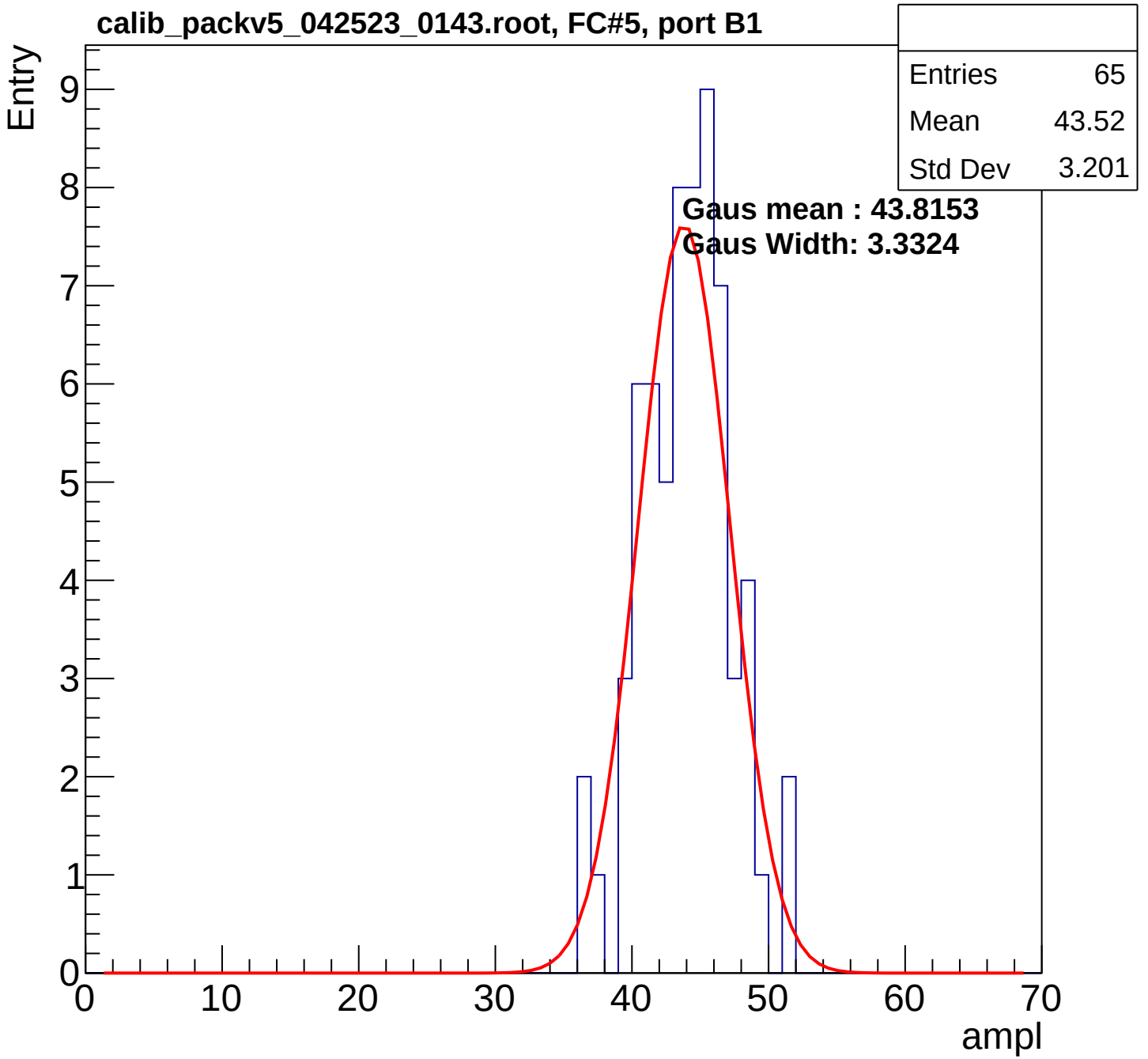
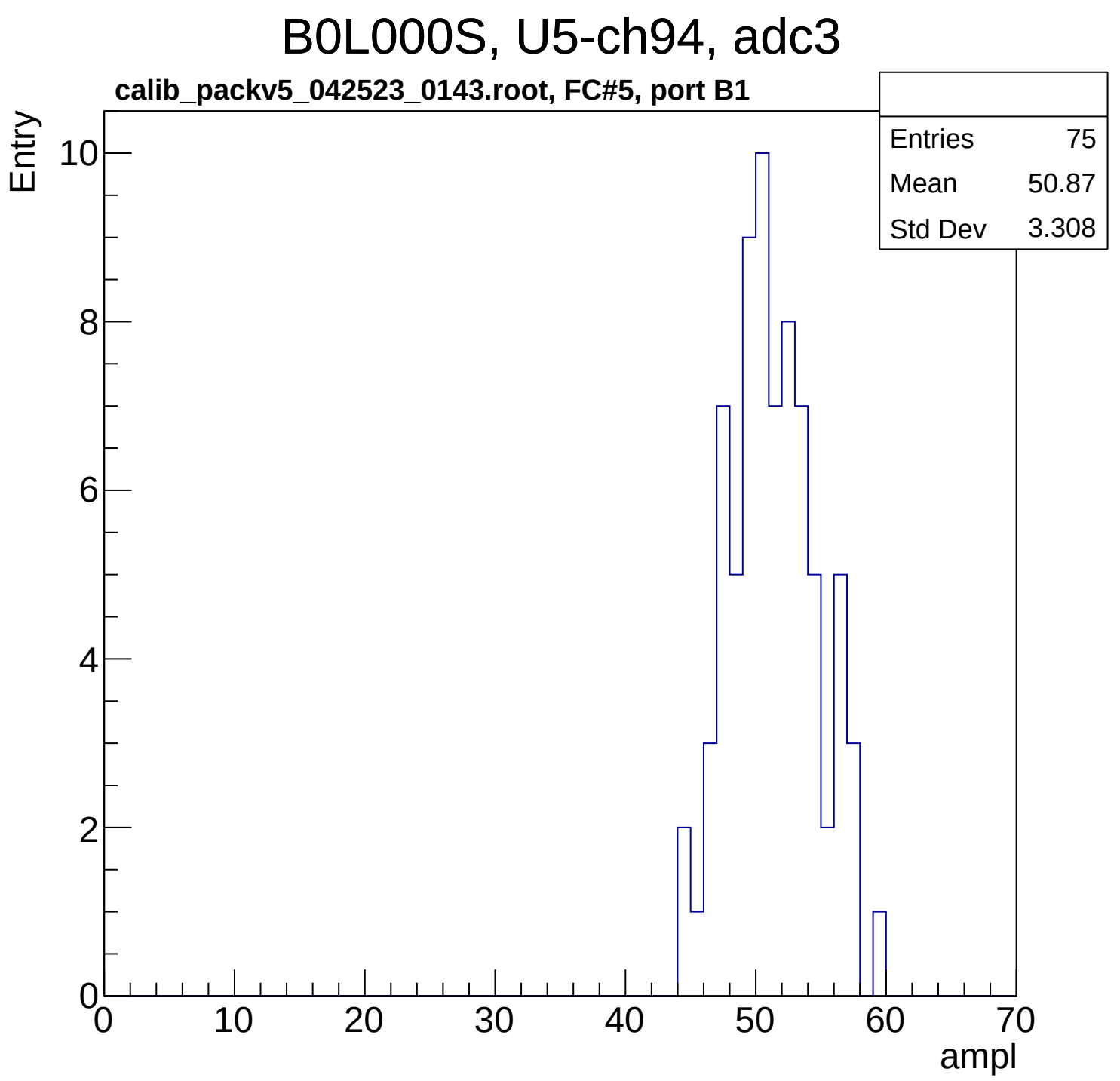

B0L000S, U5-ch93, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

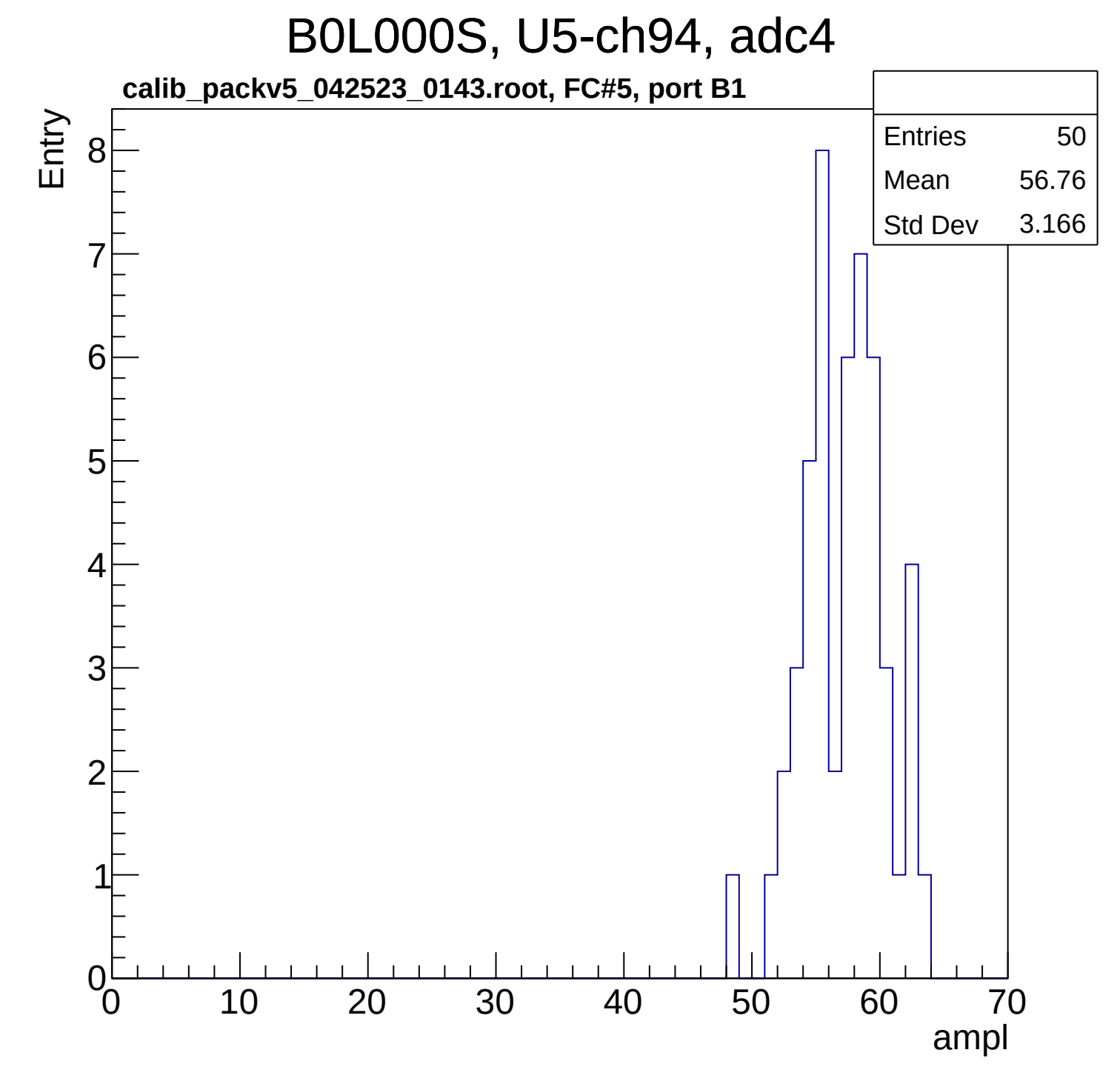

B0L000S, U5-ch93, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch94, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 30.98
Std Dev 3.443
Gaus mean : 31.4659
Gaus Width: 3.6517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch94, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 37.45
Std Dev 3.207
Gaus mean : 38.3210
Gaus Width: 3.9843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch94, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 43.52
Std Dev 3.201
Gaus mean : 43.8153
Gaus Width: 3.3324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch94, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 50.87
Std Dev 3.308
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch94, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 56.76
Std Dev 3.166
Entry
ampl
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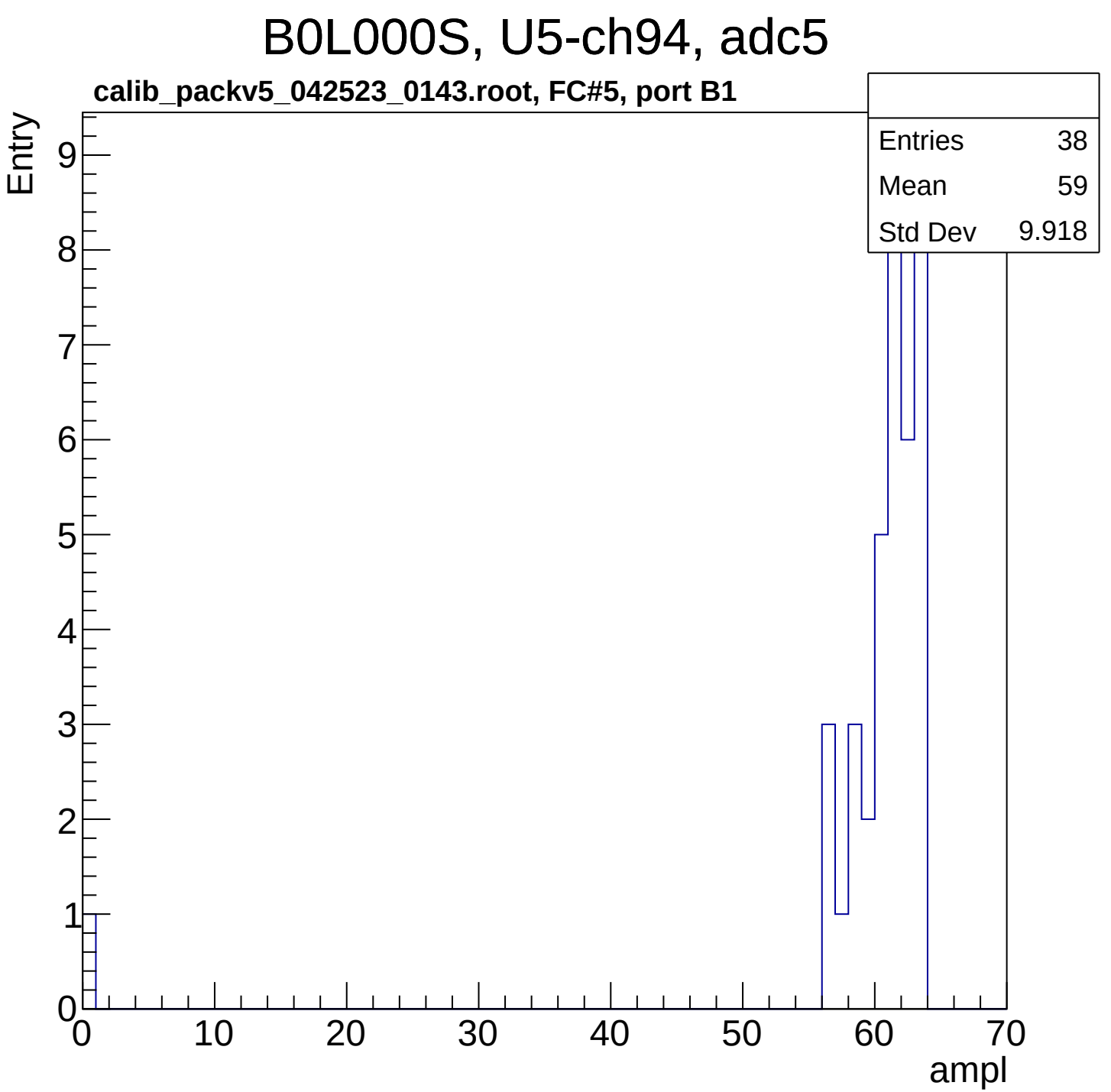
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch94, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 59
Std Dev 9.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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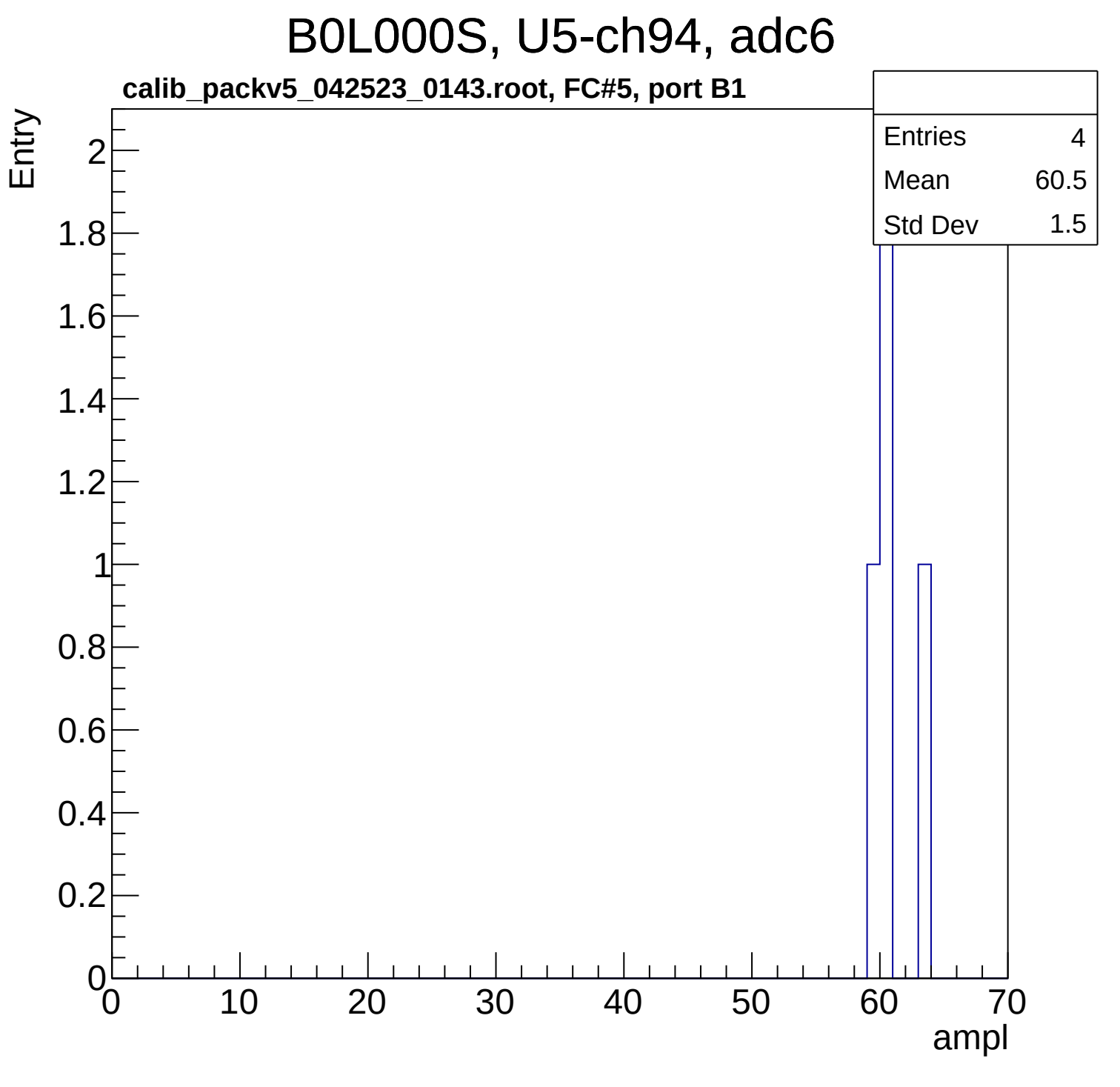
B0L000S, U5-ch94, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 60.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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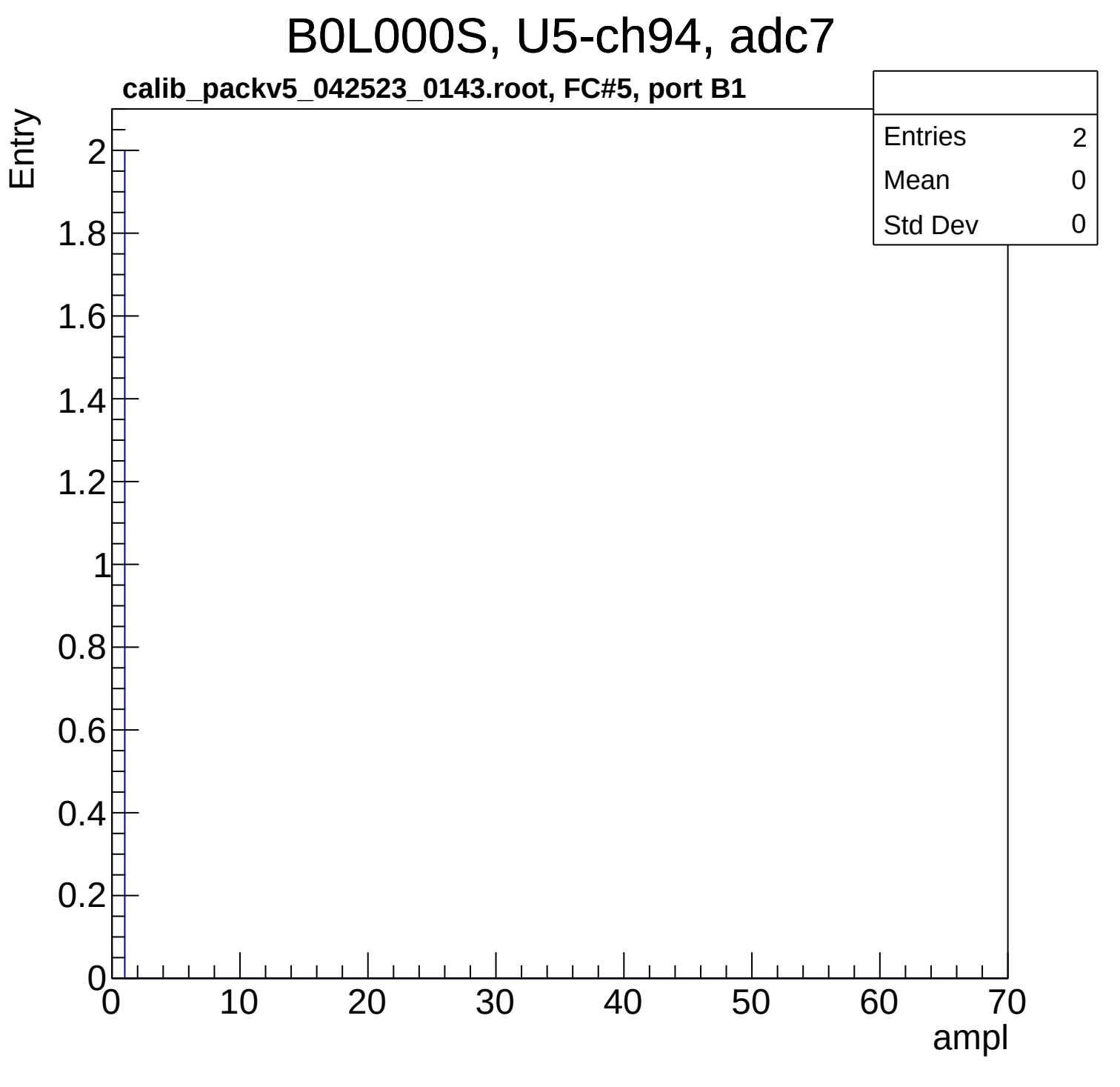
B0L000S, U5-ch94, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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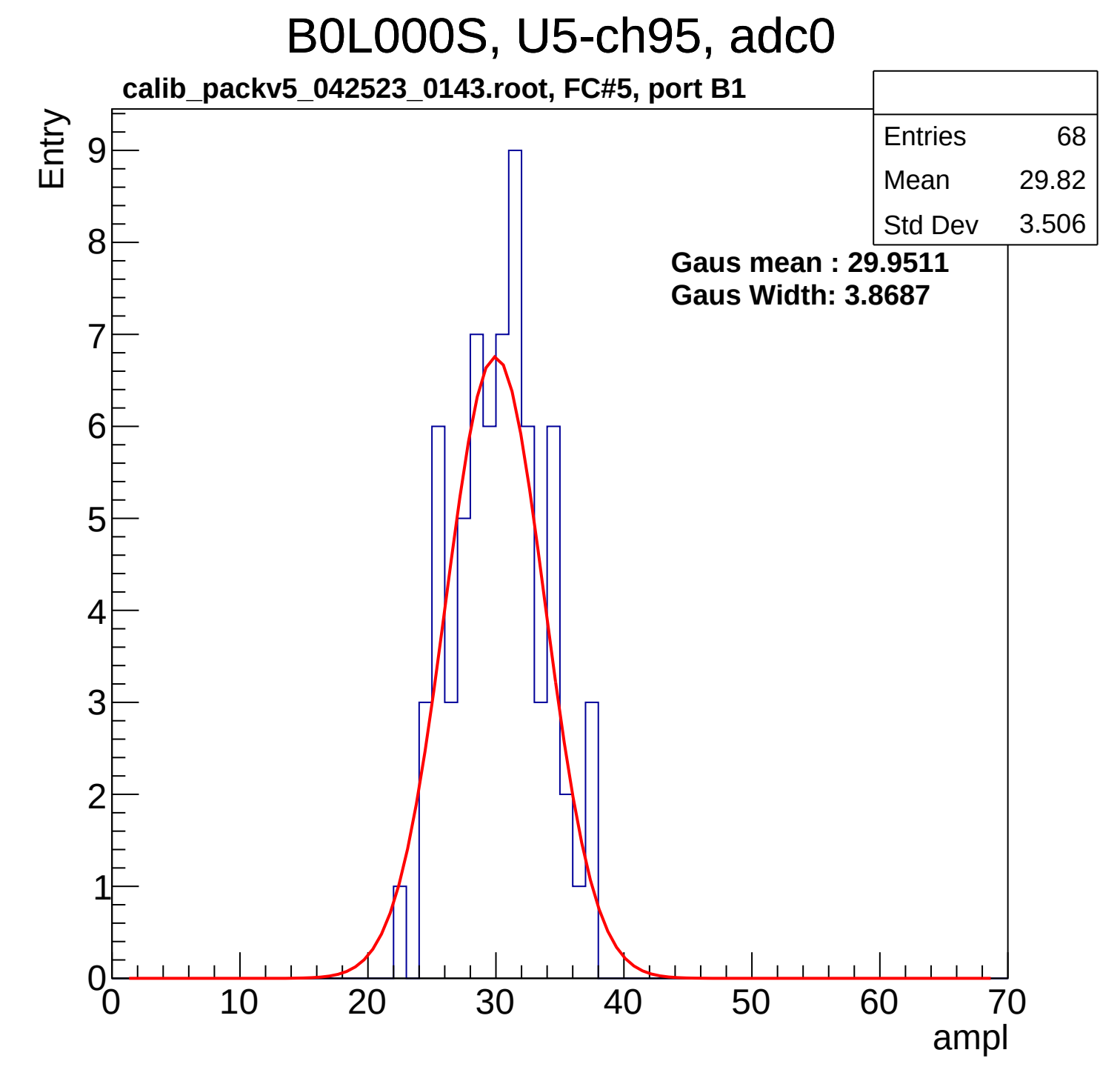
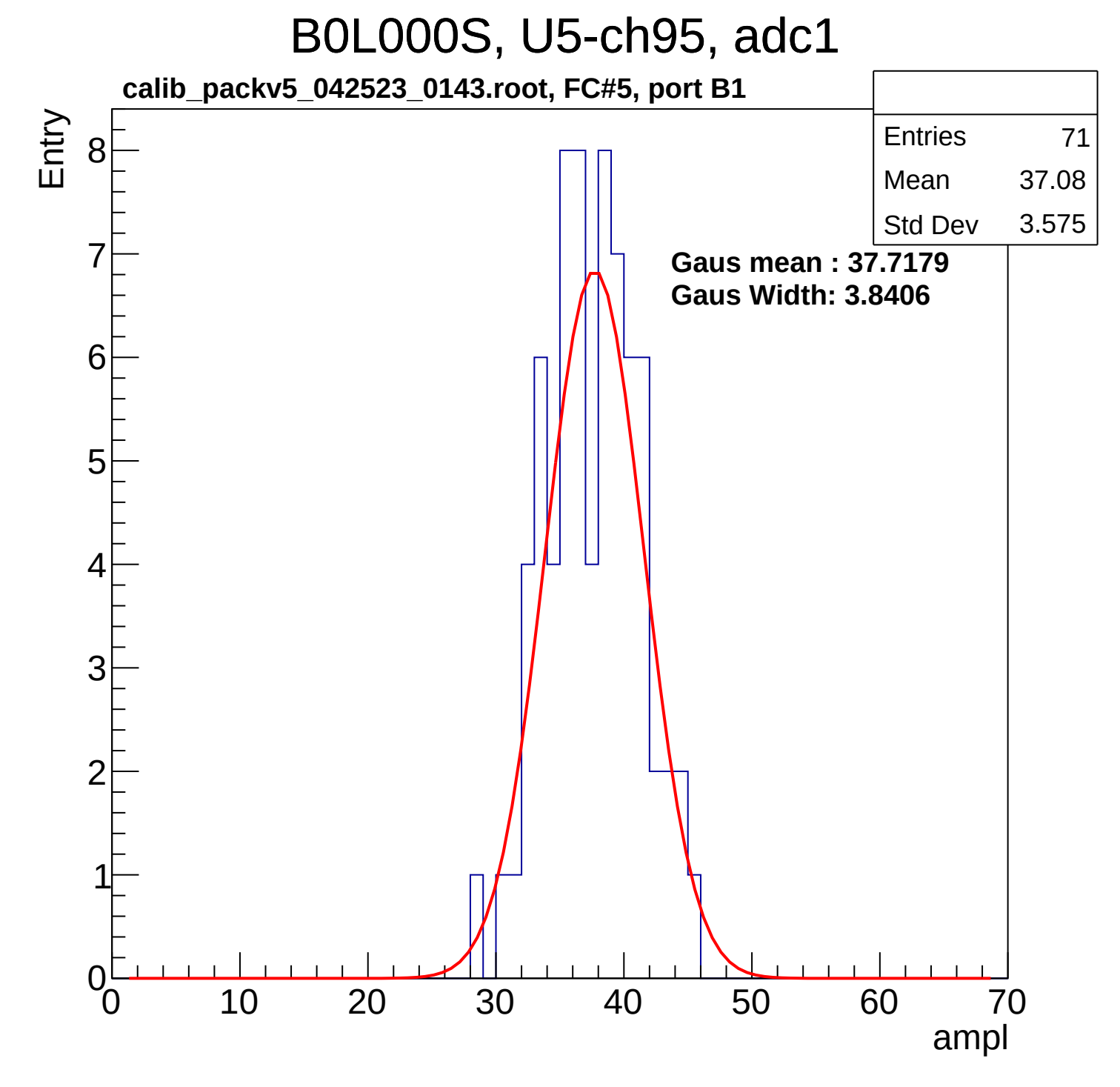
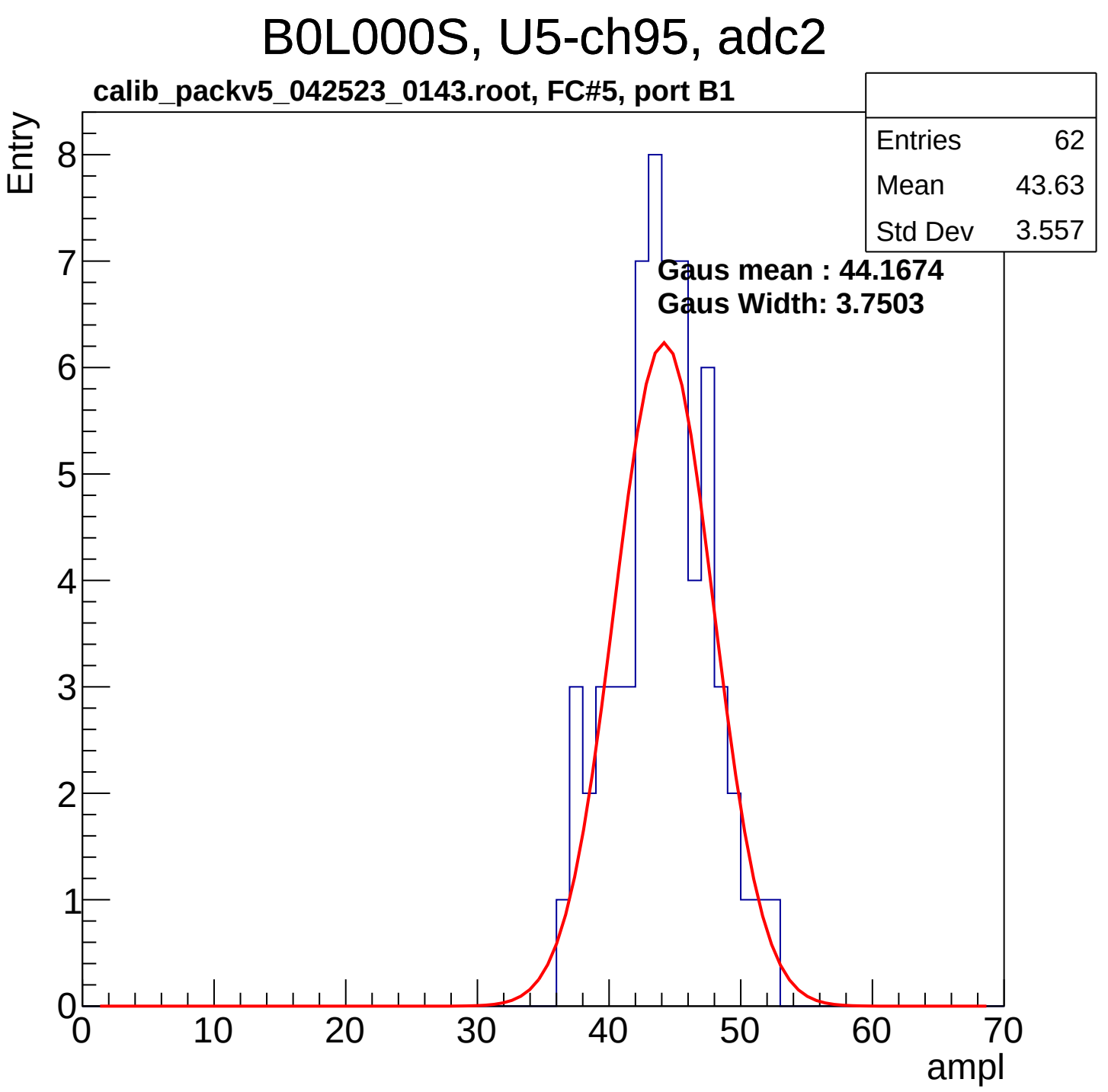

B0L000S, U5-ch95, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.82
Std Dev 3.506
Gaus mean : 29.9511
Gaus Width: 3.8687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch95, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 37.08
Std Dev 3.575
Gaus mean : 37.7179
Gaus Width: 3.8406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch95, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 43.63
Std Dev 3.557
Gaus mean : 44.1674
Gaus Width: 3.7503
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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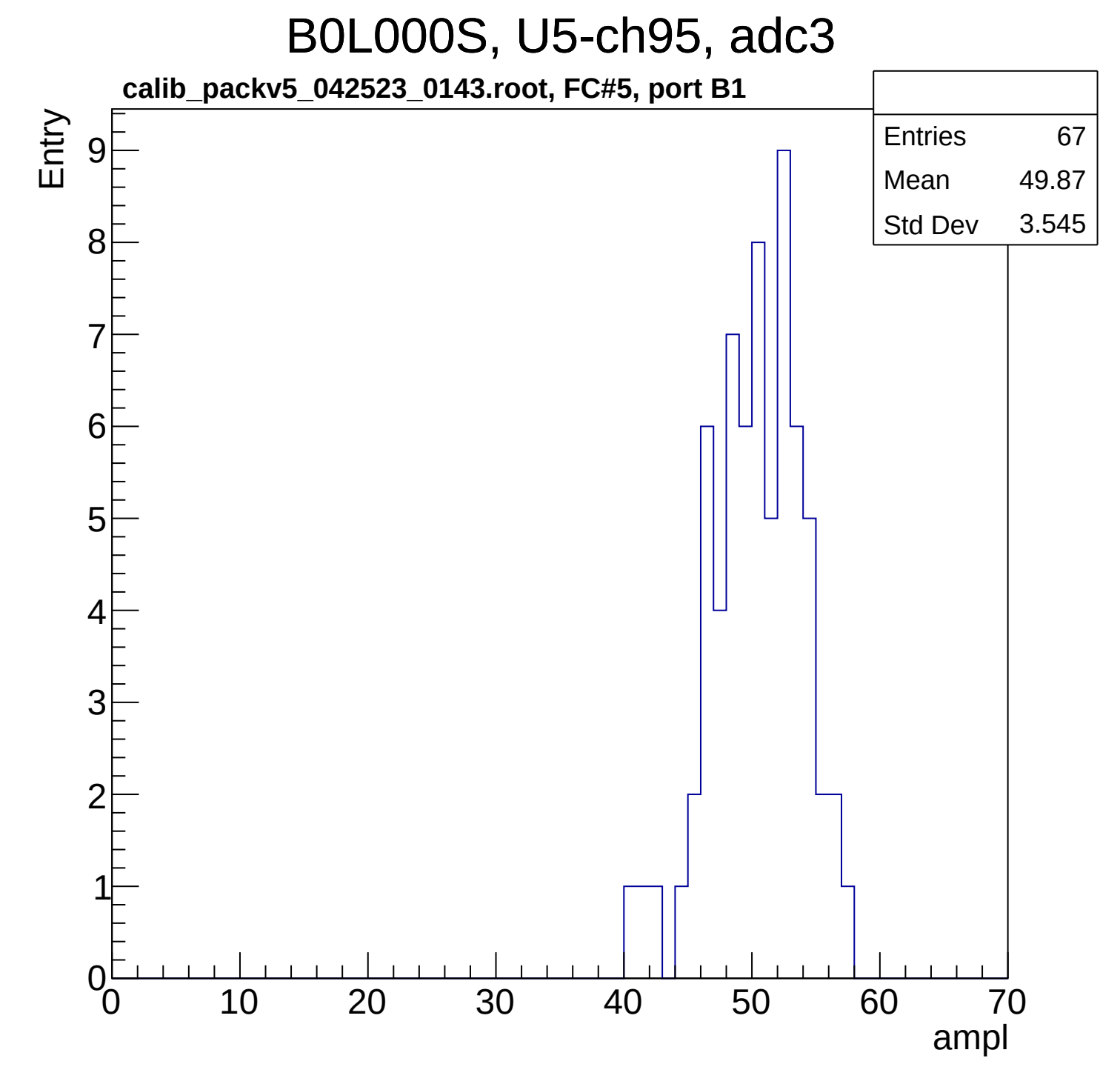
B0L000S, U5-ch95, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.87
Std Dev 3.545
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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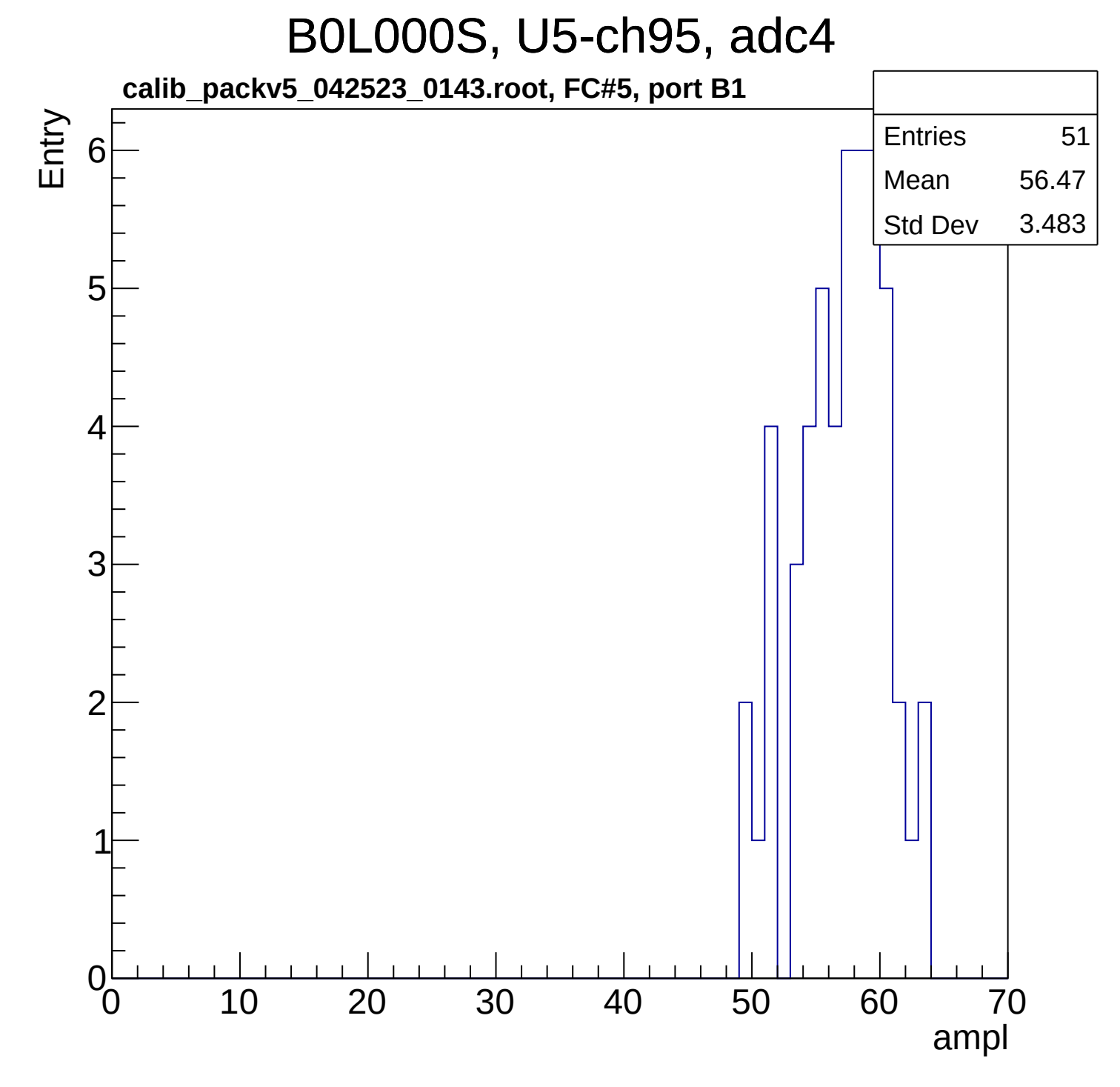
B0L000S, U5-ch95, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.47
Std Dev 3.483
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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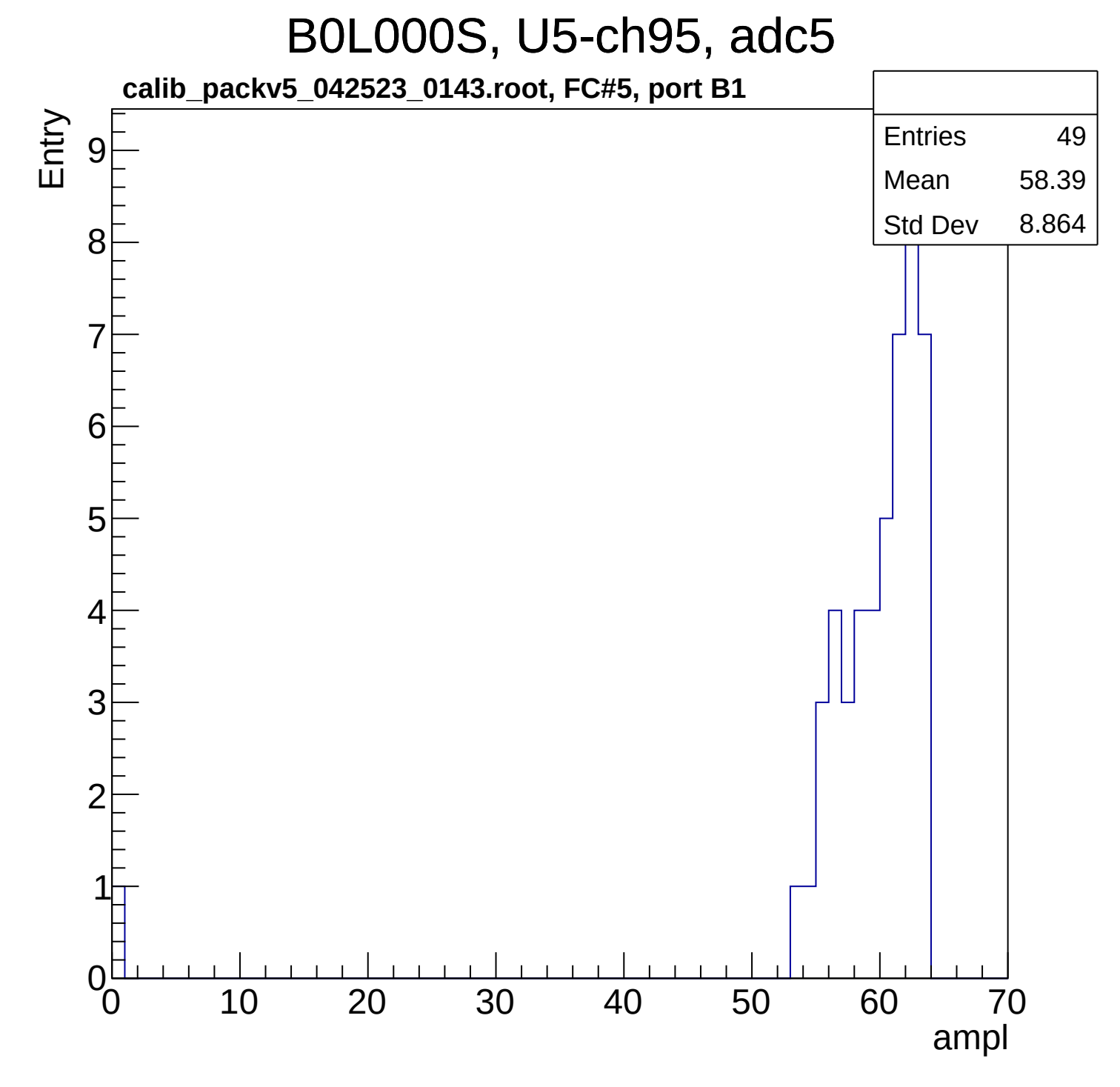
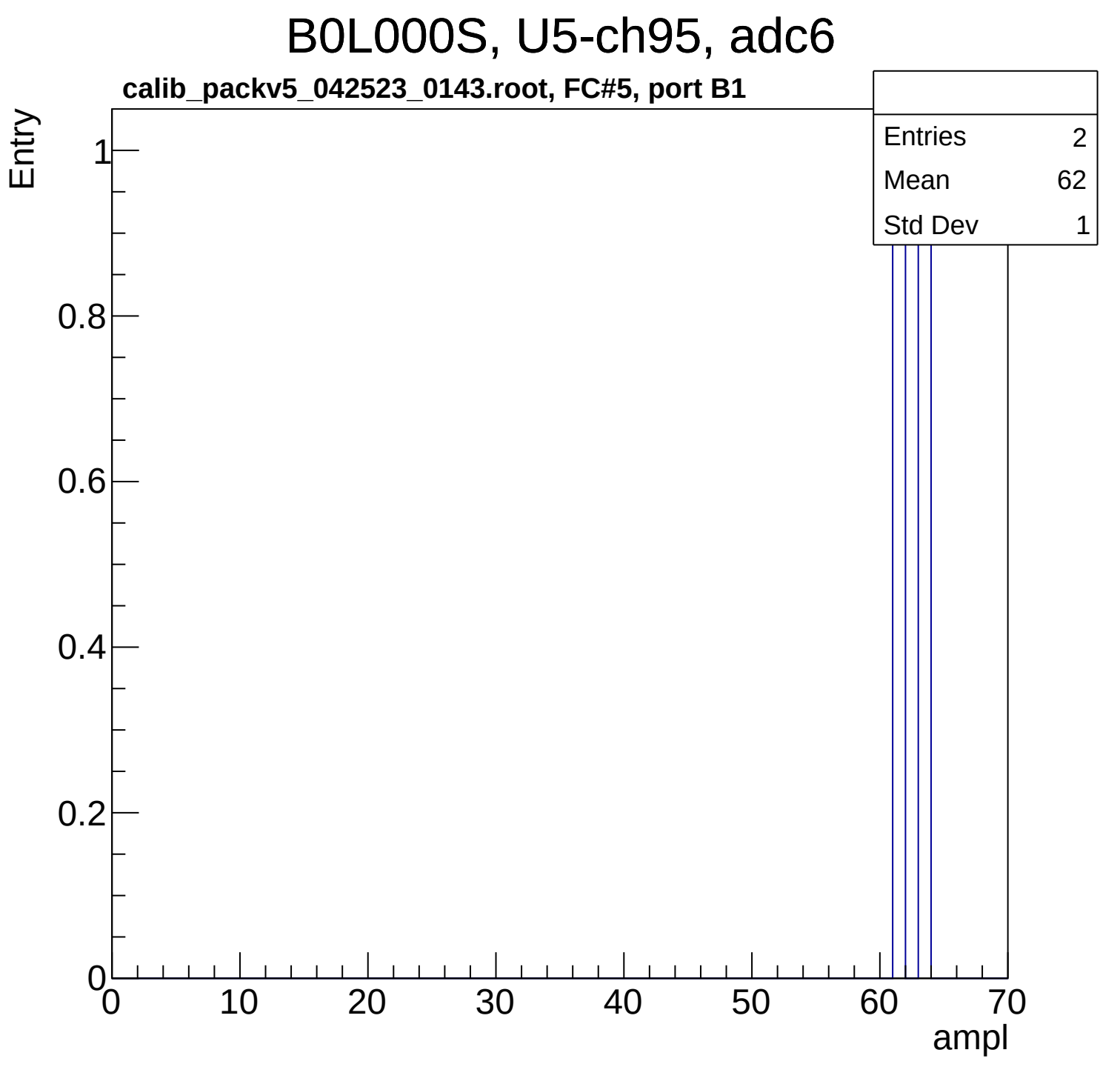
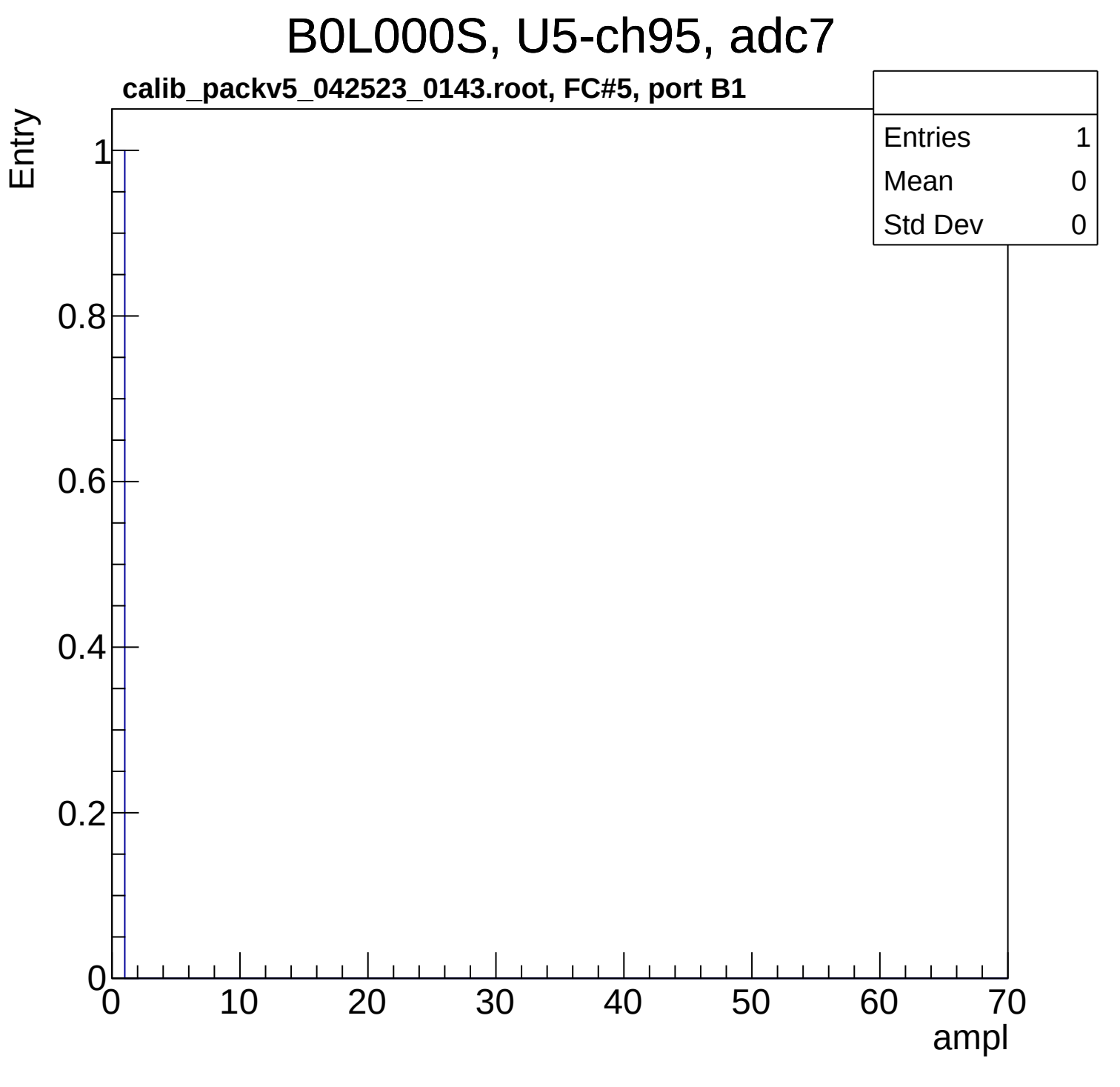
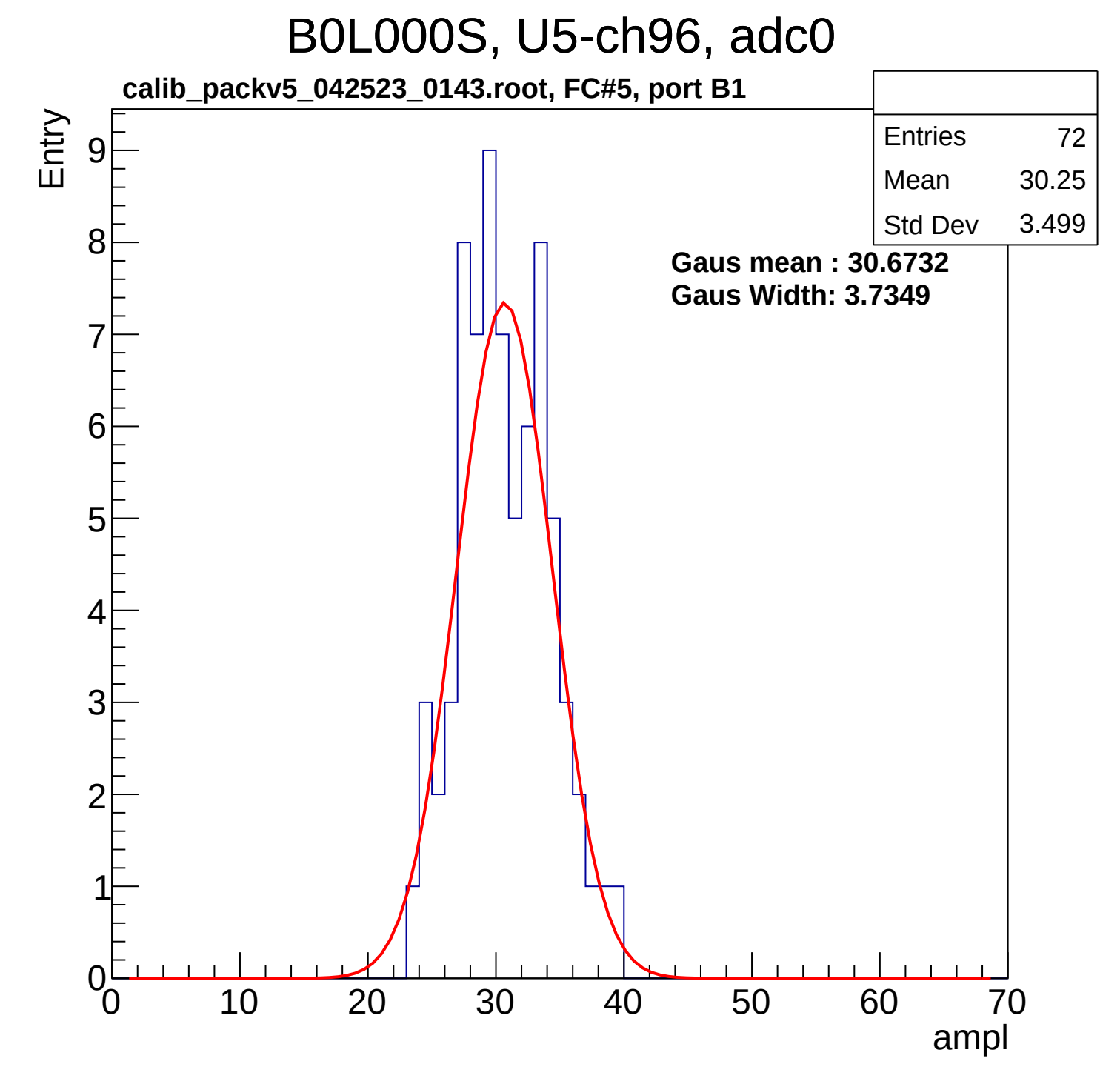

B0L000S, U5-ch95, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 49
Mean 58.39
Std Dev 8.864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch95, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch95, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch96, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 30.25
Std Dev 3.499
Gaus mean : 30.6732
Gaus Width: 3.7349
Entry
ampl
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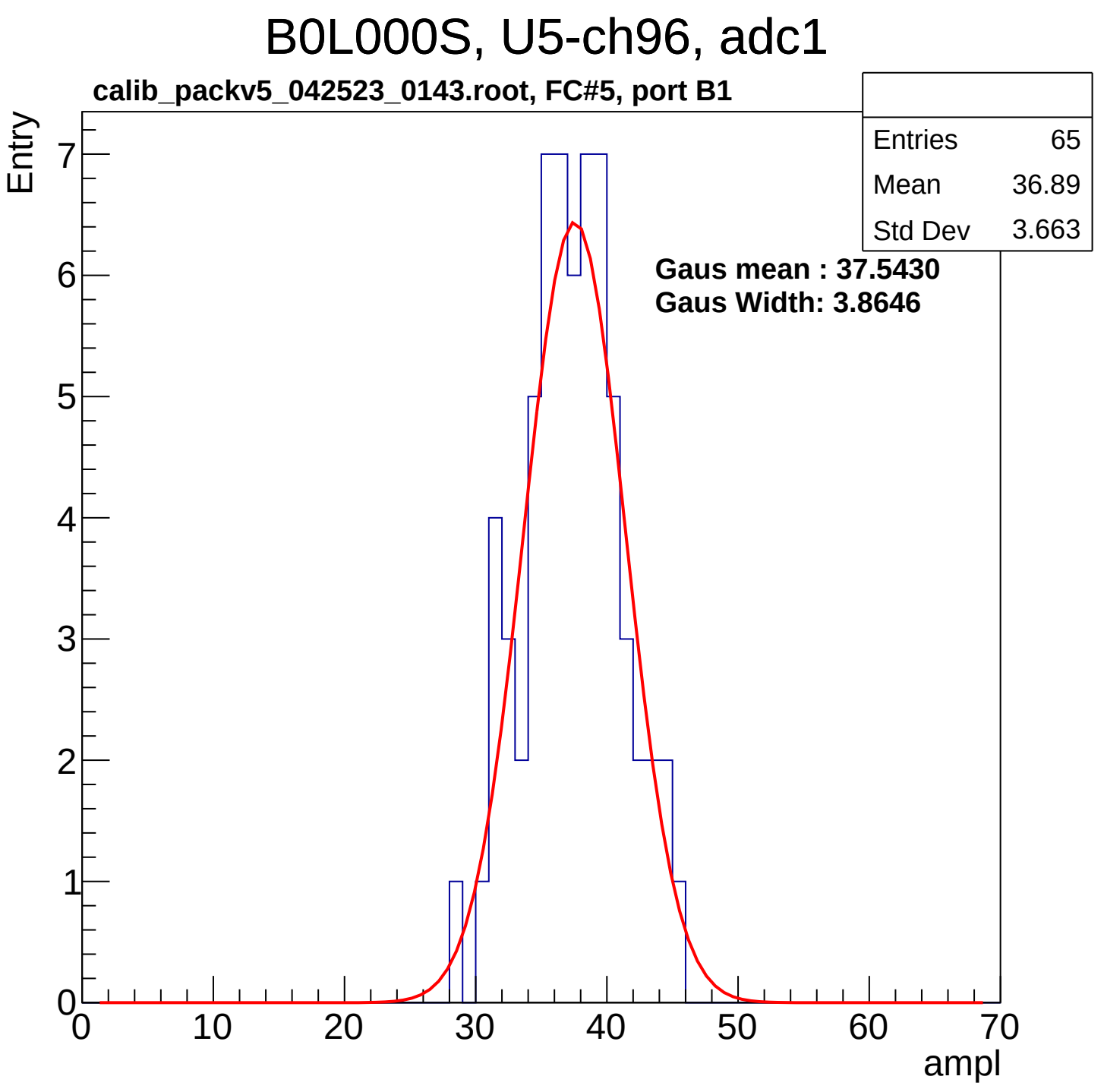
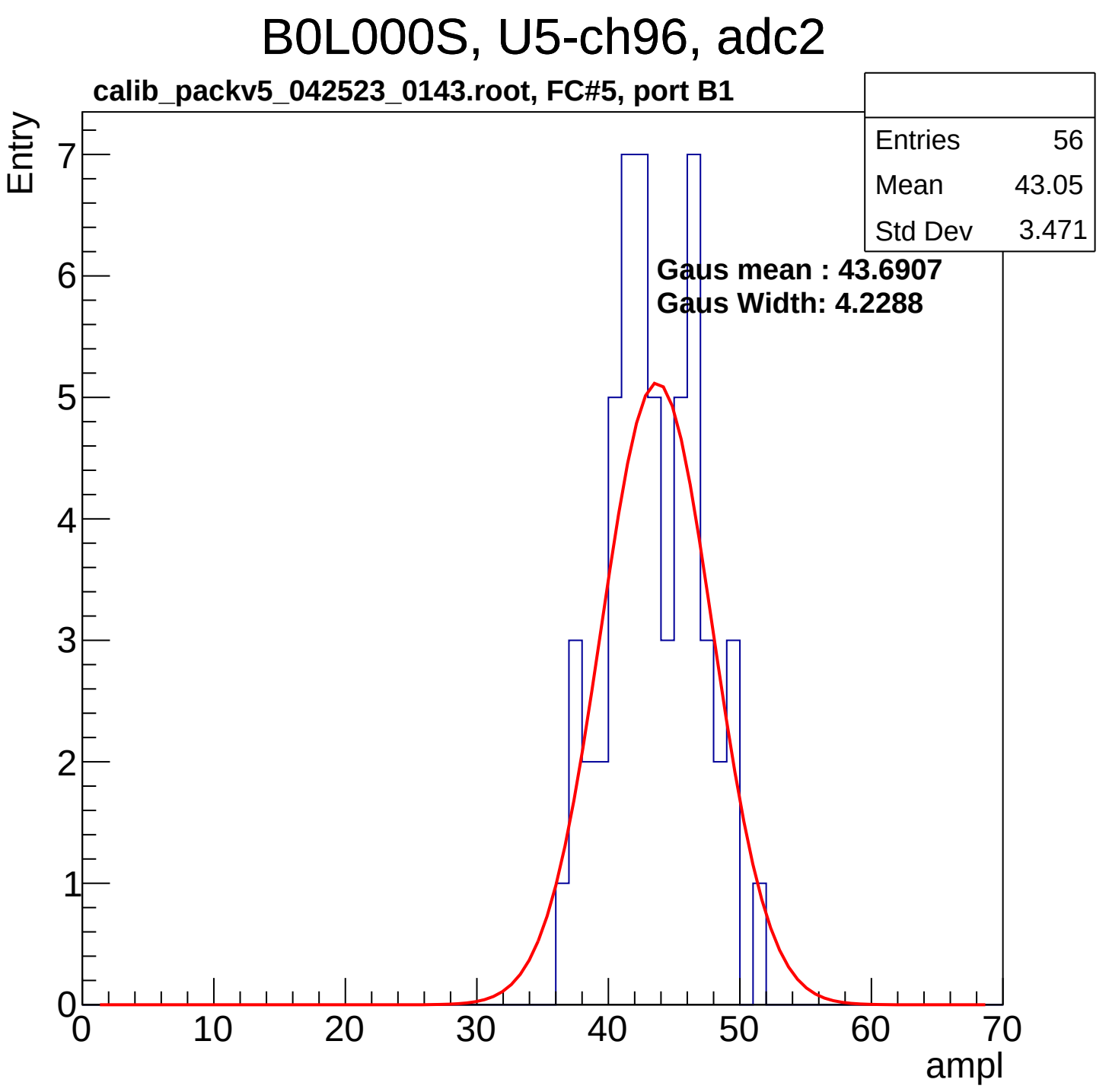
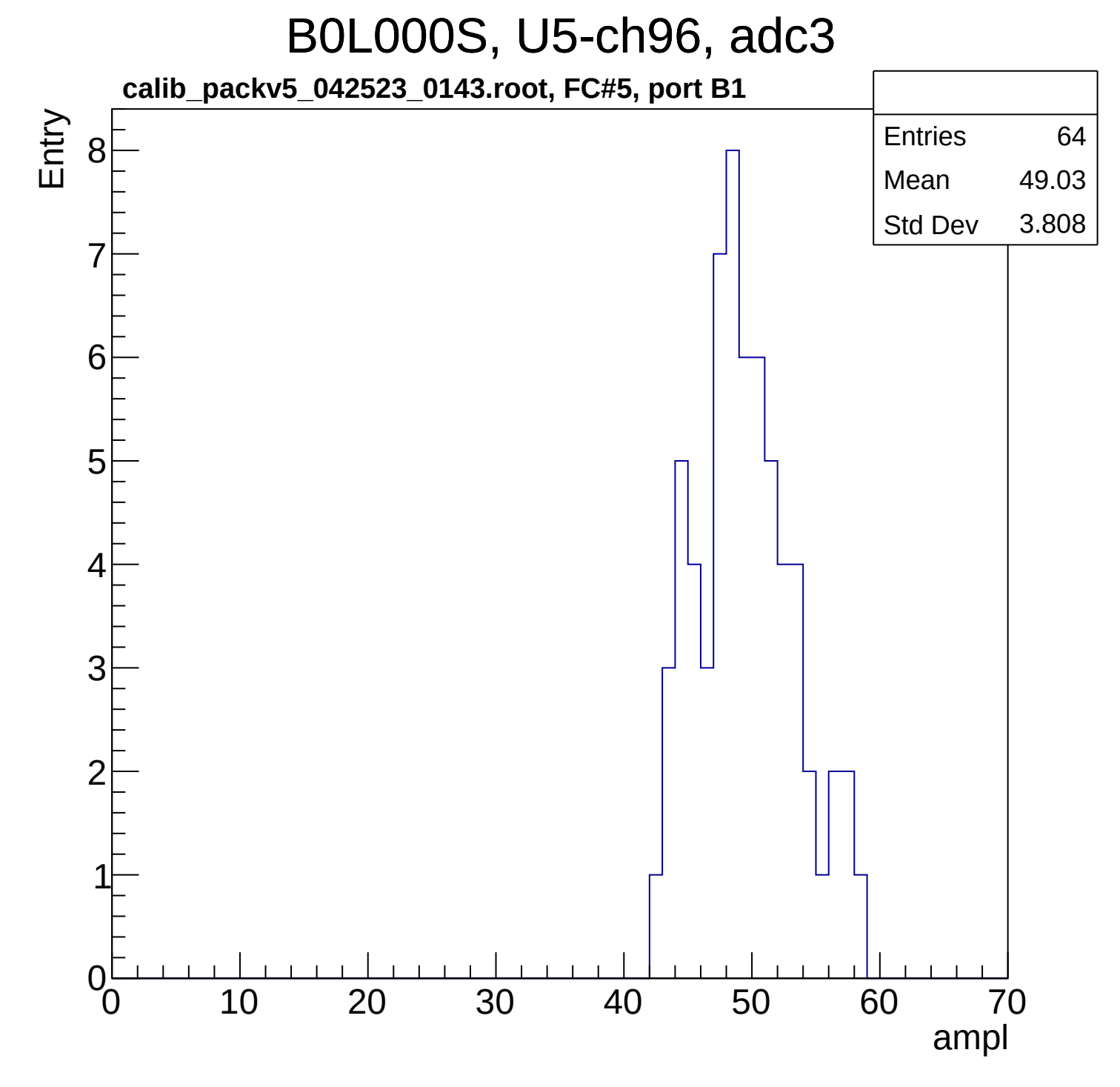
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch96, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 36.89
Std Dev 3.663
Gaus mean : 37.5430
Gaus Width: 3.8646
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch96, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 43.05
Std Dev 3.471
Gaus mean : 43.6907
Gaus Width: 4.2288
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch96, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 49.03
Std Dev 3.808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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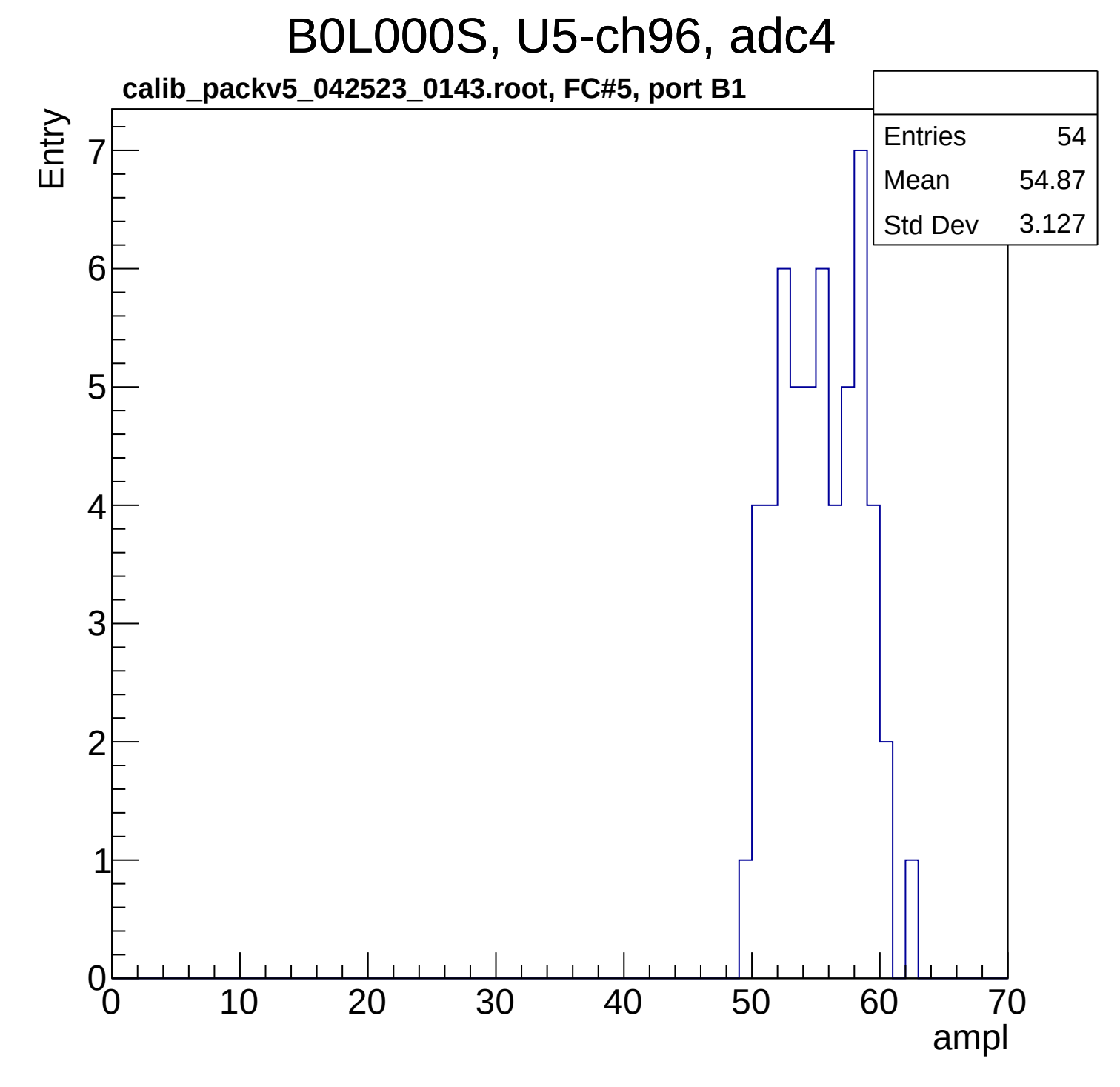
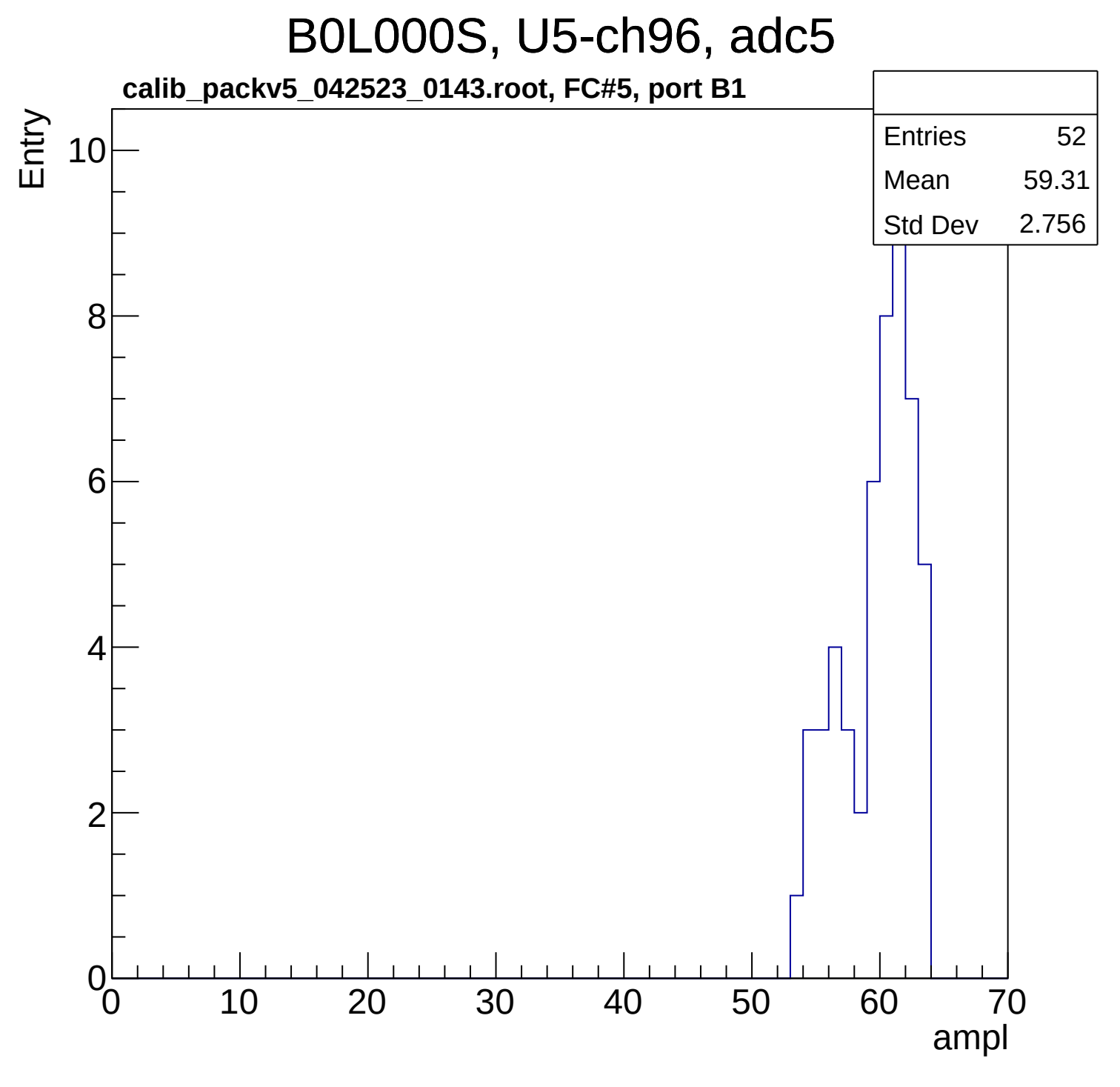
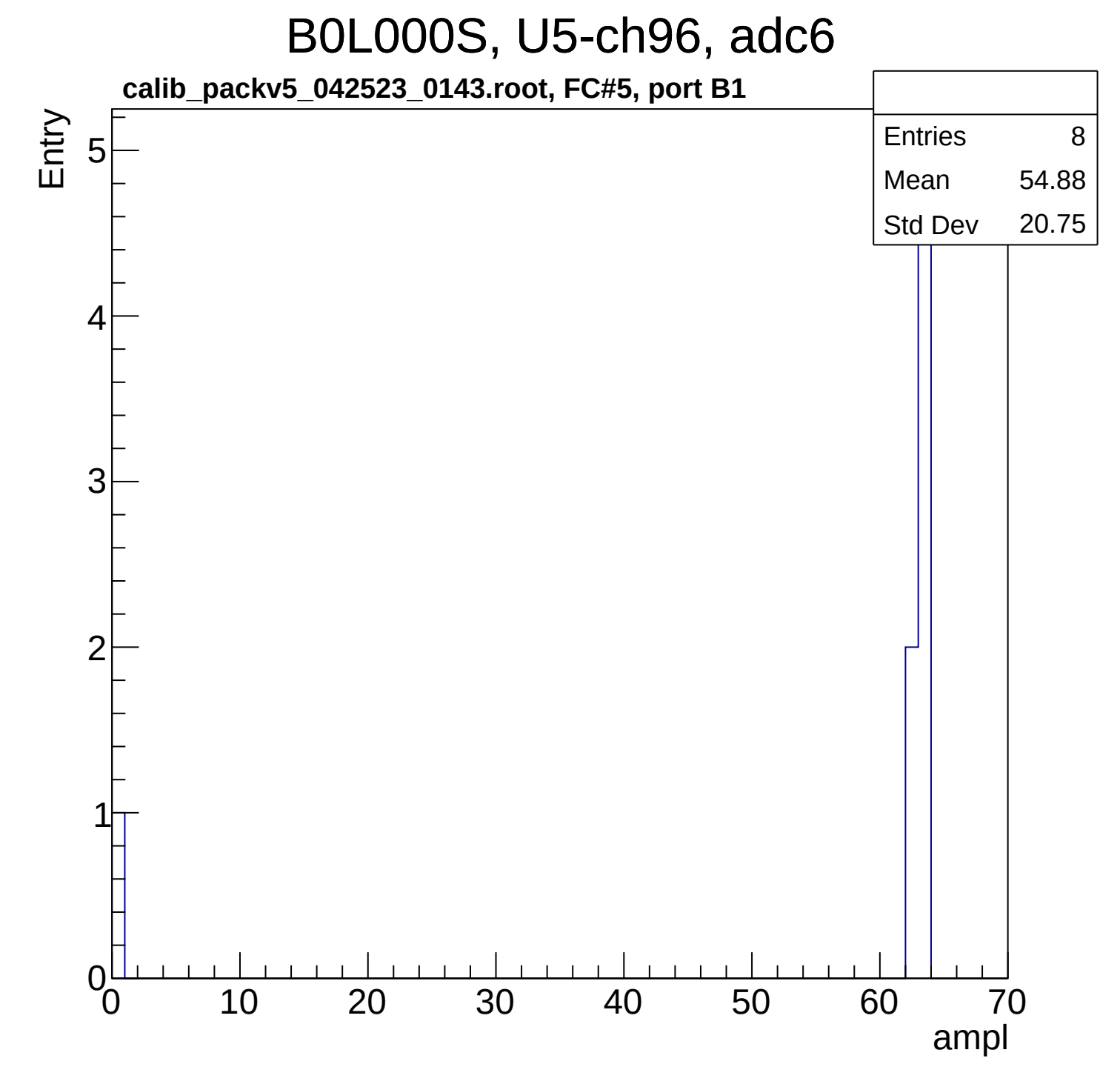
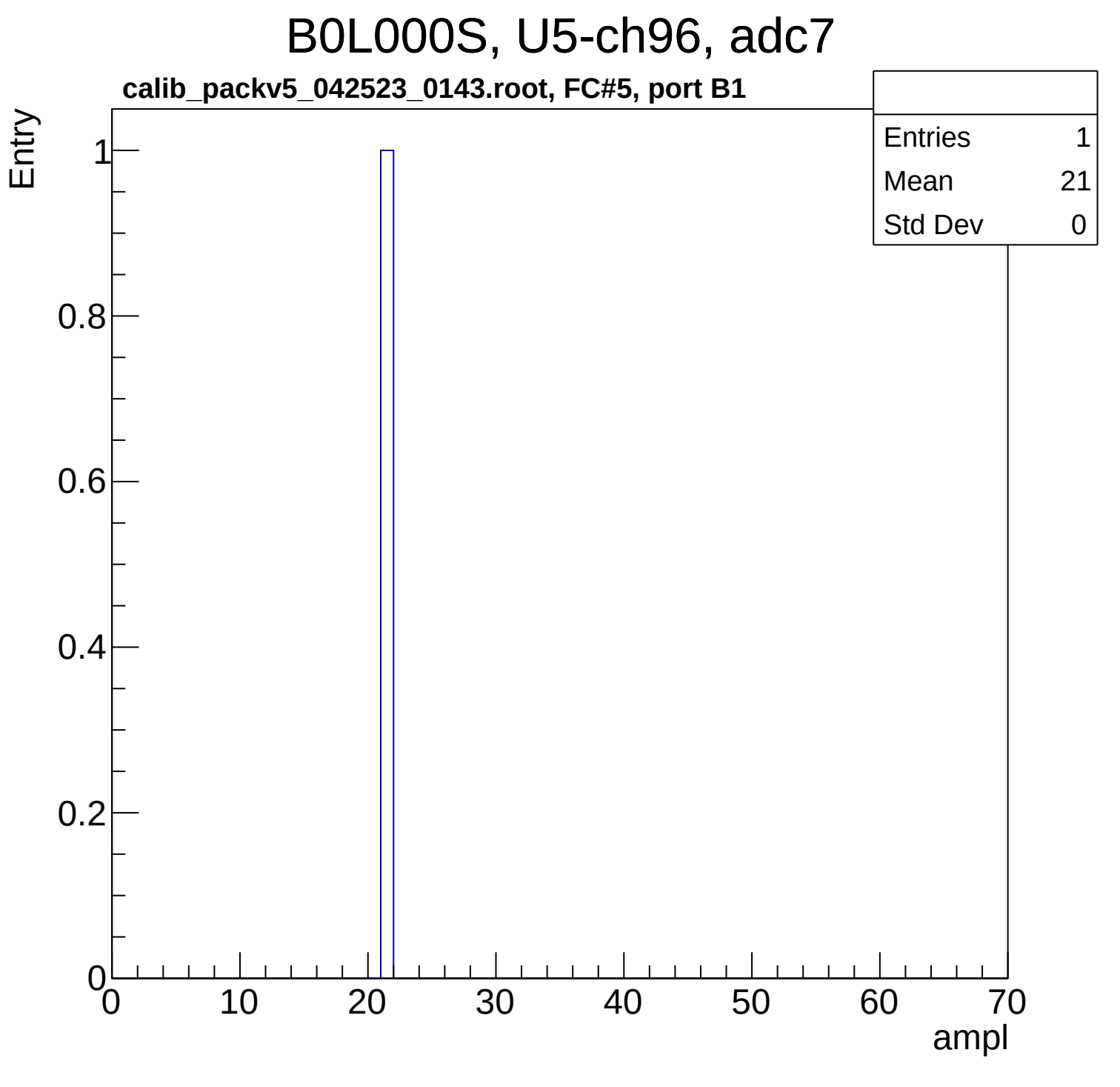
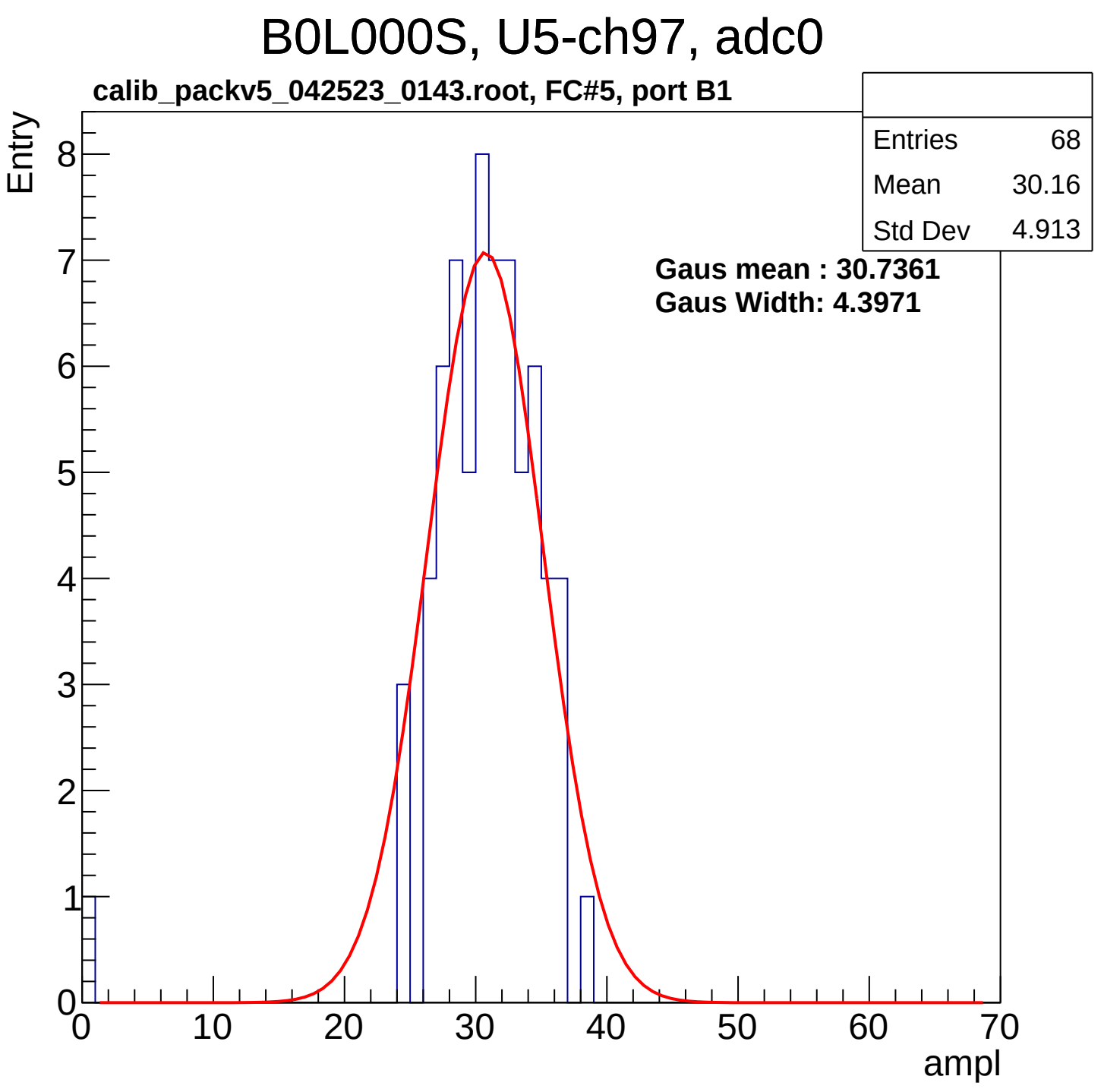
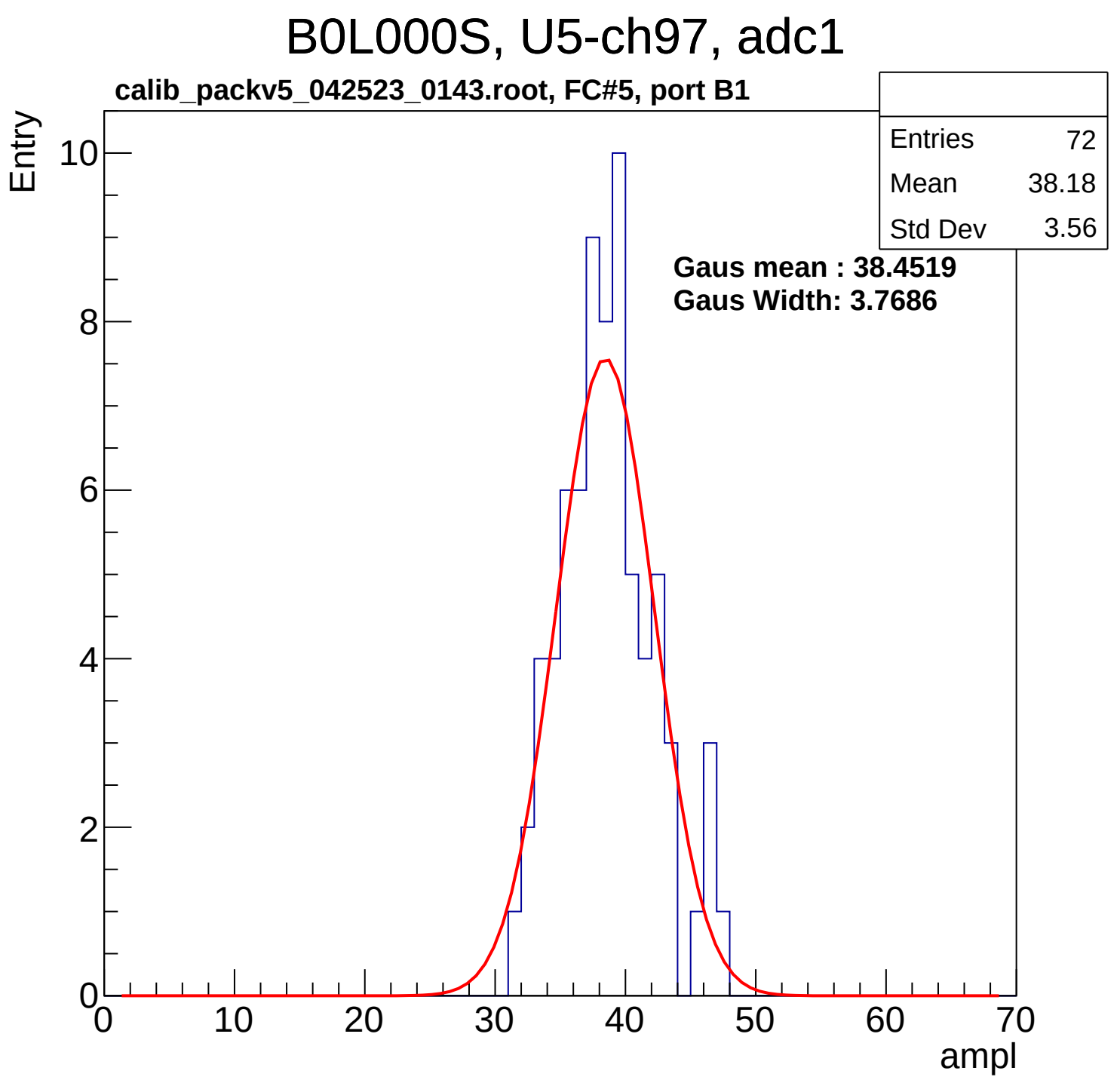

B0L000S, U5-ch96, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 54.87
Std Dev 3.127
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

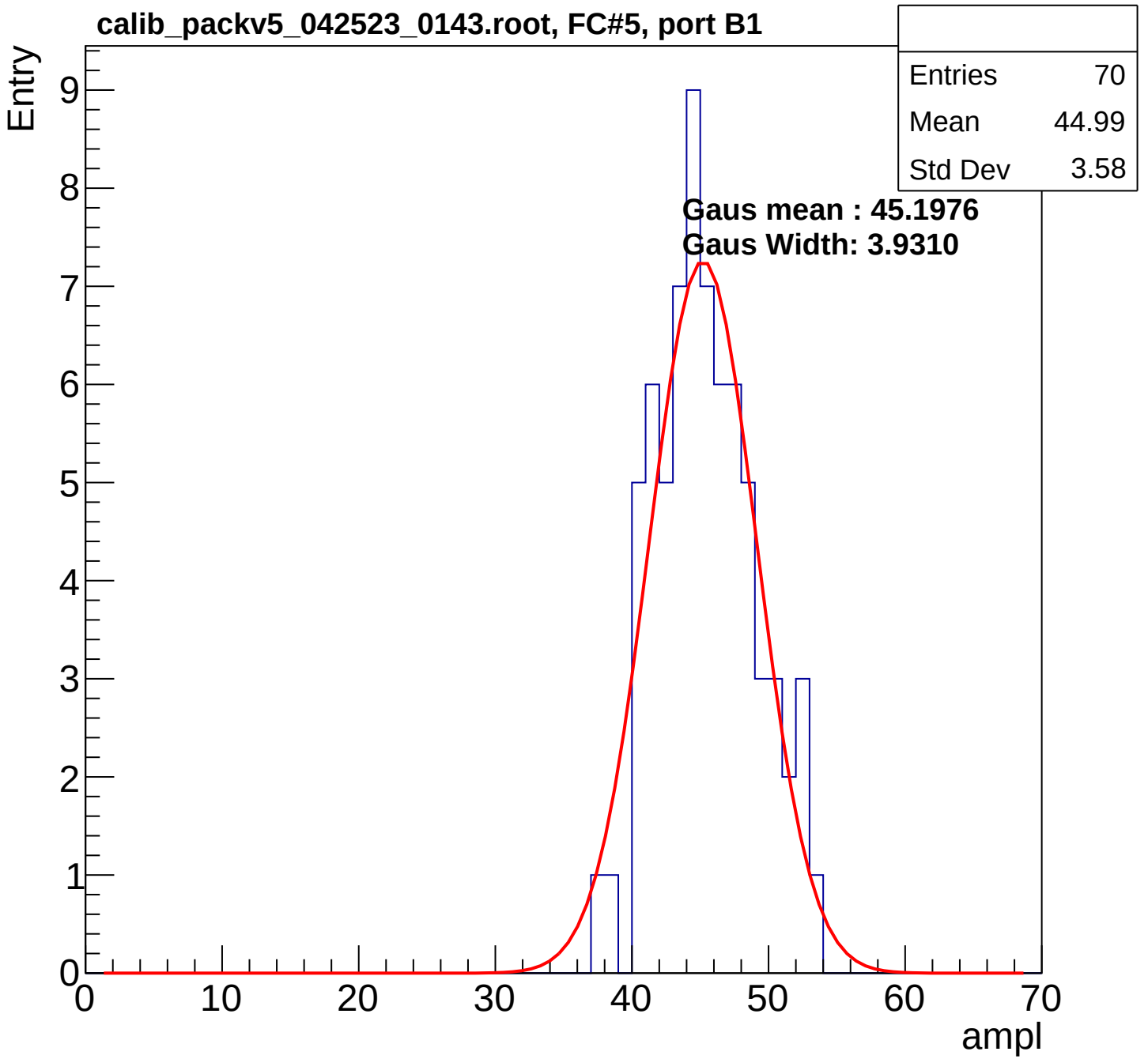

B0L000S, U5-ch96, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 59.31
Std Dev 2.756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

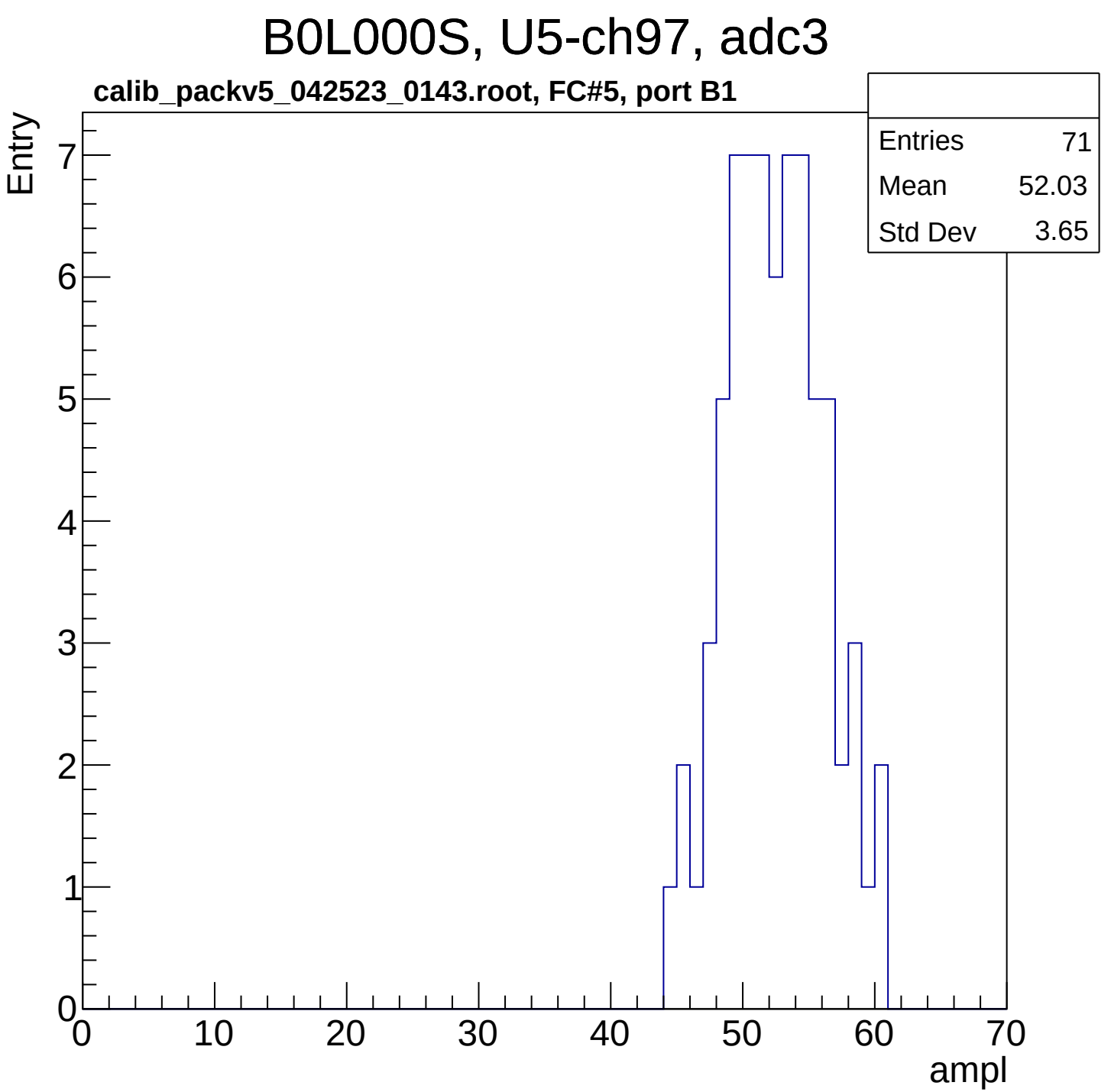

B0L000S, U5-ch96, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.88
Std Dev 20.75
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

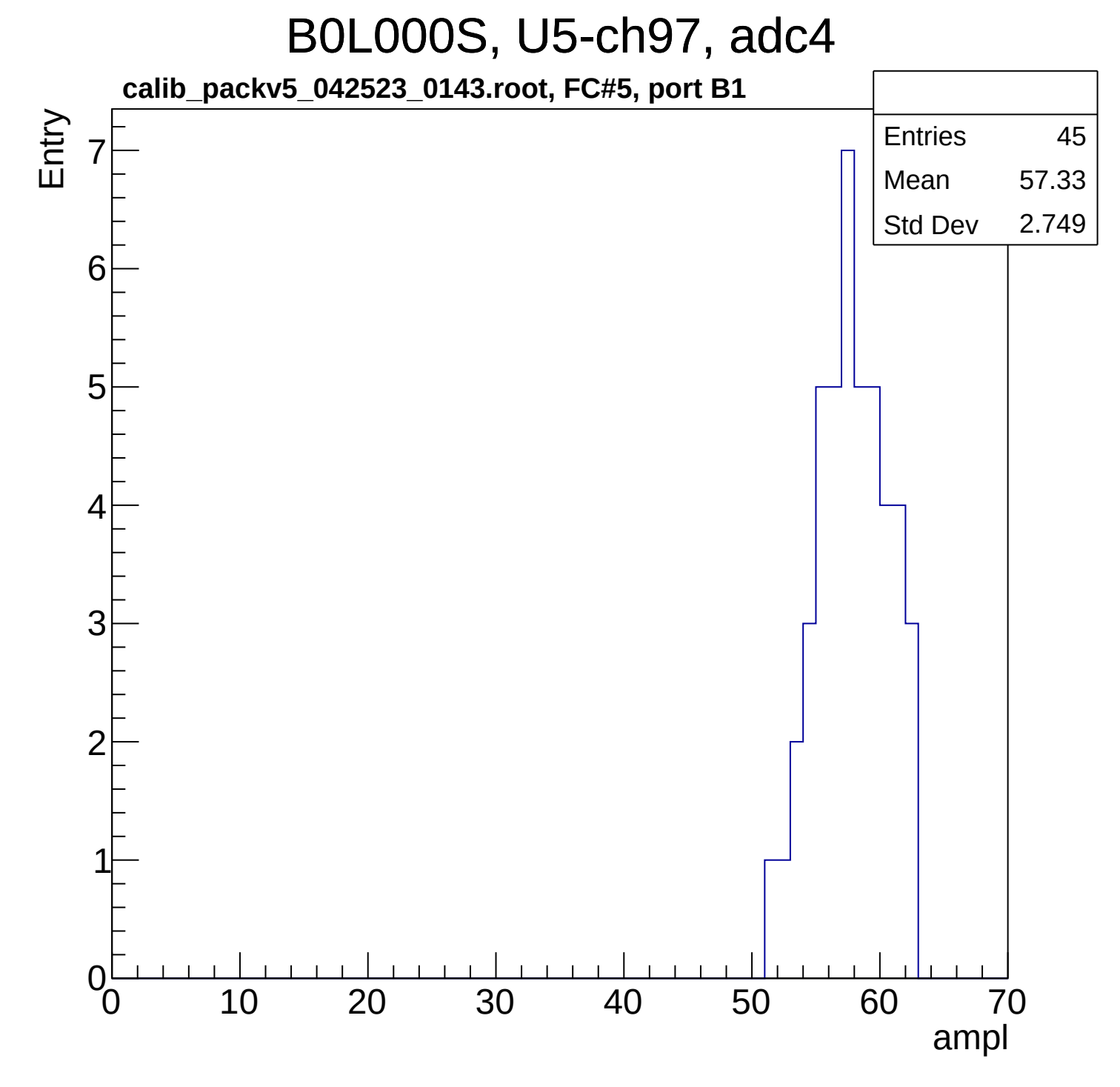

B0L000S, U5-ch96, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch97, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 30.16
Std Dev 4.913
Gaus mean : 30.7361
Gaus Width: 4.3971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch97, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 38.18
Std Dev 3.56
Gaus mean : 38.4519
Gaus Width: 3.7686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch97, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 44.99
Std Dev 3.58
Gaus mean : 45.1976
Gaus Width: 3.9310
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch97, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 52.03
Std Dev 3.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch97, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 57.33
Std Dev 2.749
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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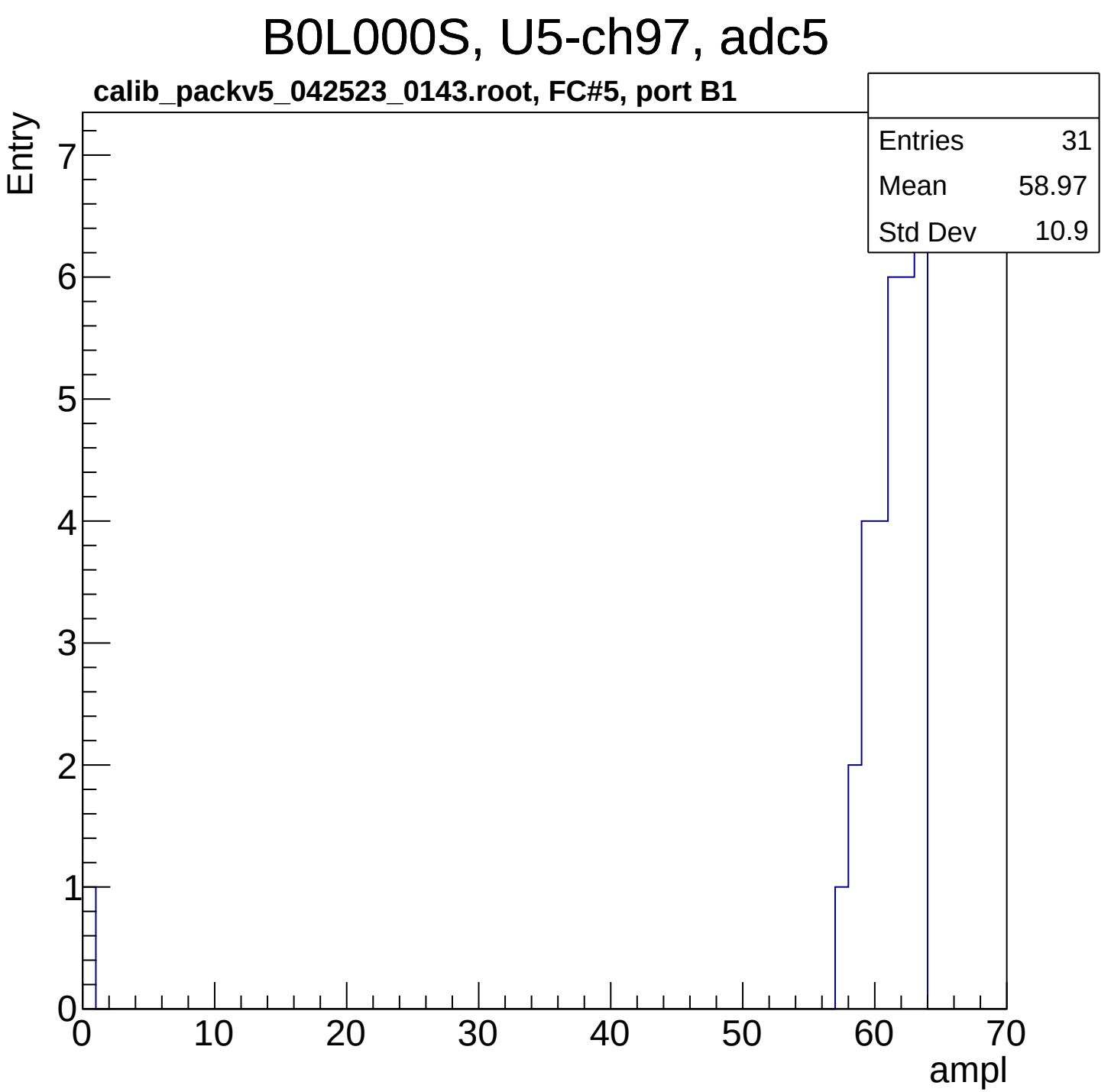
B0L000S, U5-ch97, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 58.97
Std Dev 10.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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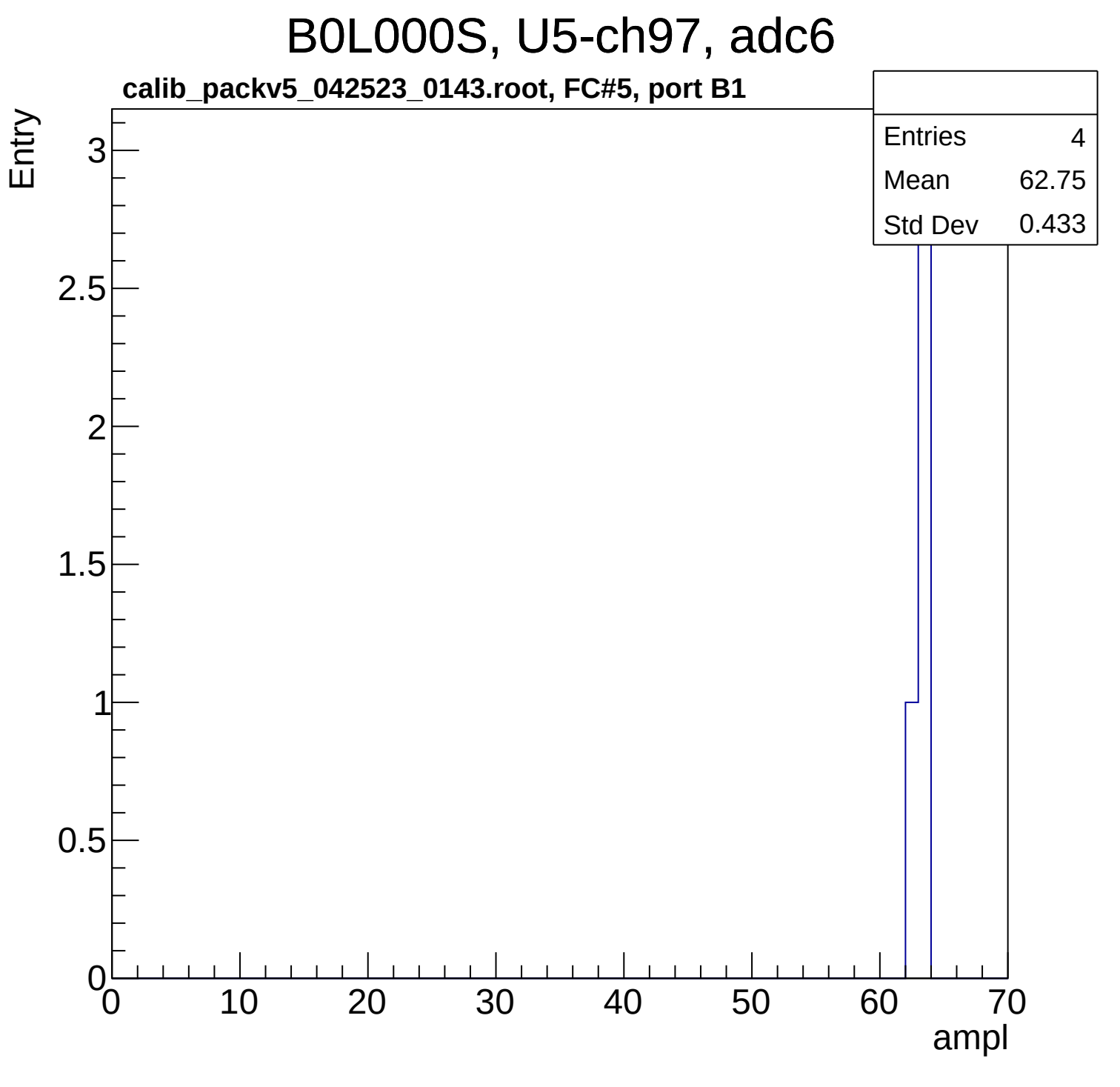
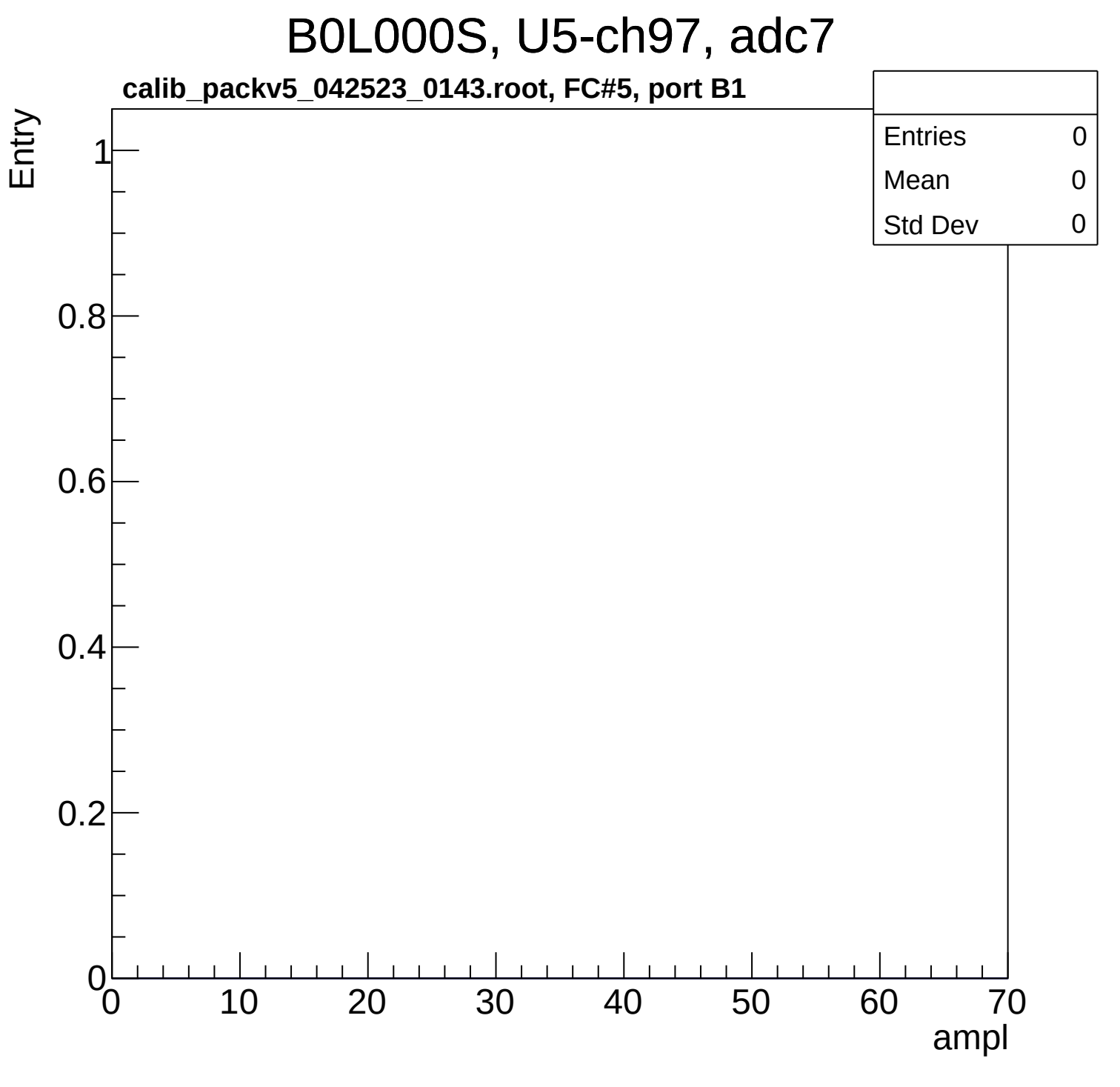
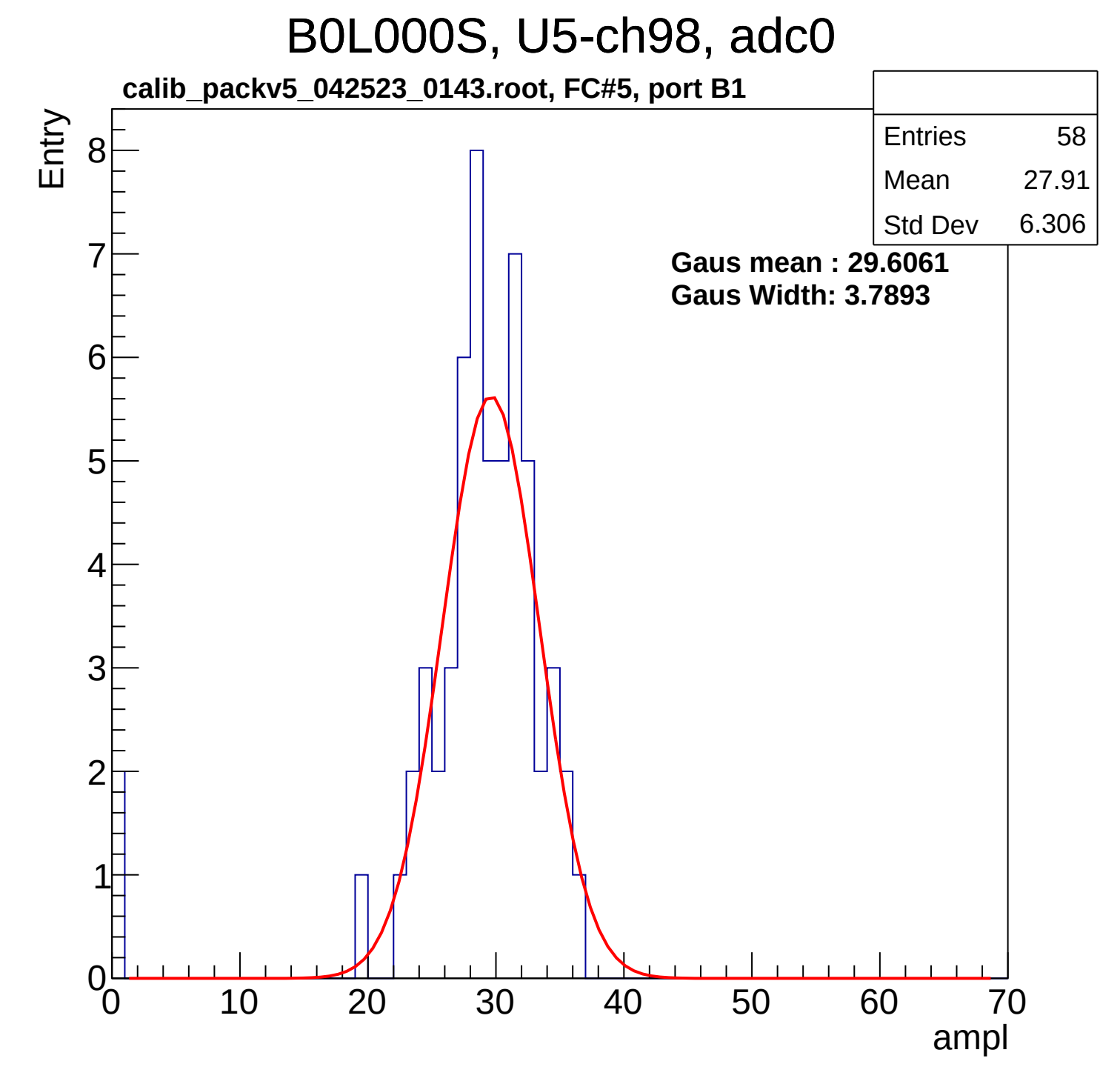
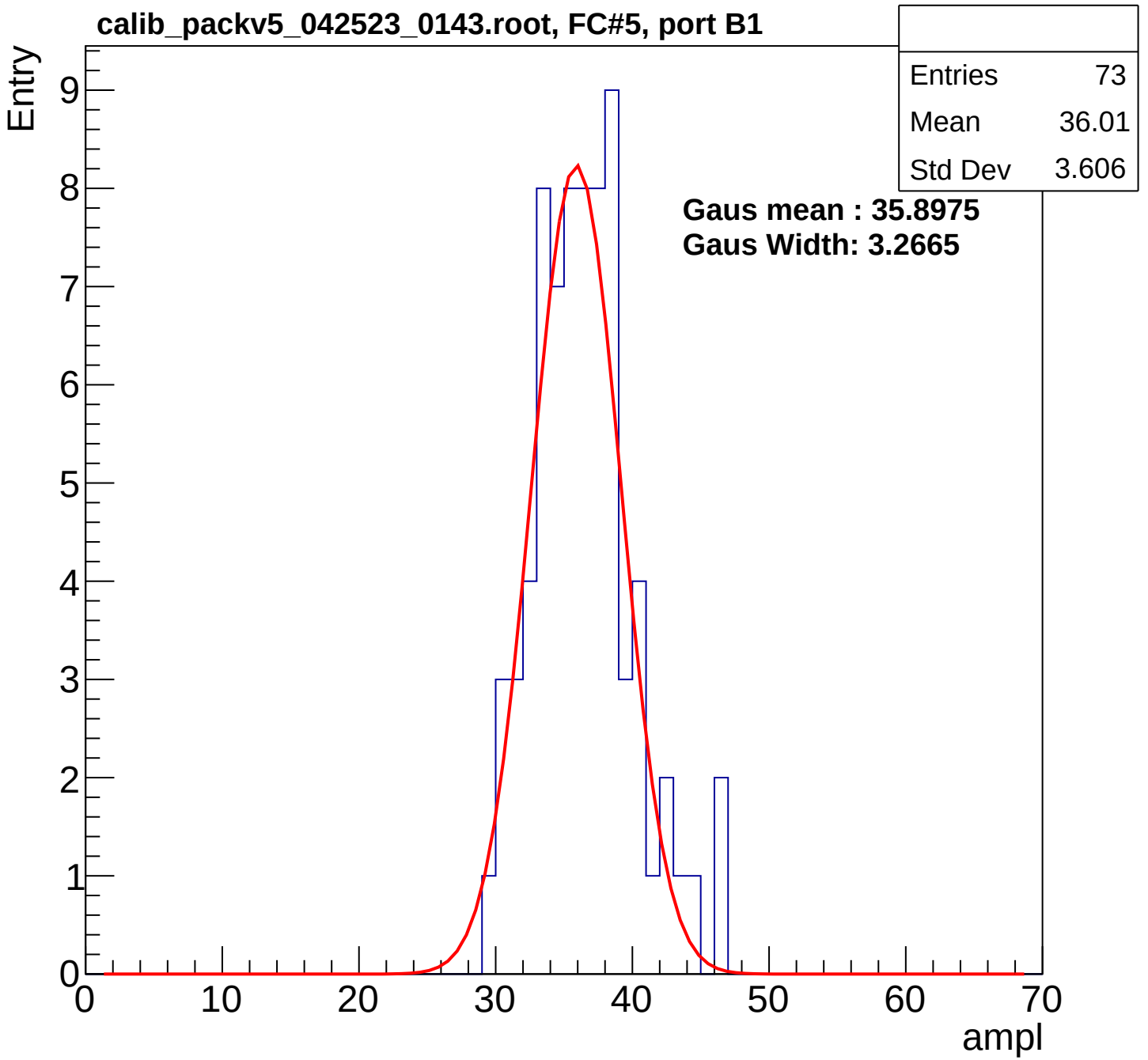

B0L000S, U5-ch97, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch97, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch98, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 27.91
Std Dev 6.306
Gaus mean : 29.6061
Gaus Width: 3.7893
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch98, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 36.01
Std Dev 3.606
Gaus mean : 35.8975
Gaus Width: 3.2665
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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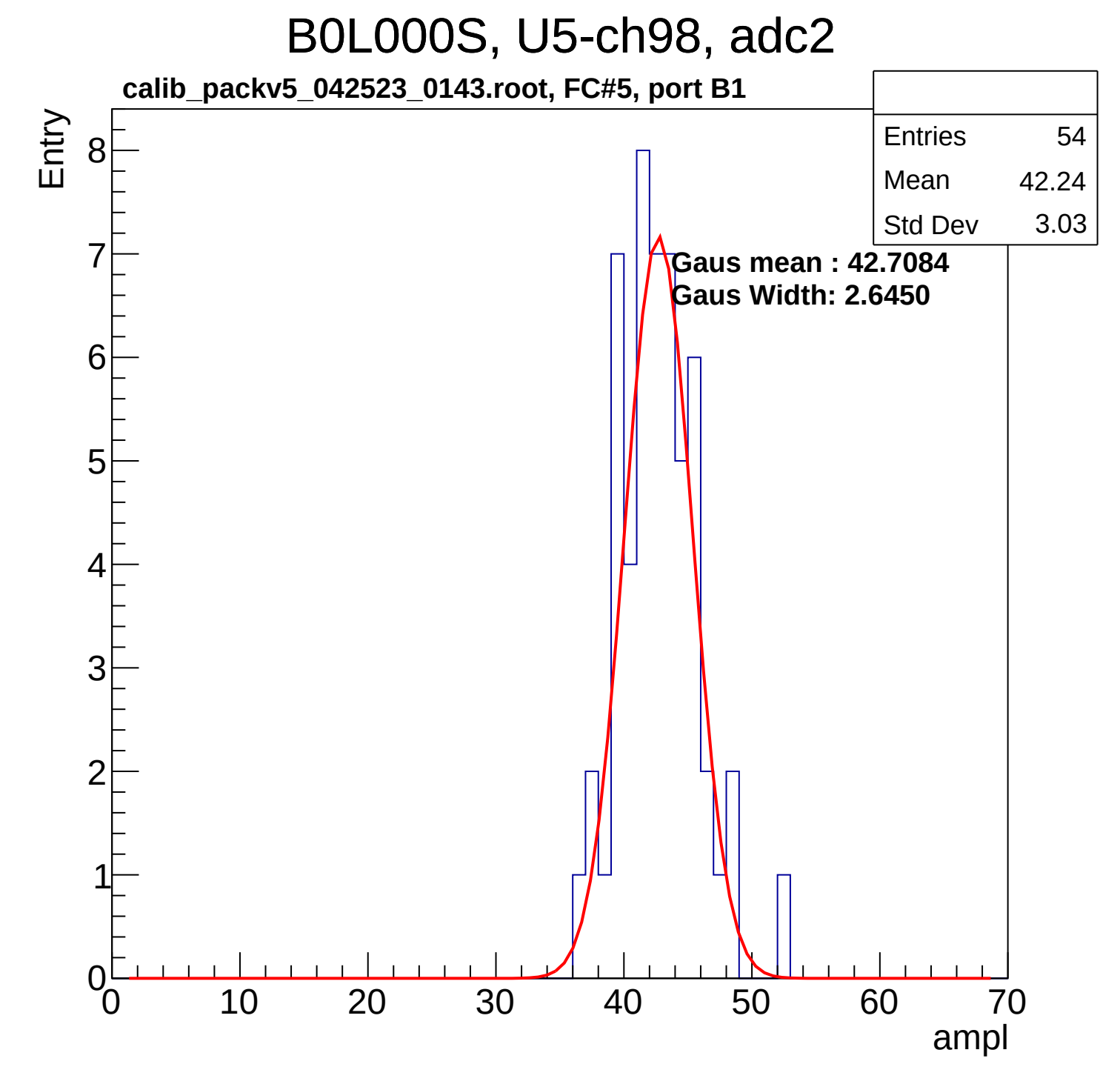
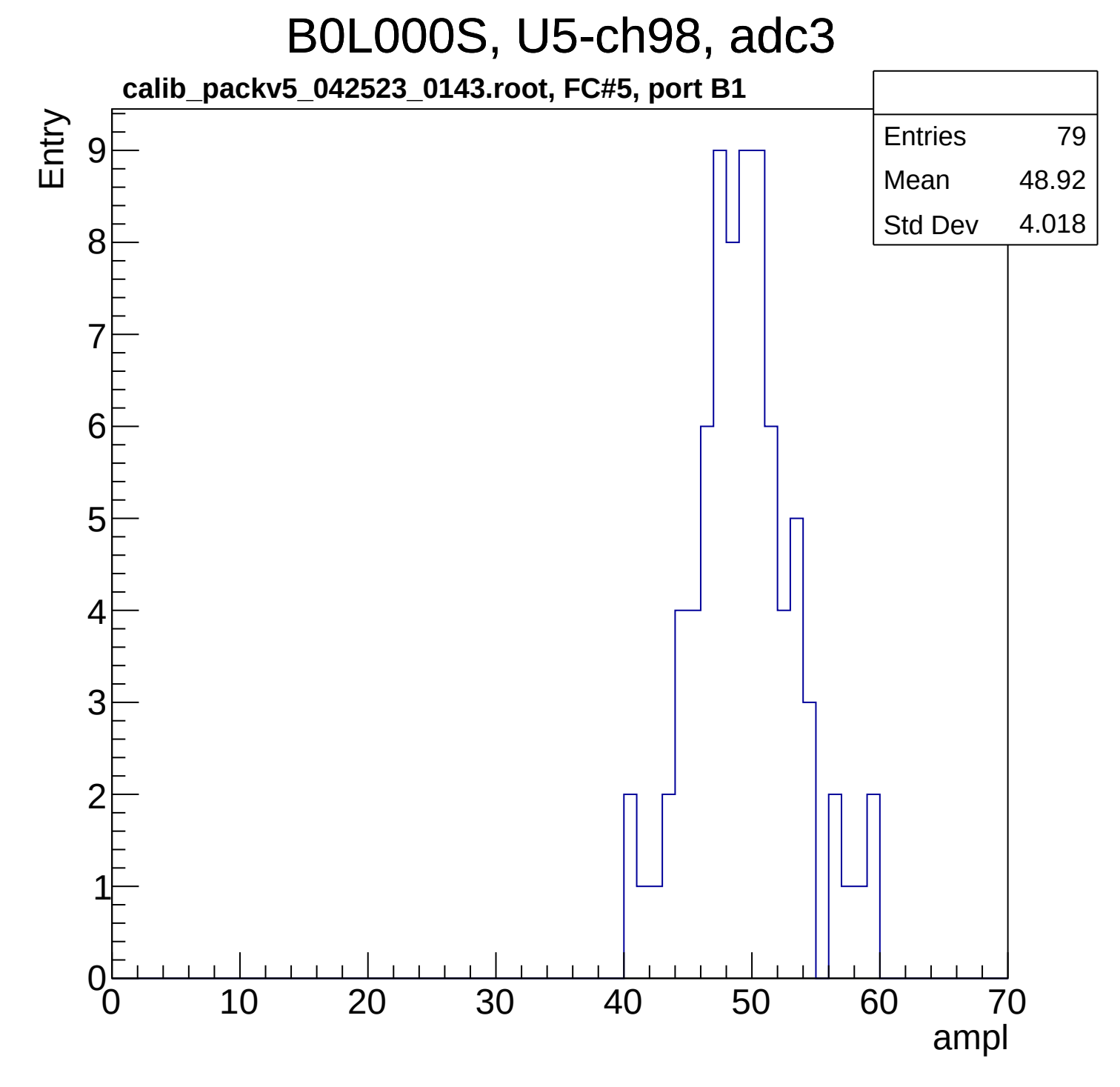
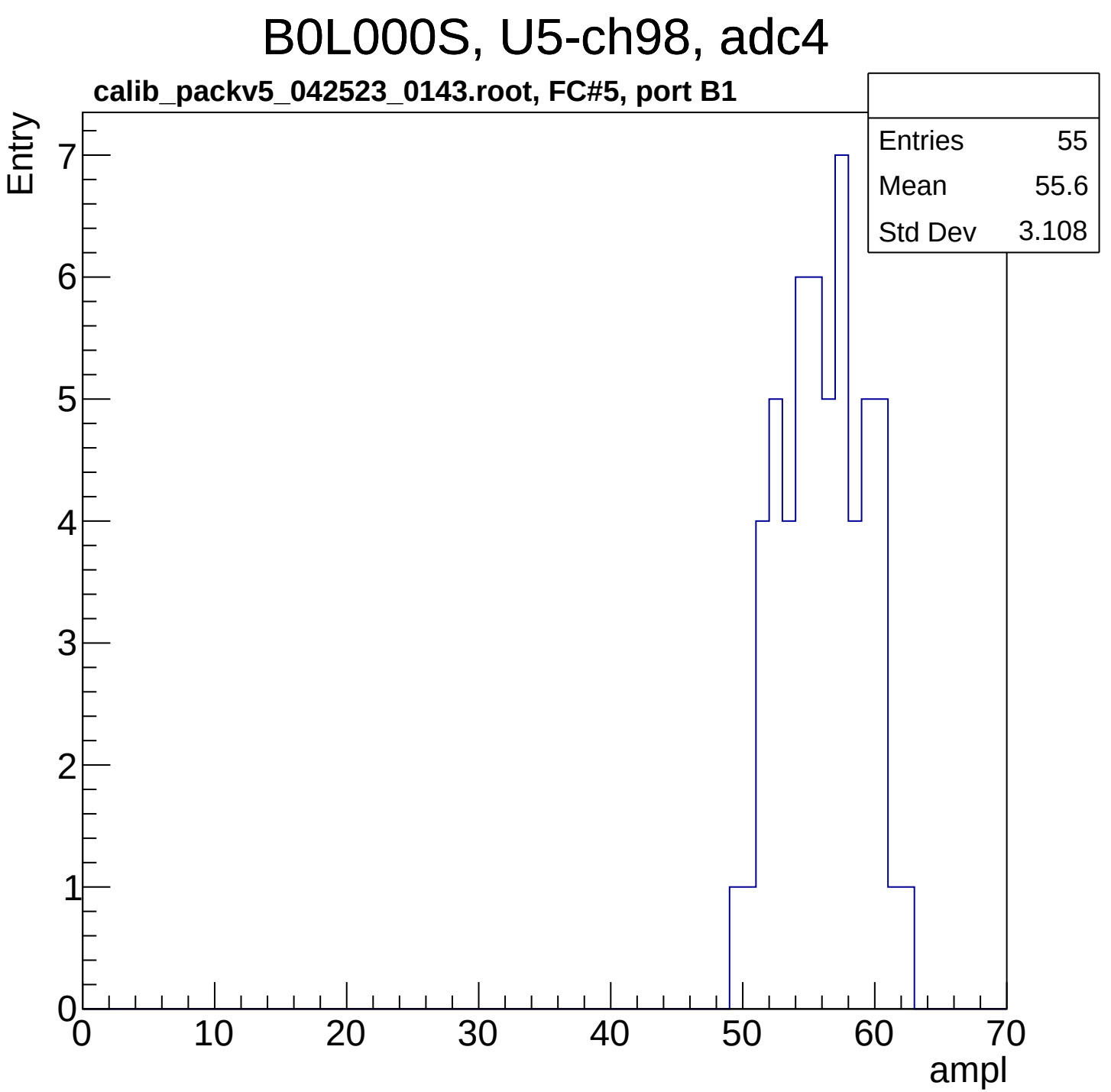

B0L000S, U5-ch98, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 42.24
Std Dev 3.03
Gaus mean : 42.7084
Gaus Width: 2.6450
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch98, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 48.92
Std Dev 4.018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch98, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 55.6
Std Dev 3.108
Entry
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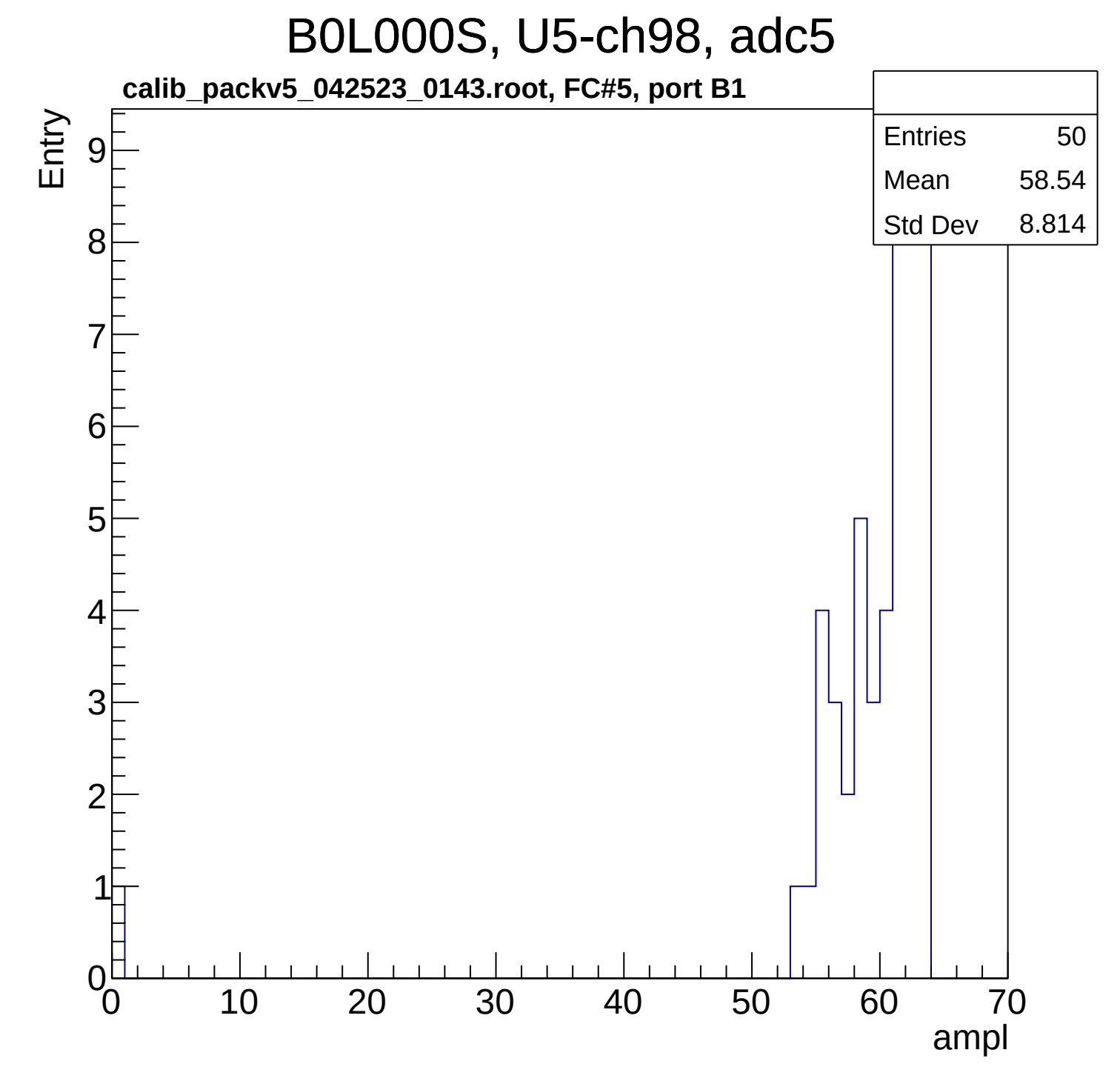
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch98, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 58.54
Std Dev 8.814
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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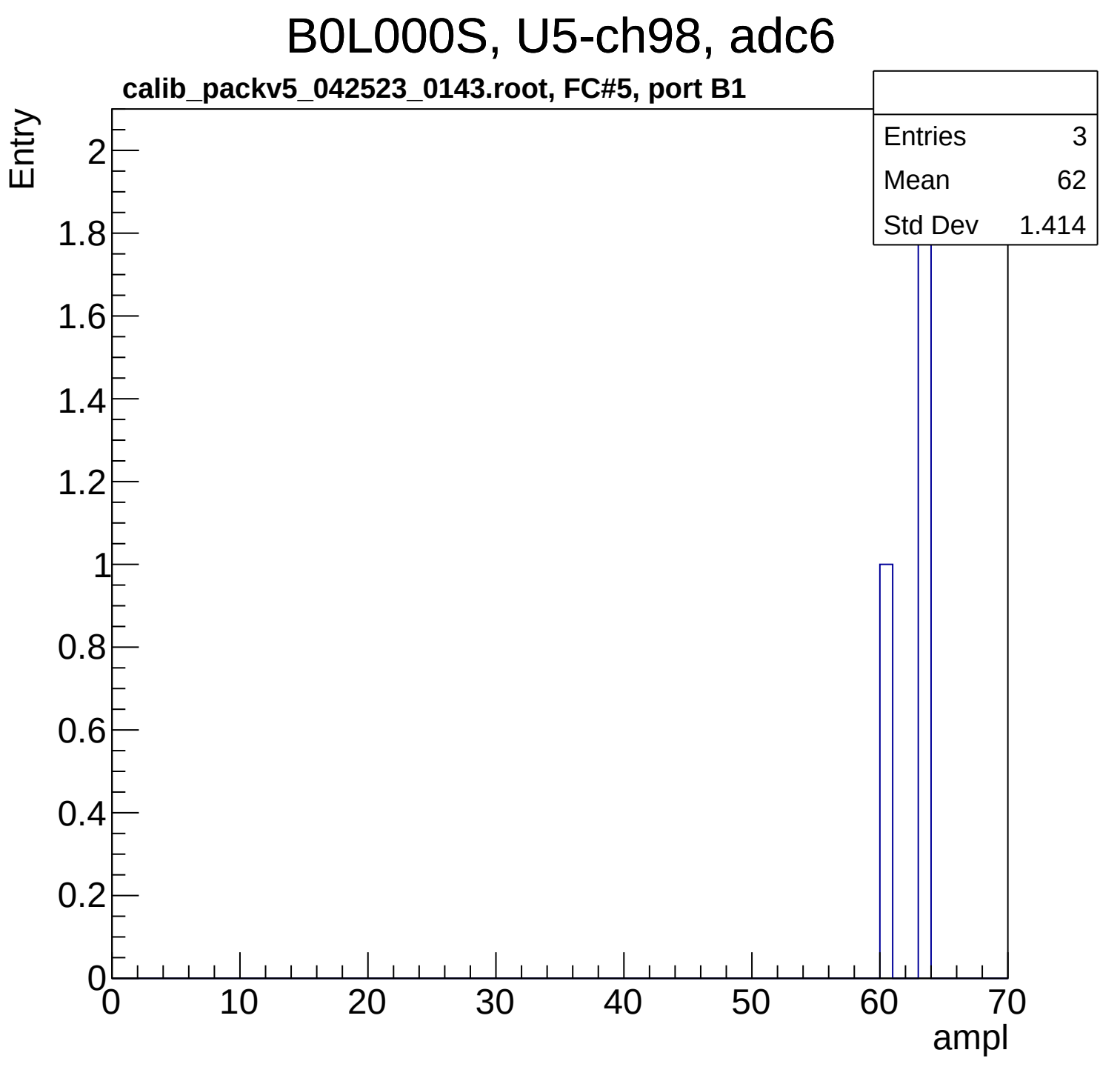
60
70


B0L000S, U5-ch98, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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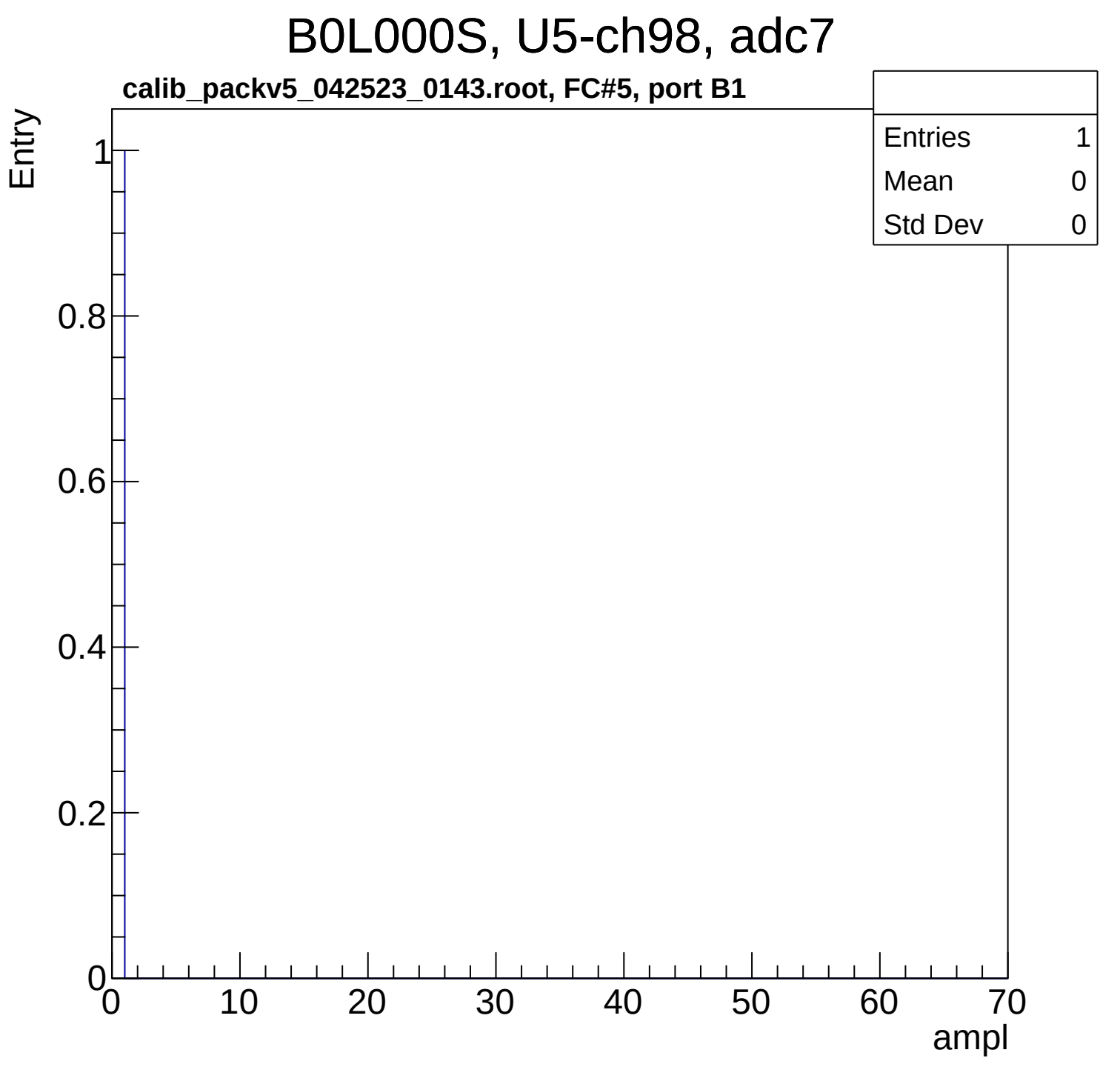
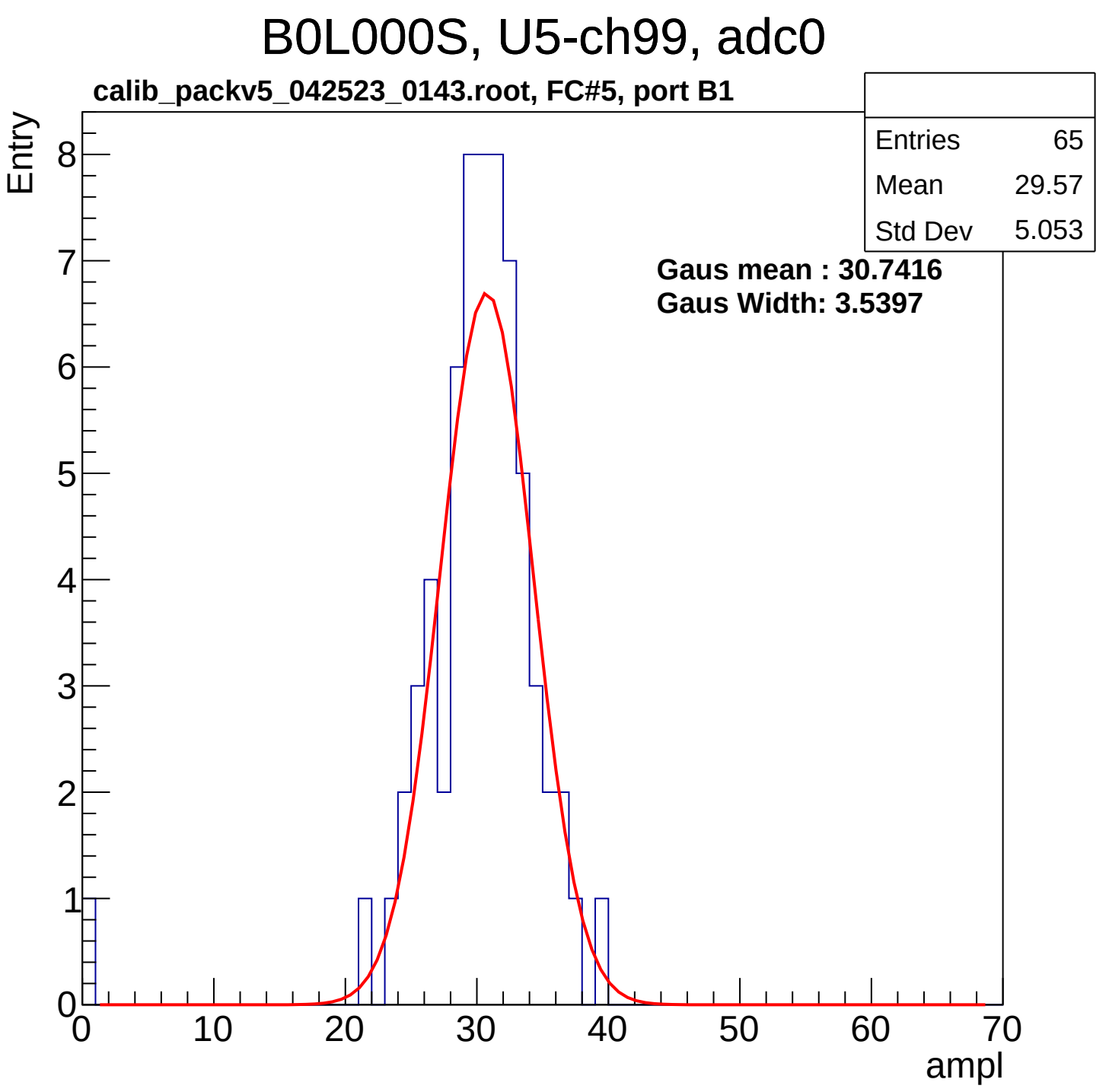
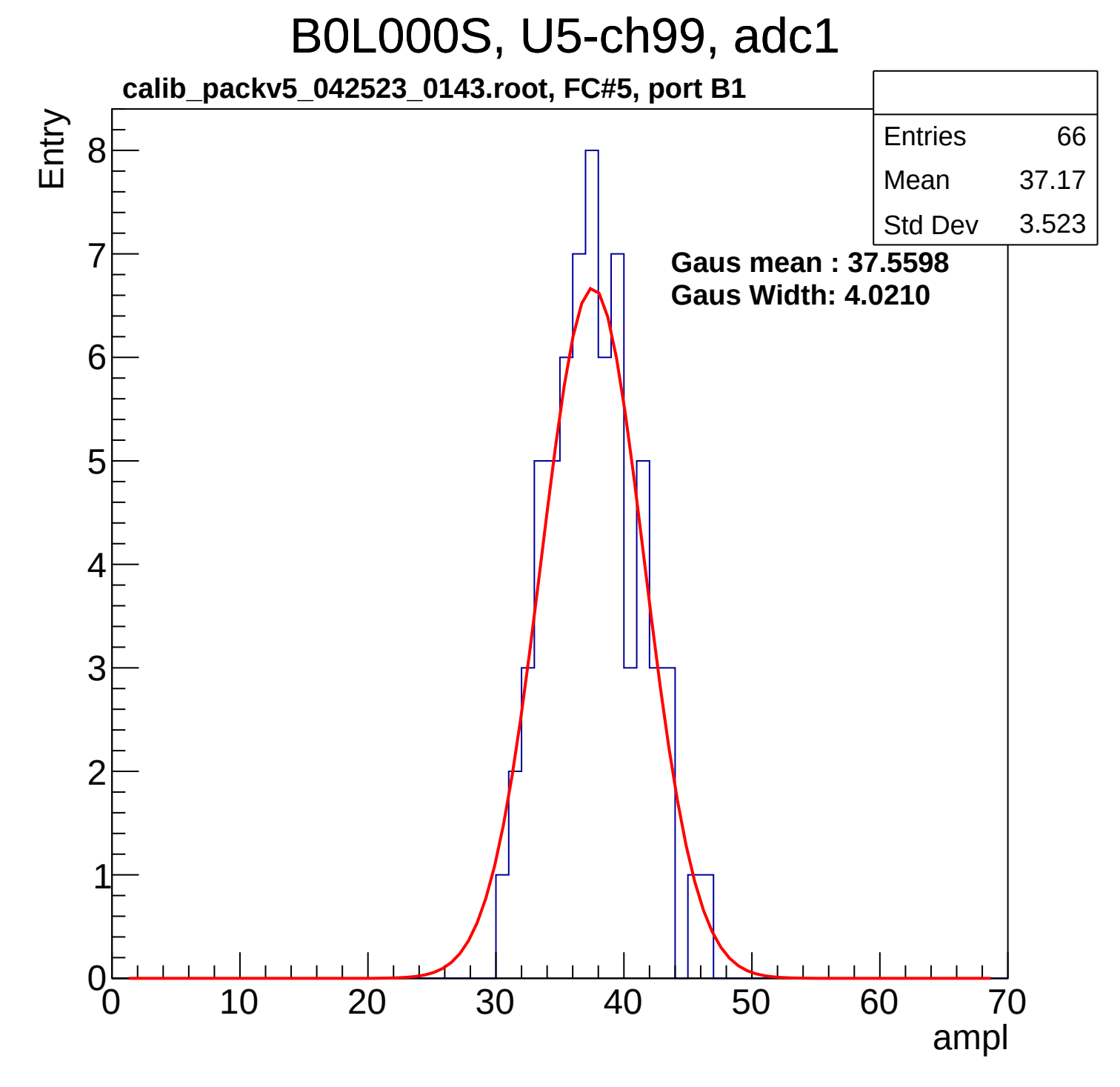
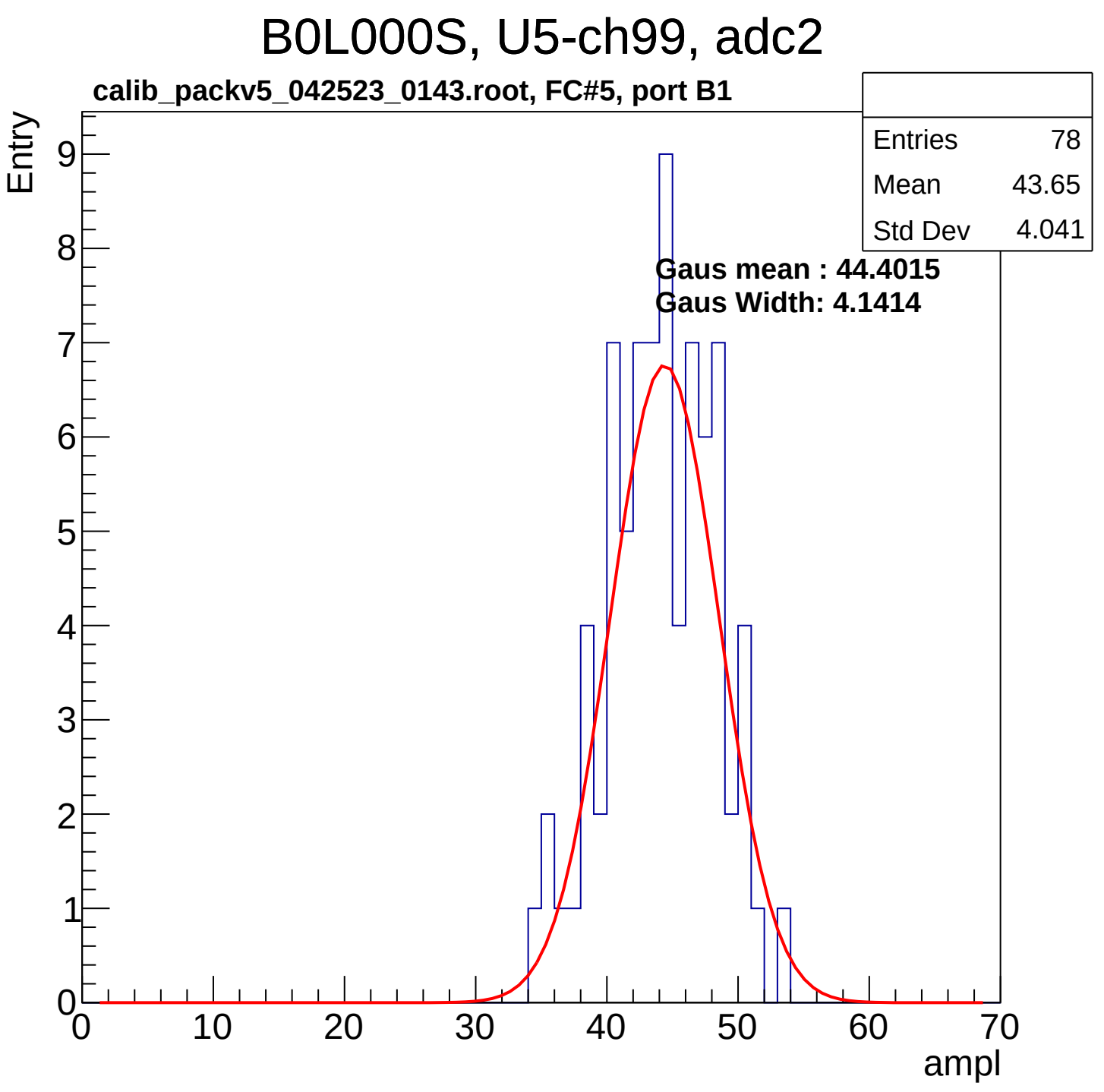
20
30
40
50
60
70


B0L000S, U5-ch98, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch99, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 29.57
Std Dev 5.053
Gaus mean : 30.7416
Gaus Width: 3.5397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch99, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 37.17
Std Dev 3.523
Gaus mean : 37.5598
Gaus Width: 4.0210
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch99, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 43.65
Std Dev 4.041
Gaus mean : 44.4015
Gaus Width: 4.1414
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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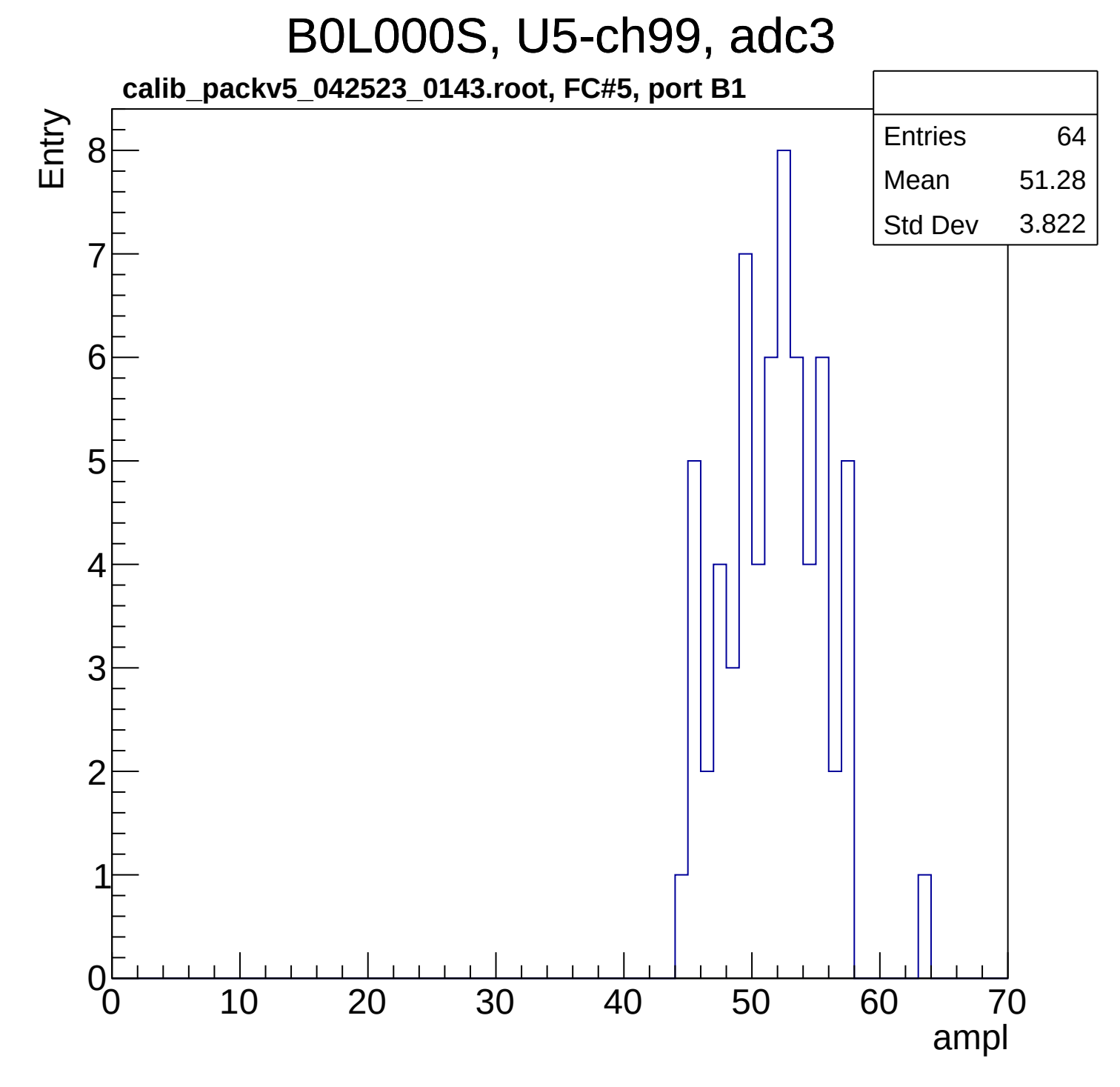
B0L000S, U5-ch99, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 51.28
Std Dev 3.822
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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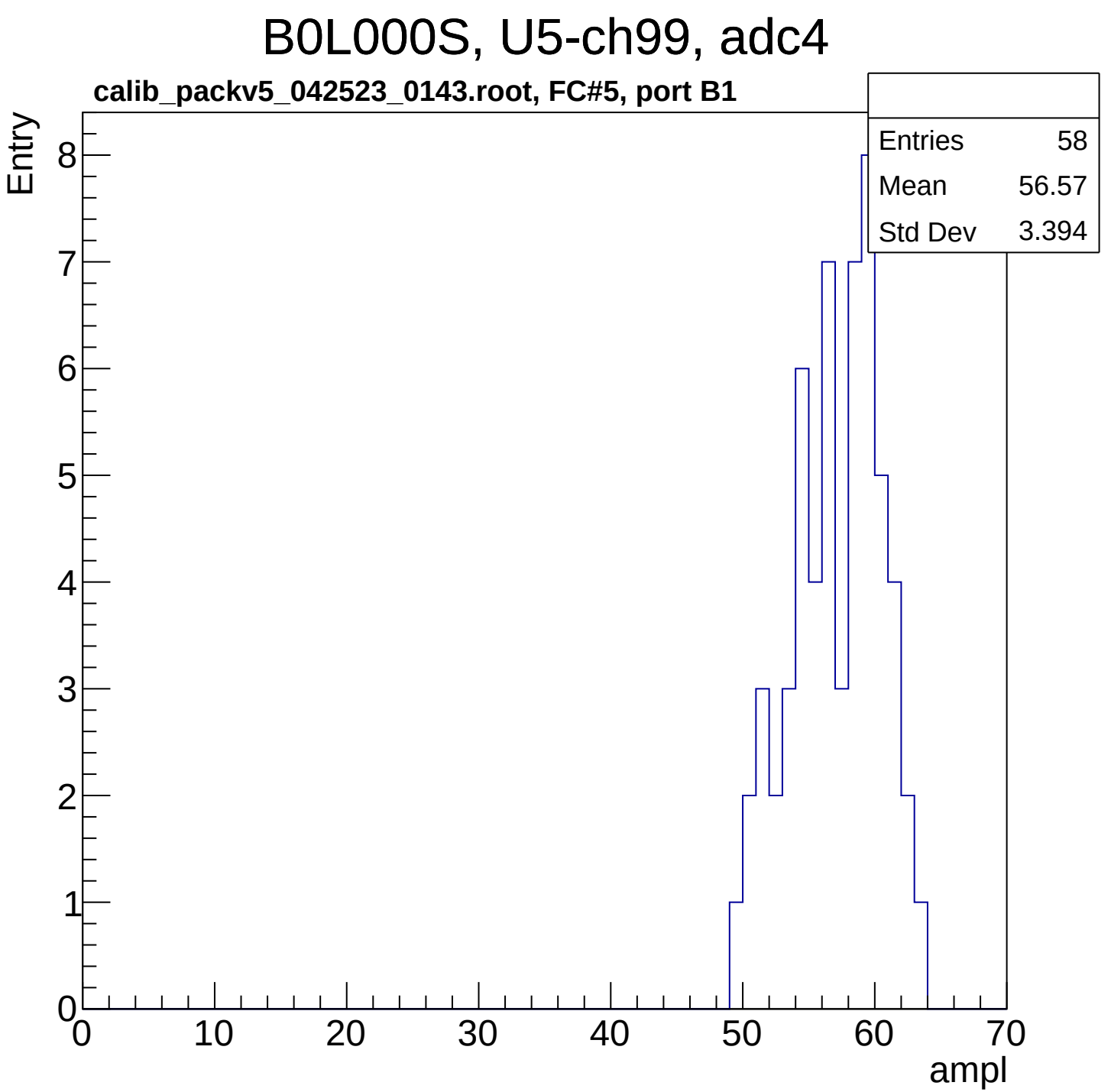
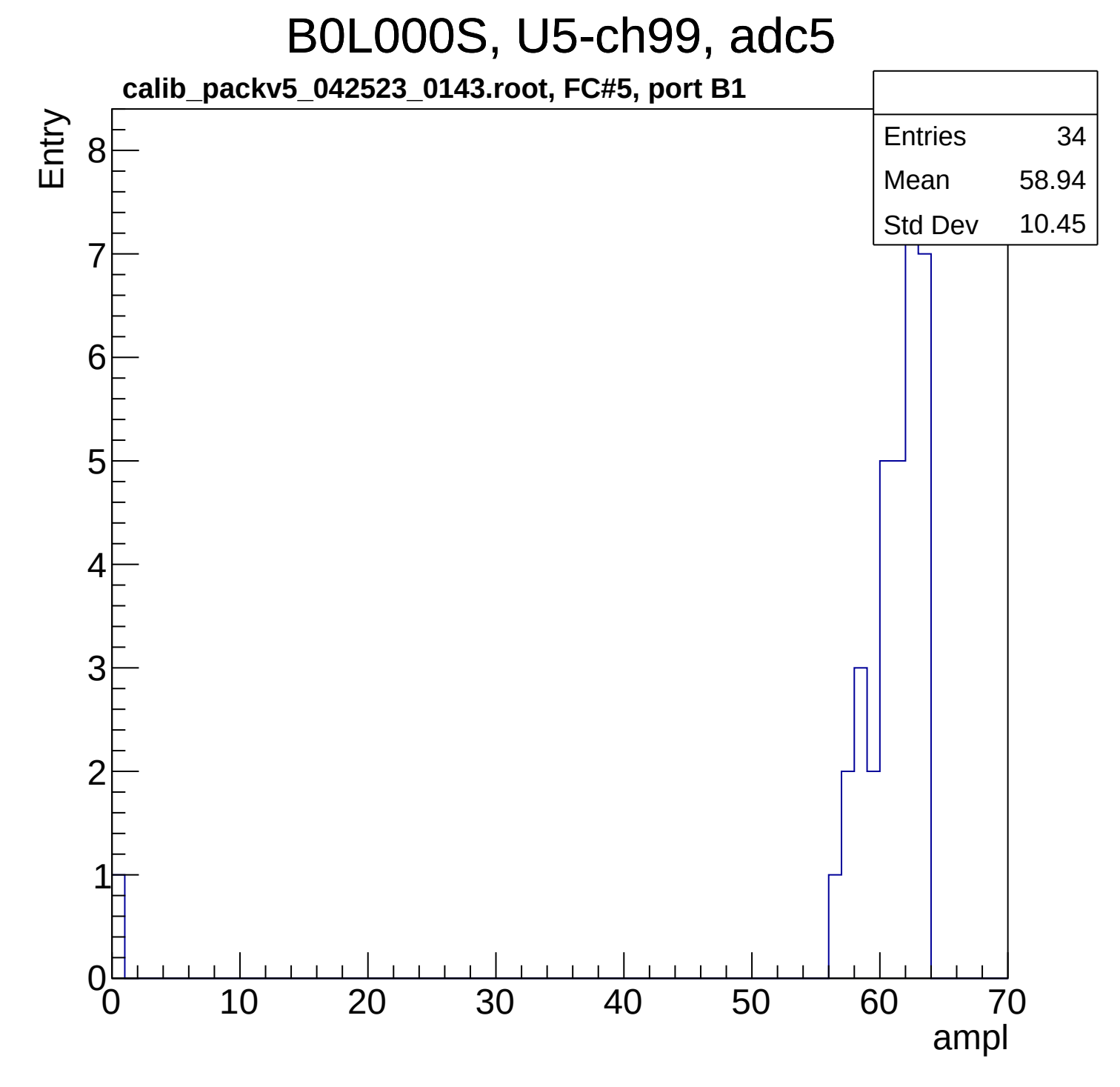

B0L000S, U5-ch99, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 56.57
Std Dev 3.394
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch99, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 58.94
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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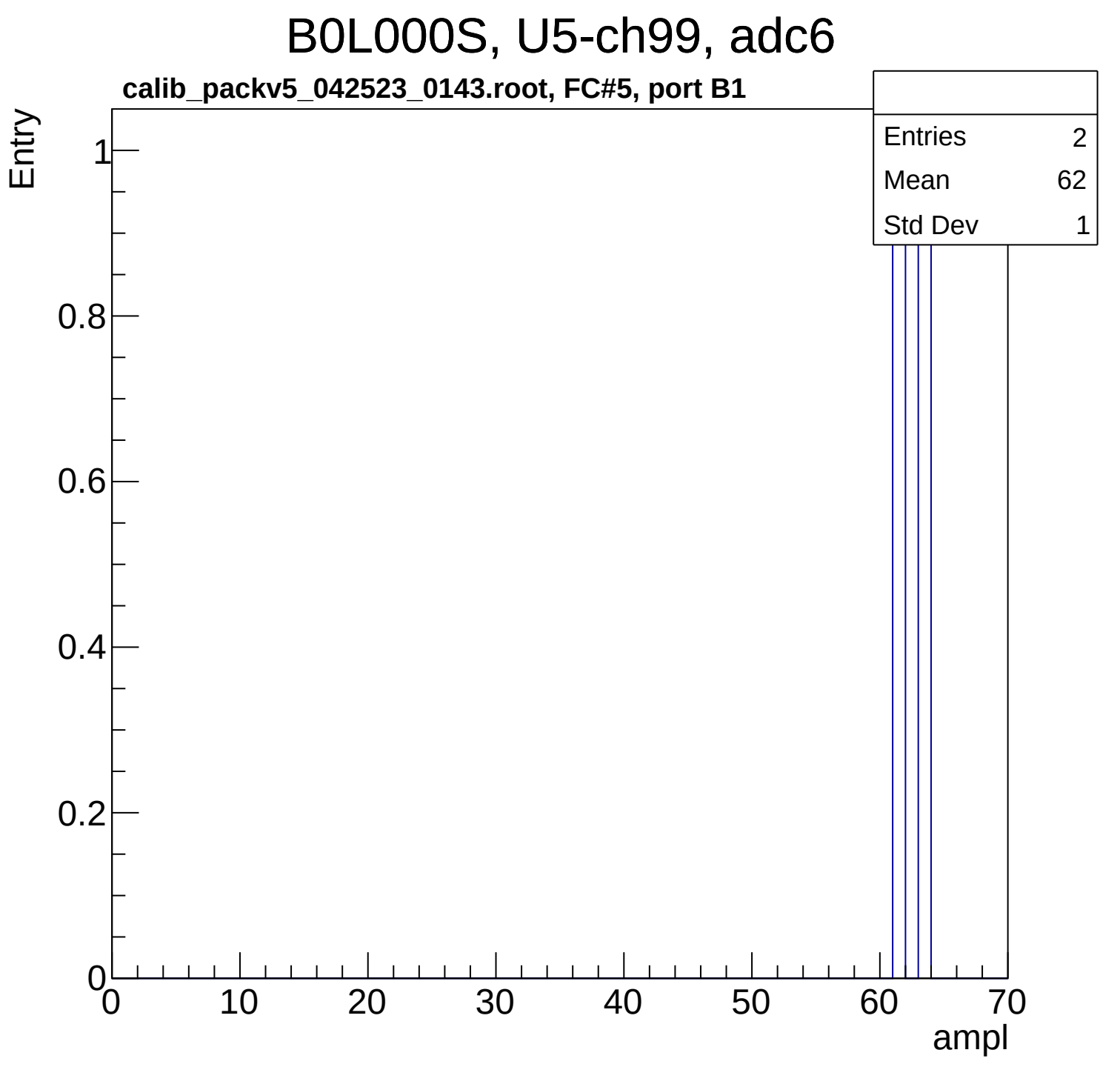
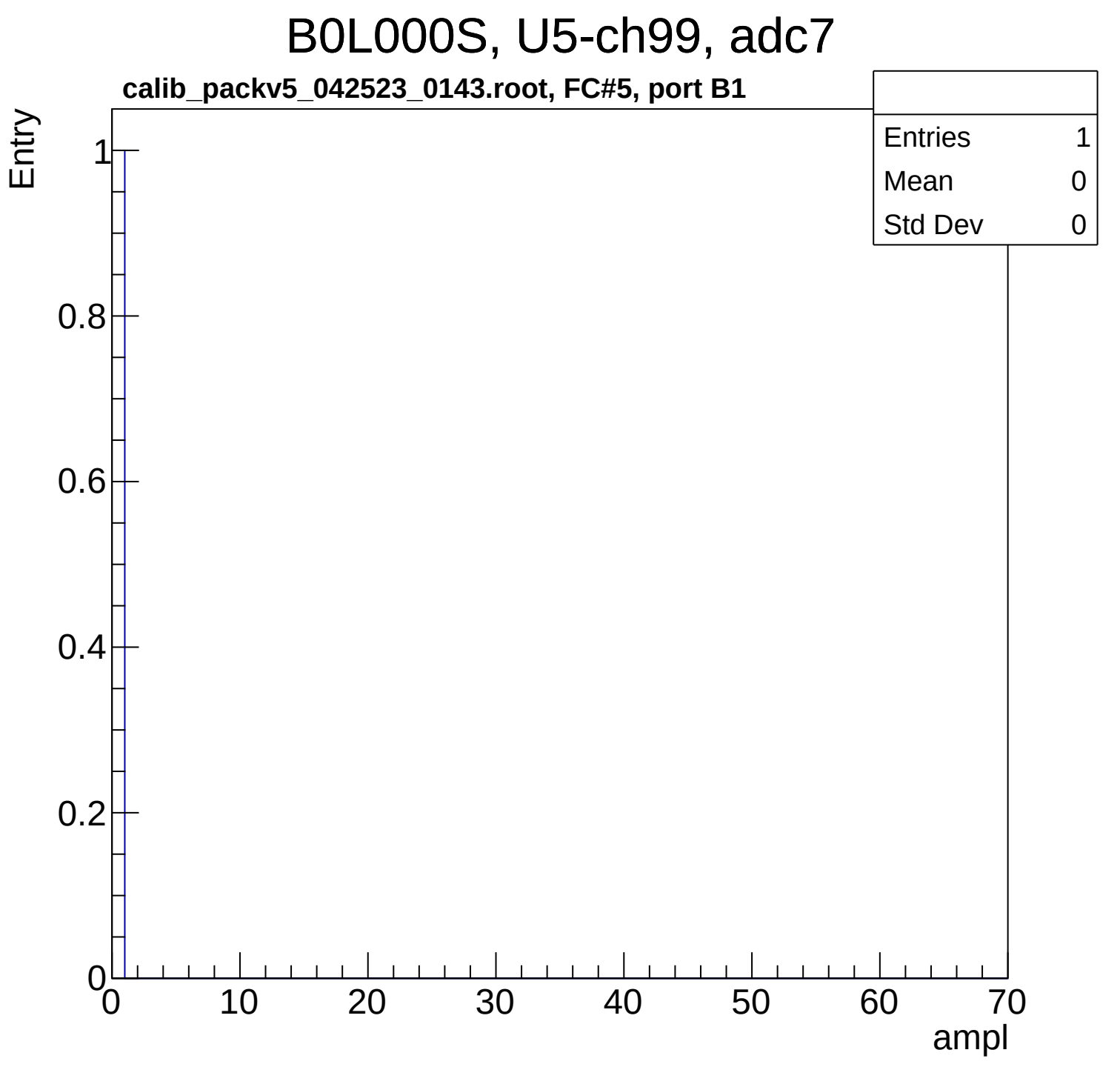
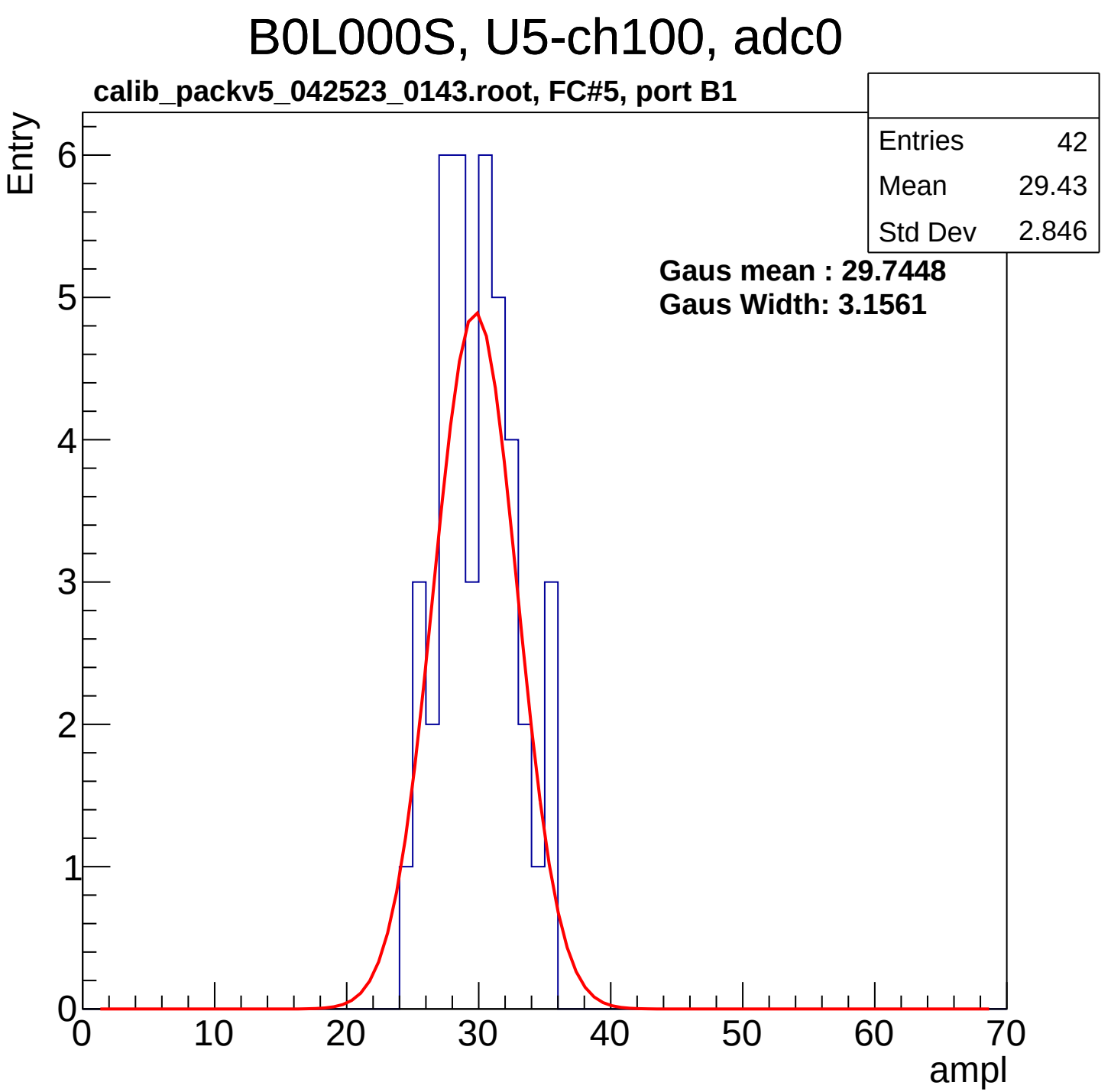
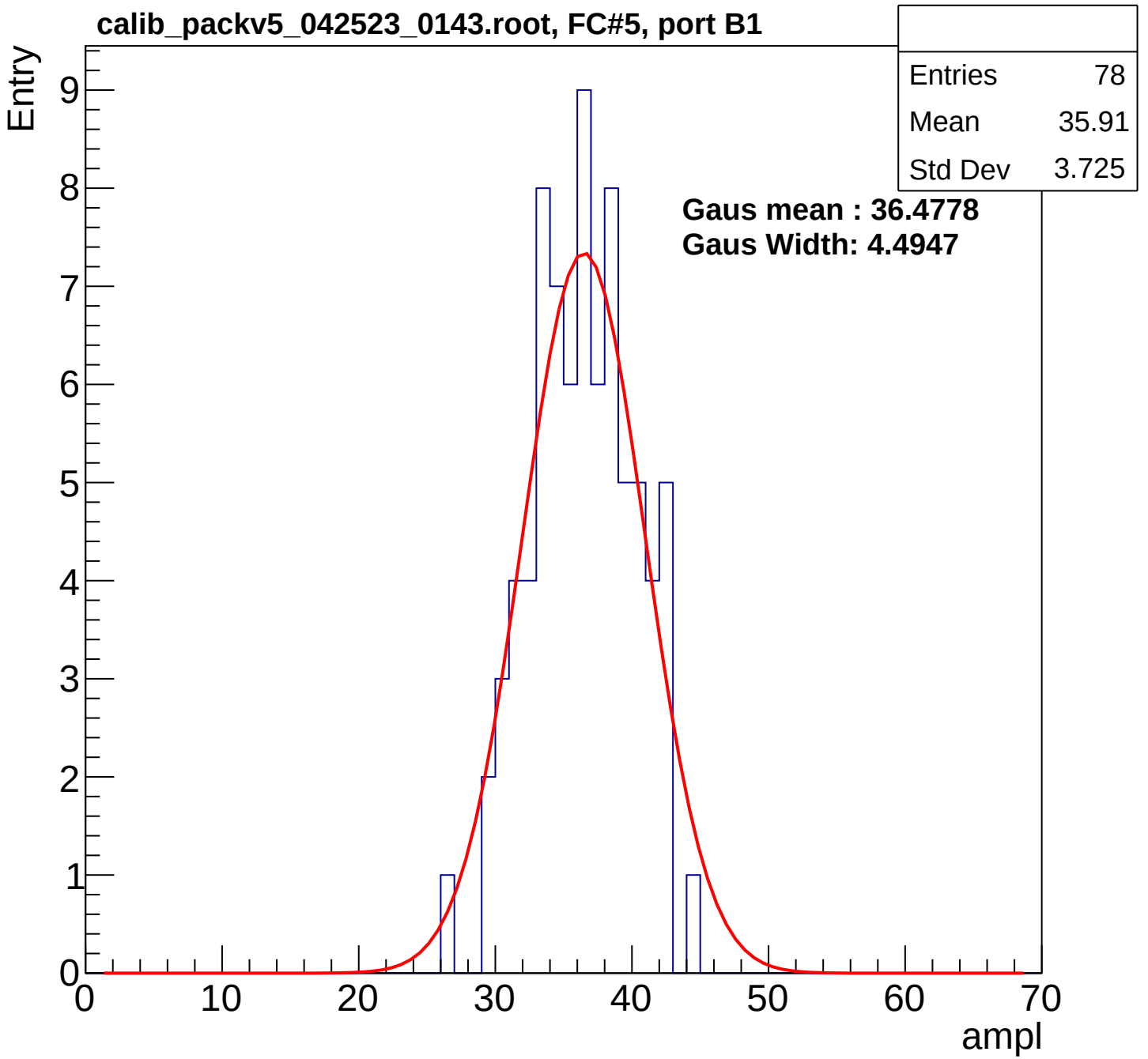

B0L000S, U5-ch99, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch99, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch100, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 29.43
Std Dev 2.846
Gaus mean : 29.7448
Gaus Width: 3.1561
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch100, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 35.91
Std Dev 3.725
Gaus mean : 36.4778
Gaus Width: 4.4947
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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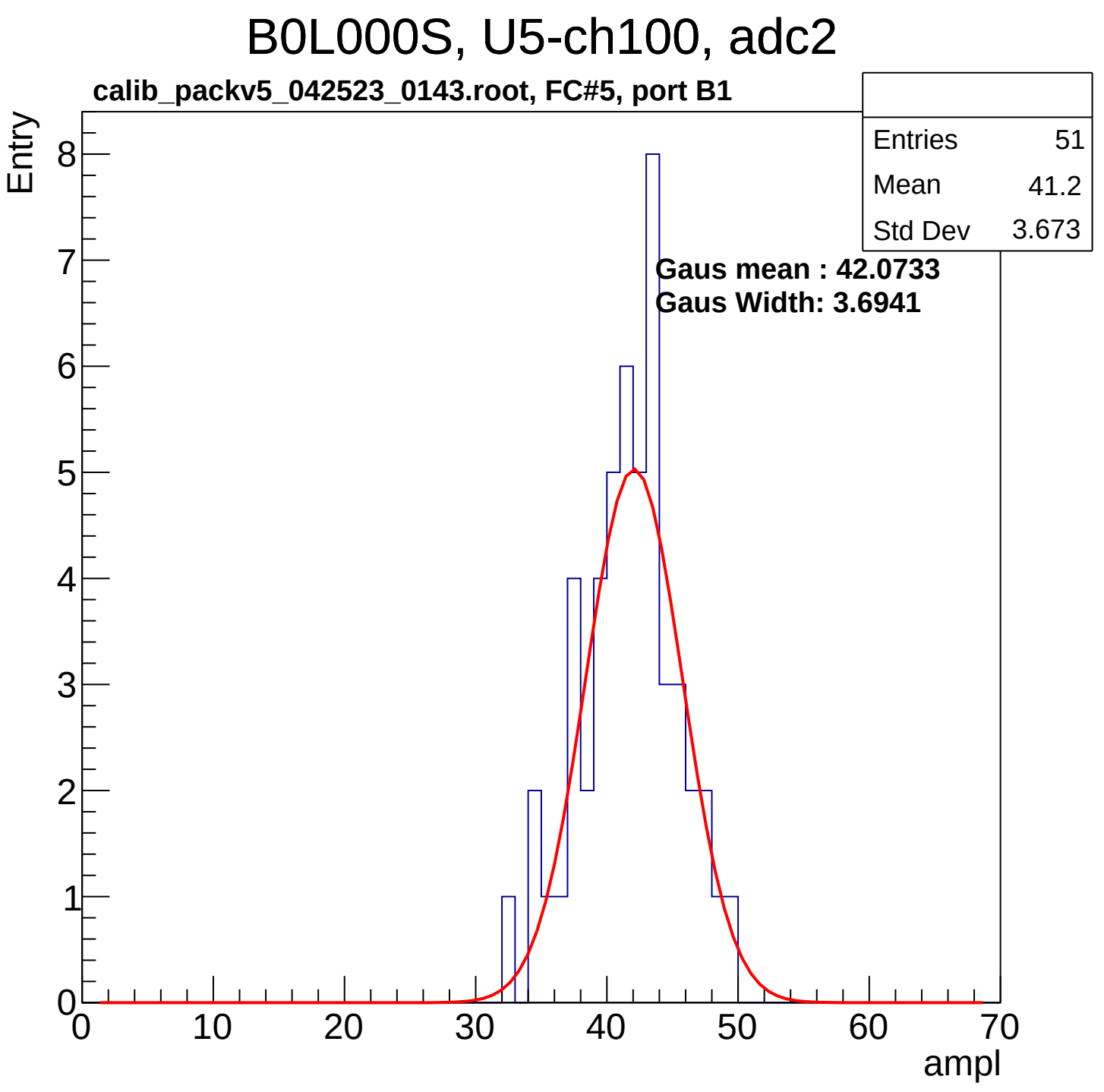
B0L000S, U5-ch100, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 41.2
Std Dev 3.673
Gaus mean : 42.0733
Gaus Width: 3.6941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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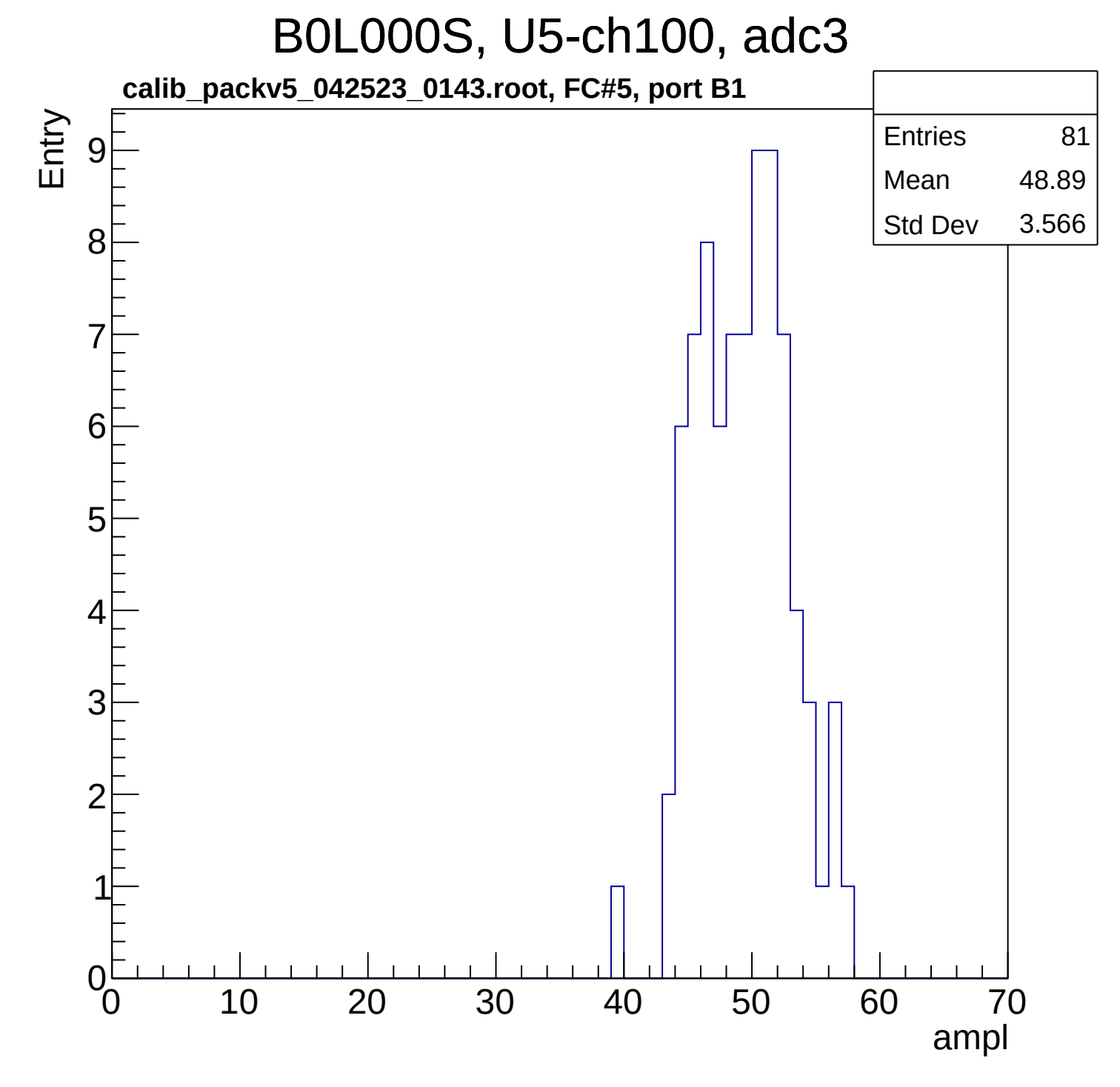
B0L000S, U5-ch100, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 48.89
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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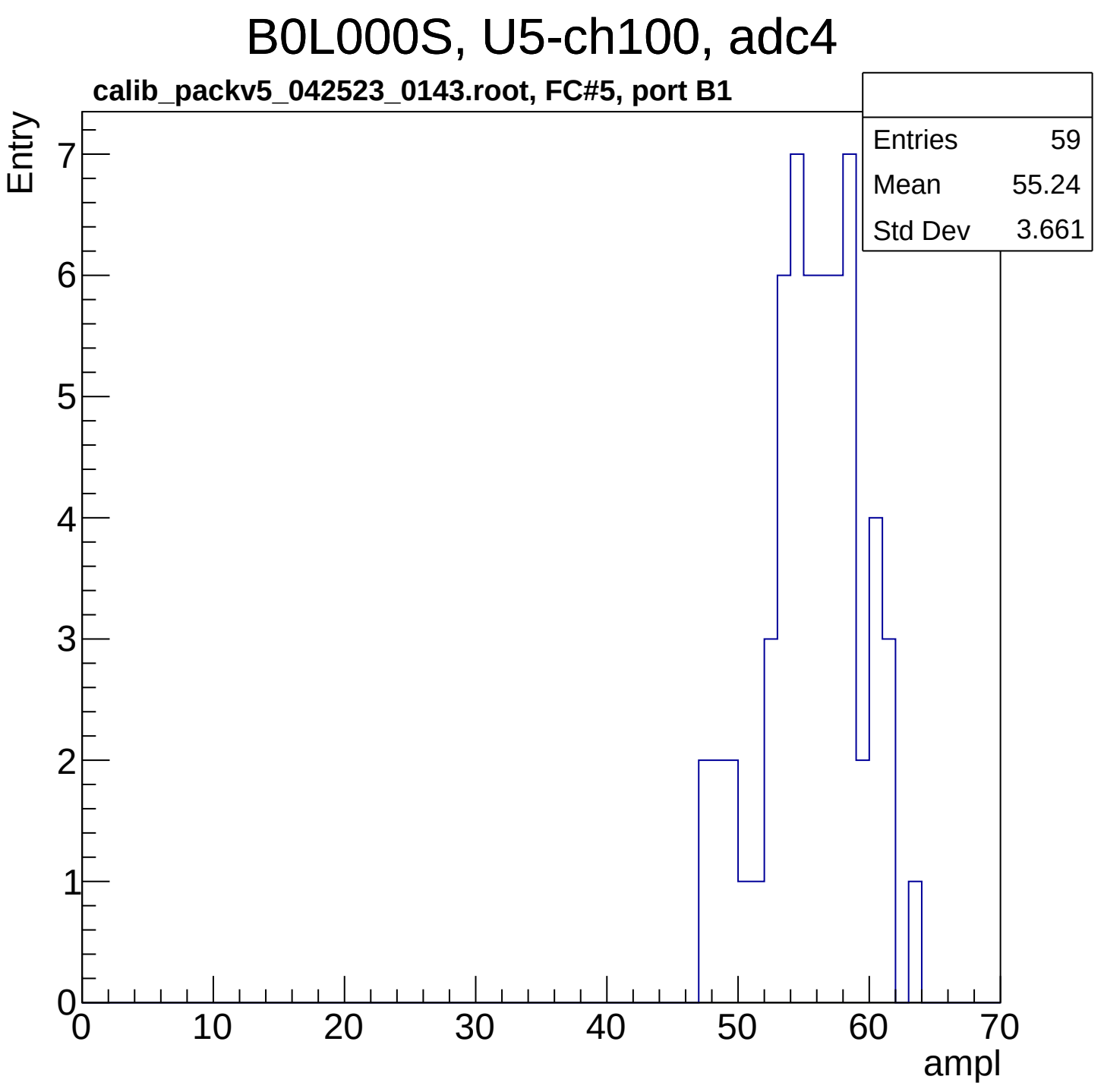
B0L000S, U5-ch100, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 55.24
Std Dev 3.661
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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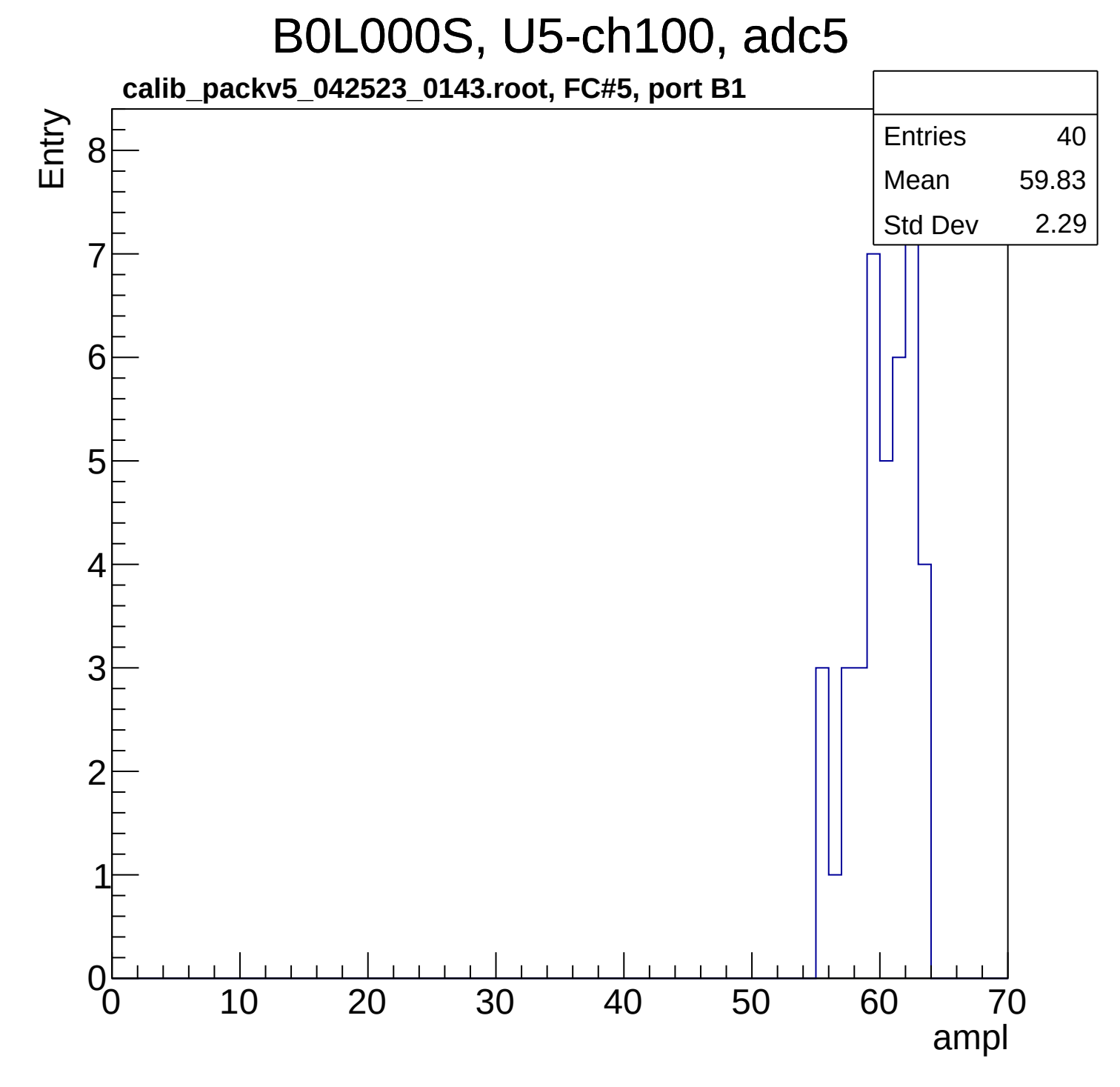
B0L000S, U5-ch100, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 59.83
Std Dev 2.29
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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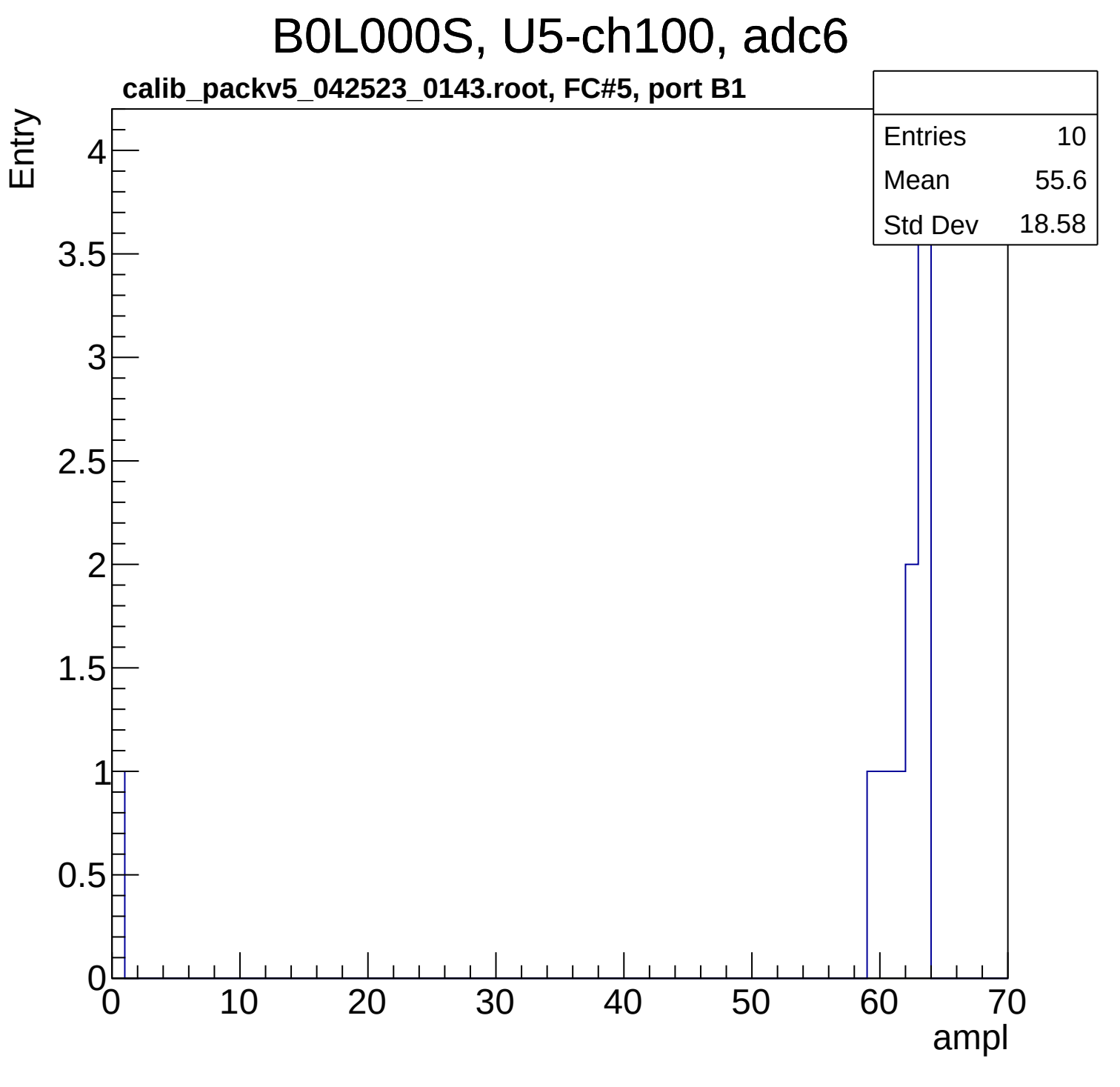
B0L000S, U5-ch100, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 55.6
Std Dev 18.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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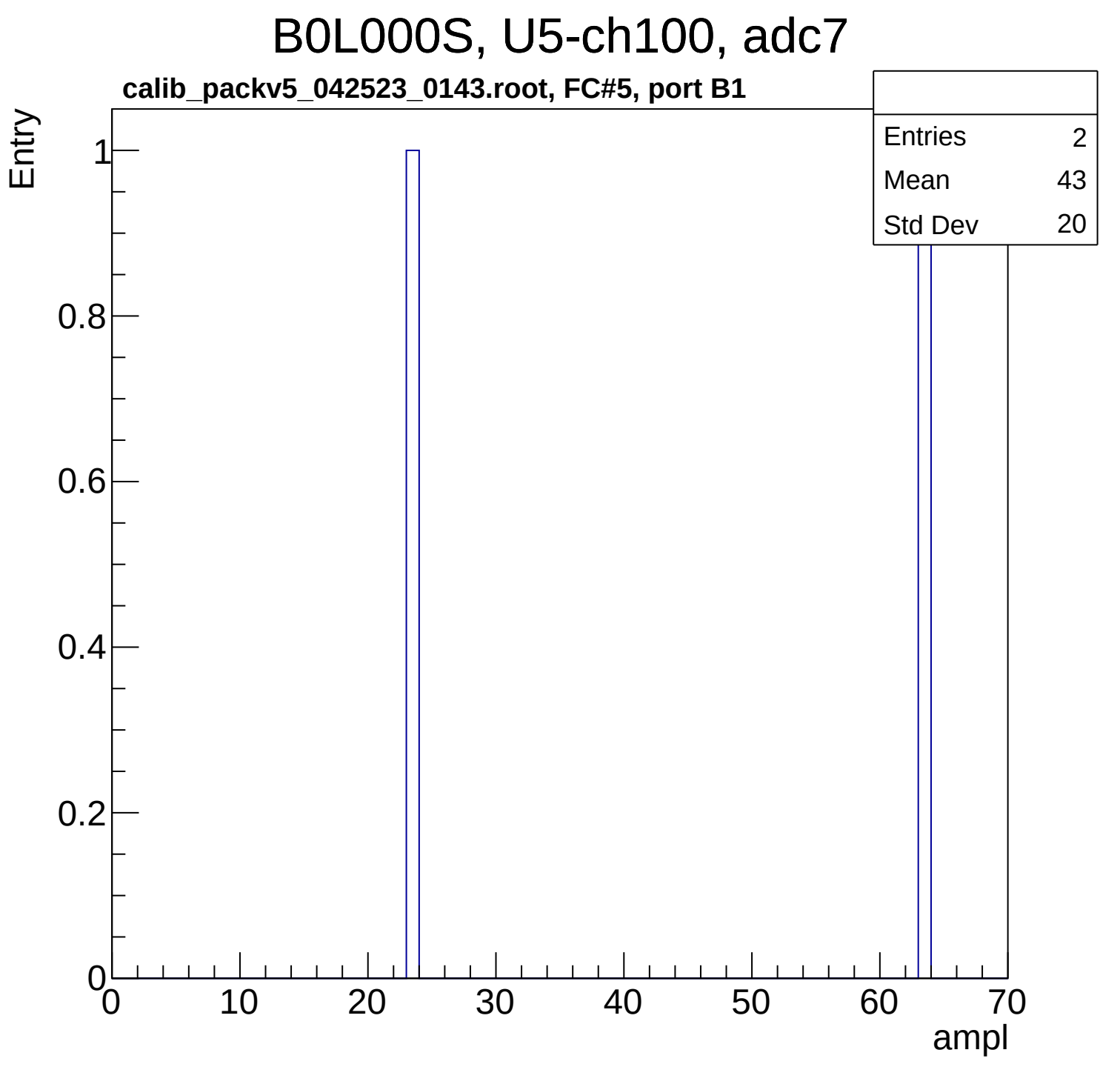
B0L000S, U5-ch100, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 43
Std Dev 20
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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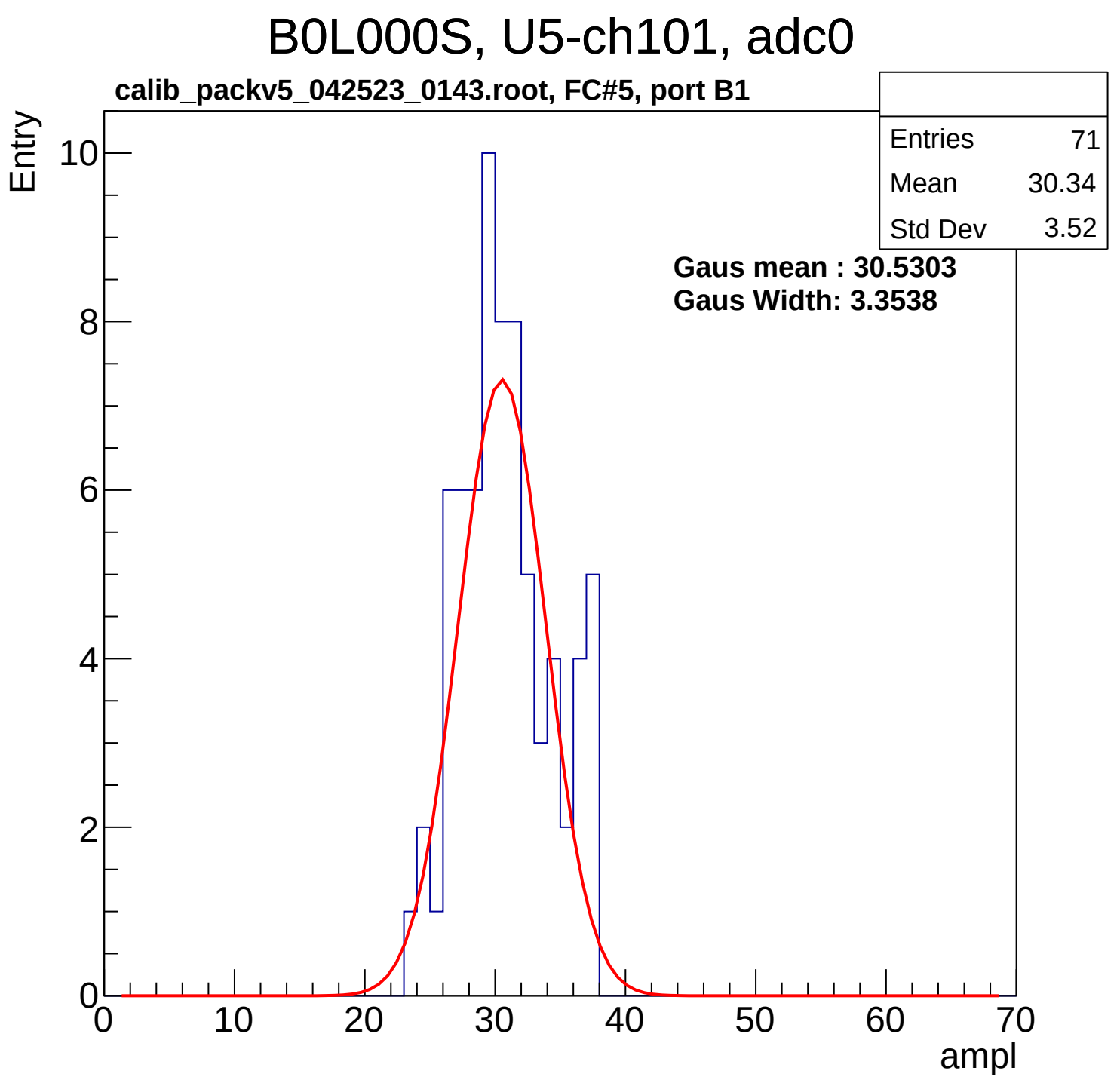
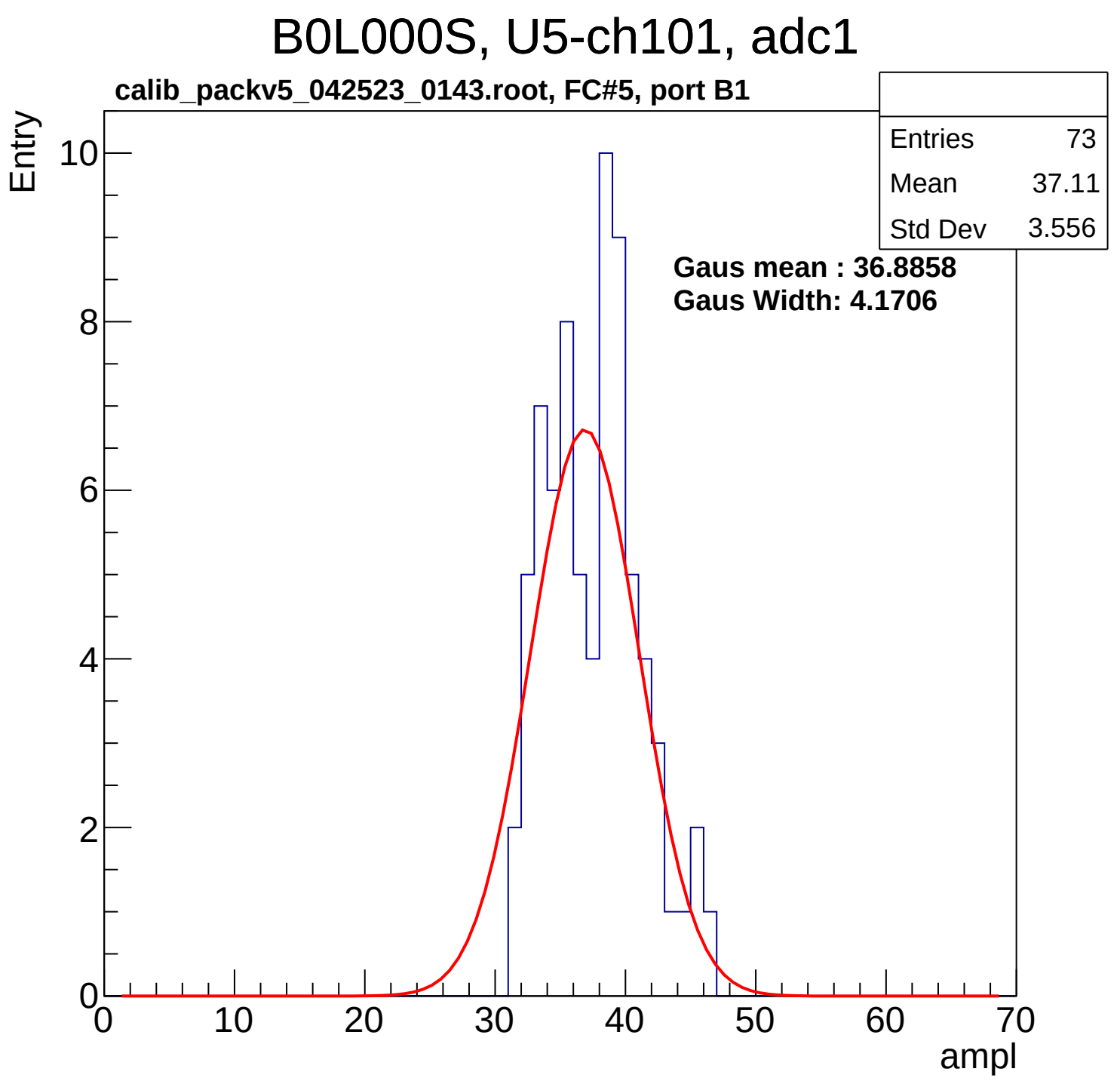
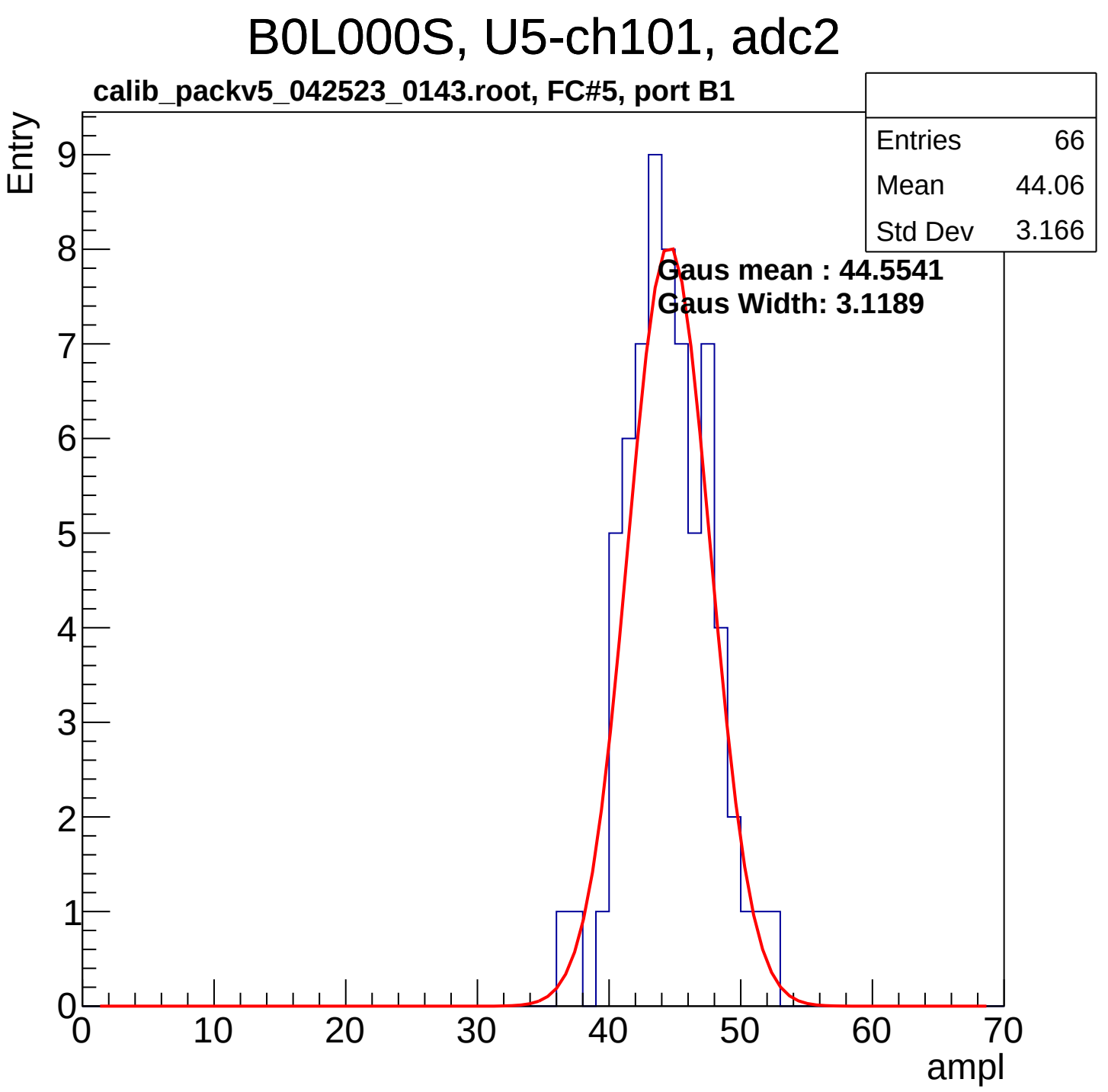
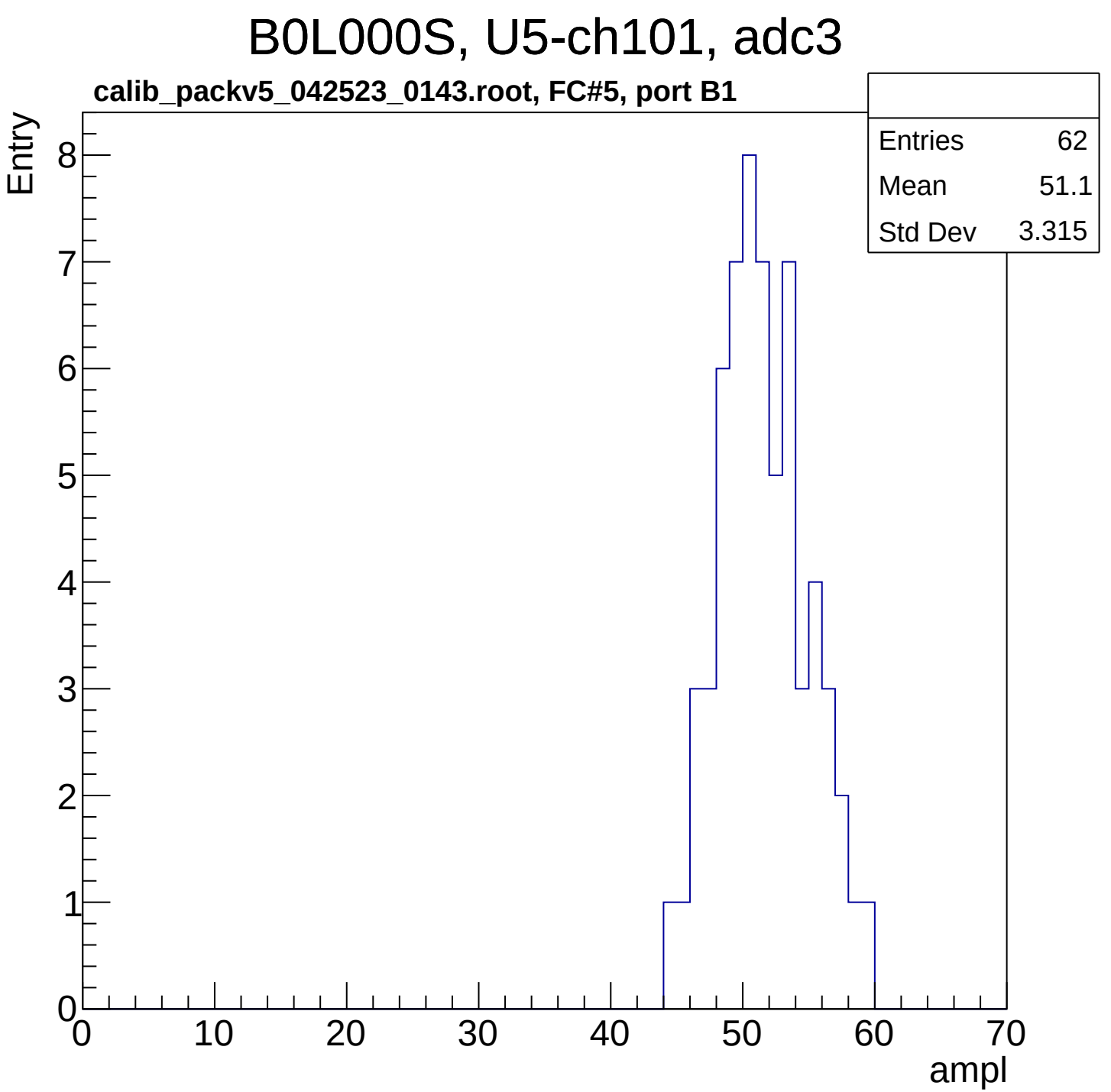
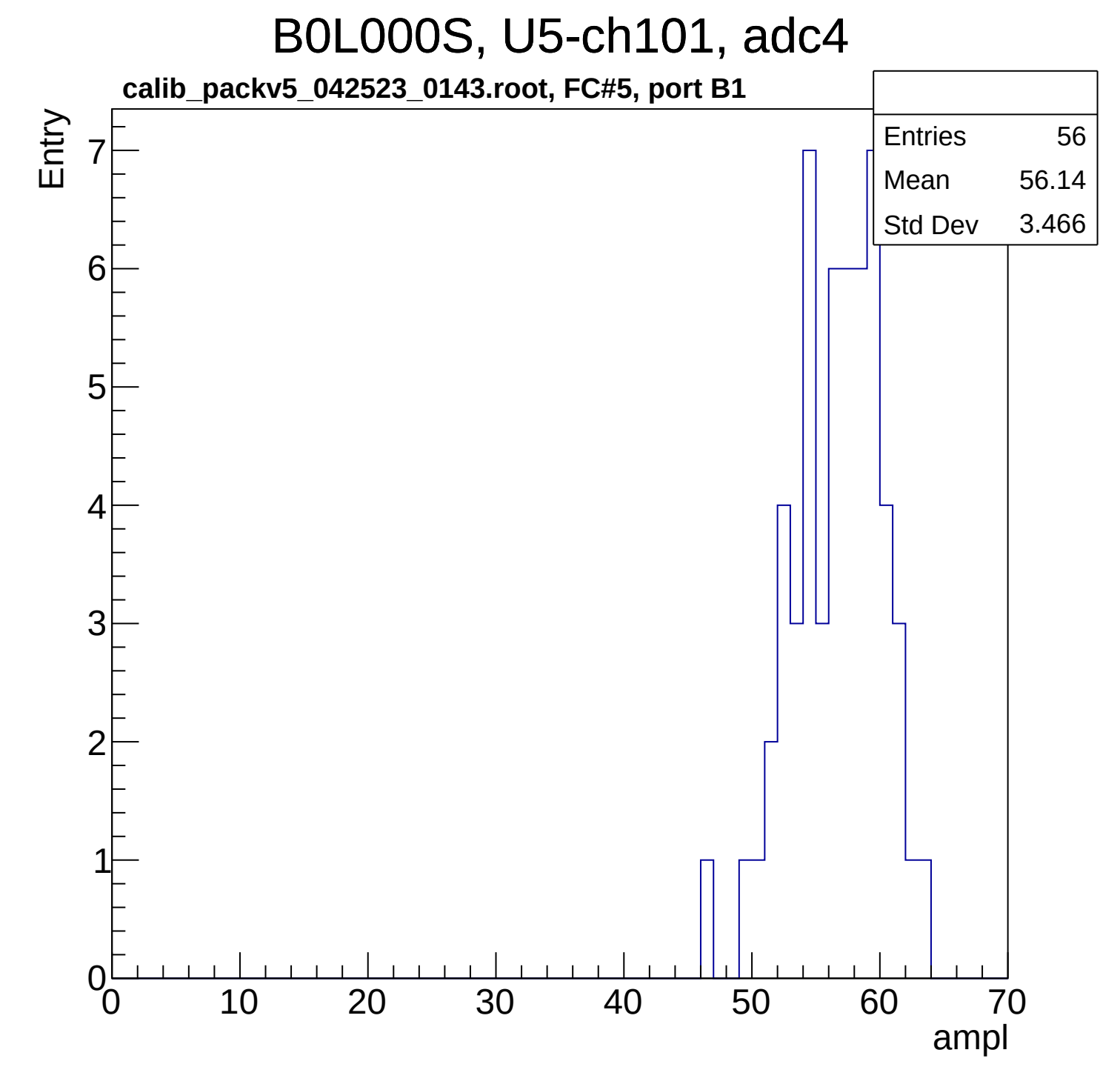
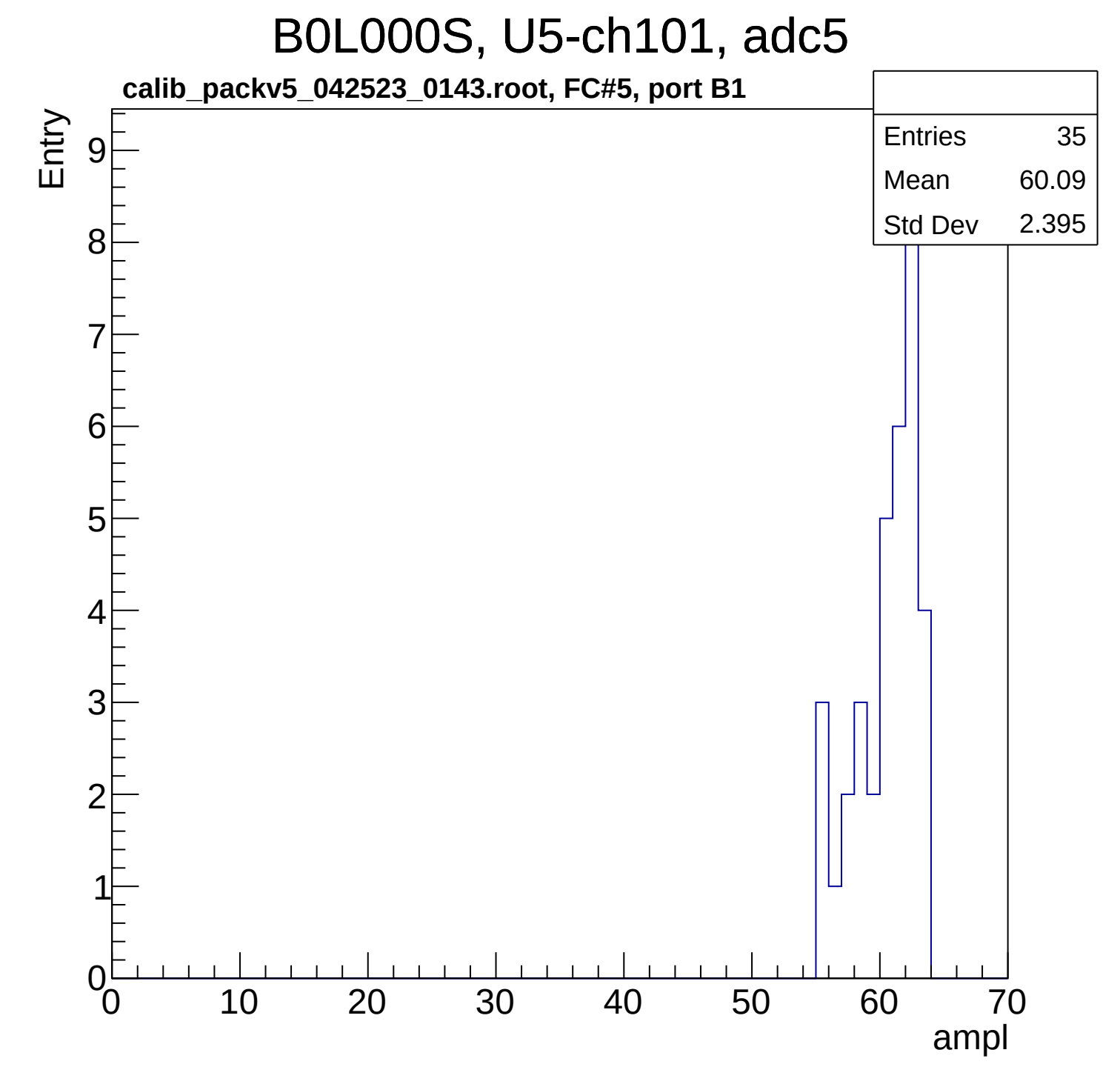

B0L000S, U5-ch101, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.34
Std Dev 3.52
Gaus mean : 30.5303
Gaus Width: 3.3538
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

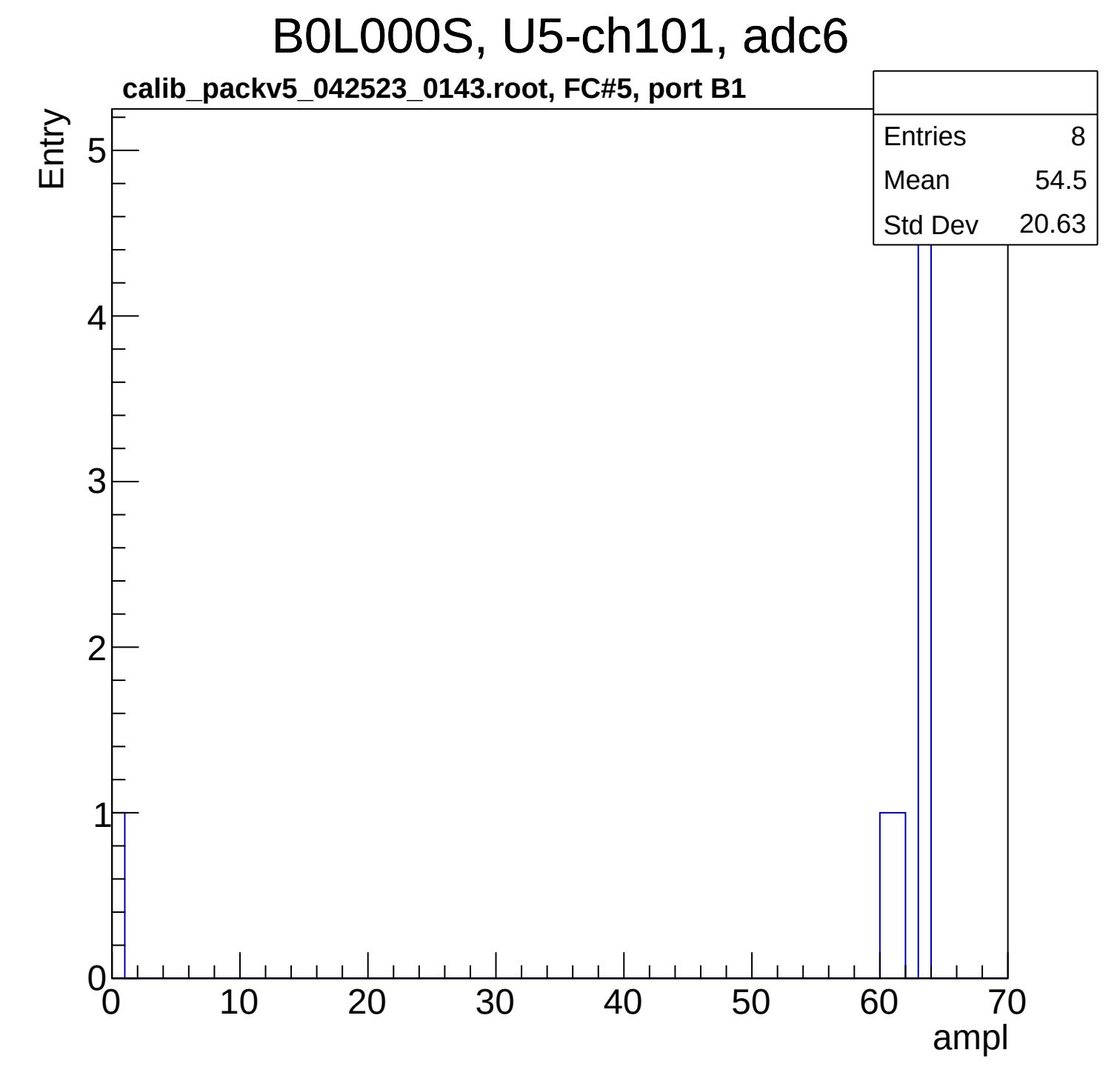

B0L000S, U5-ch101, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 37.11
Std Dev 3.556
Gaus mean : 36.8858
Gaus Width: 4.1706
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

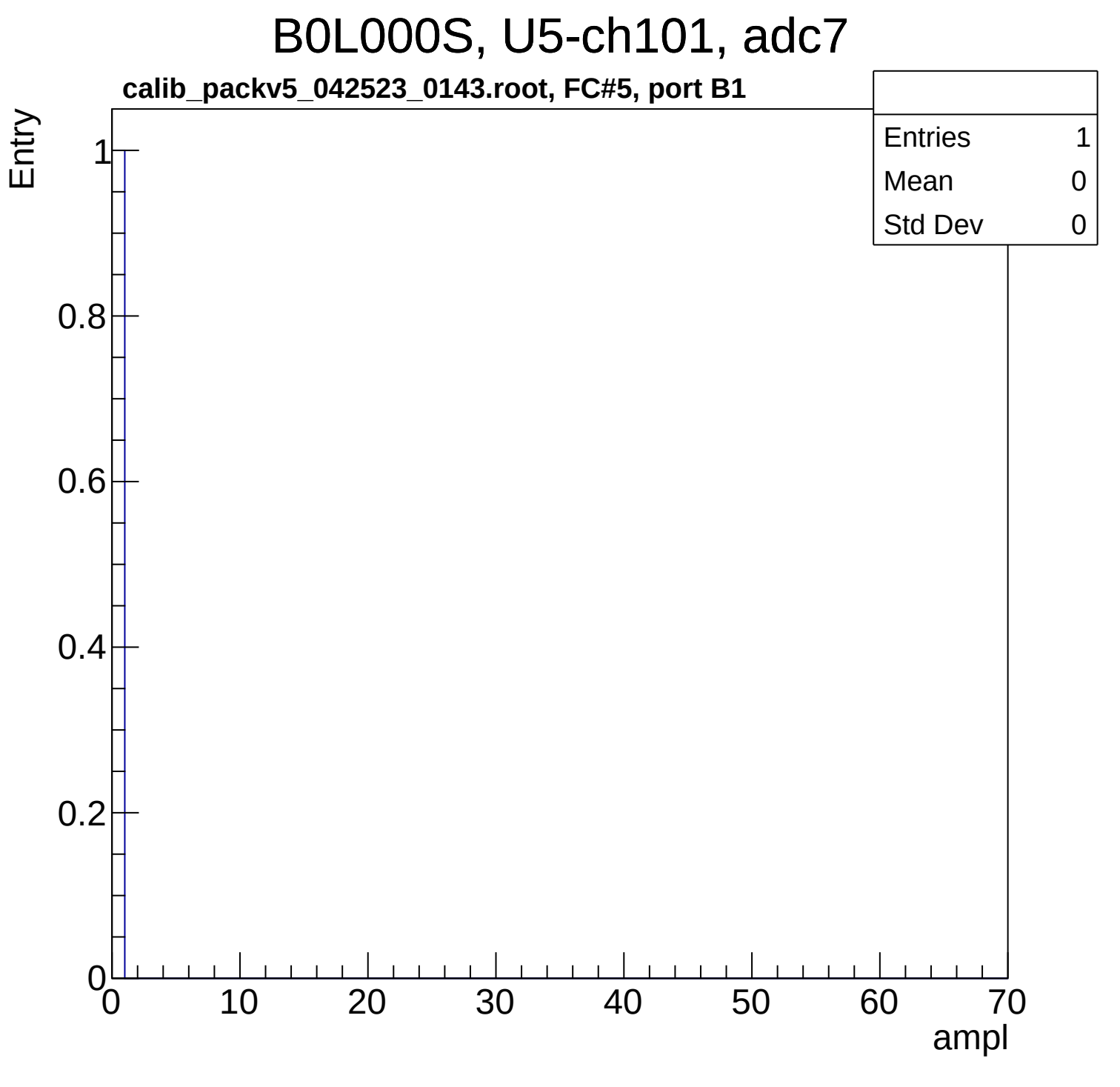

B0L000S, U5-ch101, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 44.06
Std Dev 3.166
Gaus mean : 44.5541
Gaus Width: 3.1189
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

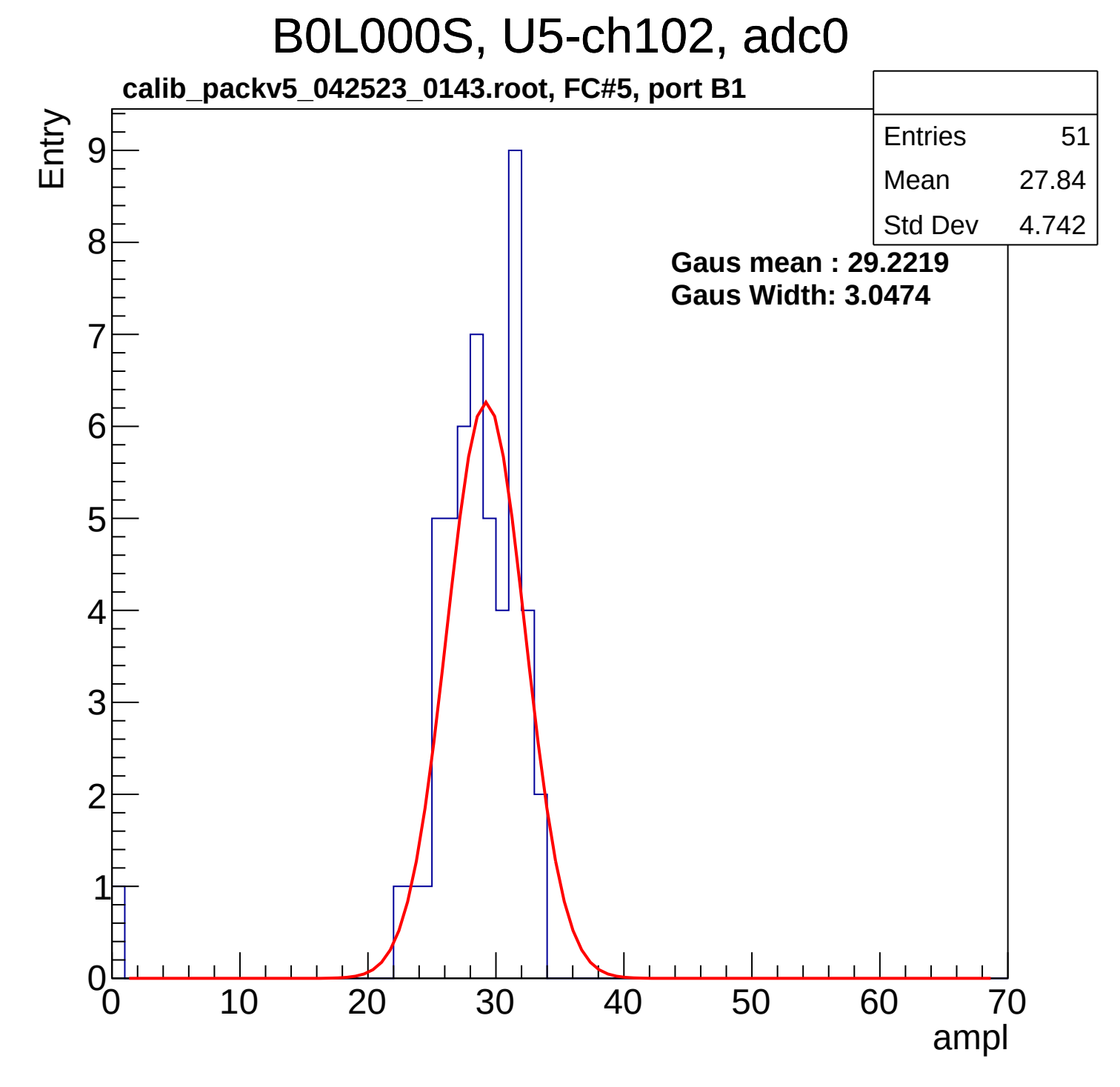

B0L000S, U5-ch101, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 51.1
Std Dev 3.315
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

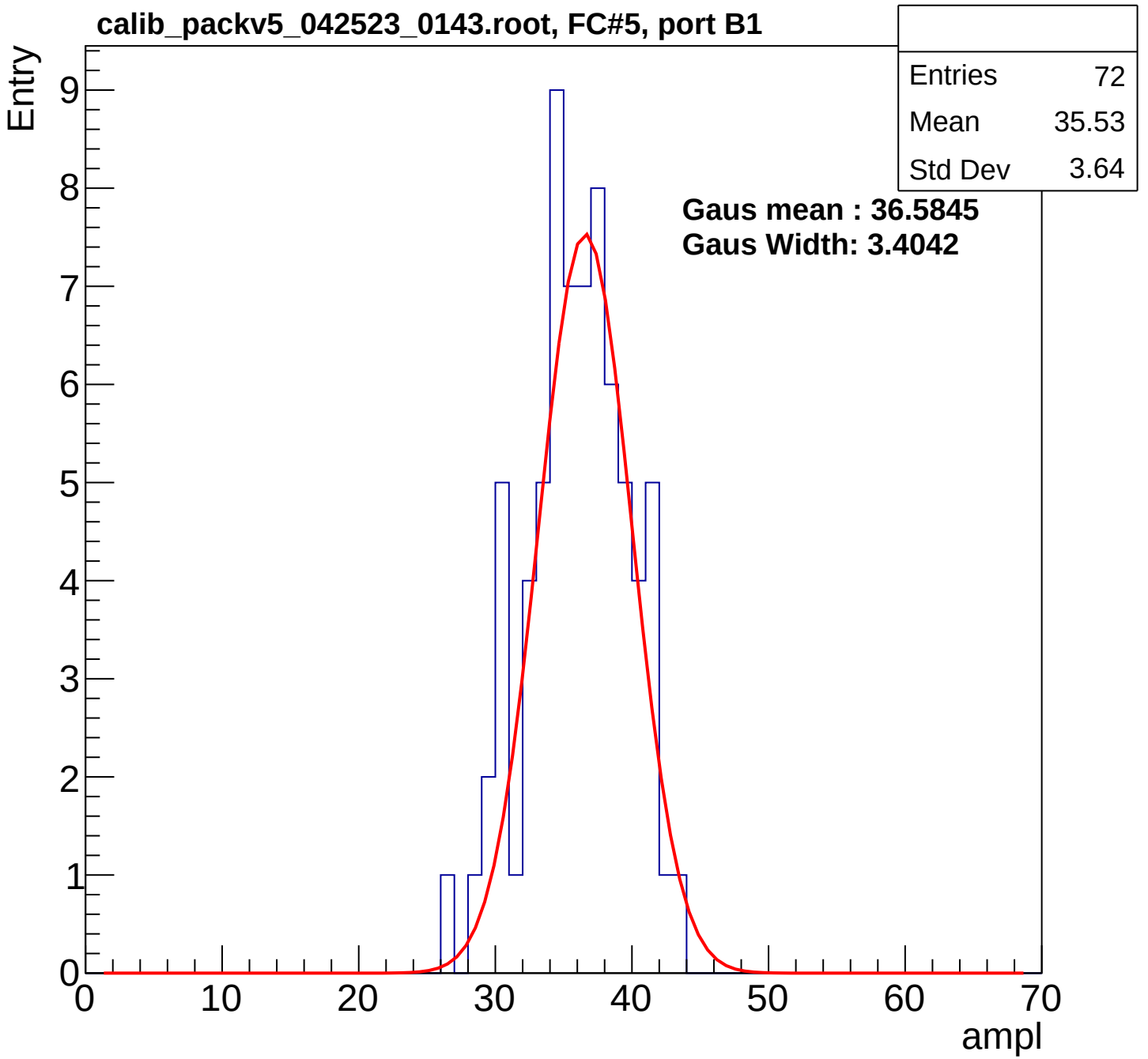

B0L000S, U5-ch101, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 56.14
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

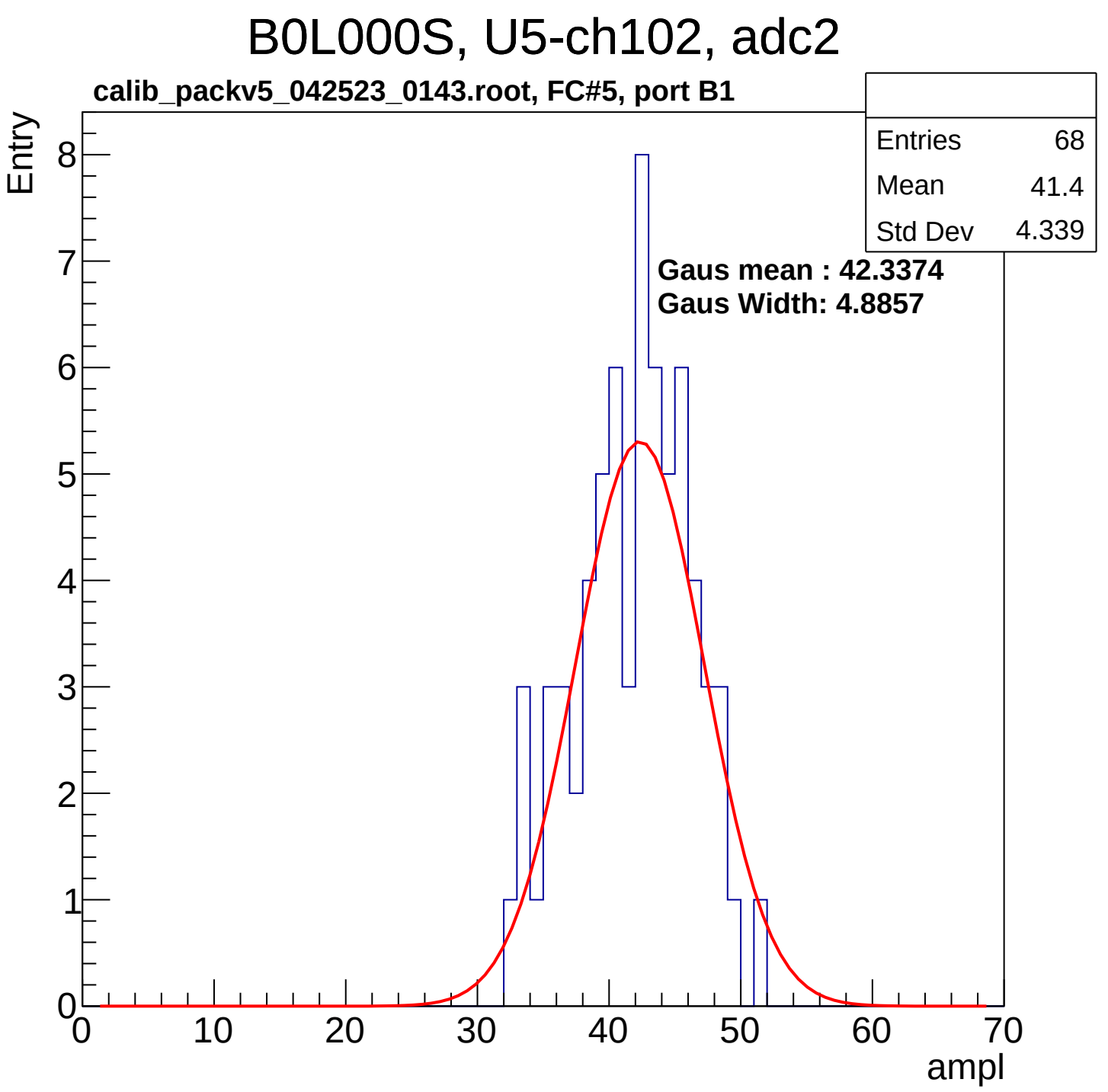

B0L000S, U5-ch101, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 60.09
Std Dev 2.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

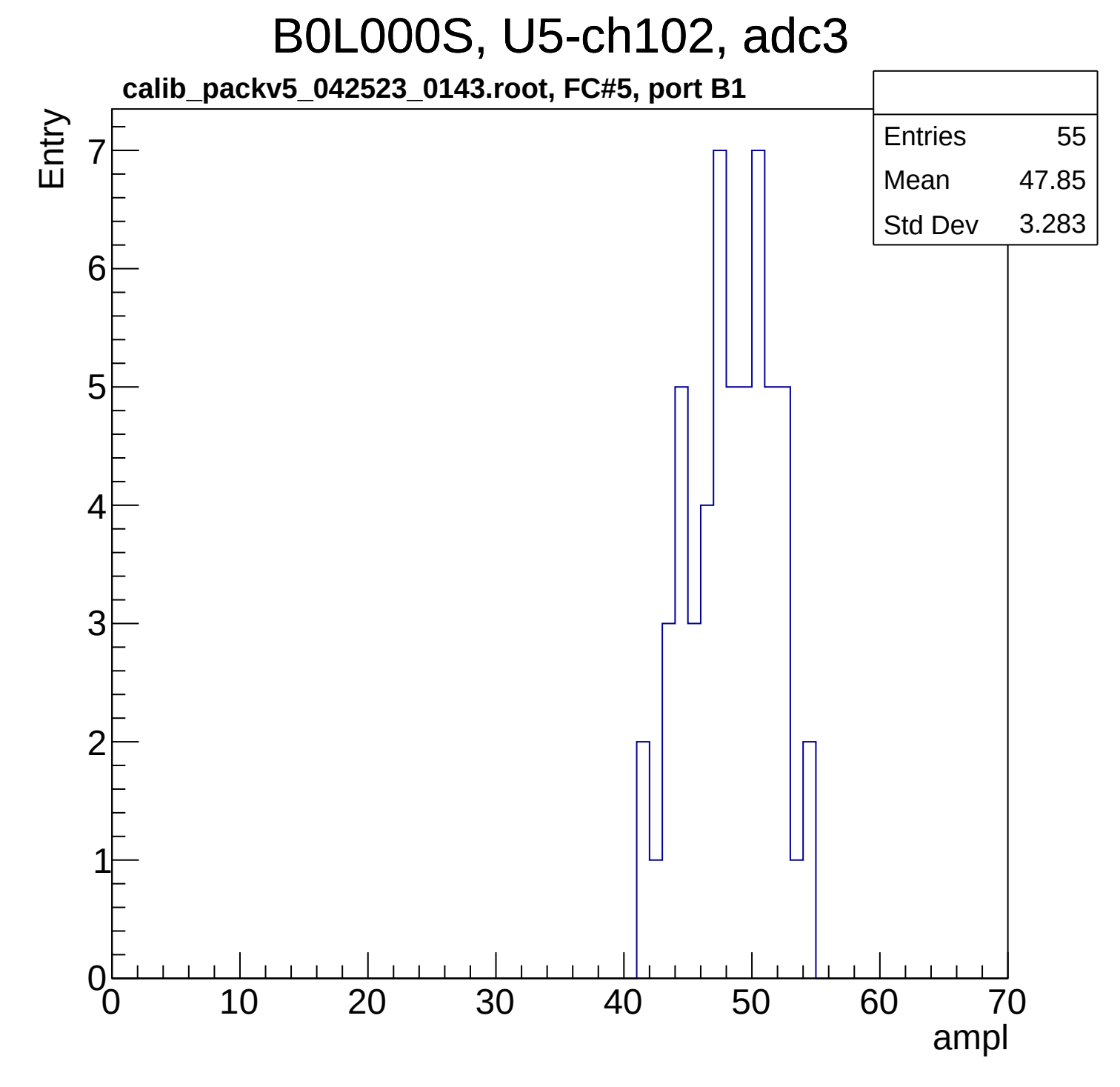

B0L000S, U5-ch101, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 8
Mean 54.5
Std Dev 20.63
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

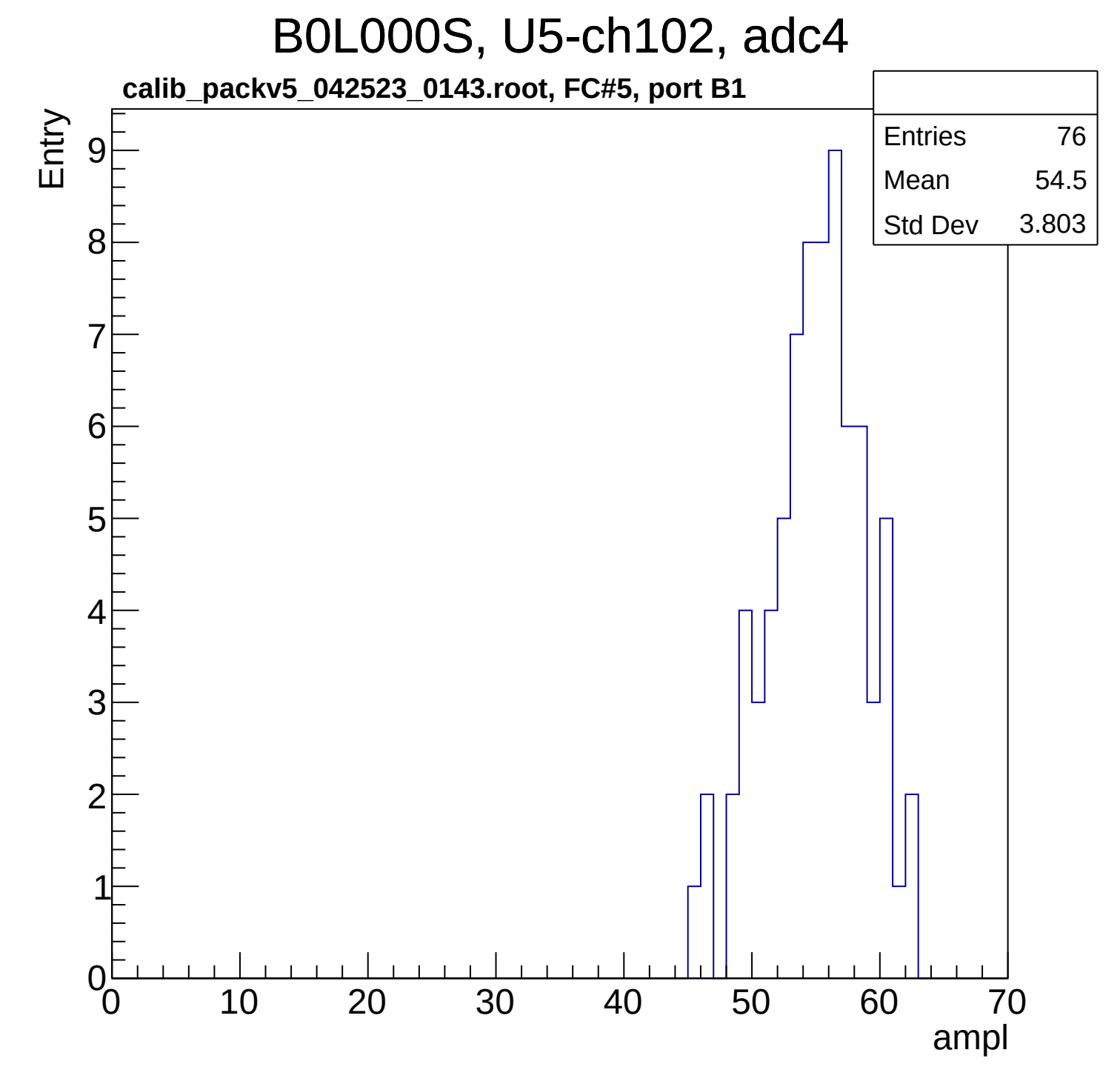

B0L000S, U5-ch101, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch102, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 27.84
Std Dev 4.742
Gaus mean : 29.2219
Gaus Width: 3.0474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch102, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 35.53
Std Dev 3.64
Gaus mean : 36.5845
Gaus Width: 3.4042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch102, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 41.4
Std Dev 4.339
Gaus mean : 42.3374
Gaus Width: 4.8857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch102, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 47.85
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch102, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 54.5
Std Dev 3.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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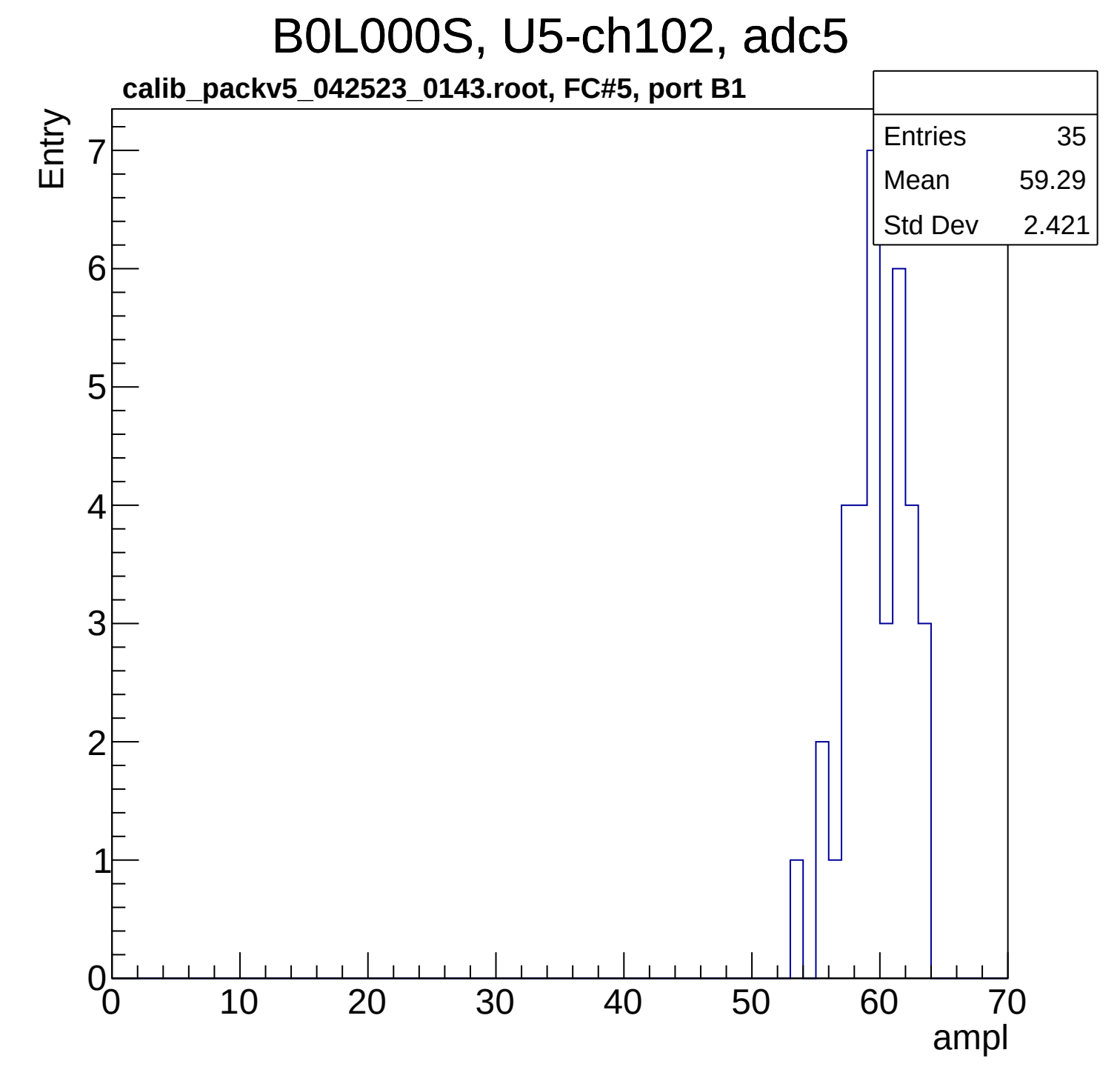
B0L000S, U5-ch102, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 59.29
Std Dev 2.421
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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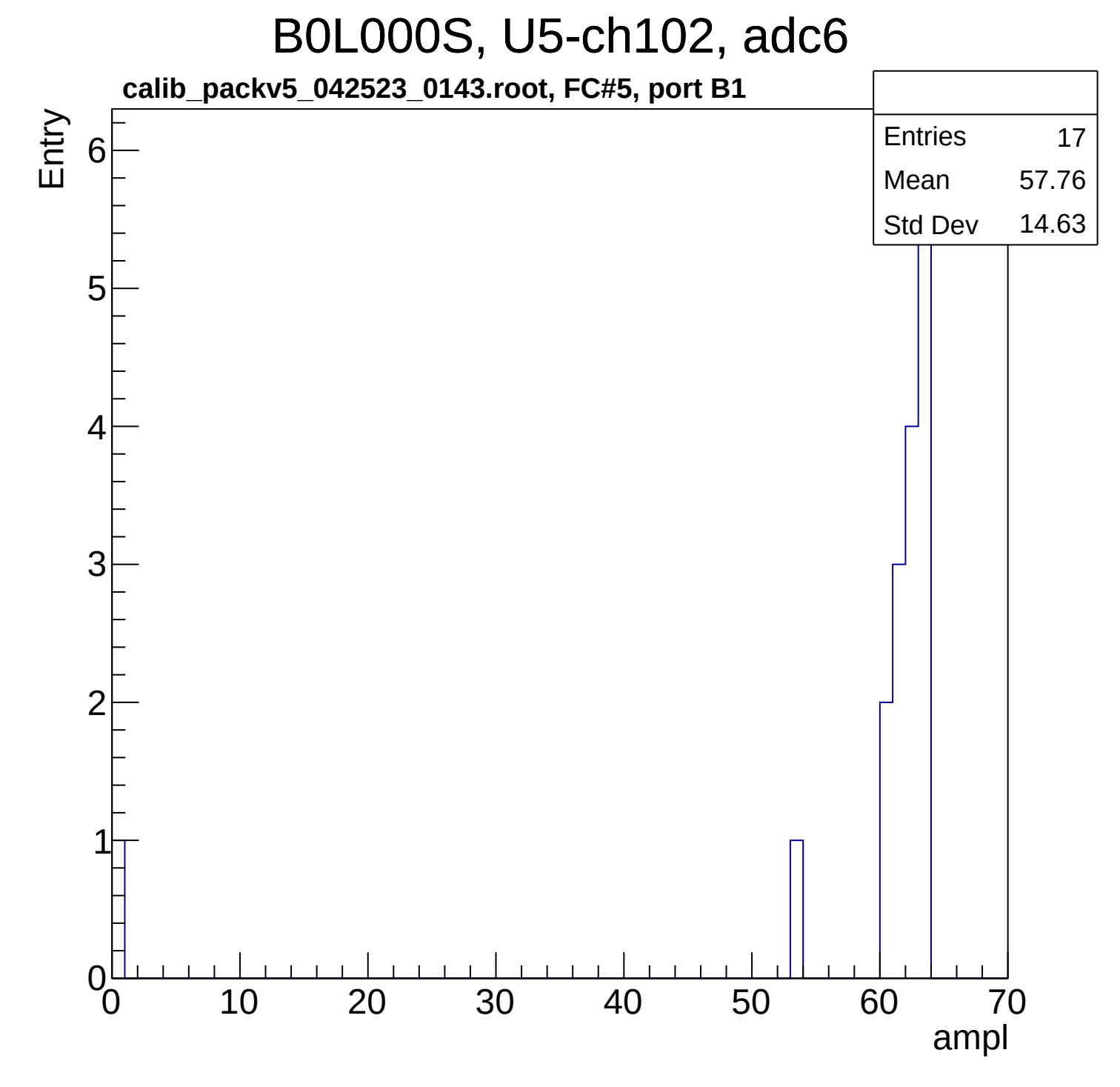
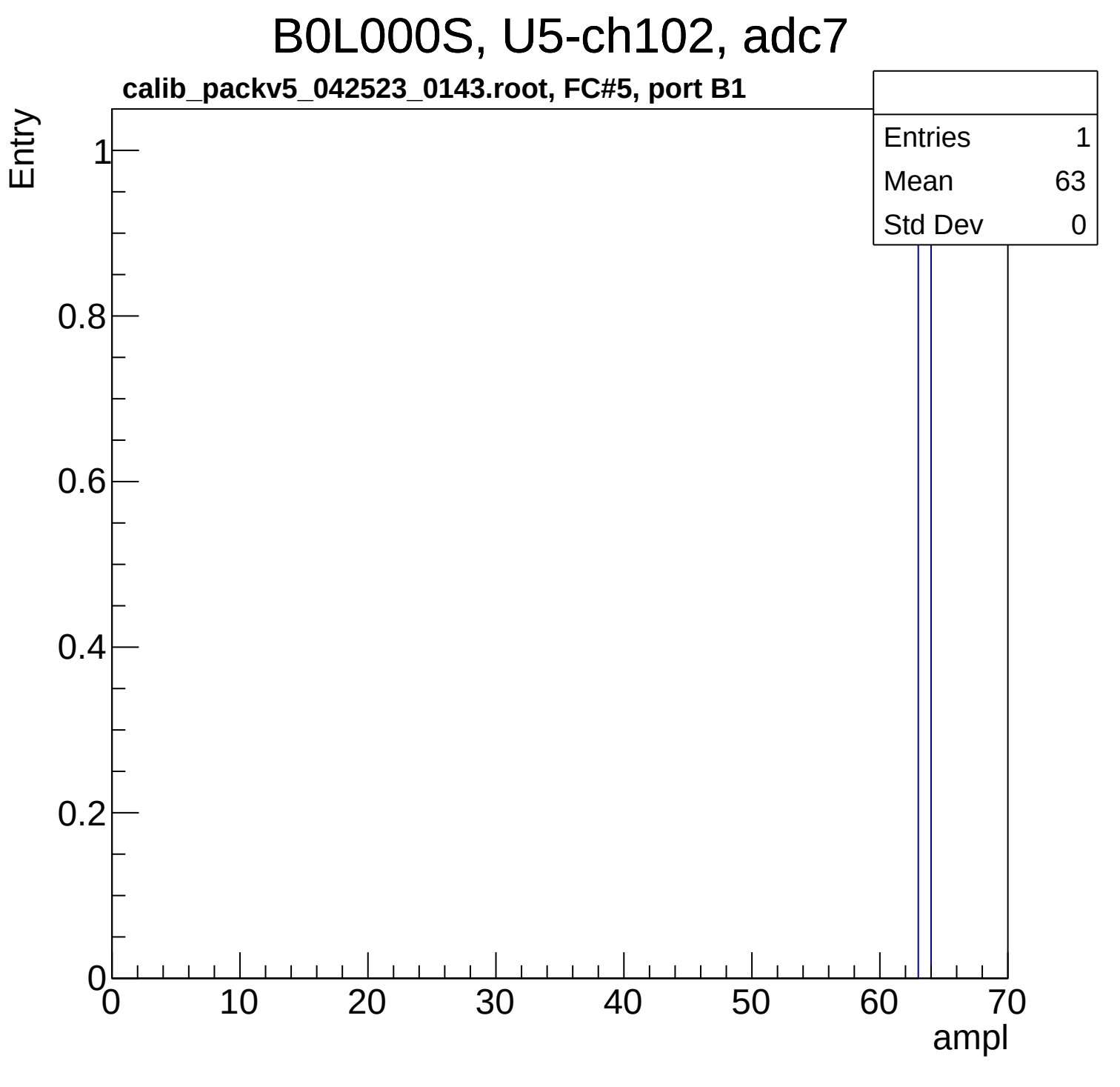

B0L000S, U5-ch102, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 17
Mean 57.76
Std Dev 14.63
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch102, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 63
Std Dev 0
Entry
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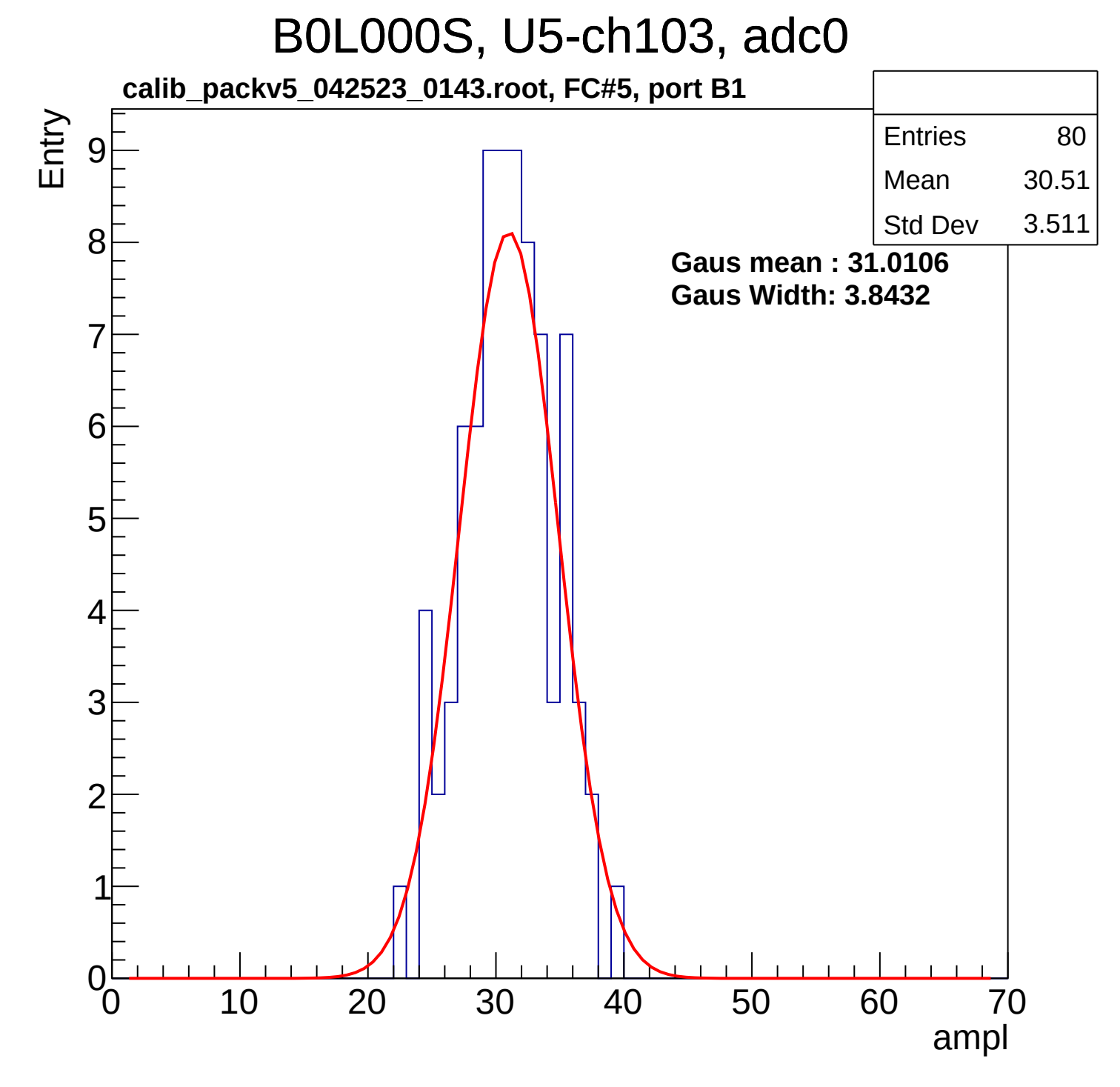
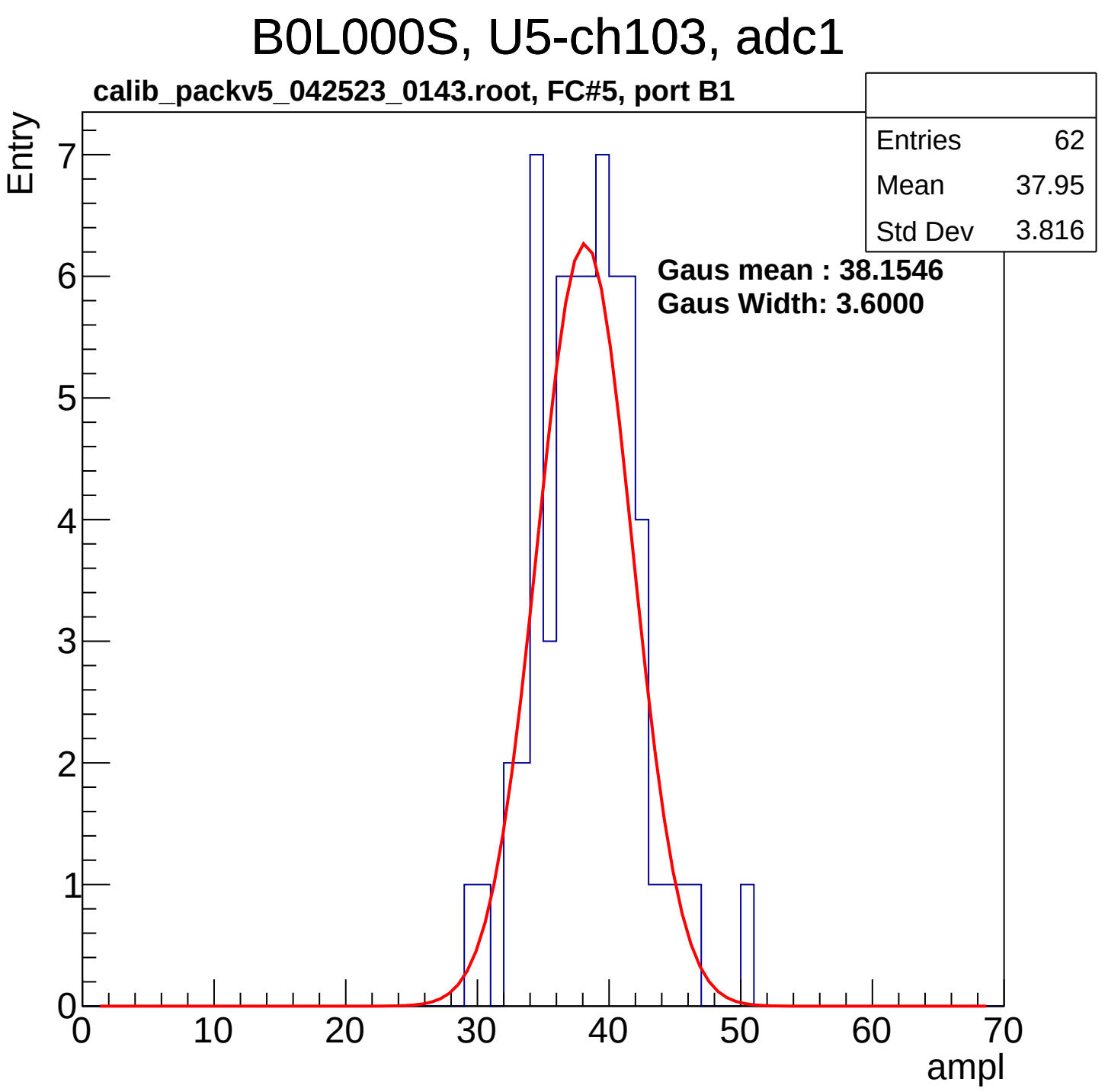
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch103, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 30.51
Std Dev 3.511
Gaus mean : 31.0106
Gaus Width: 3.8432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch103, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 37.95
Std Dev 3.816
Gaus mean : 38.1546
Gaus Width: 3.6000
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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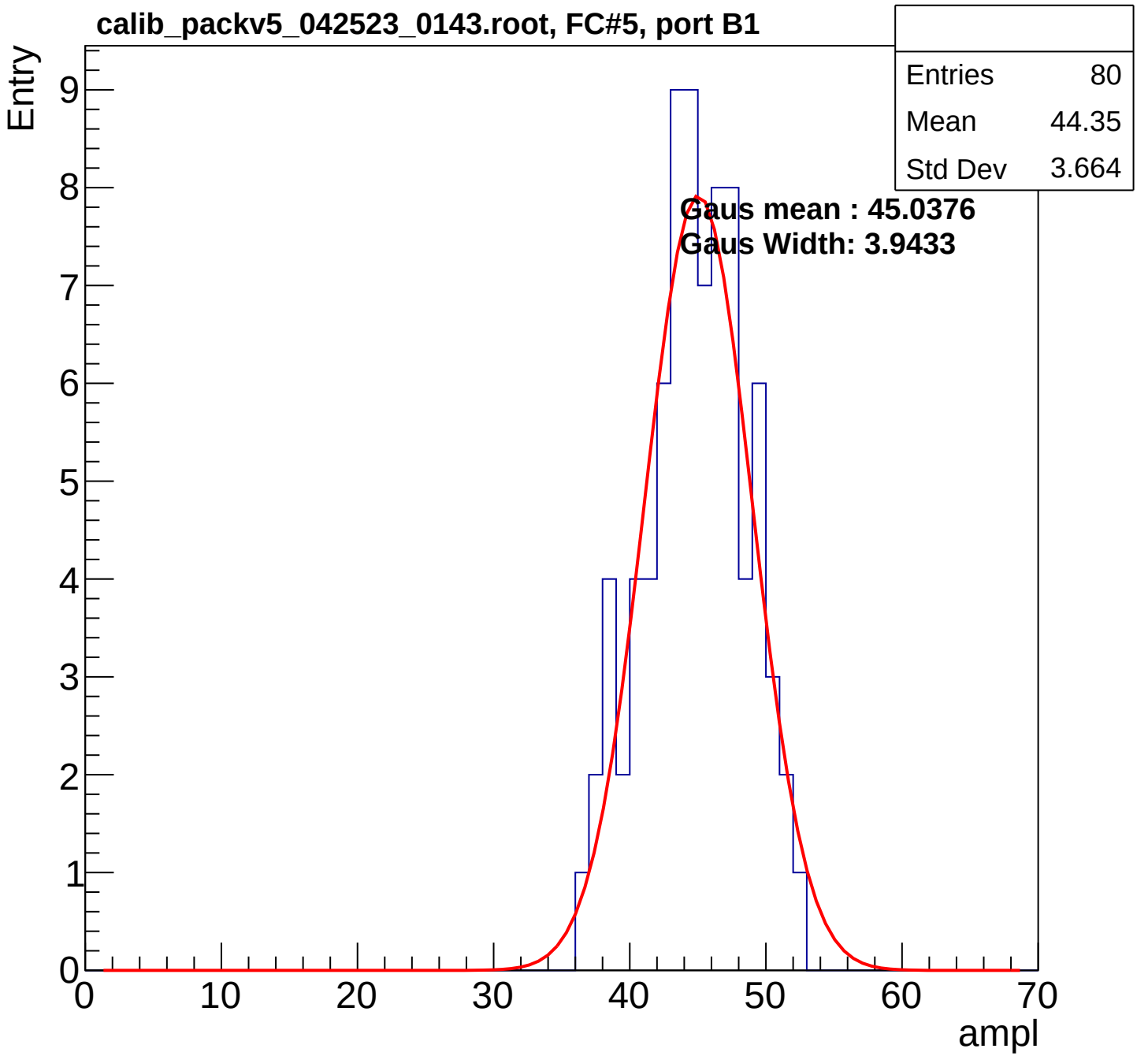
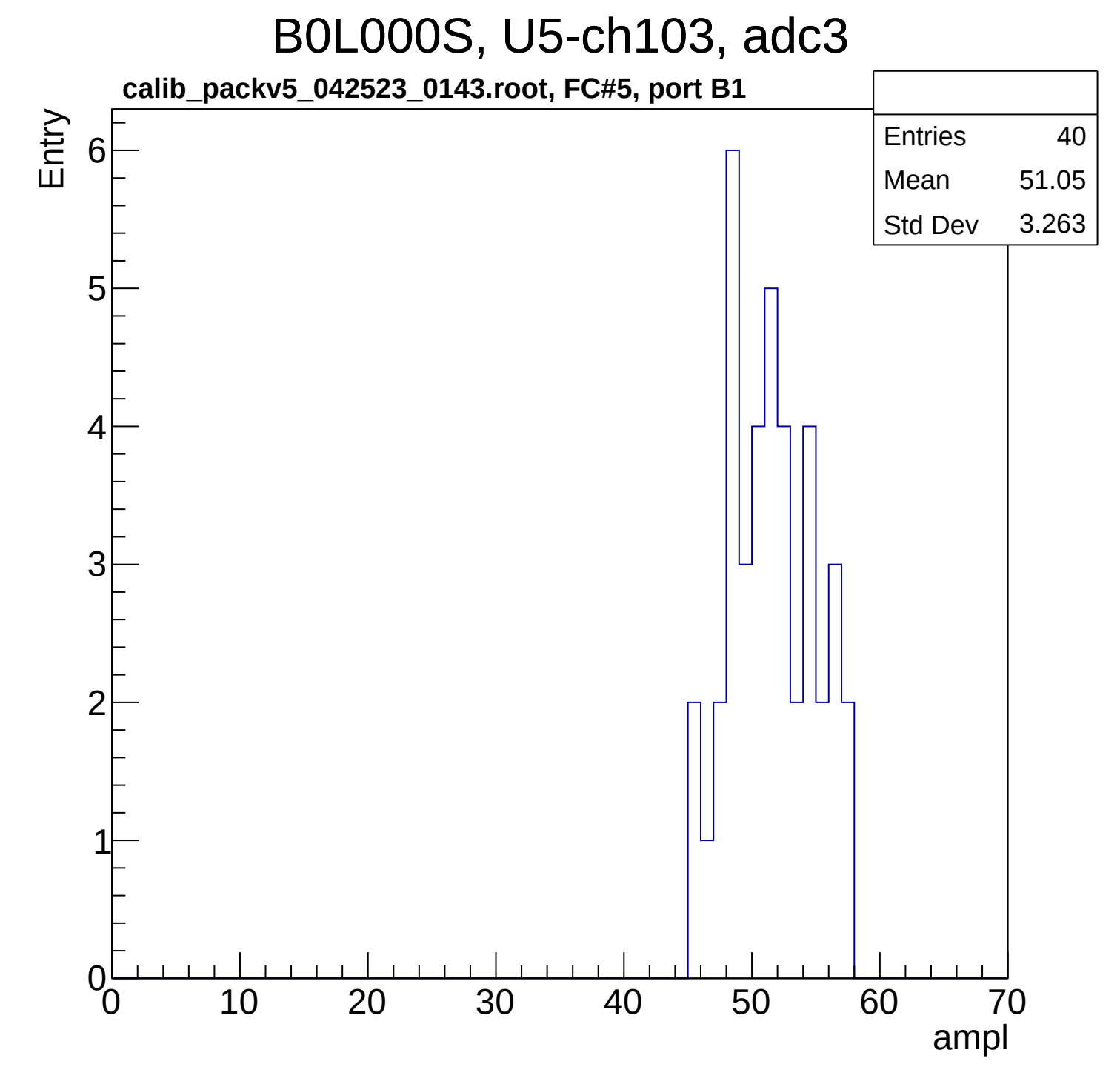

B0L000S, U5-ch103, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 44.35
Std Dev 3.664
Gaus mean : 45.0376
Gaus Width: 3.9433
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch103, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 51.05
Std Dev 3.263
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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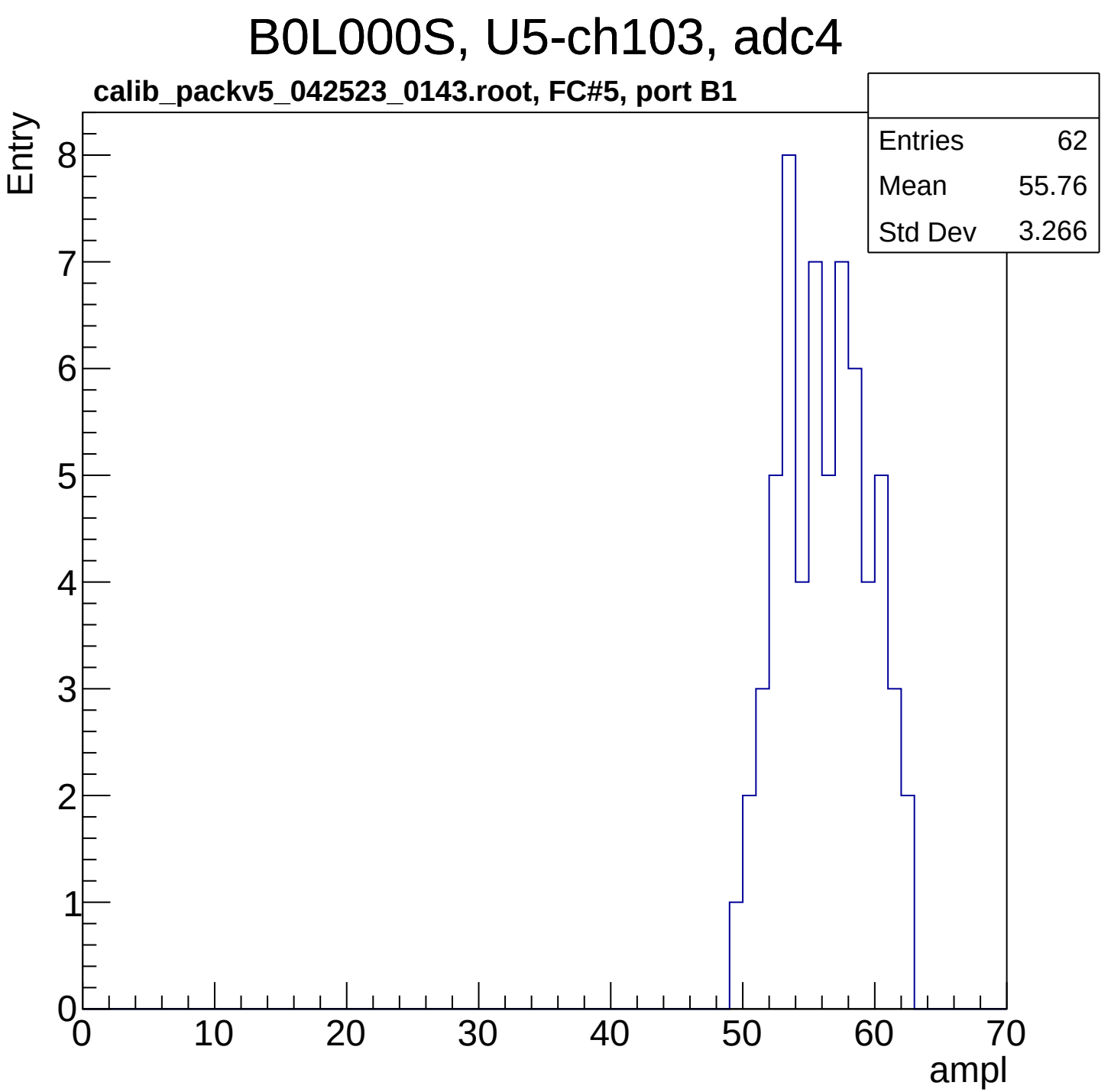
B0L000S, U5-ch103, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 55.76
Std Dev 3.266
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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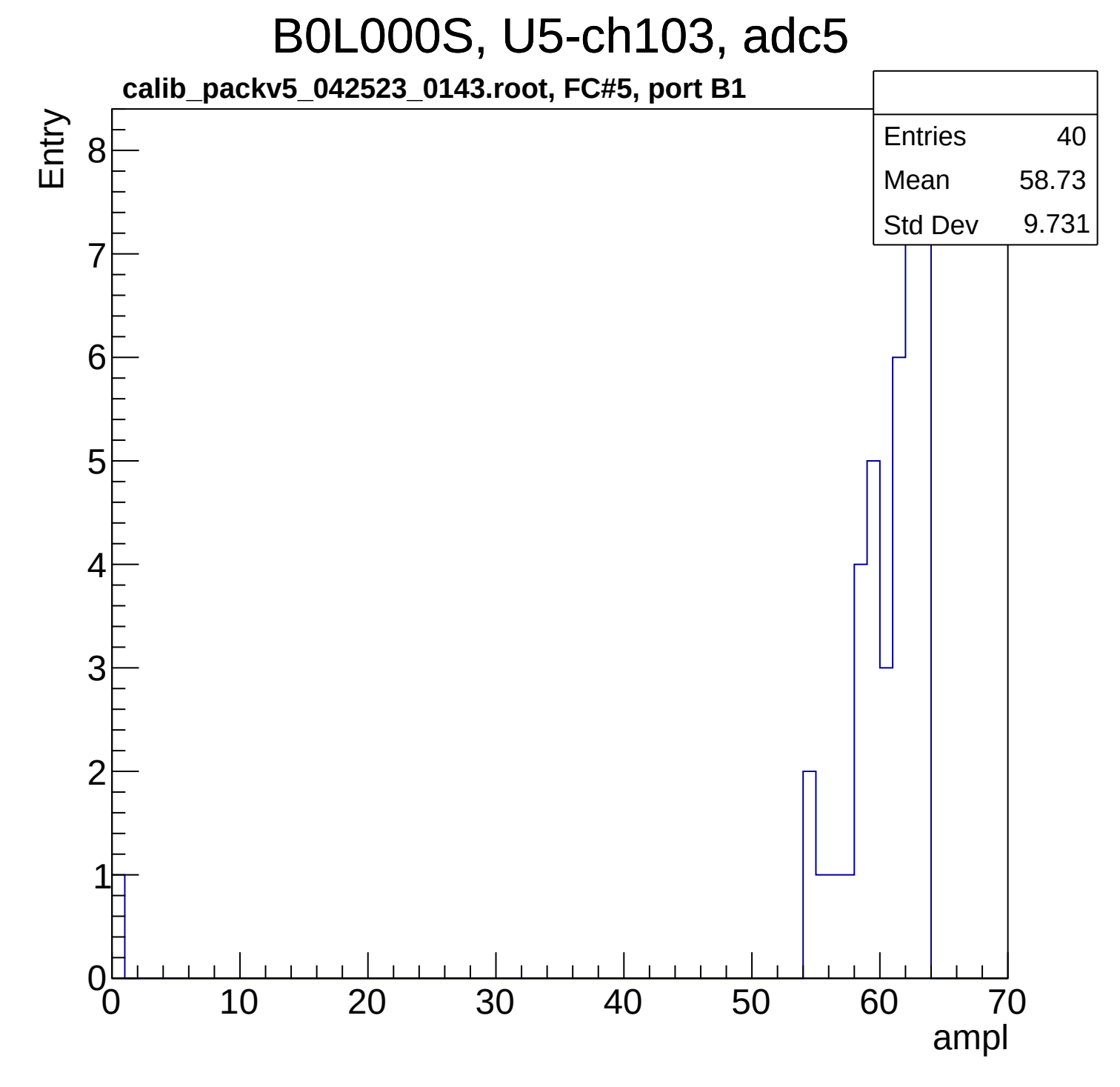
B0L000S, U5-ch103, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 40
Mean 58.73
Std Dev 9.731
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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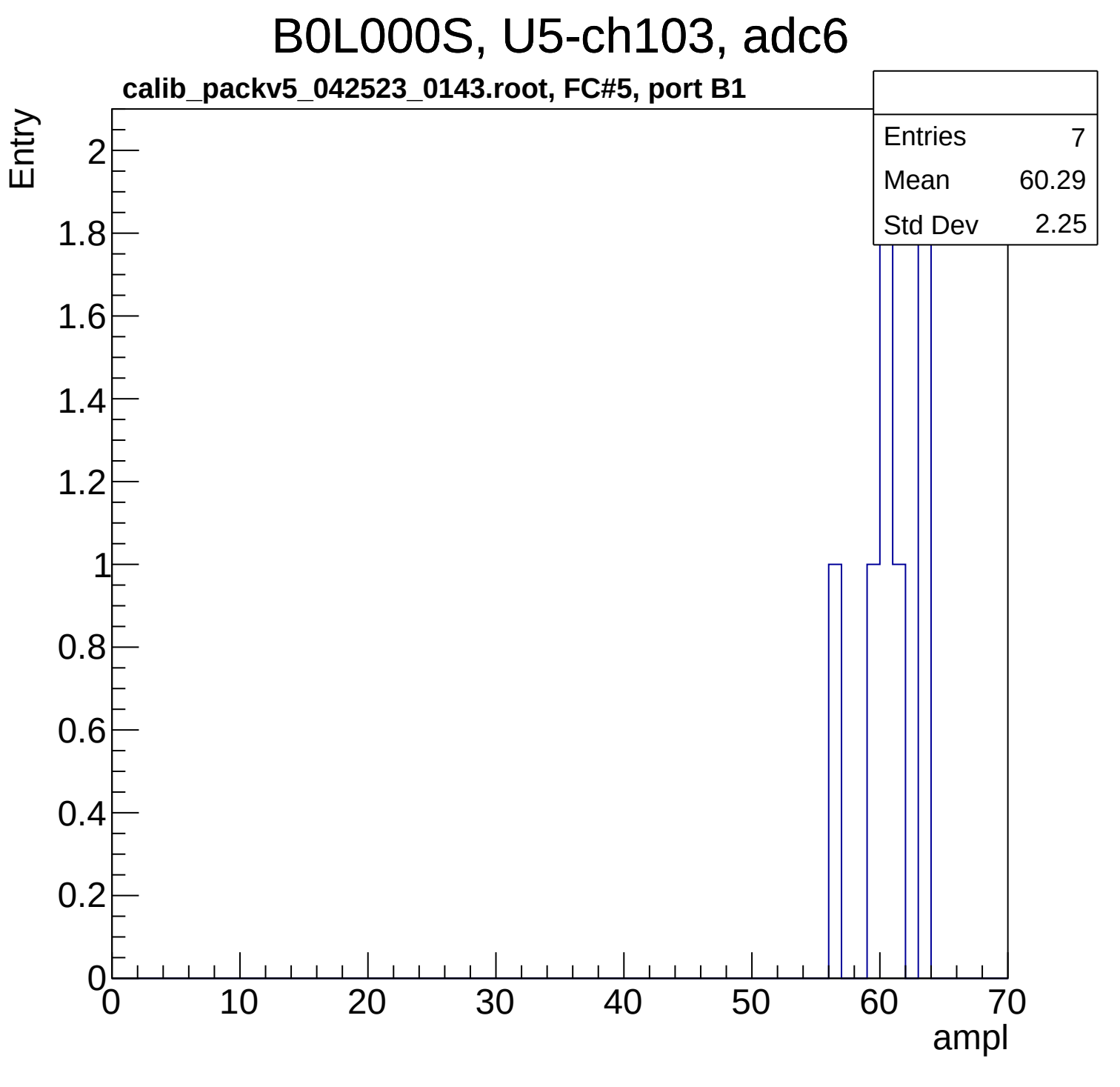
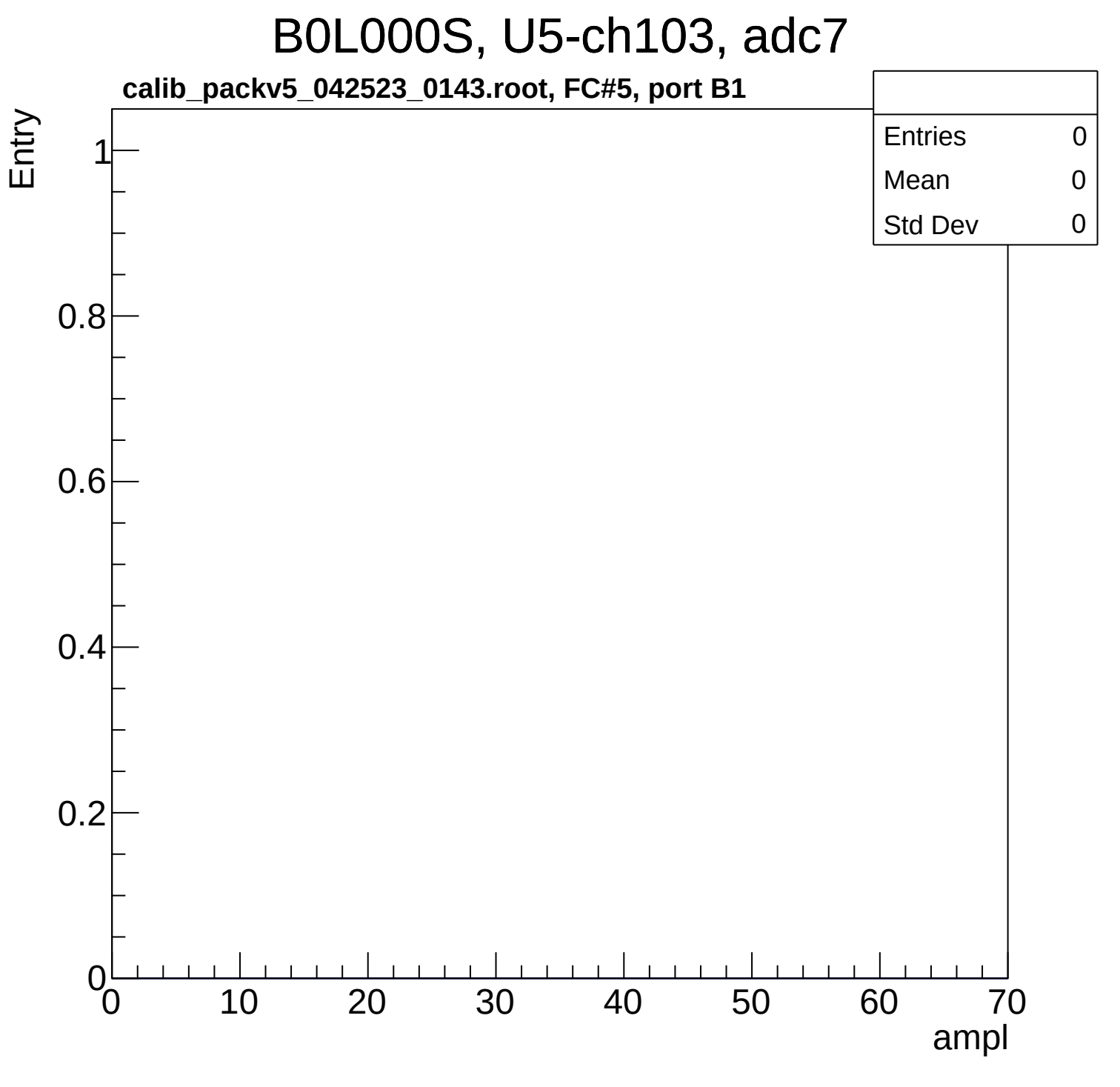
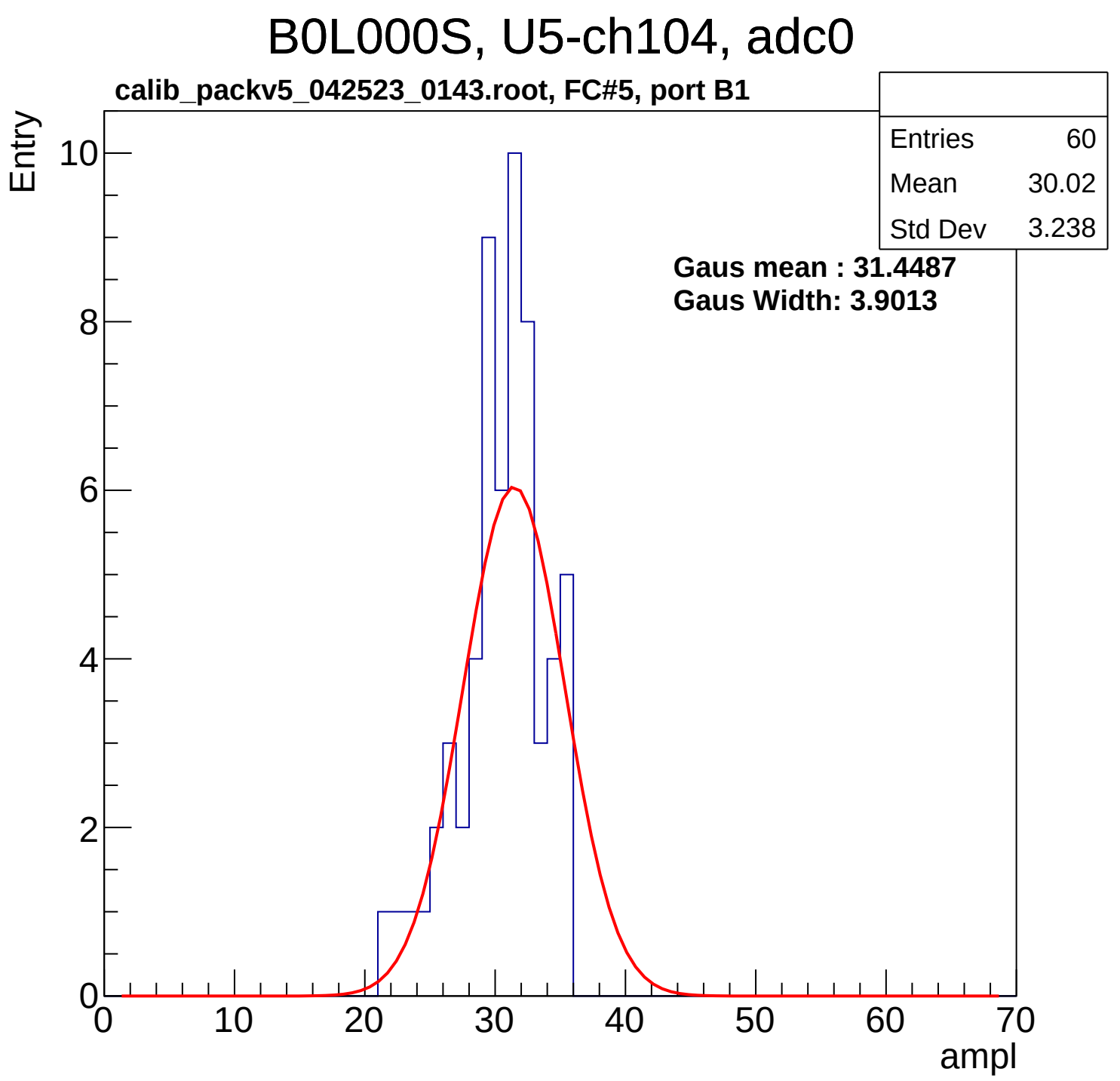
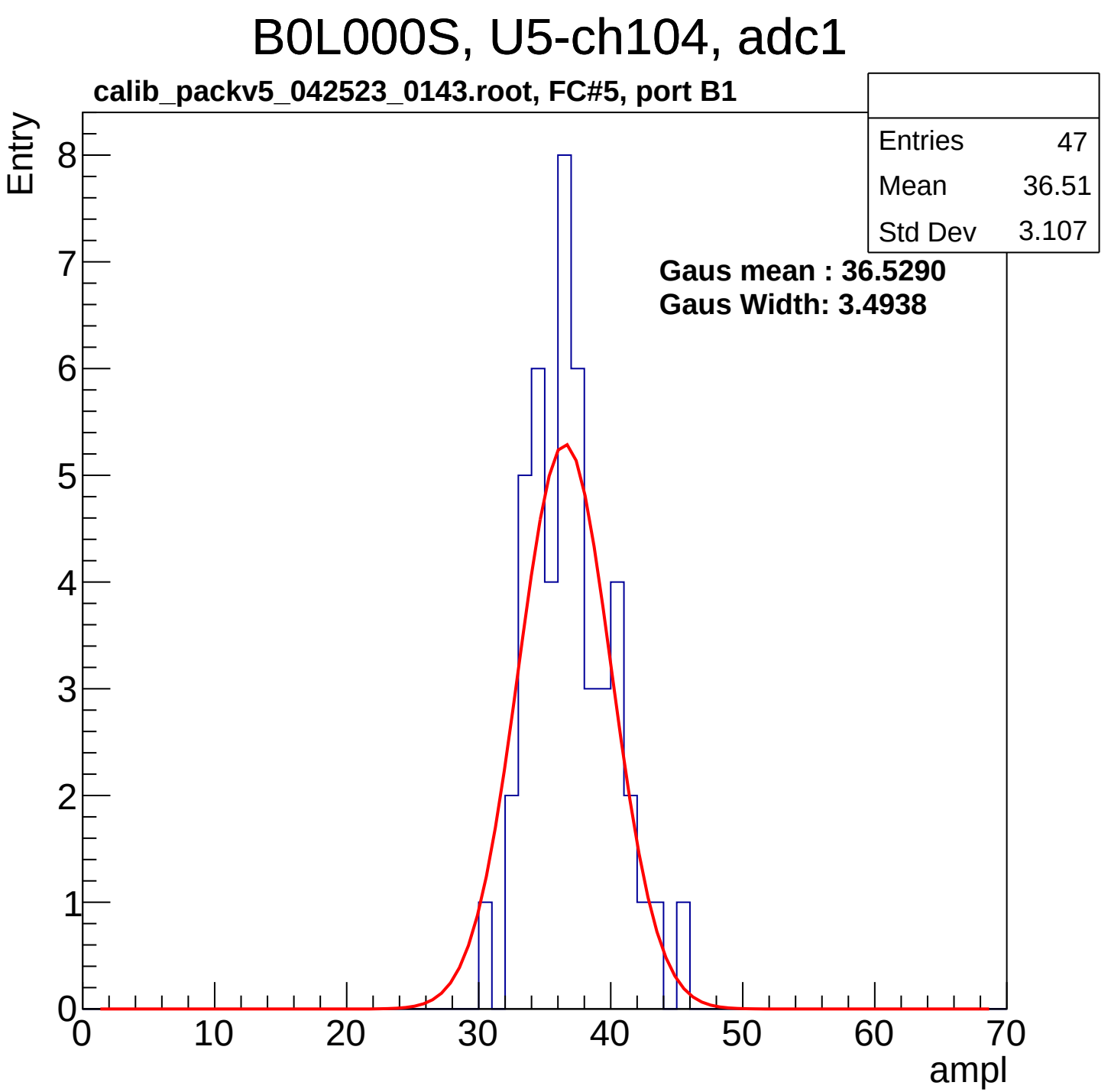
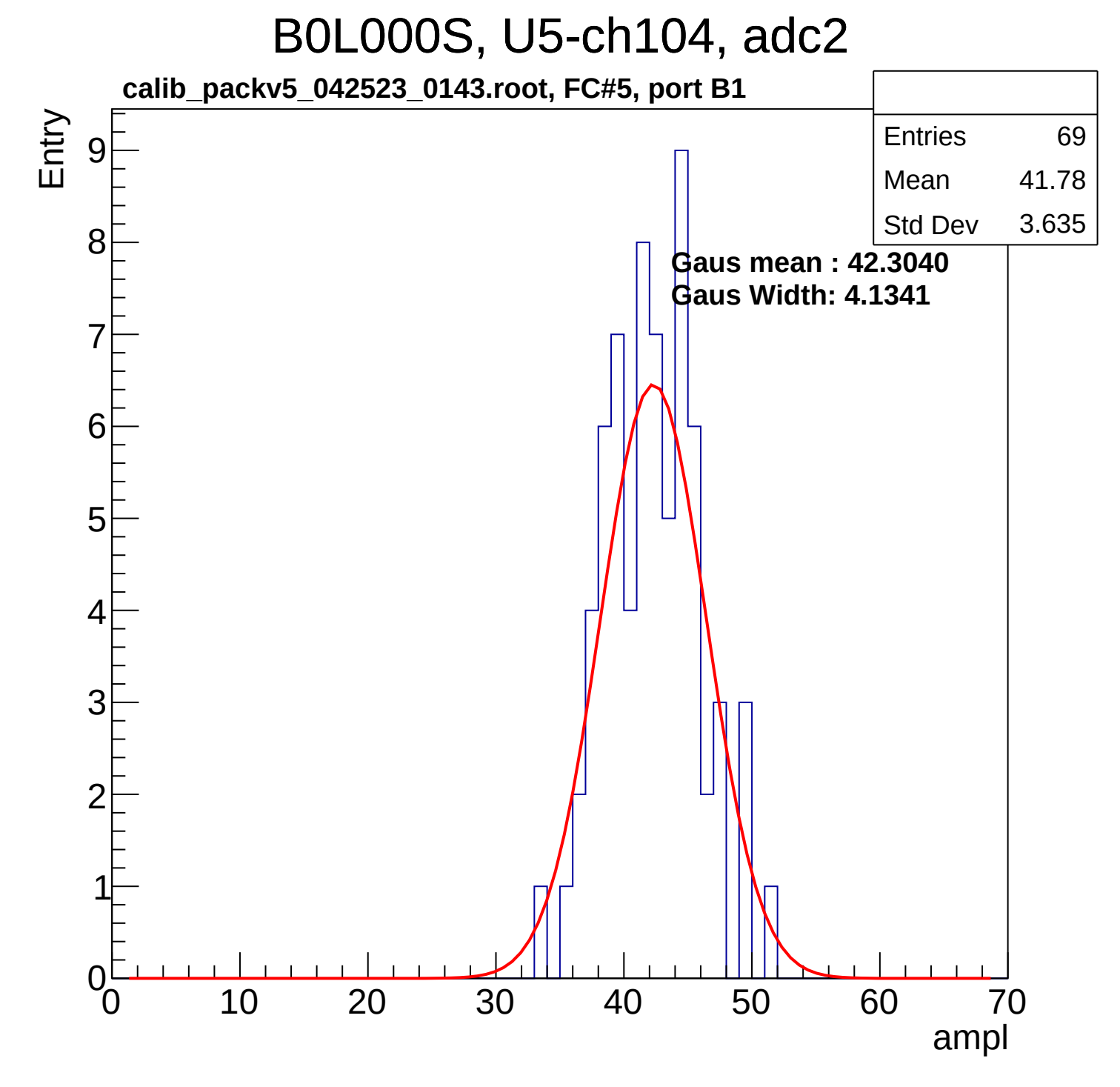

B0L000S, U5-ch103, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 60.29
Std Dev 2.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch103, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch104, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 30.02
Std Dev 3.238
Gaus mean : 31.4487
Gaus Width: 3.9013
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch104, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 36.51
Std Dev 3.107
Gaus mean : 36.5290
Gaus Width: 3.4938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch104, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 41.78
Std Dev 3.635
Gaus mean : 42.3040
Gaus Width: 4.1341
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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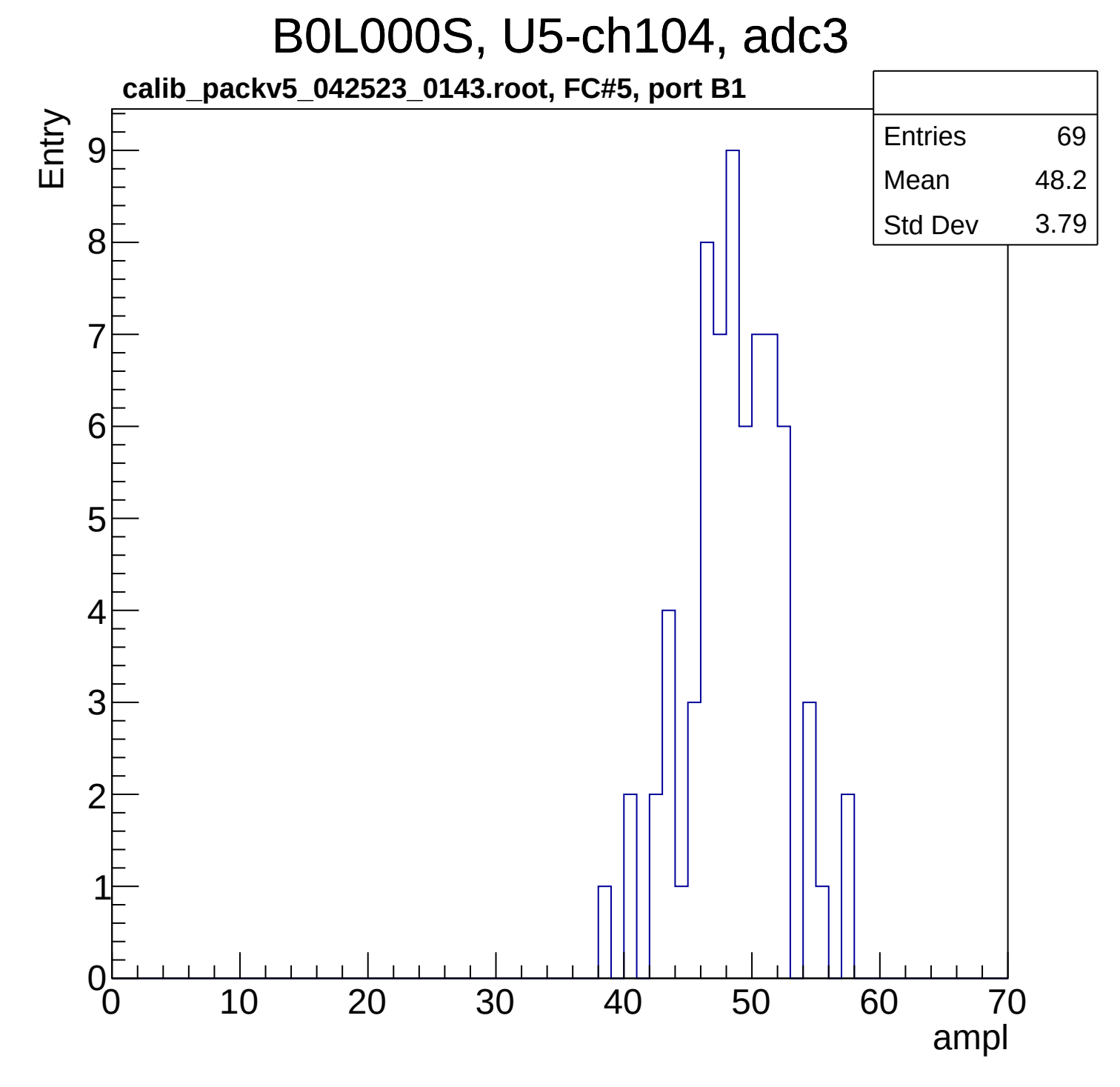
B0L000S, U5-ch104, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 48.2
Std Dev 3.79
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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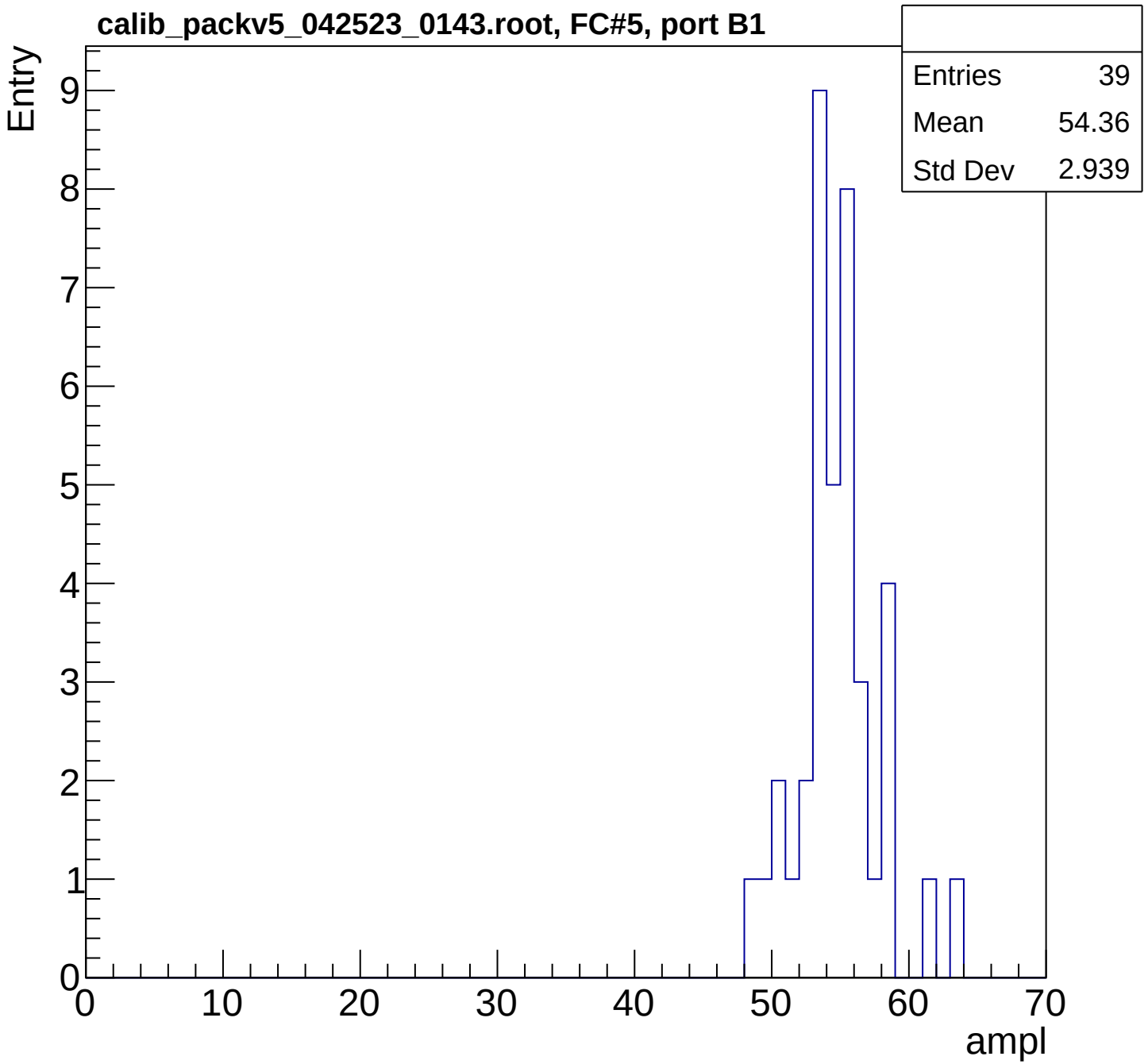
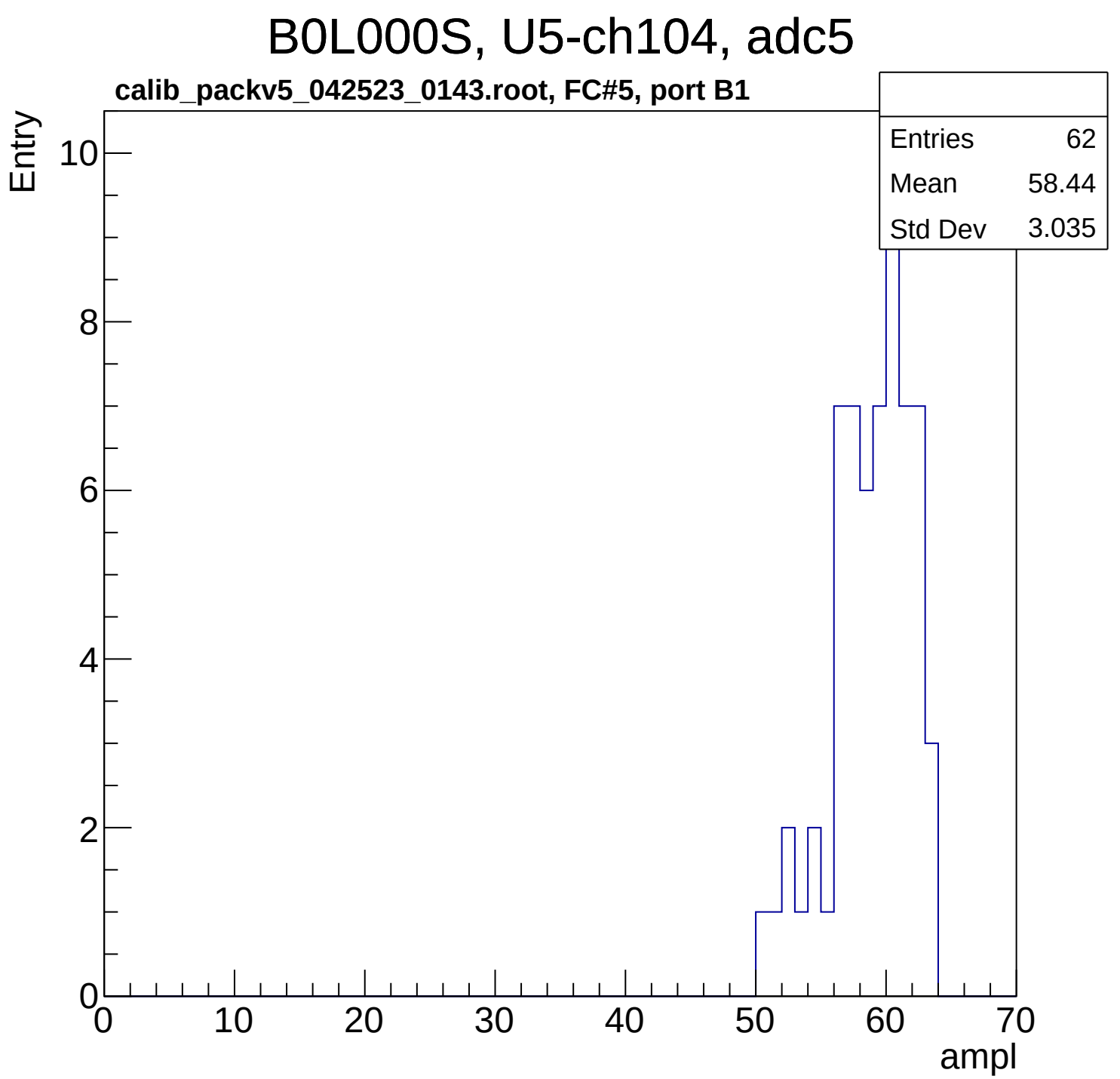
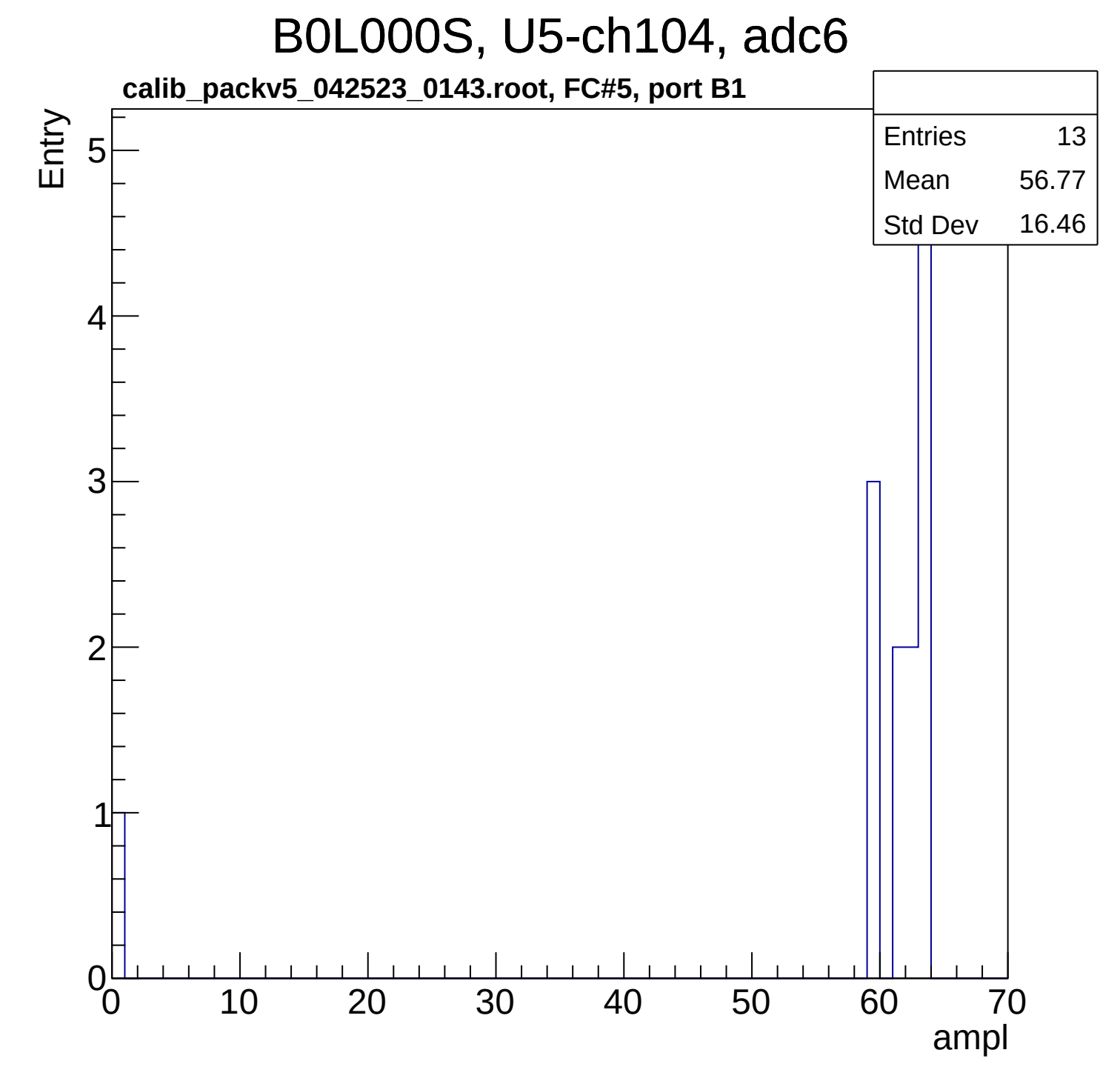
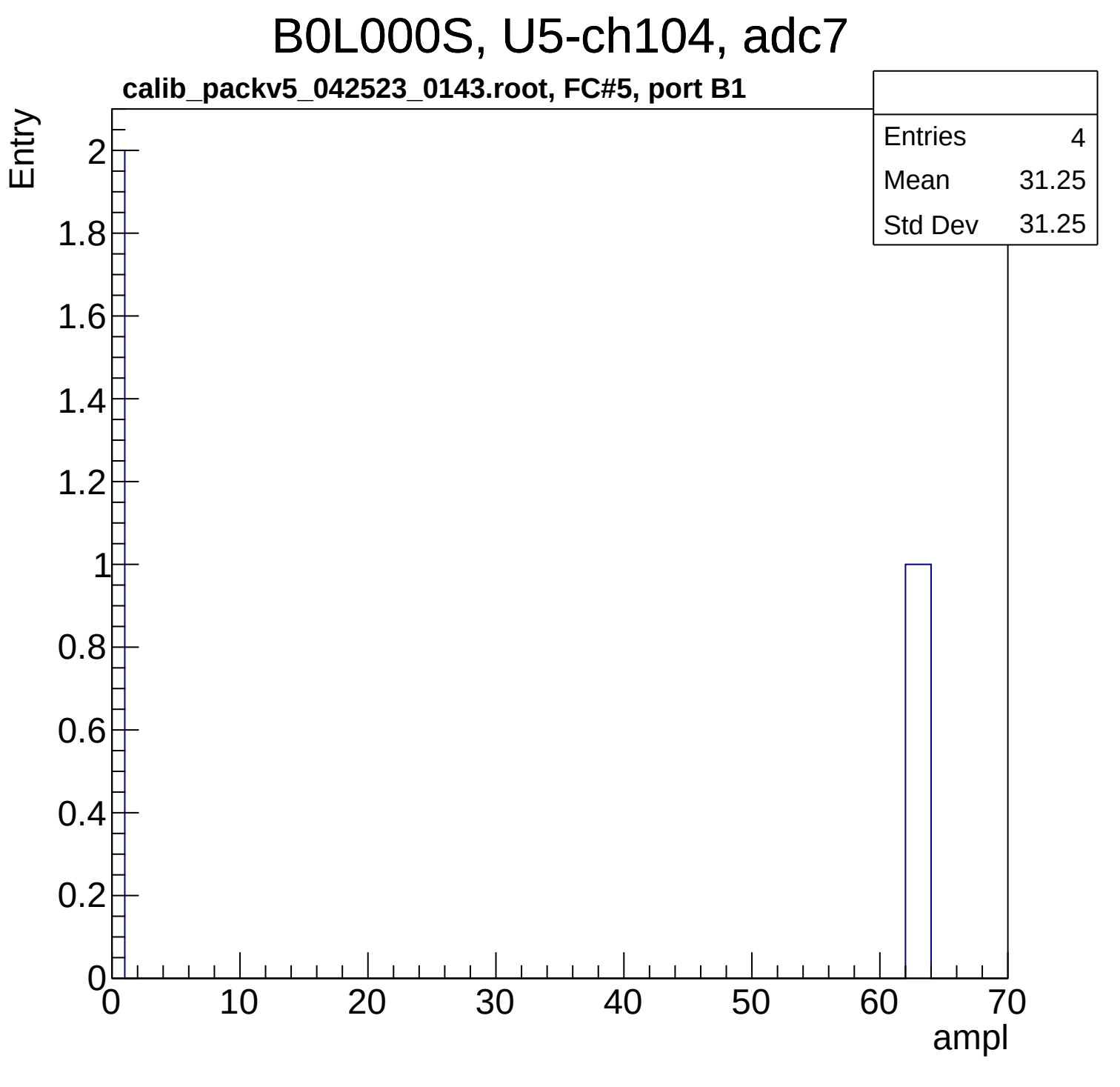
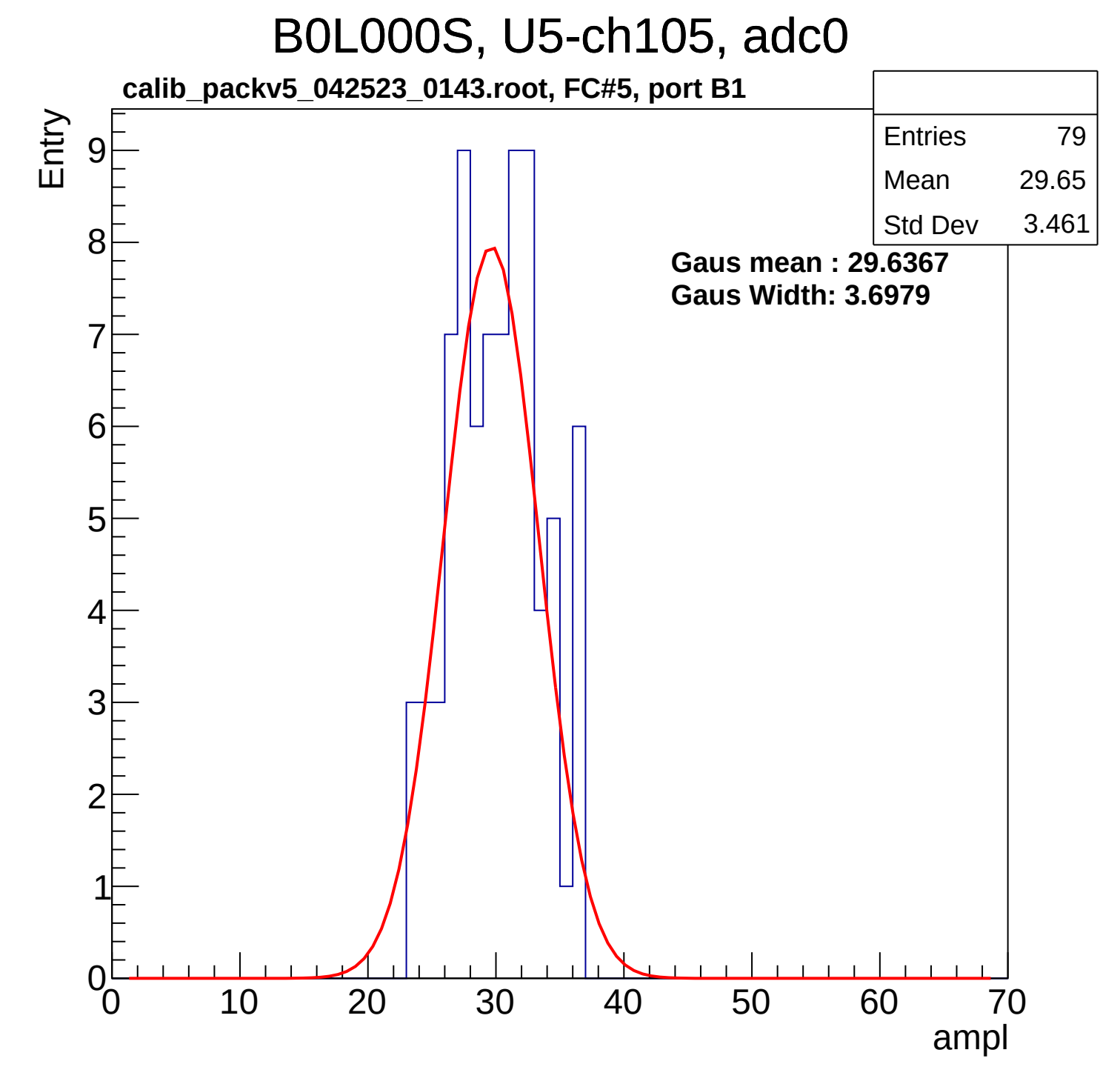
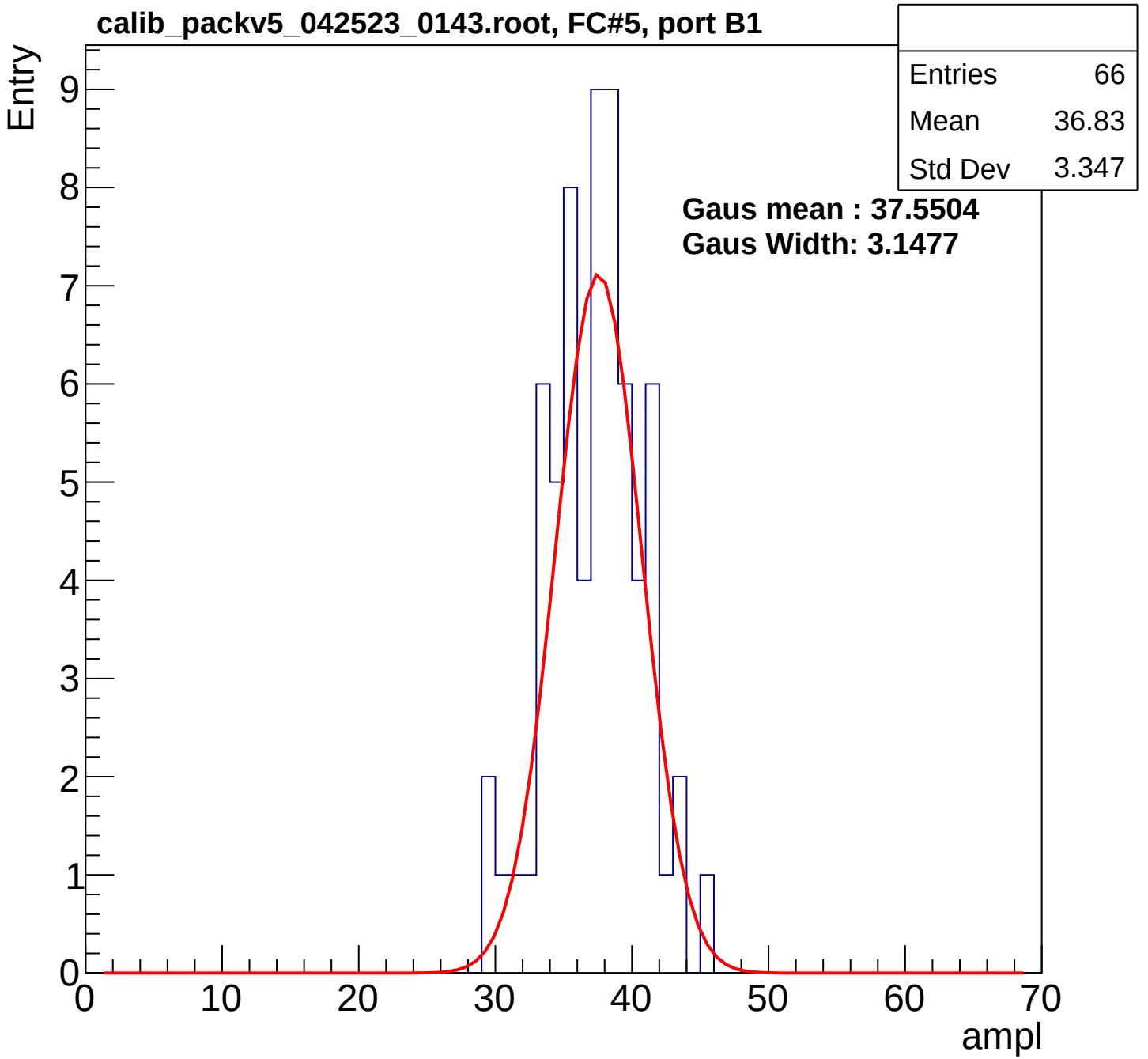

B0L000S, U5-ch104, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 39
Mean 54.36
Std Dev 2.939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

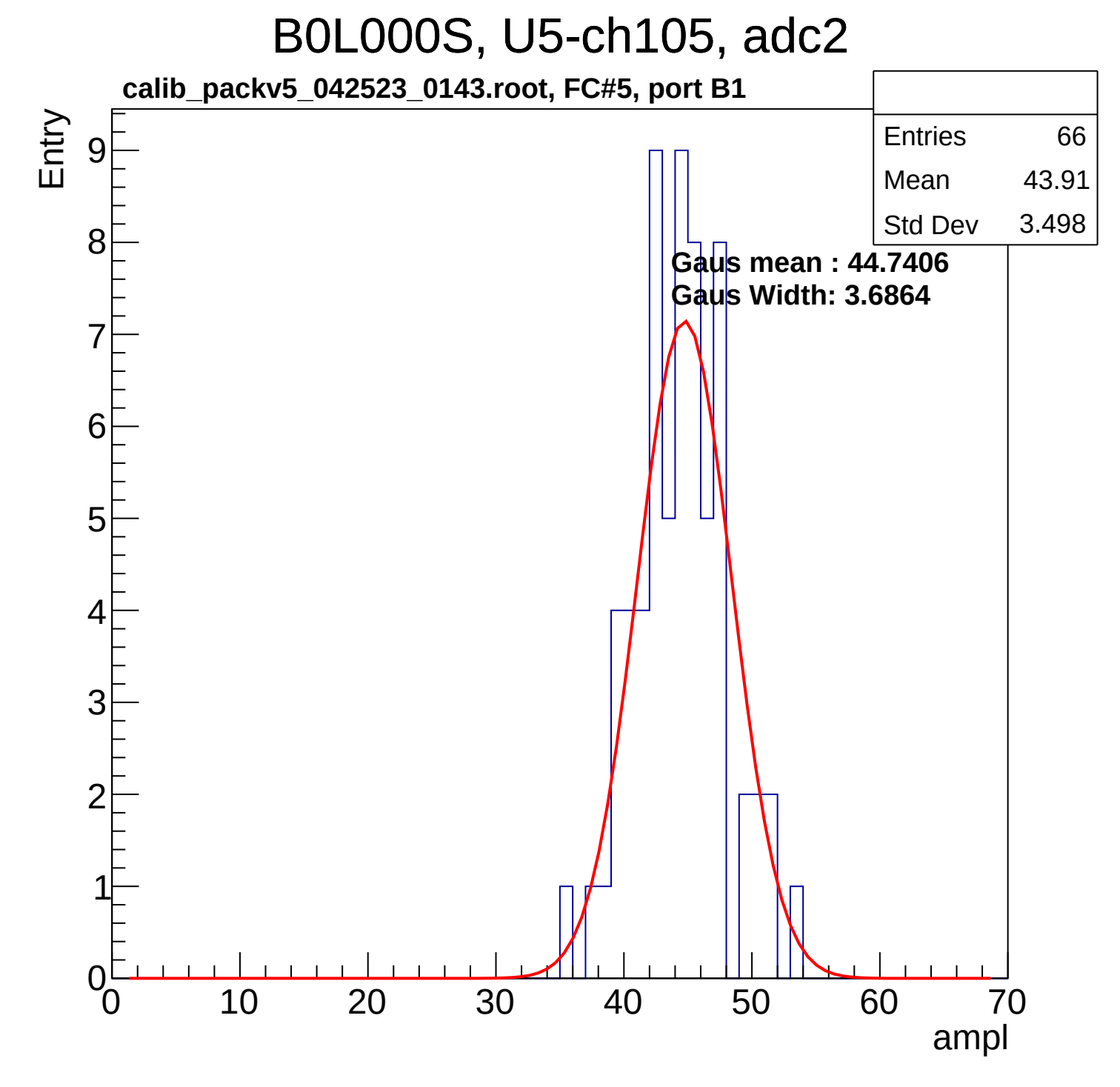

B0L000S, U5-ch104, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 58.44
Std Dev 3.035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

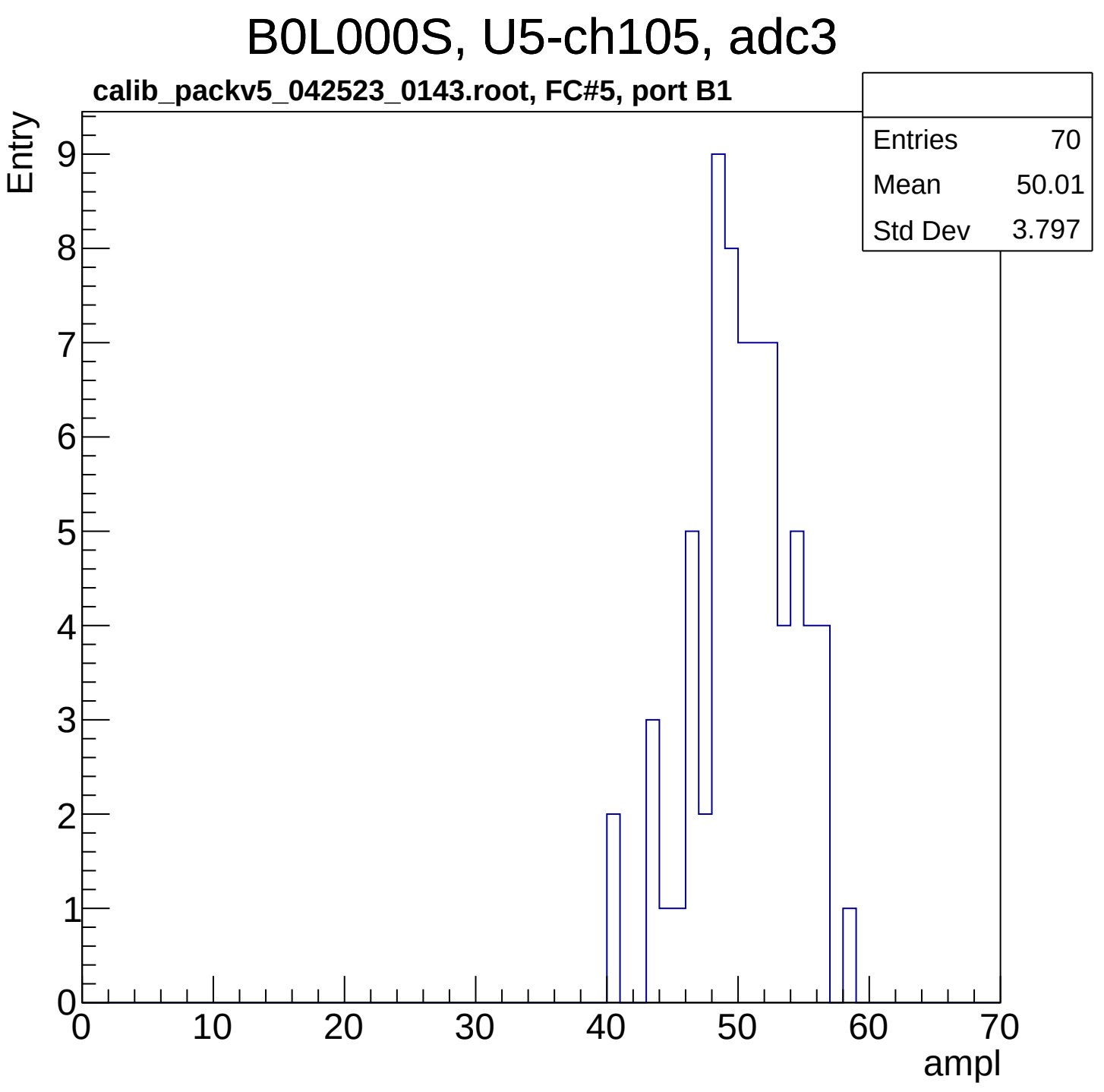

B0L000S, U5-ch104, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 13
Mean 56.77
Std Dev 16.46
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch104, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L000S, U5-ch105, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 29.65
Std Dev 3.461
Gaus mean : 29.6367
Gaus Width: 3.6979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch105, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 36.83
Std Dev 3.347
Gaus mean : 37.5504
Gaus Width: 3.1477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch105, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 43.91
Std Dev 3.498
Gaus mean : 44.7406
Gaus Width: 3.6864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch105, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 50.01
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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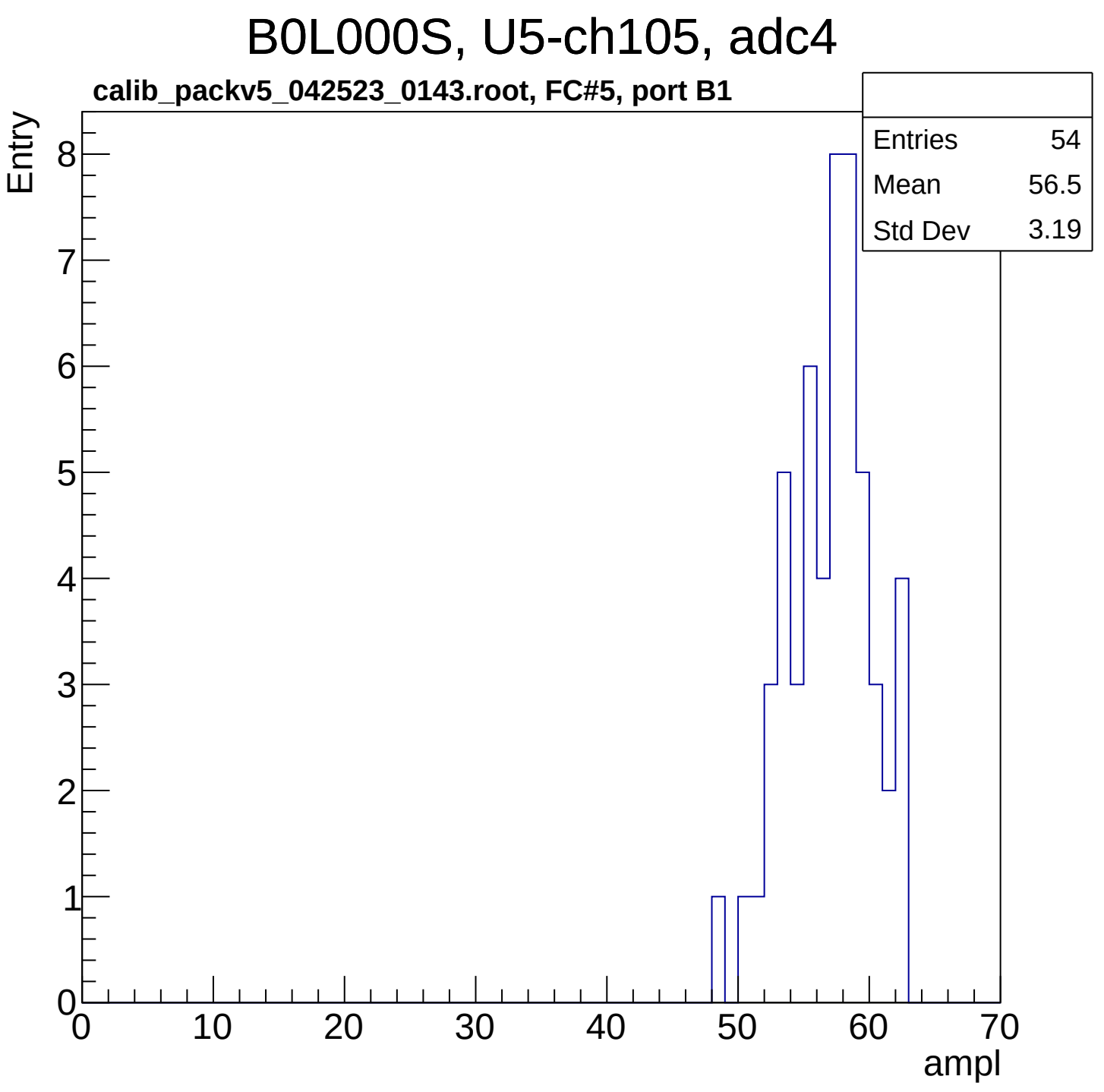
B0L000S, U5-ch105, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 56.5
Std Dev 3.19
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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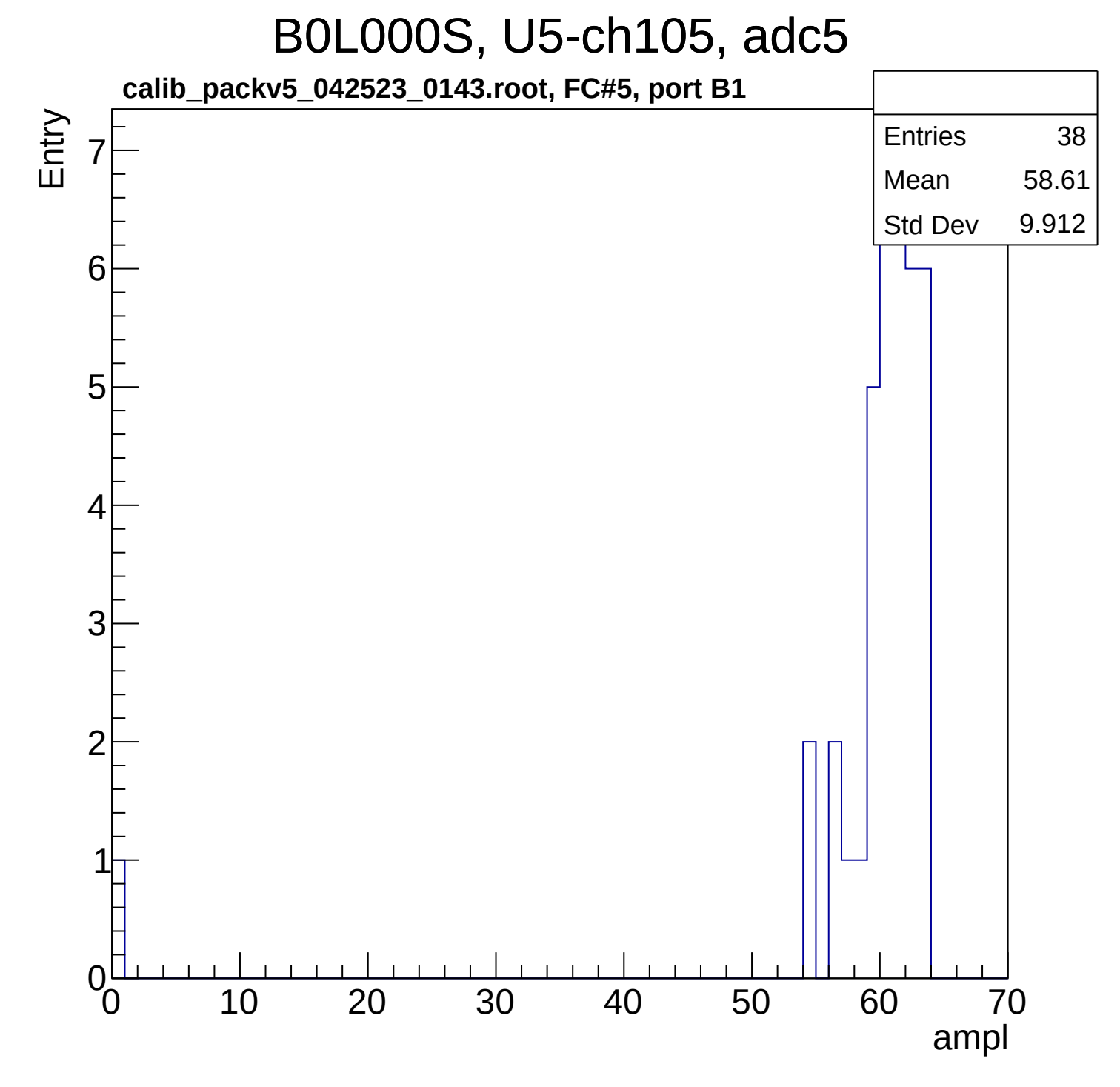
B0L000S, U5-ch105, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 38
Mean 58.61
Std Dev 9.912
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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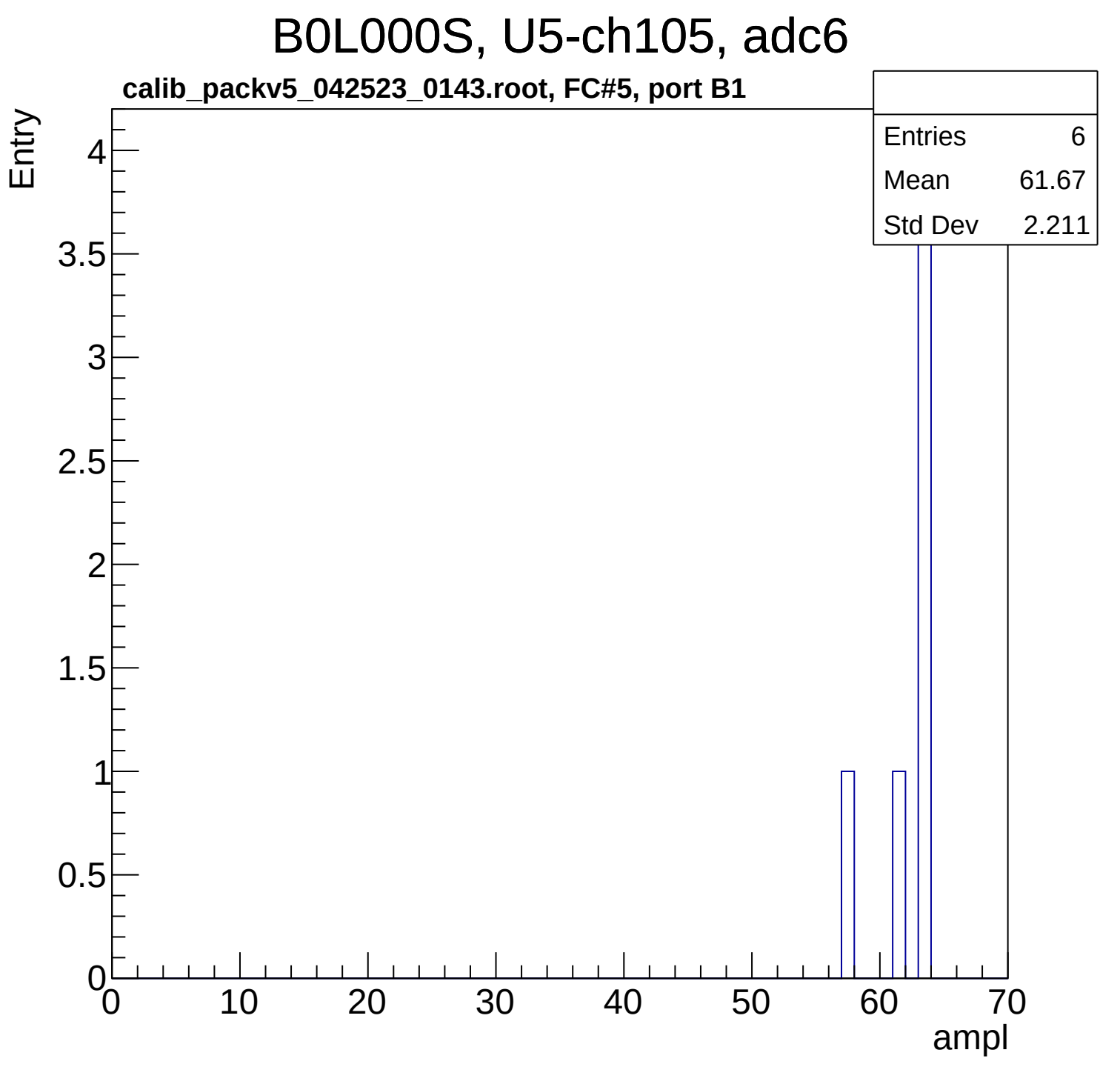
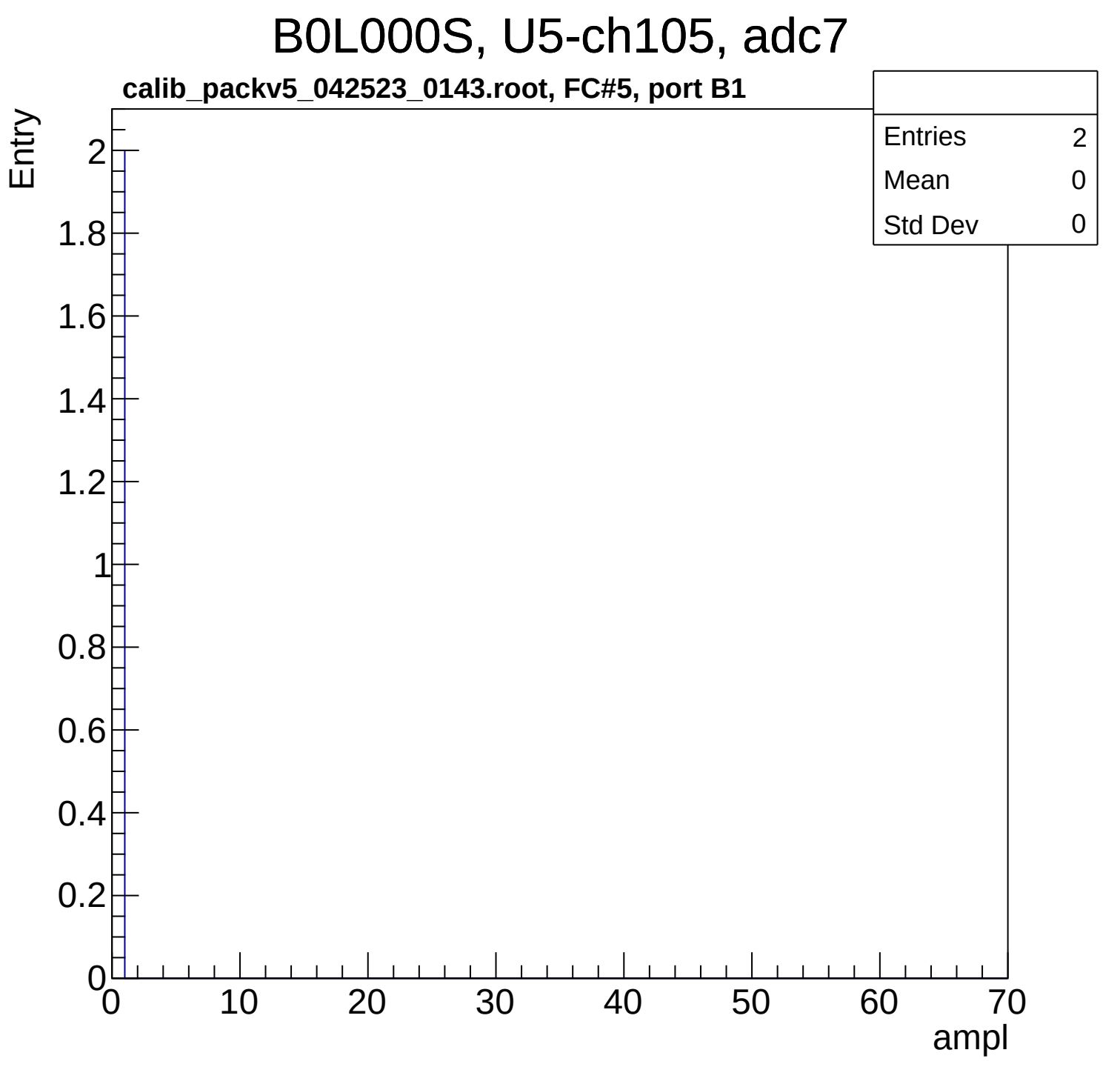
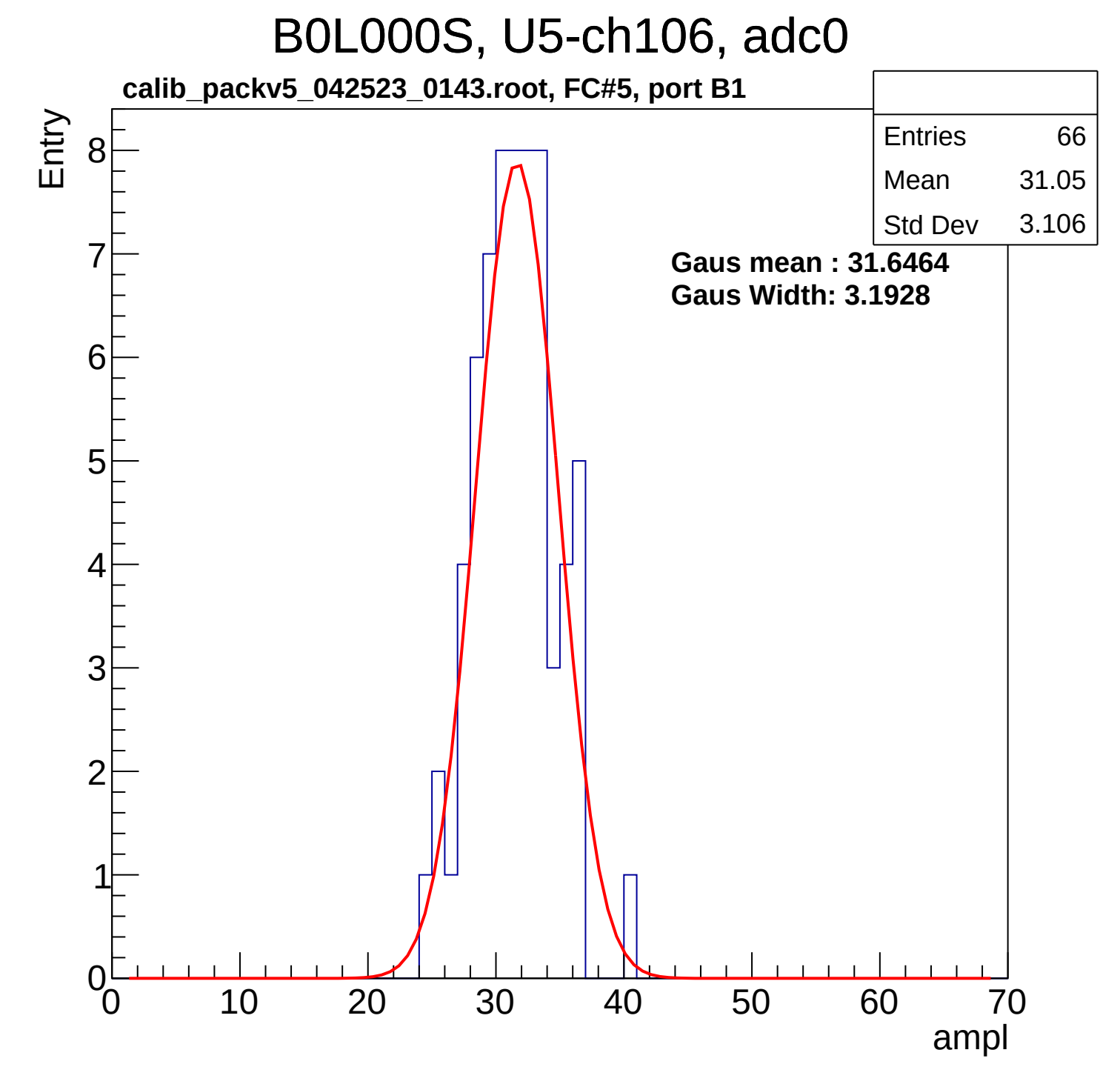
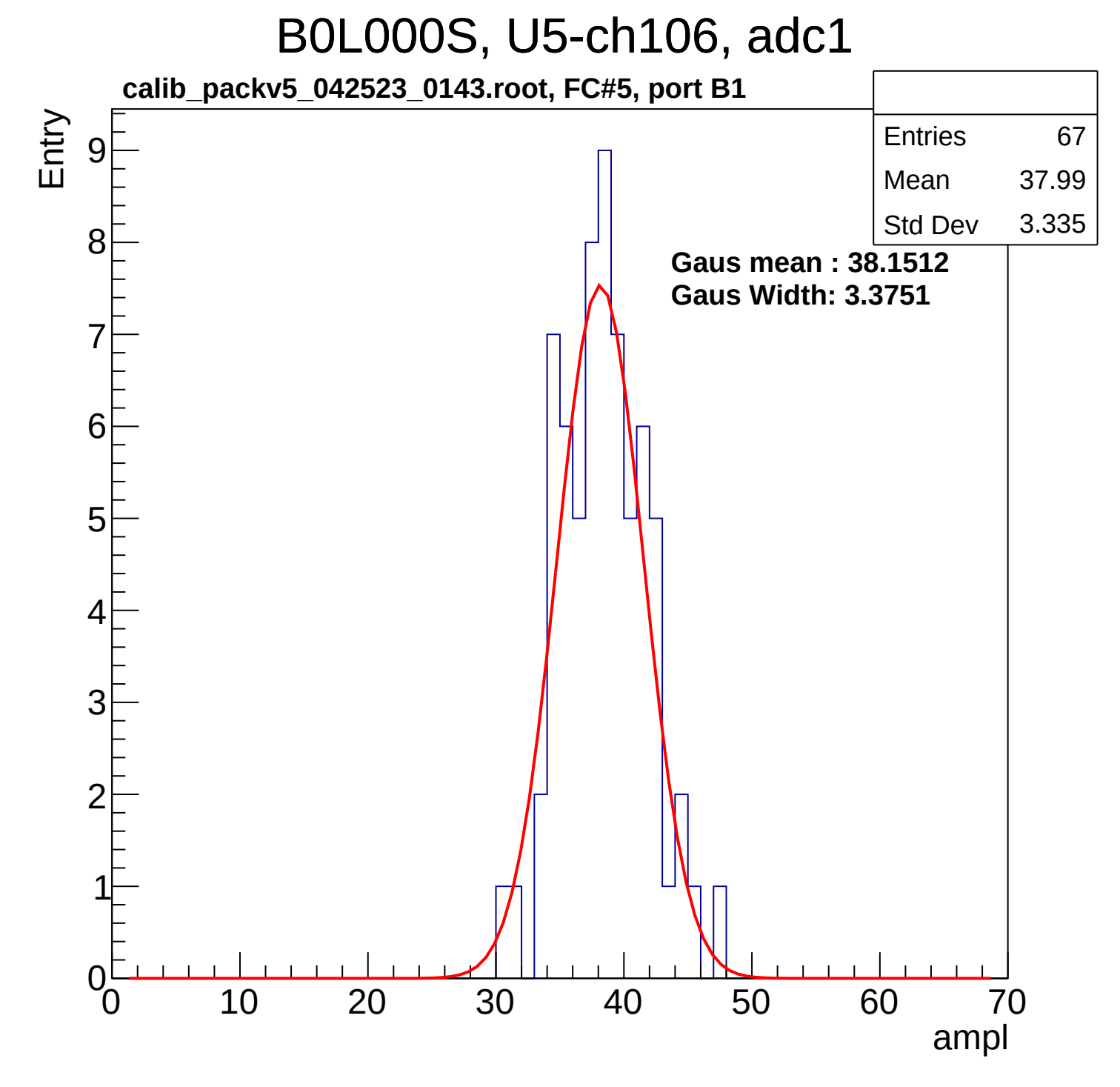
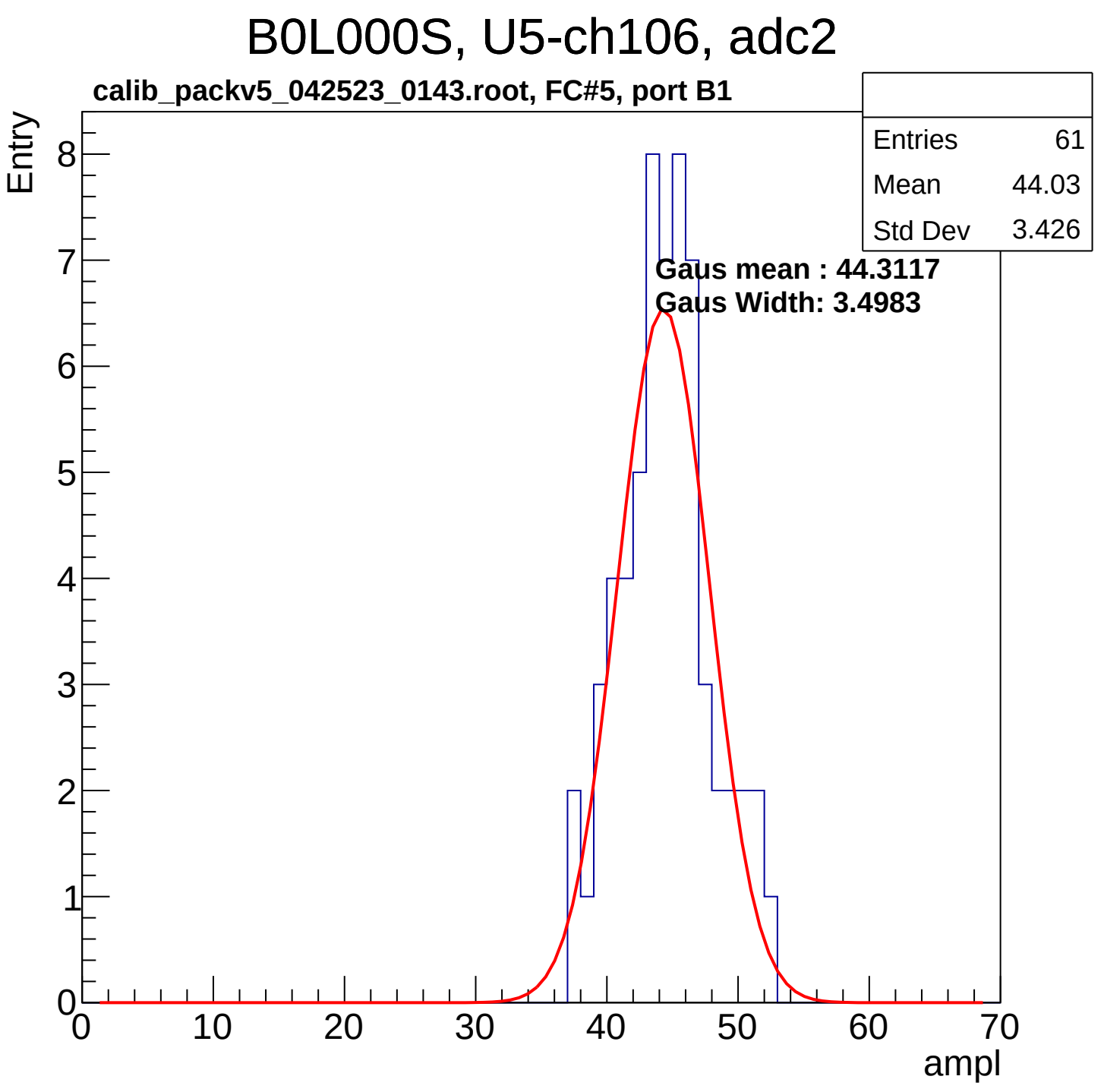
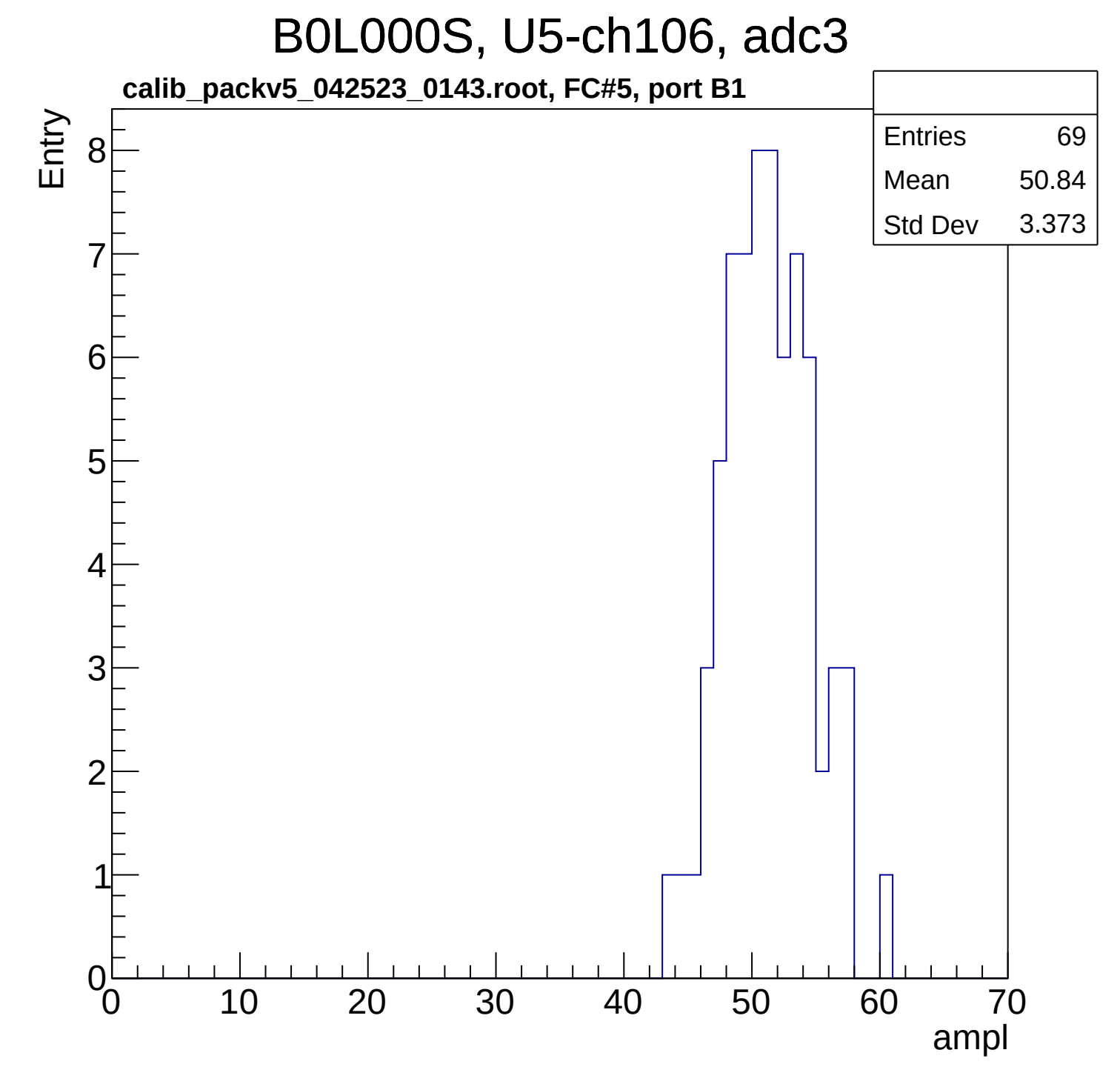

B0L000S, U5-ch105, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.67
Std Dev 2.211
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U5-ch105, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch106, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 31.05
Std Dev 3.106
Gaus mean : 31.6464
Gaus Width: 3.1928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch106, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 37.99
Std Dev 3.335
Gaus mean : 38.1512
Gaus Width: 3.3751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch106, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 44.03
Std Dev 3.426
Gaus mean : 44.3117
Gaus Width: 3.4983
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch106, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 50.84
Std Dev 3.373
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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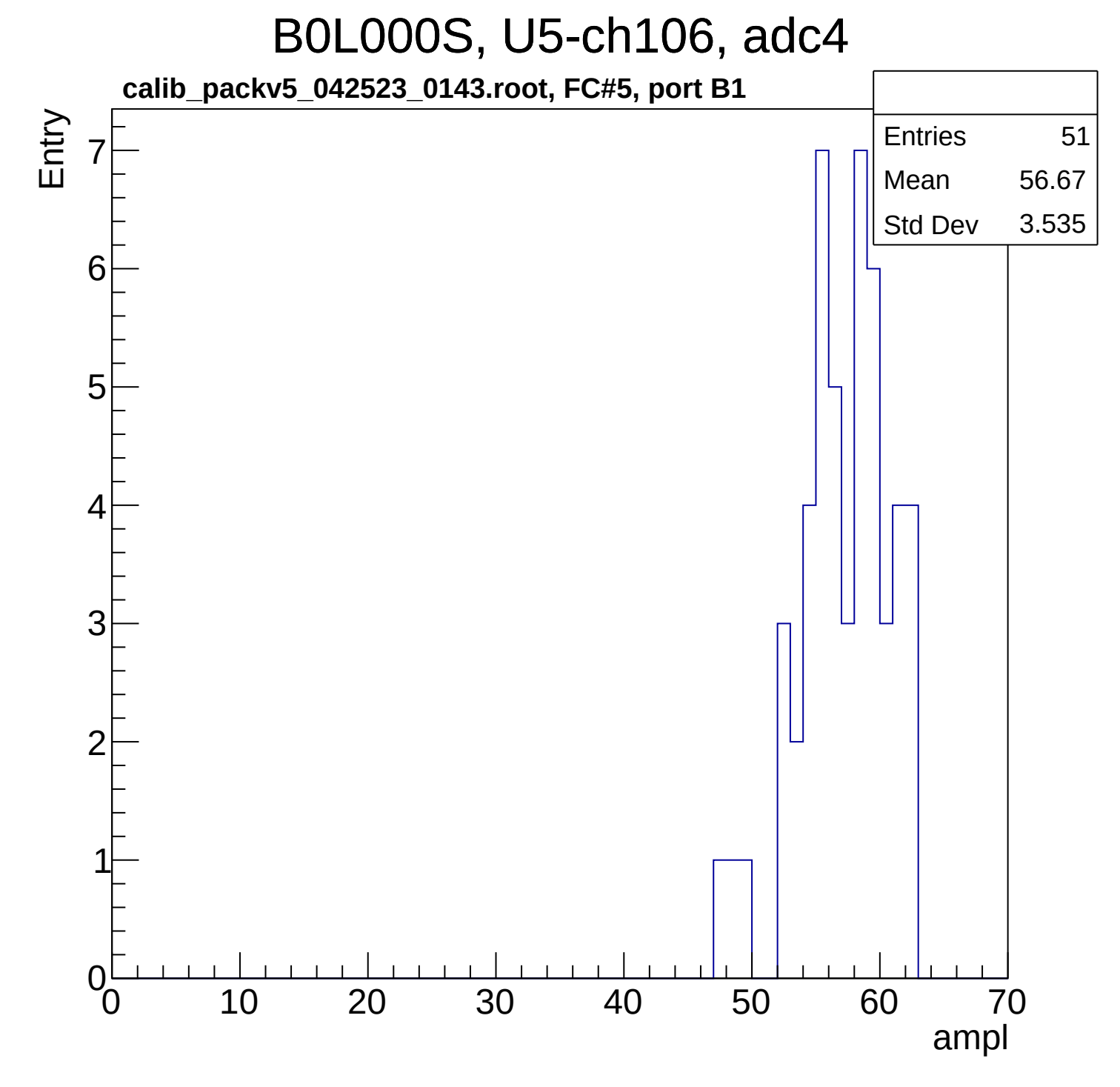
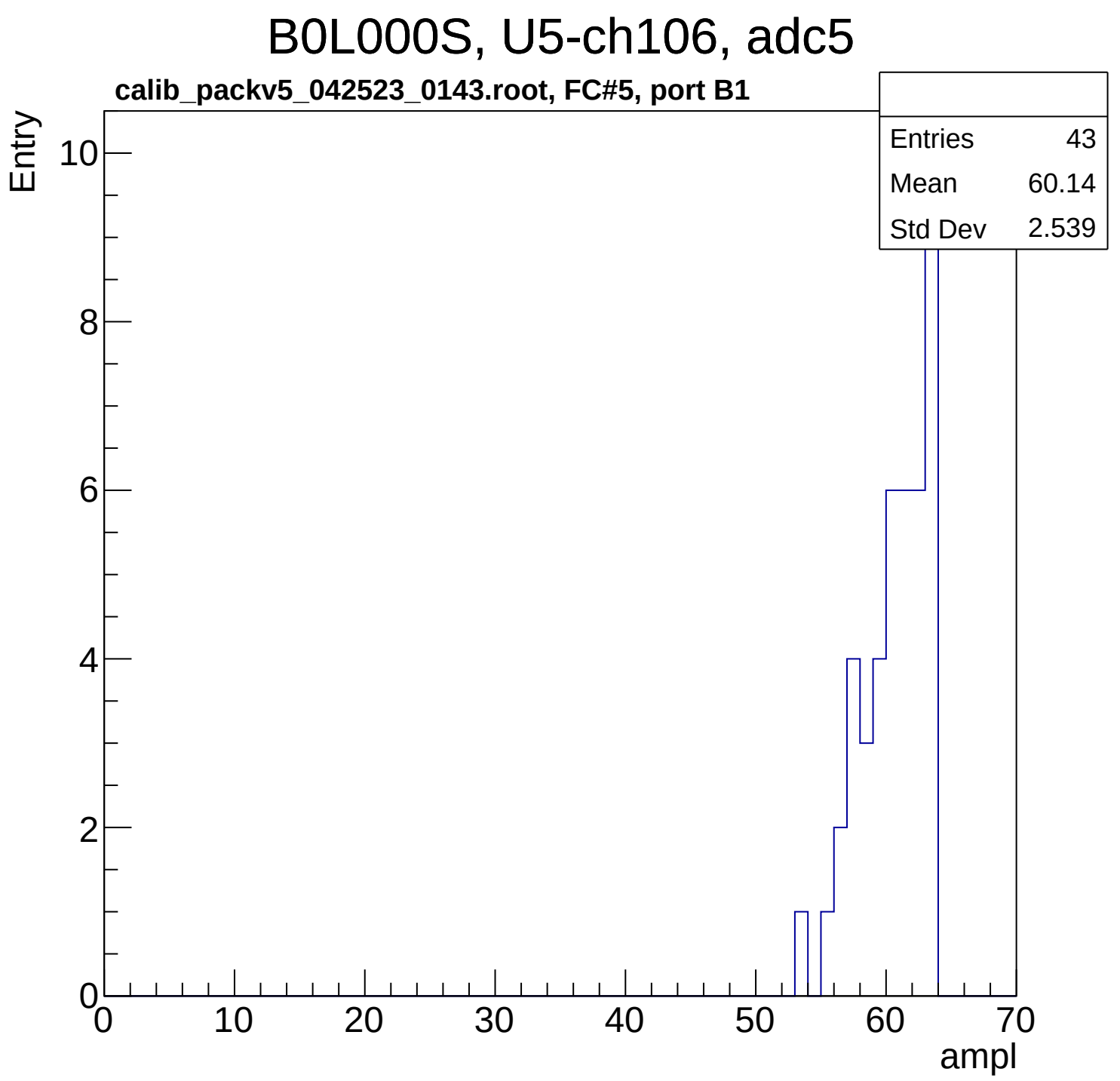
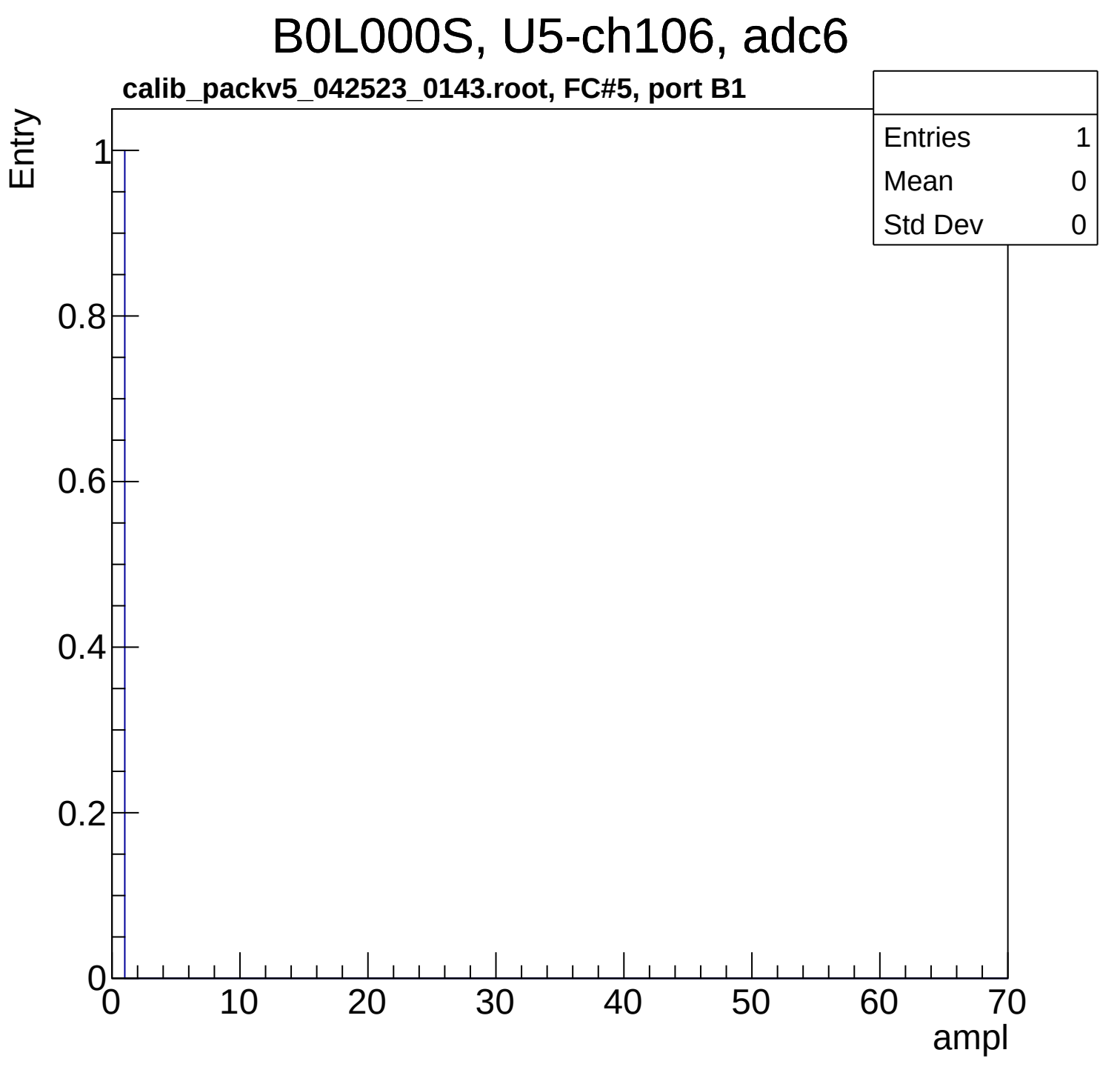
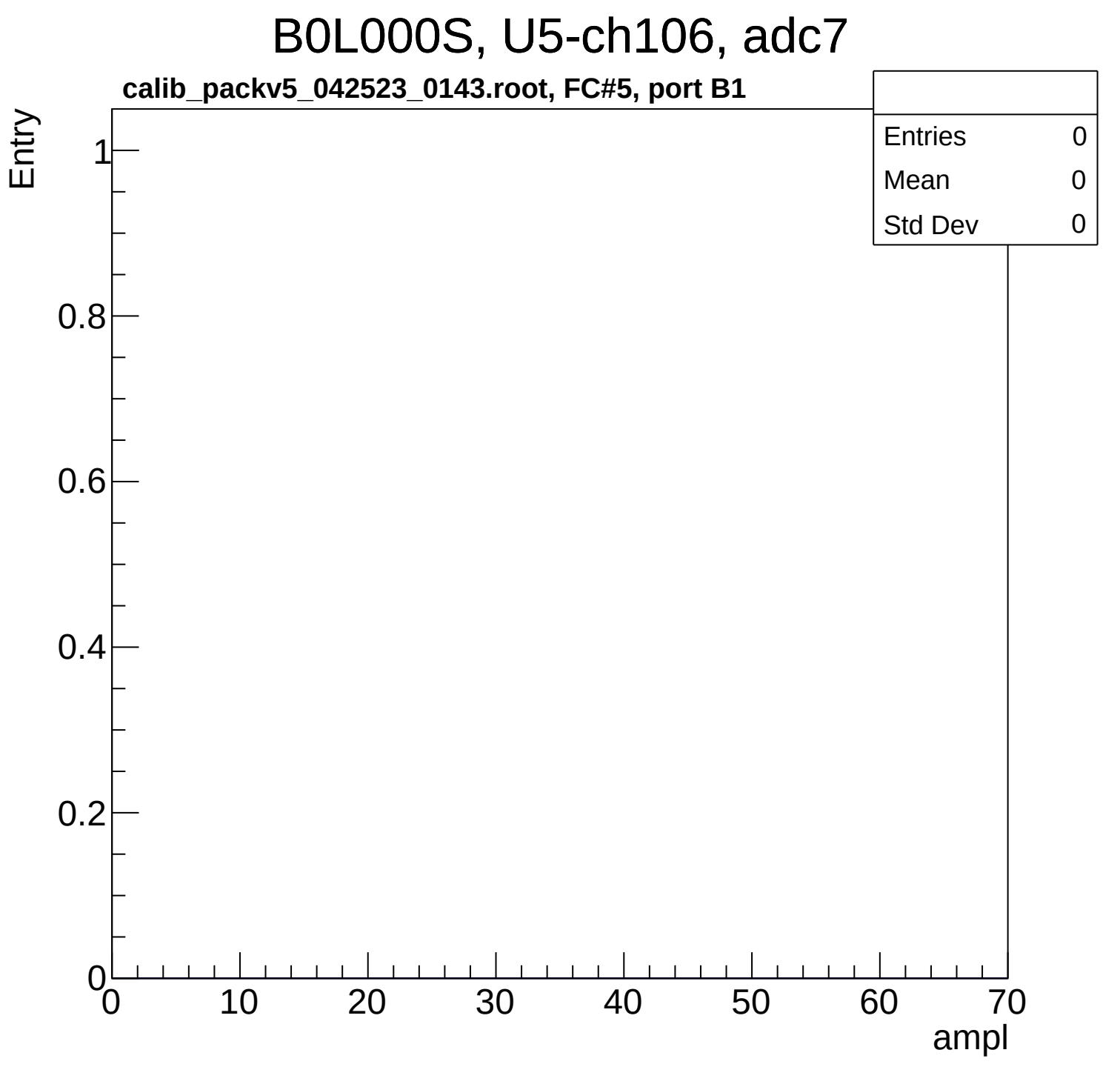
40
50
60
70


B0L000S, U5-ch106, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 56.67
Std Dev 3.535
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch106, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 43
Mean 60.14
Std Dev 2.539
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch106, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch106, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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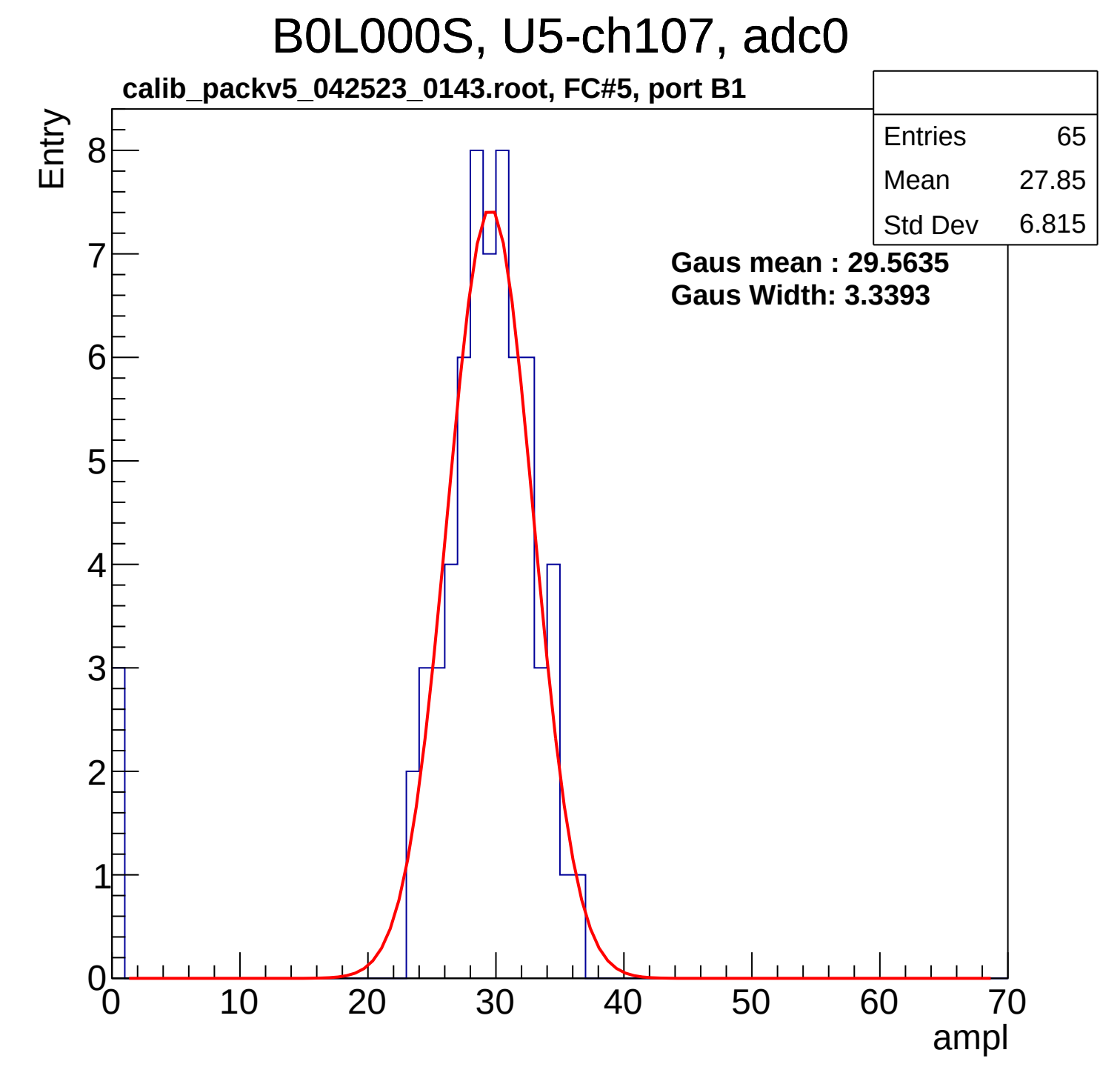
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch107, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 27.85
Std Dev 6.815
Gaus mean : 29.5635
Gaus Width: 3.3393
Entry
ampl
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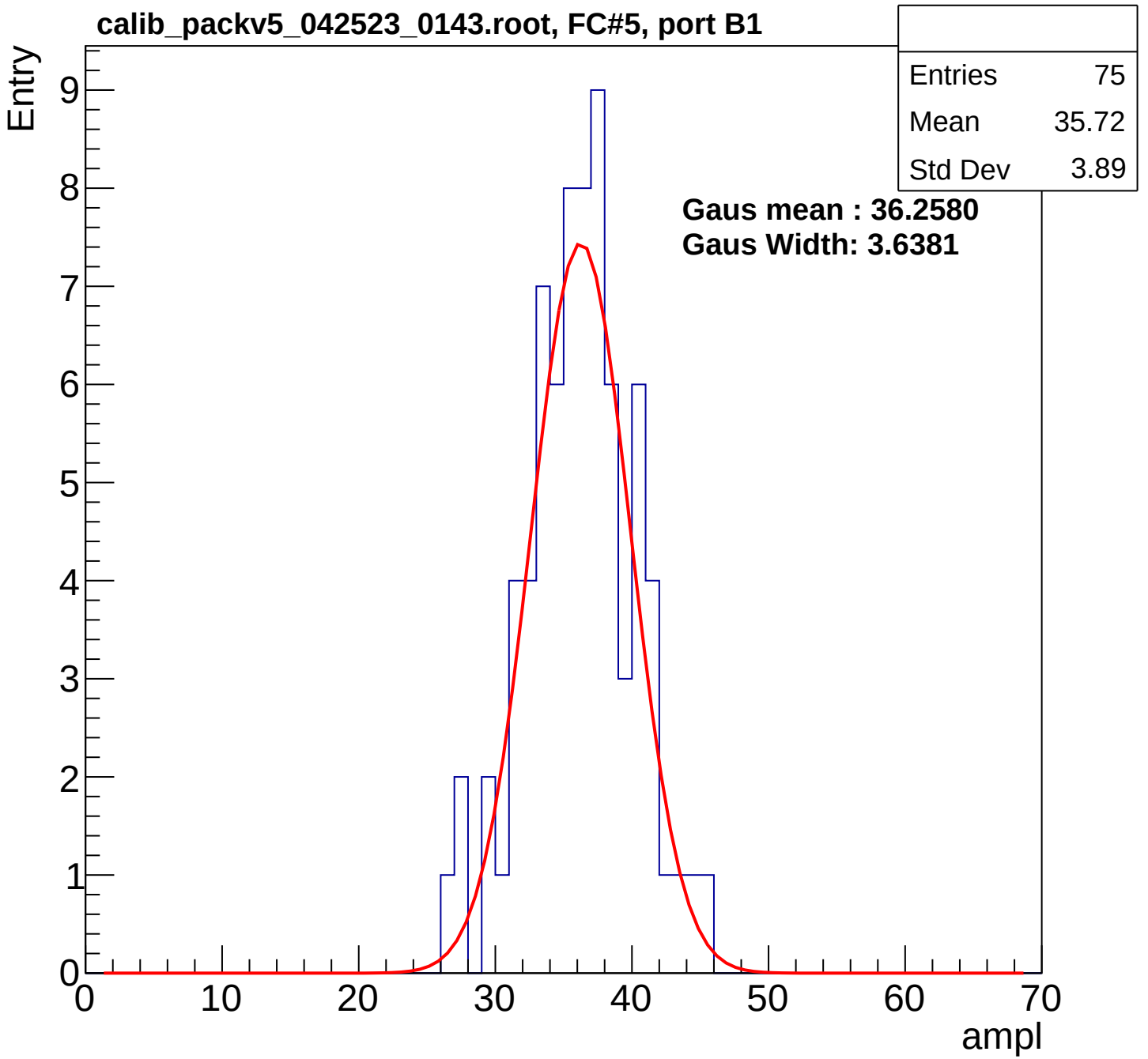
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch107, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 35.72
Std Dev 3.89
Gaus mean : 36.2580
Gaus Width: 3.6381
Entry
ampl
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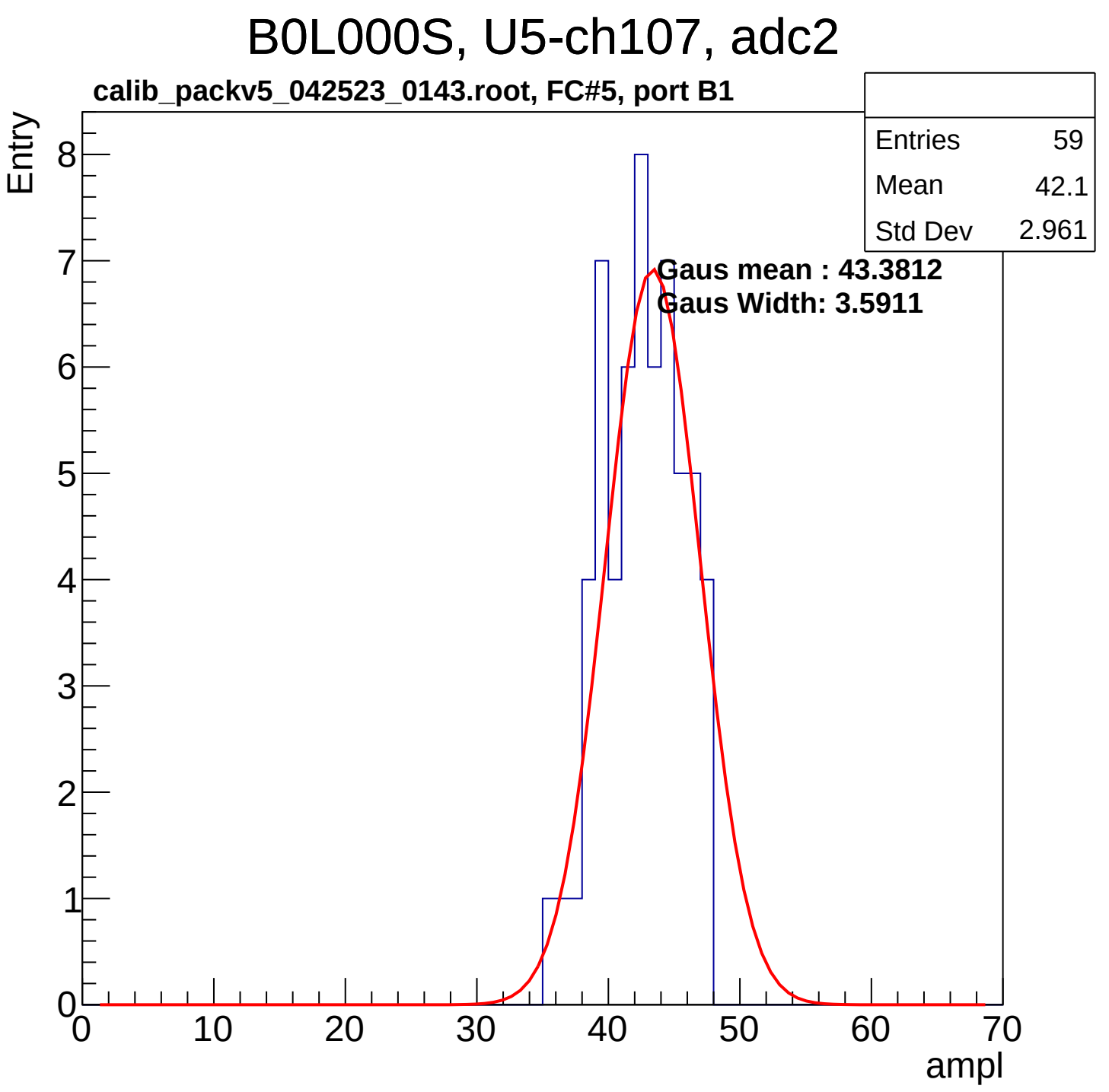
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch107, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 42.1
Std Dev 2.961
Gaus mean : 43.3812
Gaus Width: 3.5911
Entry
ampl
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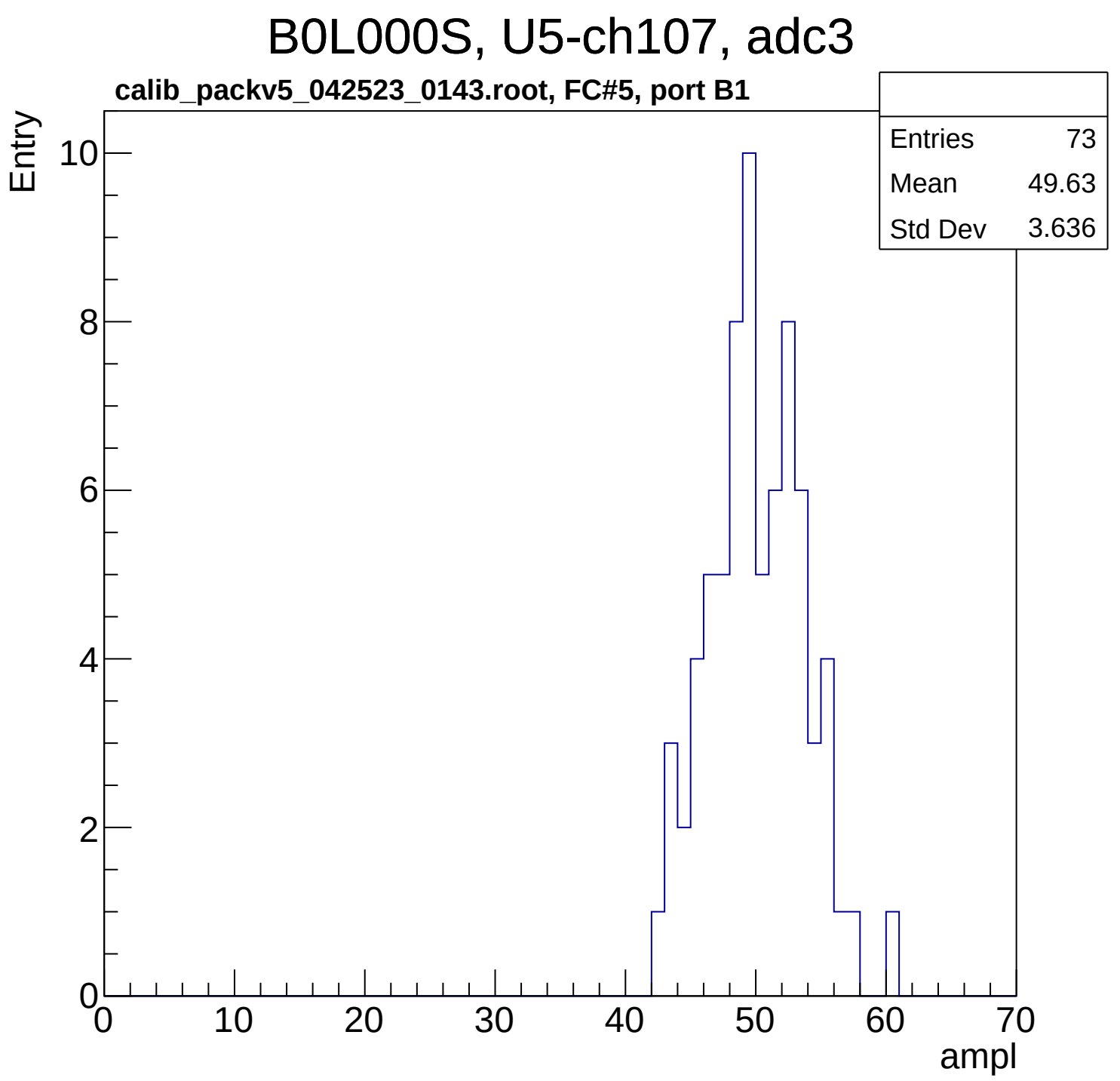
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch107, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 49.63
Std Dev 3.636
Entry
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2
4
6
8
10
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10
20
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50
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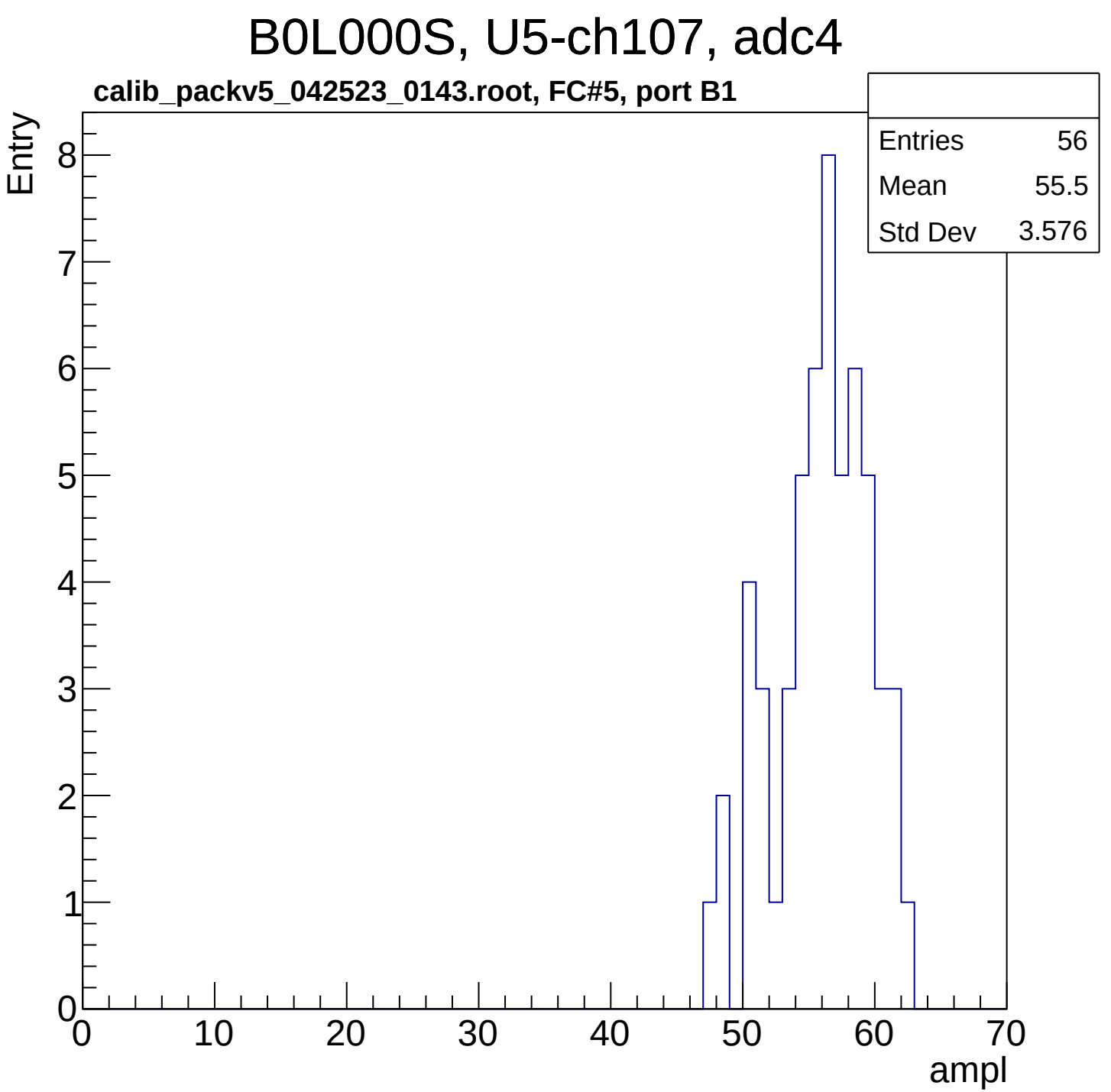
70
ampl


B0L000S, U5-ch107, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.5
Std Dev 3.576
Entry
ampl
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2
3
4
5
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60
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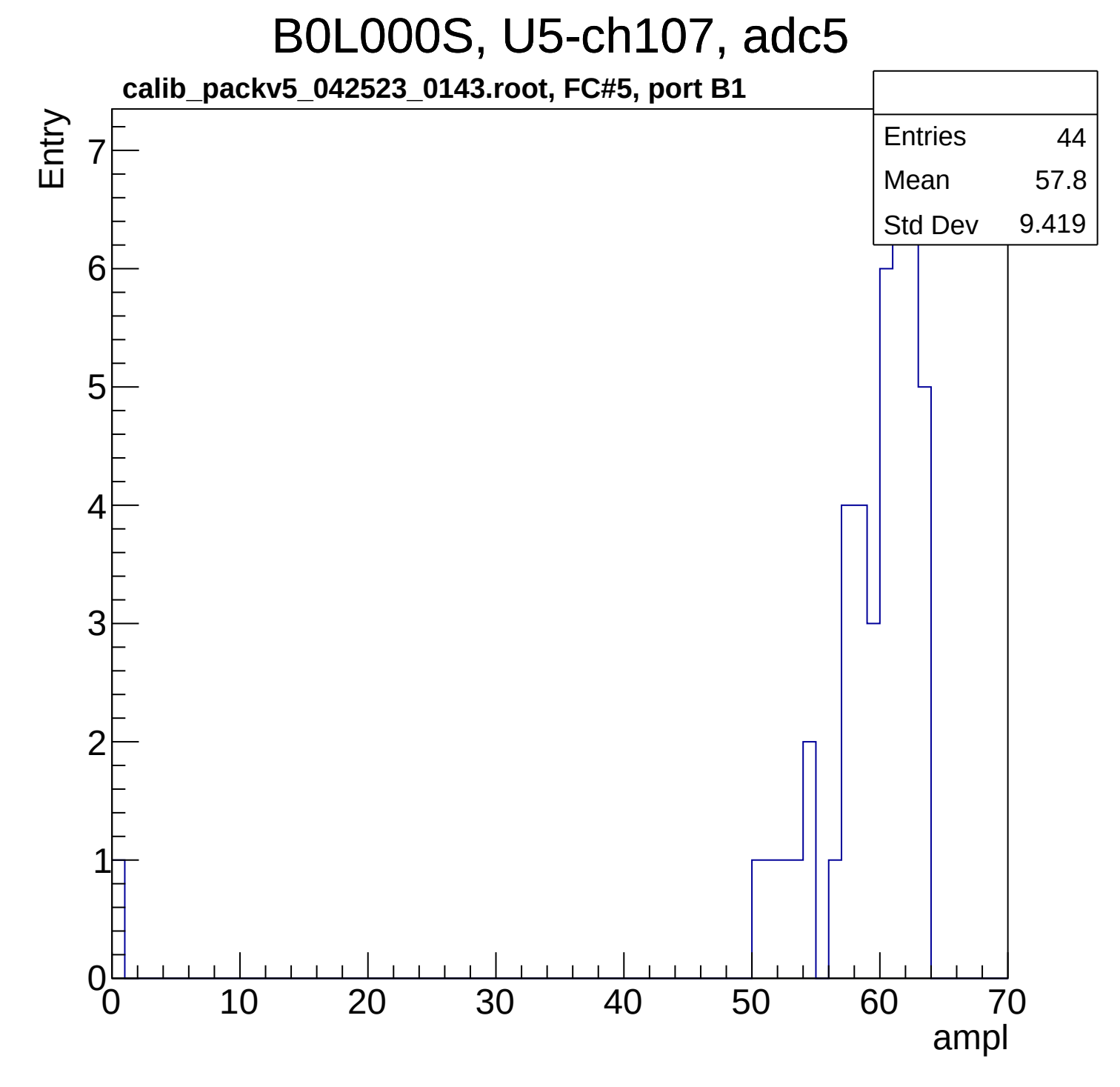
B0L000S, U5-ch107, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 57.8
Std Dev 9.419
Entry
ampl
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3
4
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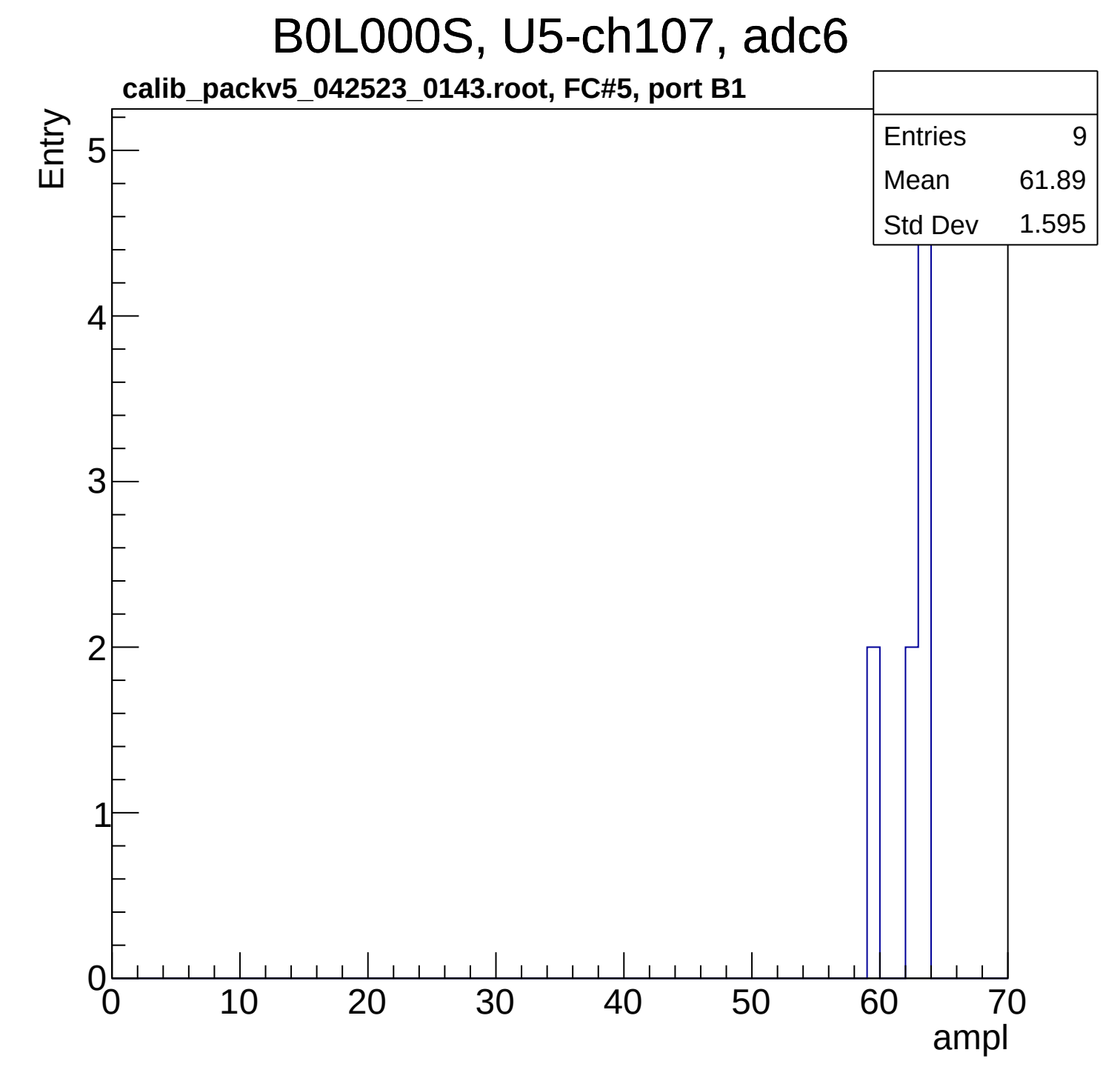
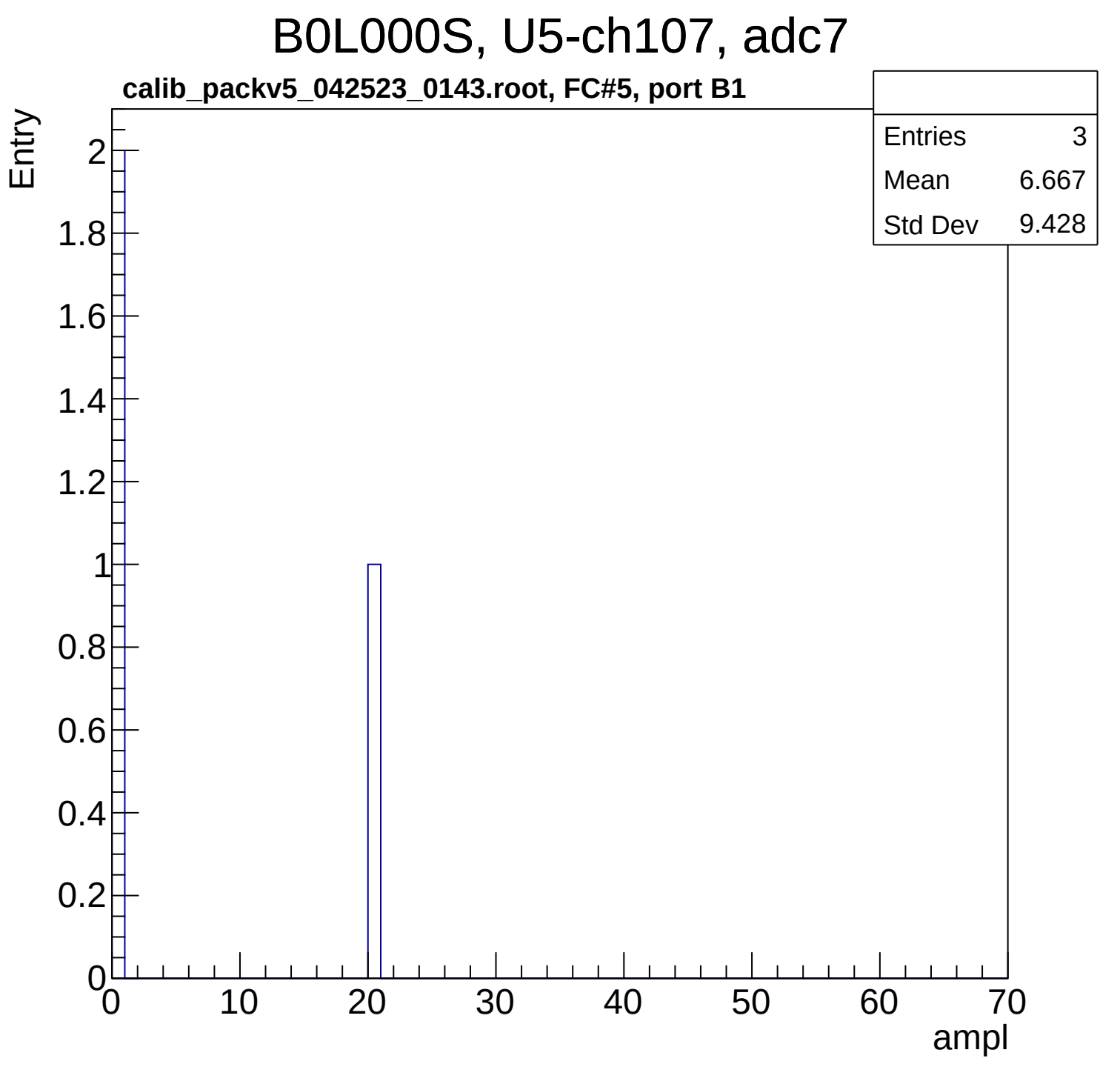

B0L000S, U5-ch107, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.89
Std Dev 1.595
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch107, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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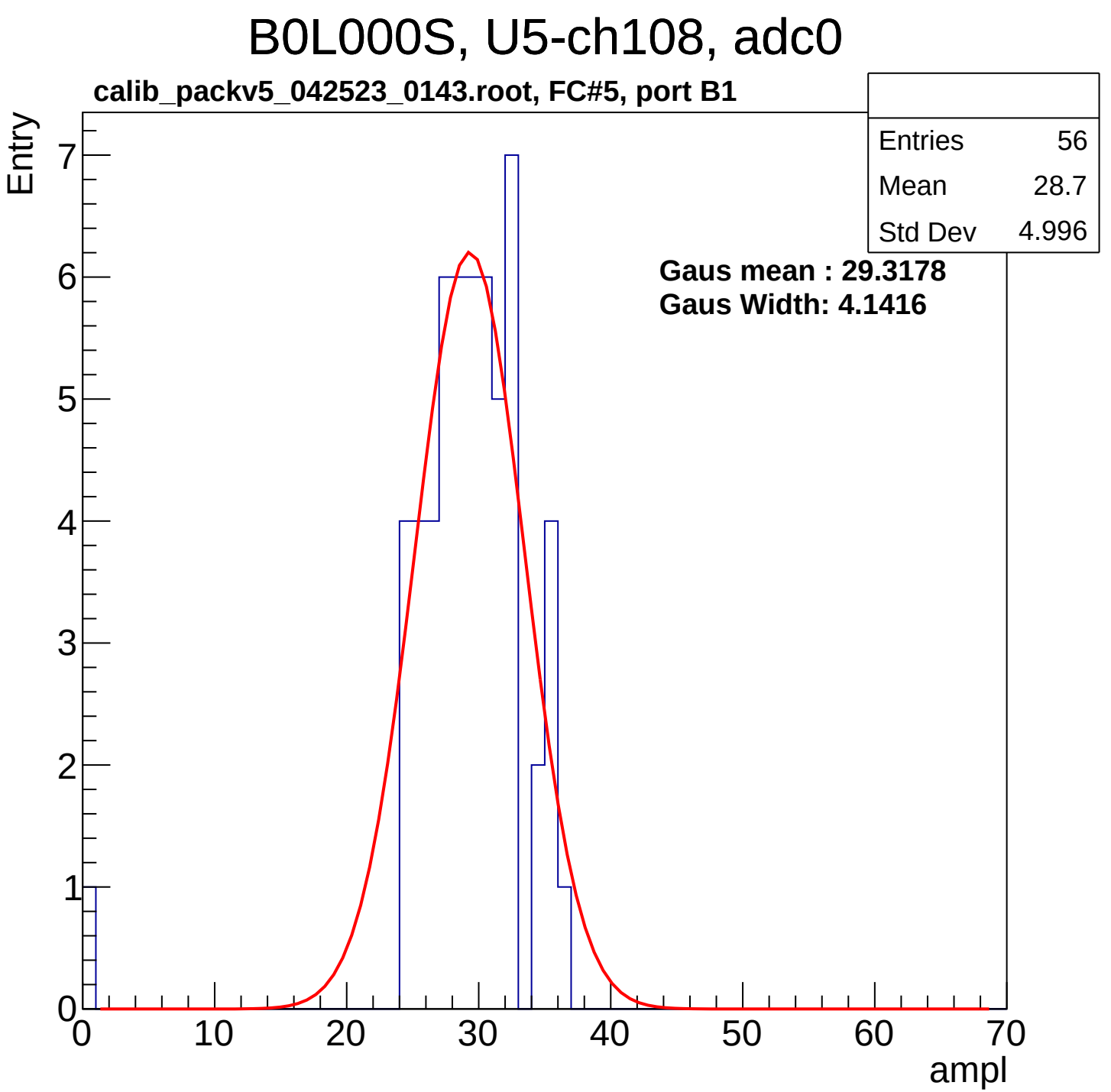
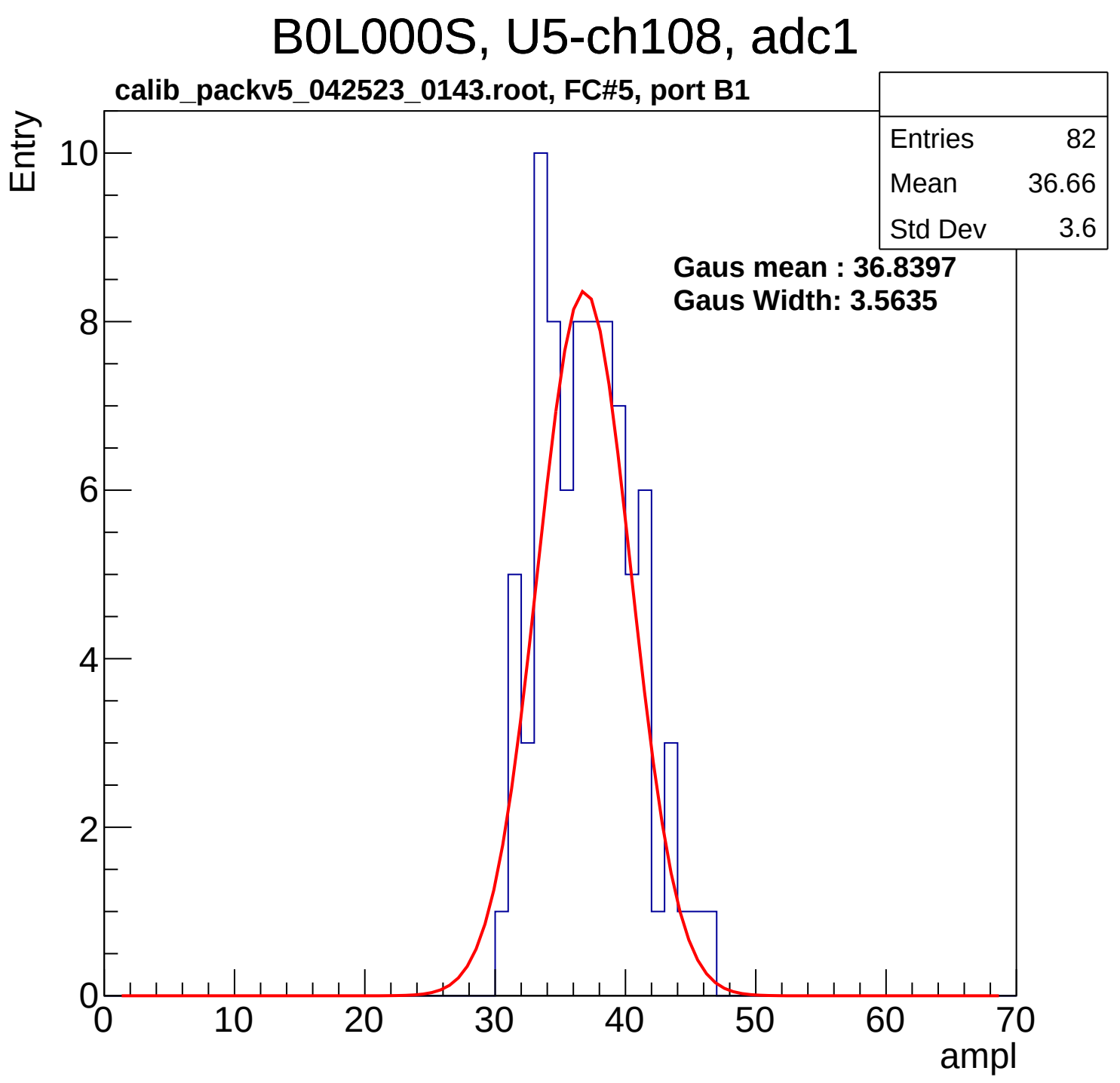
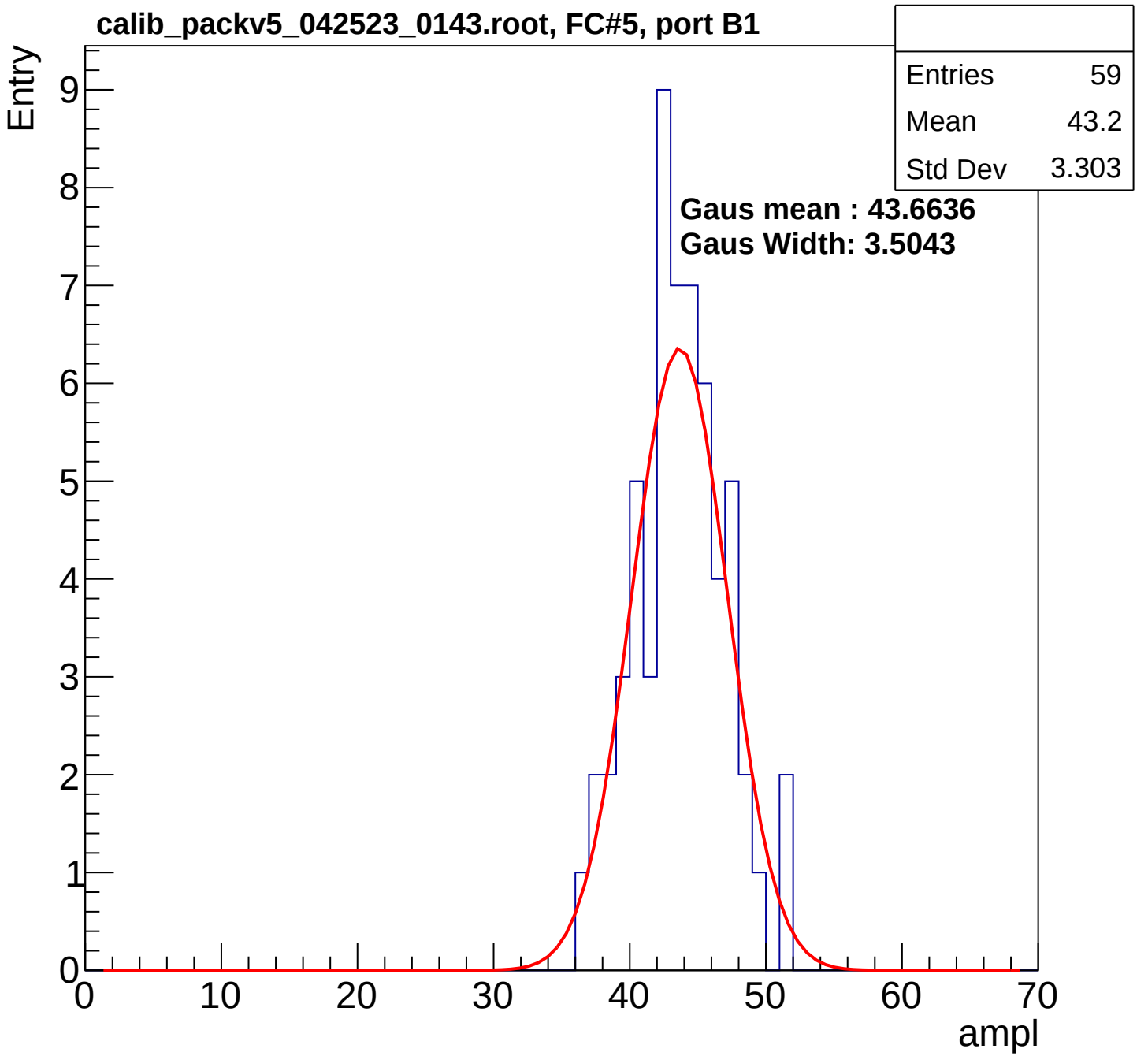
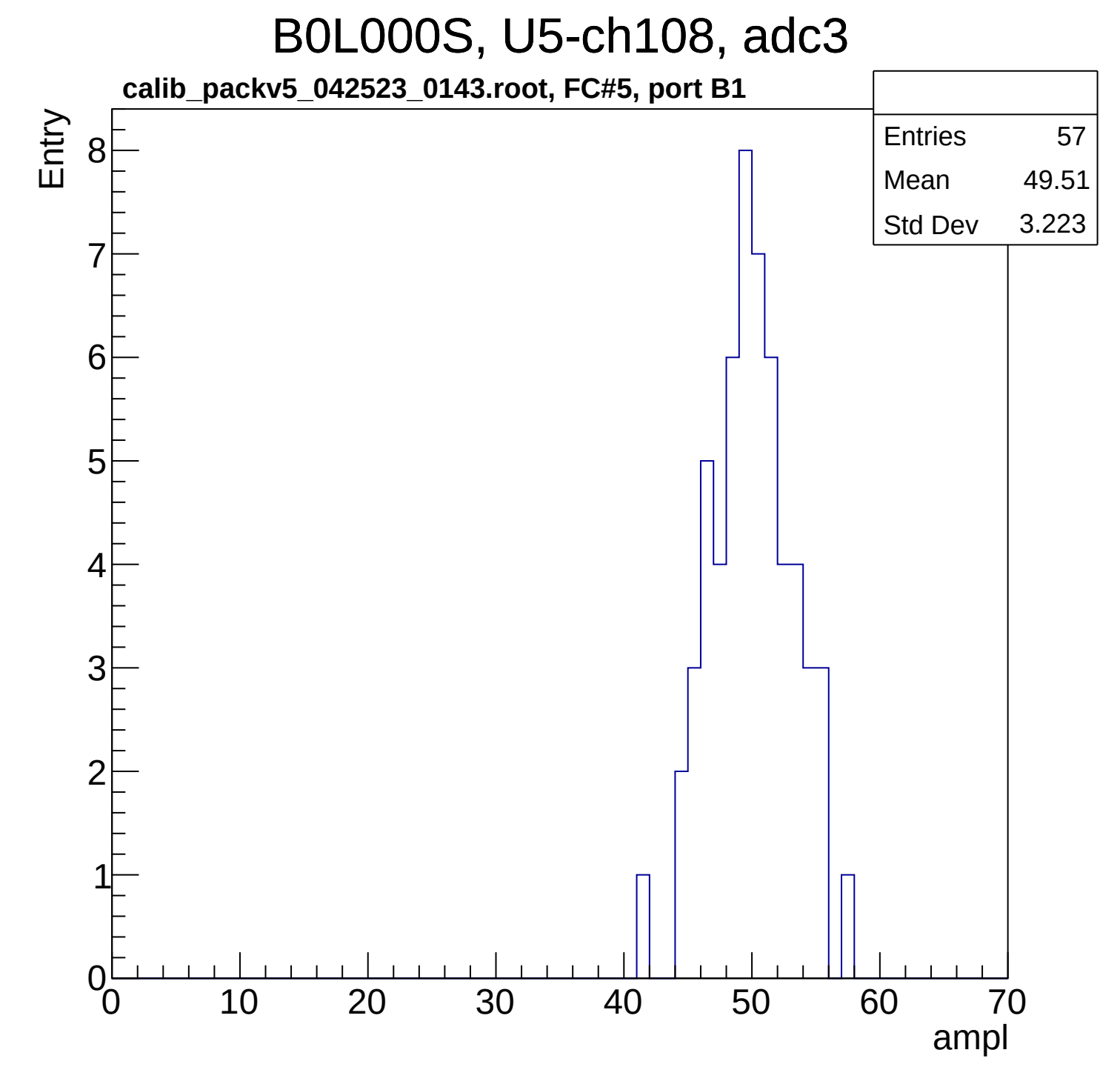

B0L000S, U5-ch108, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 28.7
Std Dev 4.996
Gaus mean : 29.3178
Gaus Width: 4.1416
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch108, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 82
Mean 36.66
Std Dev 3.6
Gaus mean : 36.8397
Gaus Width: 3.5635
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch108, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 43.2
Std Dev 3.303
Gaus mean : 43.6636
Gaus Width: 3.5043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch108, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 49.51
Std Dev 3.223
Entry
ampl
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1
2
3
4
5
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7
8
0
10
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40
50
60
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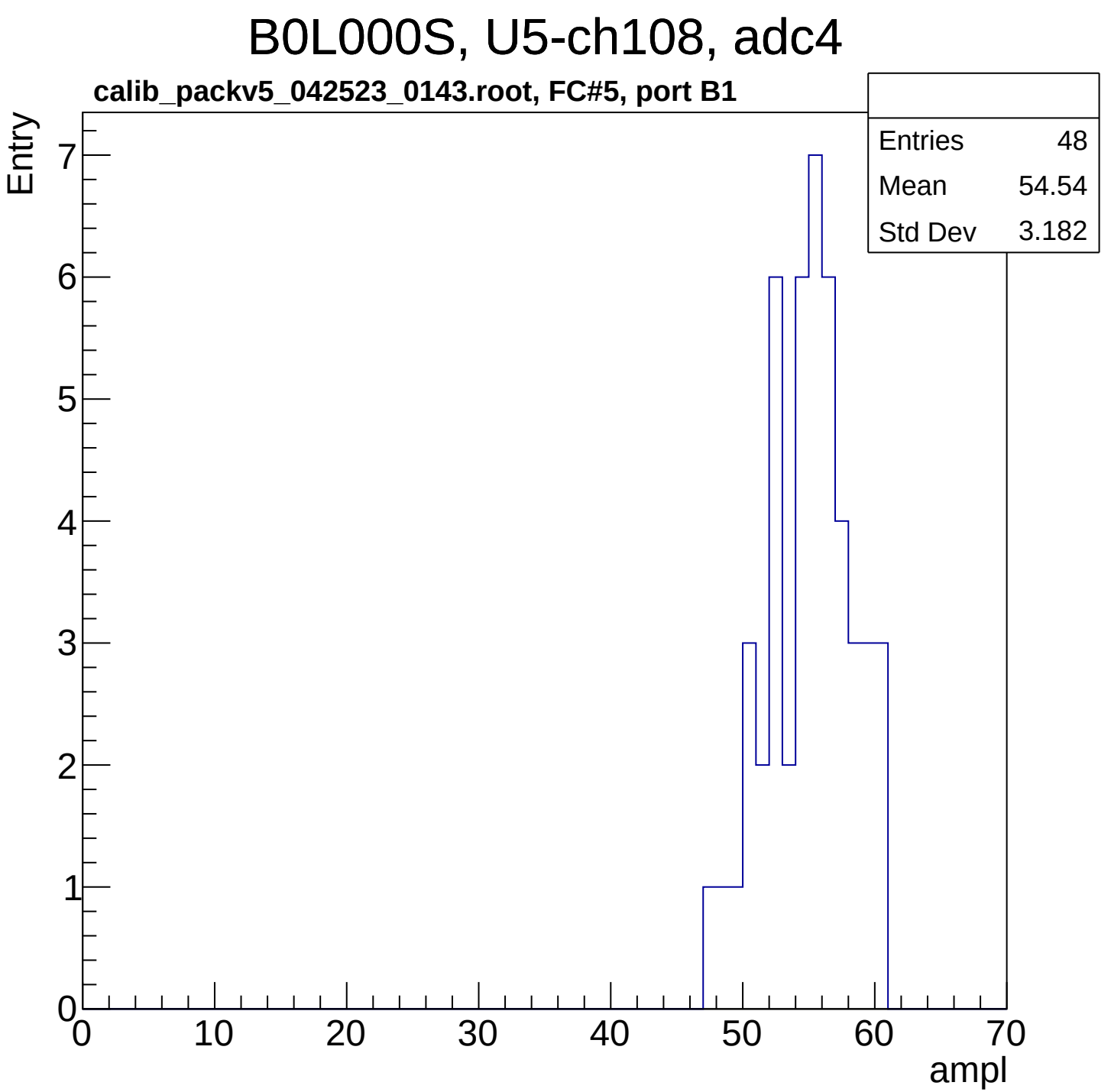
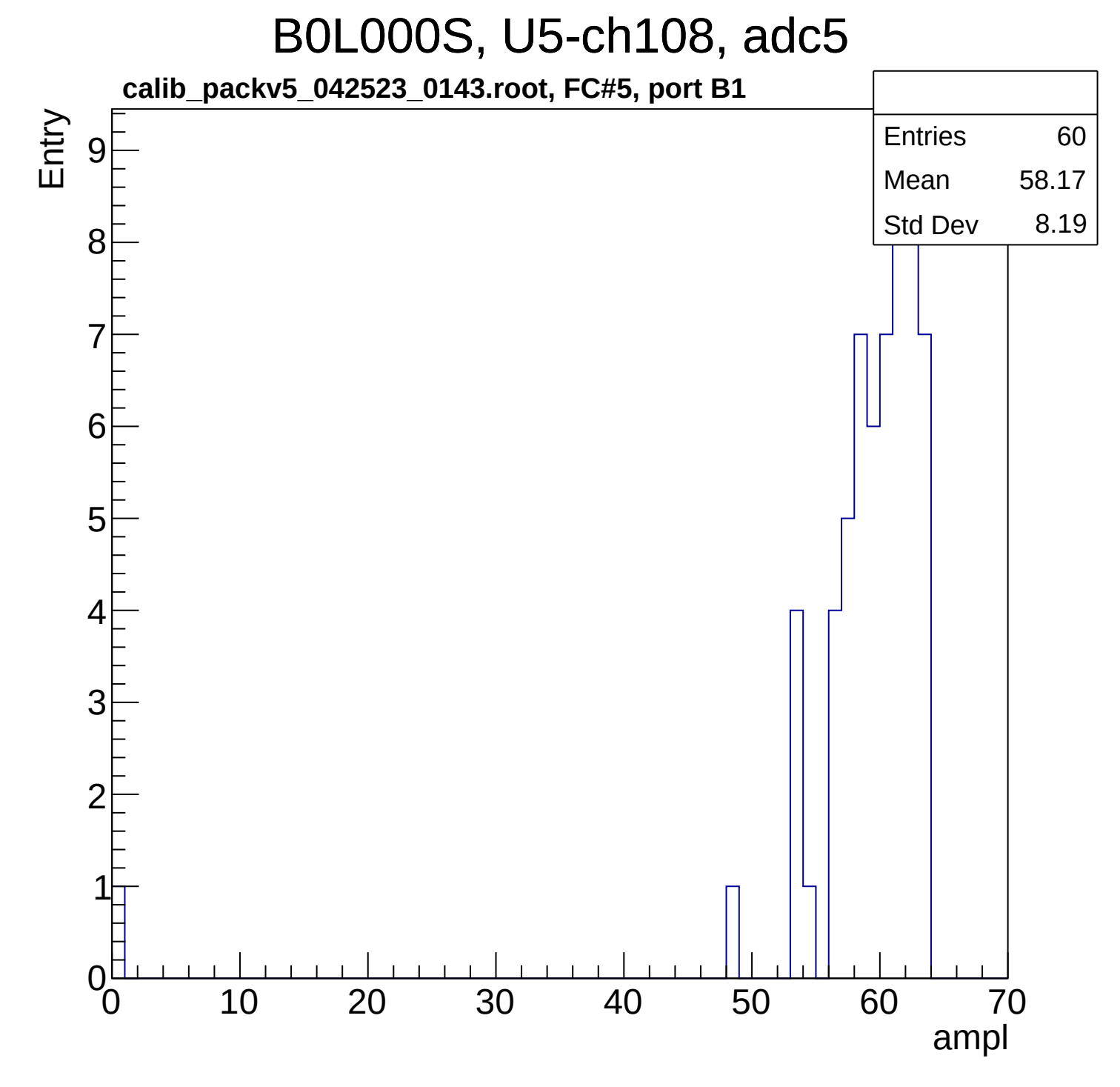

B0L000S, U5-ch108, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 54.54
Std Dev 3.182
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch108, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 58.17
Std Dev 8.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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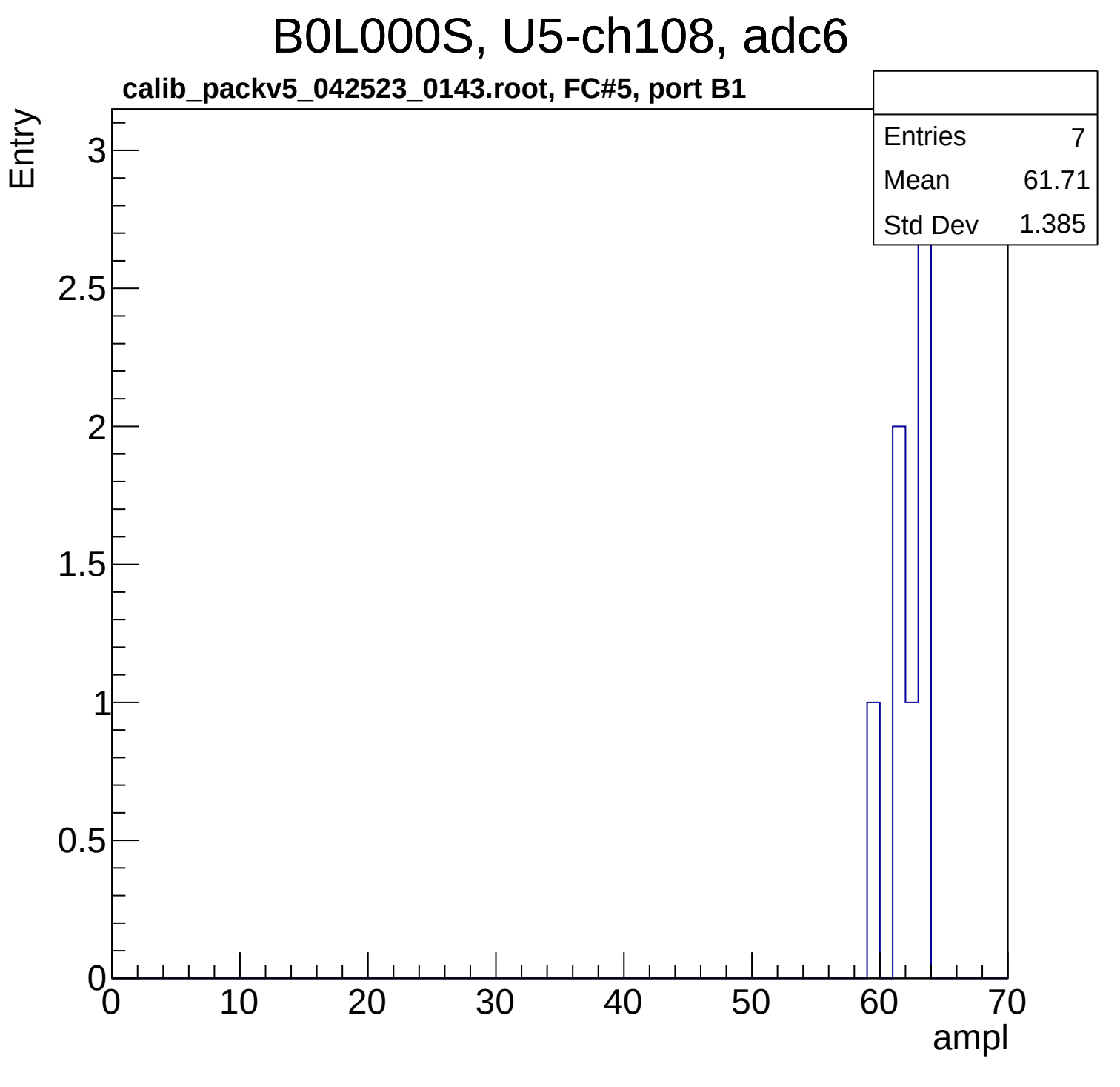
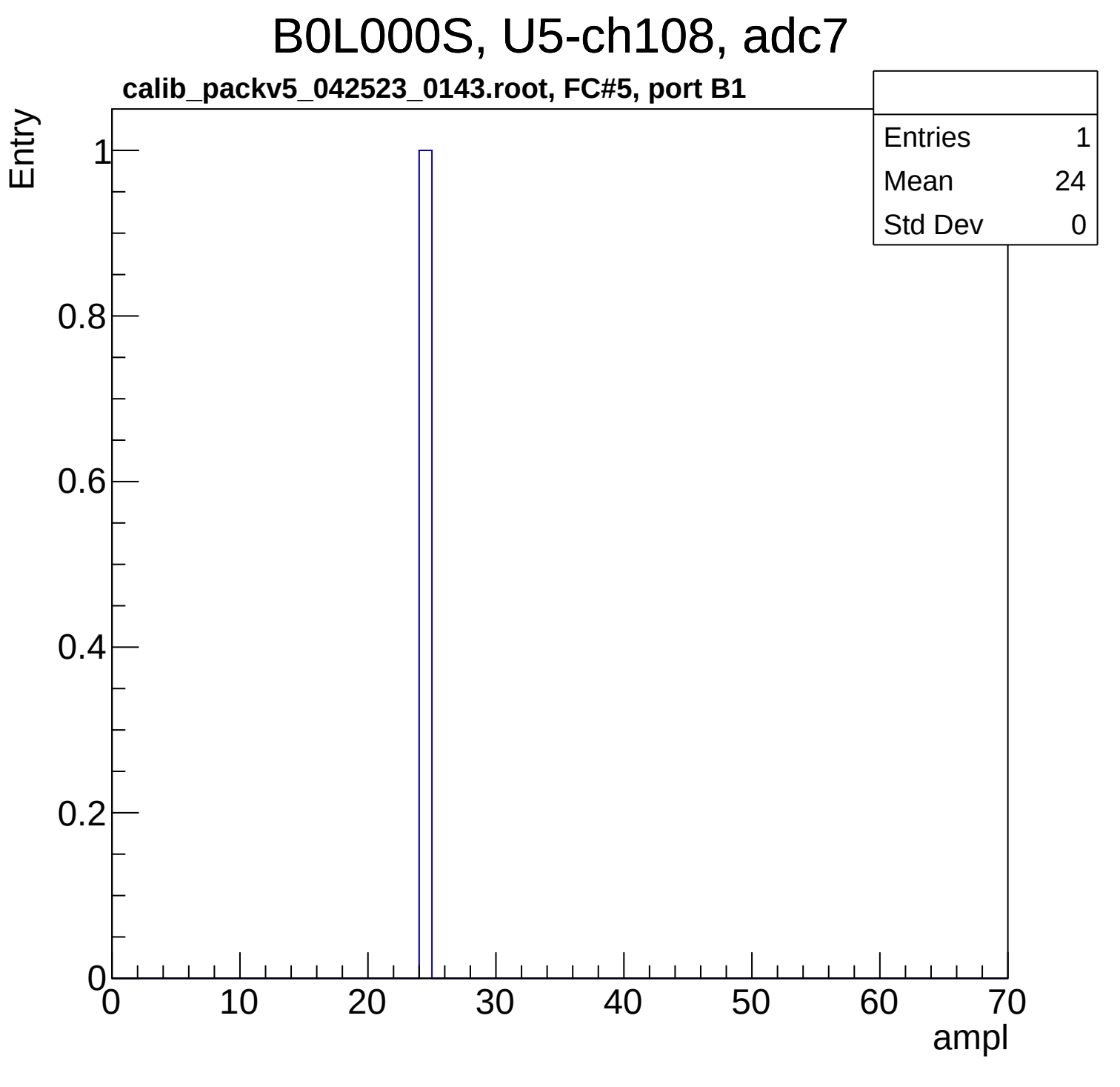
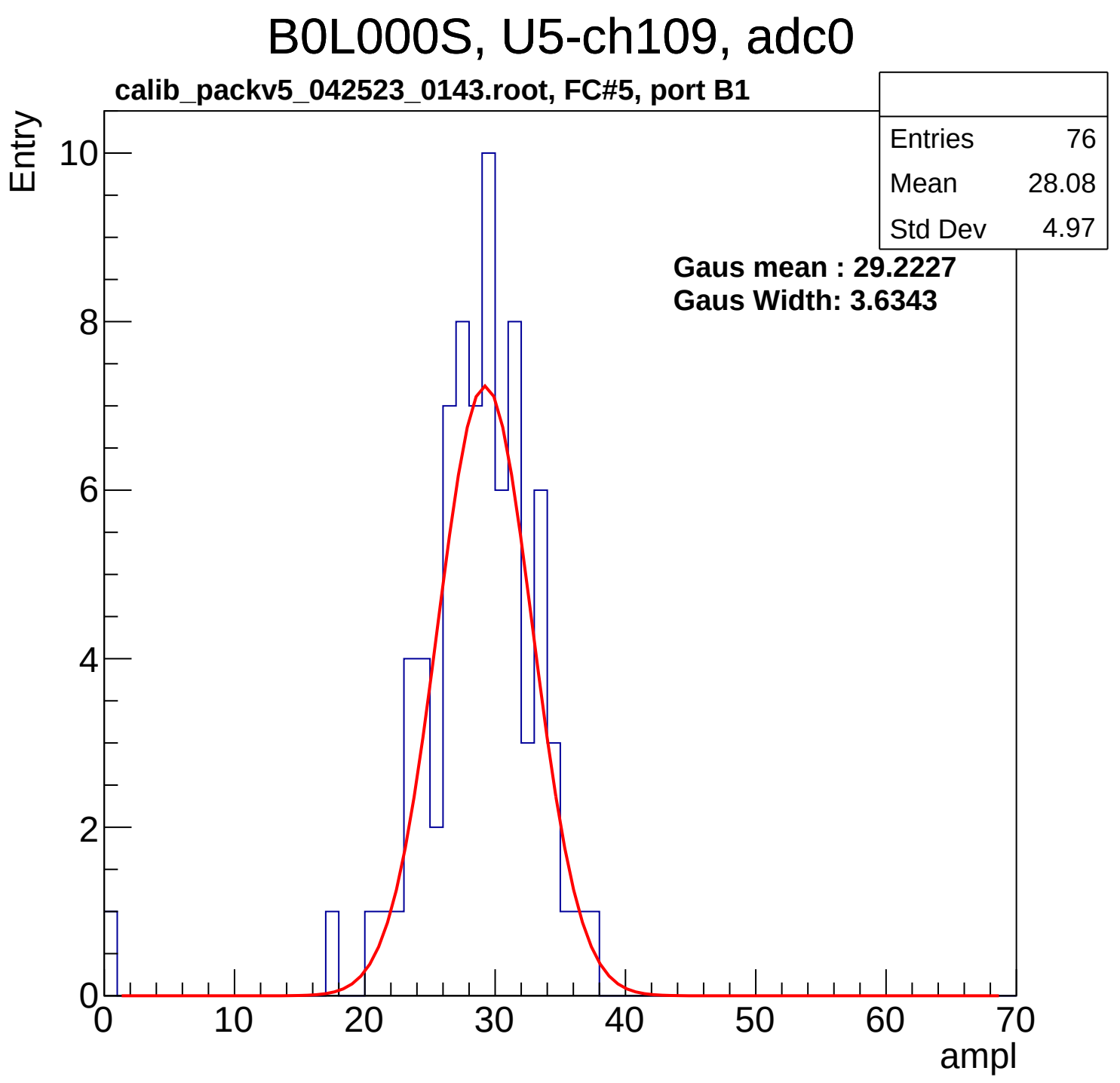
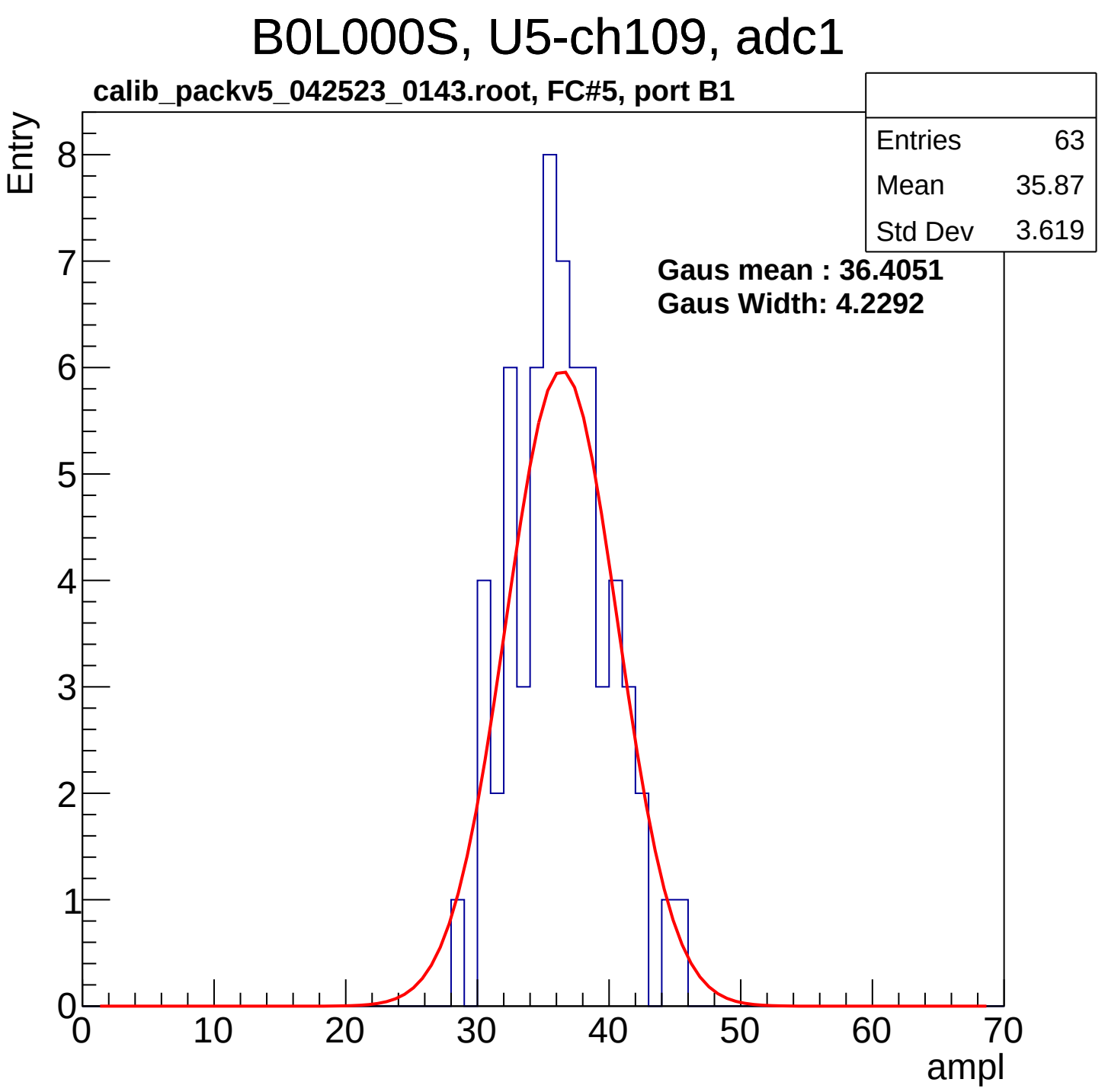
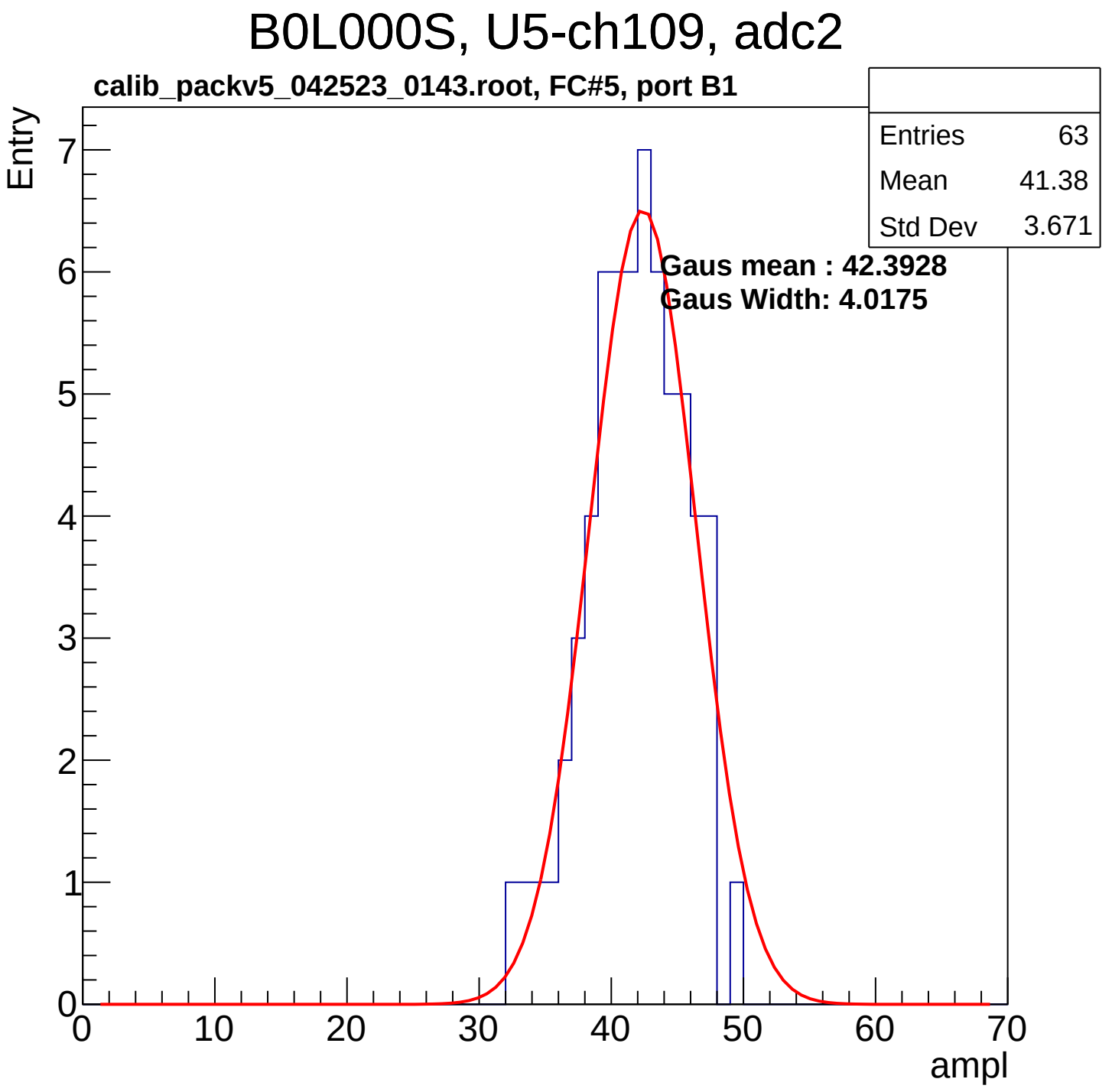
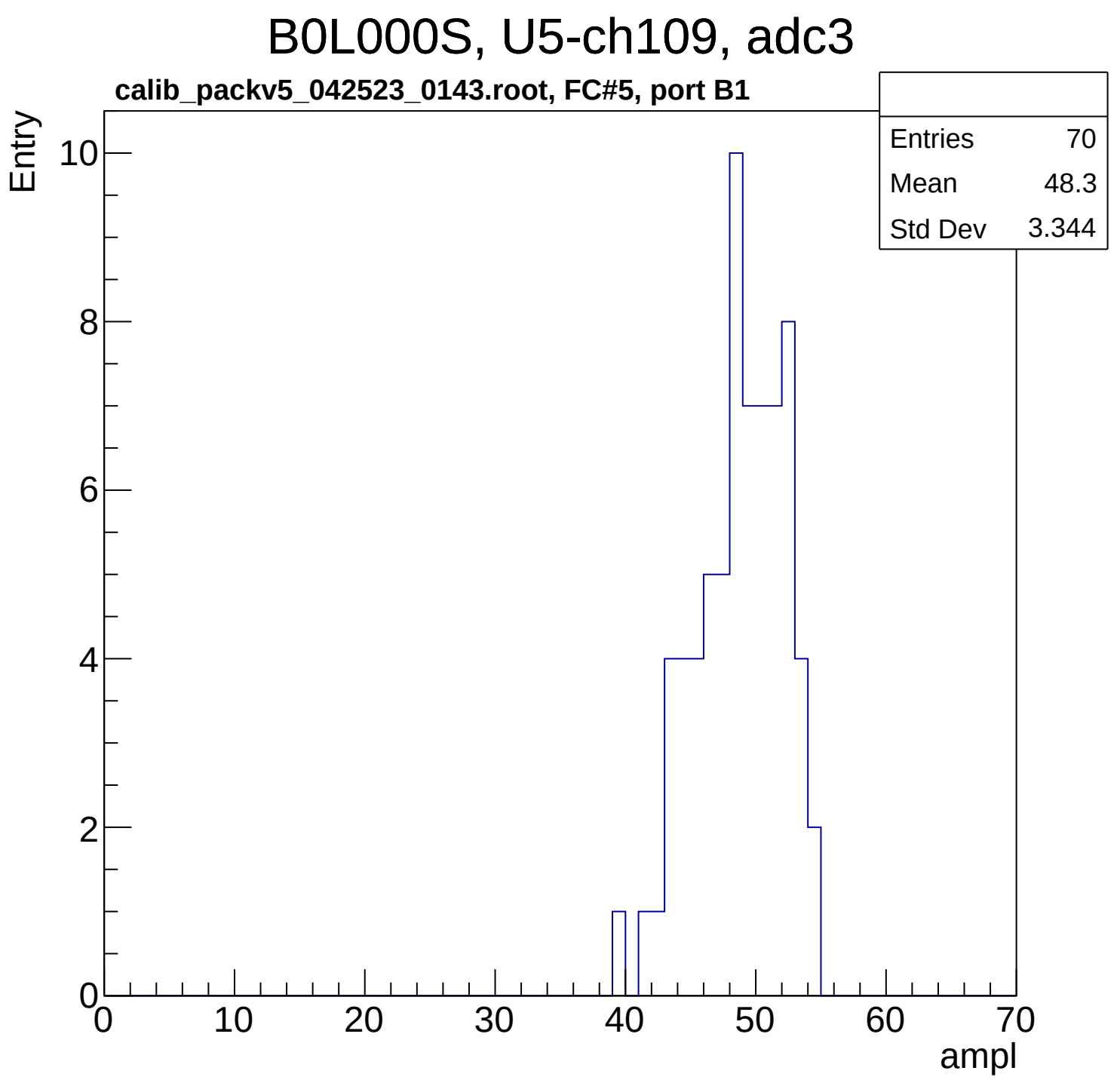

B0L000S, U5-ch108, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 1.385
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

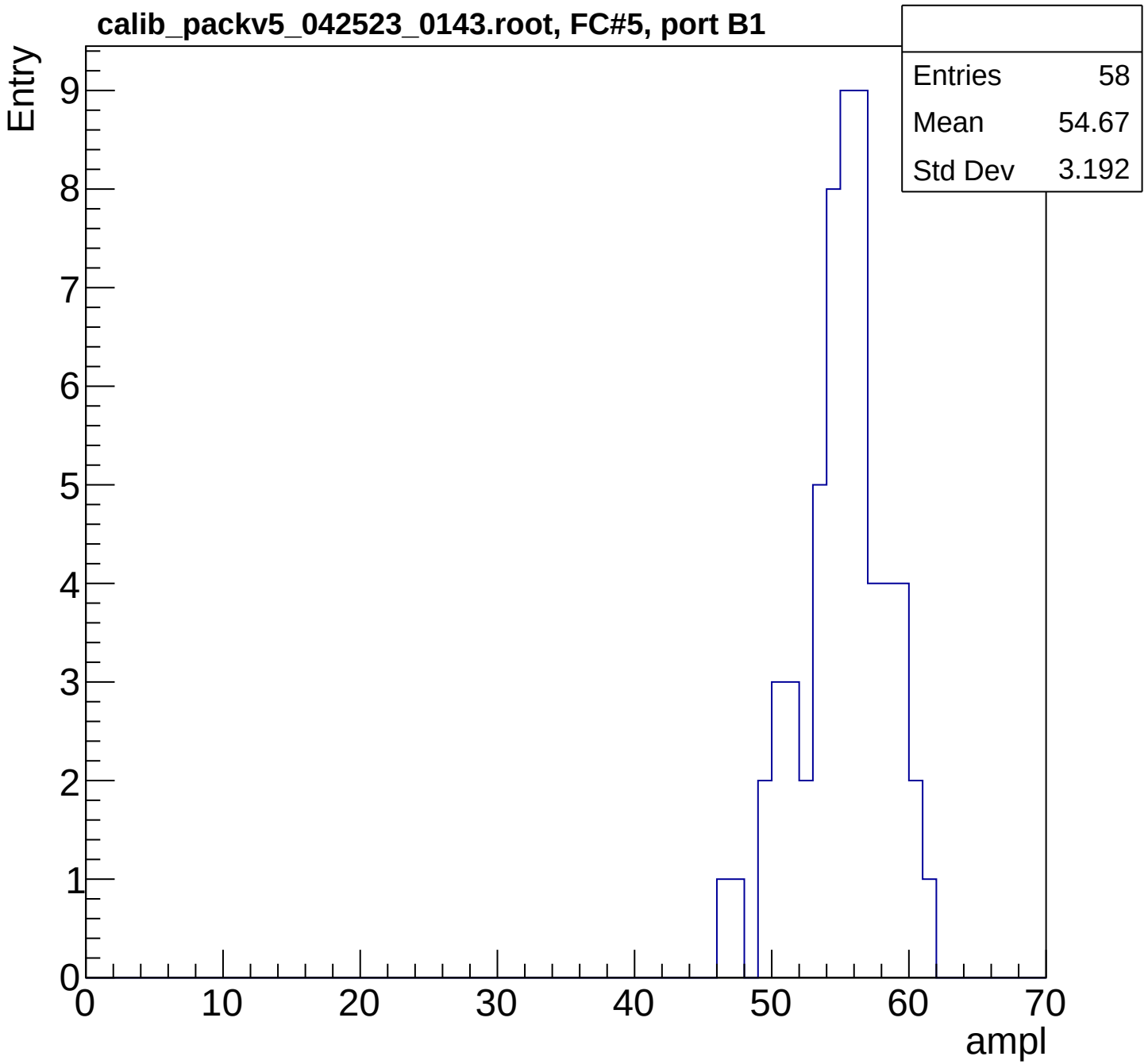

B0L000S, U5-ch108, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

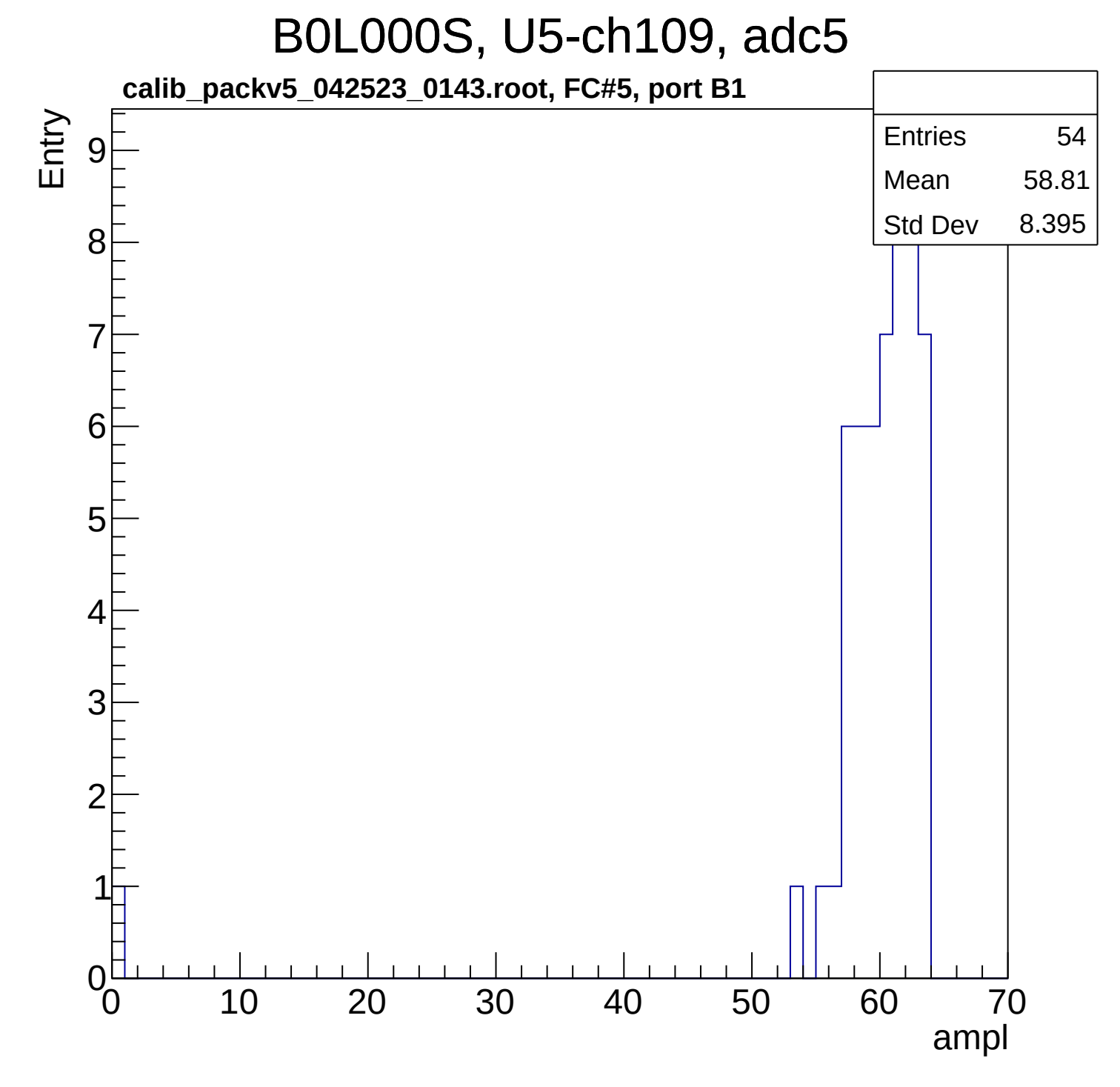

B0L000S, U5-ch109, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 28.08
Std Dev 4.97
Gaus mean : 29.2227
Gaus Width: 3.6343
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch109, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 35.87
Std Dev 3.619
Gaus mean : 36.4051
Gaus Width: 4.2292
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch109, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 41.38
Std Dev 3.671
Gaus mean : 42.3928
Gaus Width: 4.0175
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L000S, U5-ch109, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 48.3
Std Dev 3.344
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch109, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 58
Mean 54.67
Std Dev 3.192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch109, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.81
Std Dev 8.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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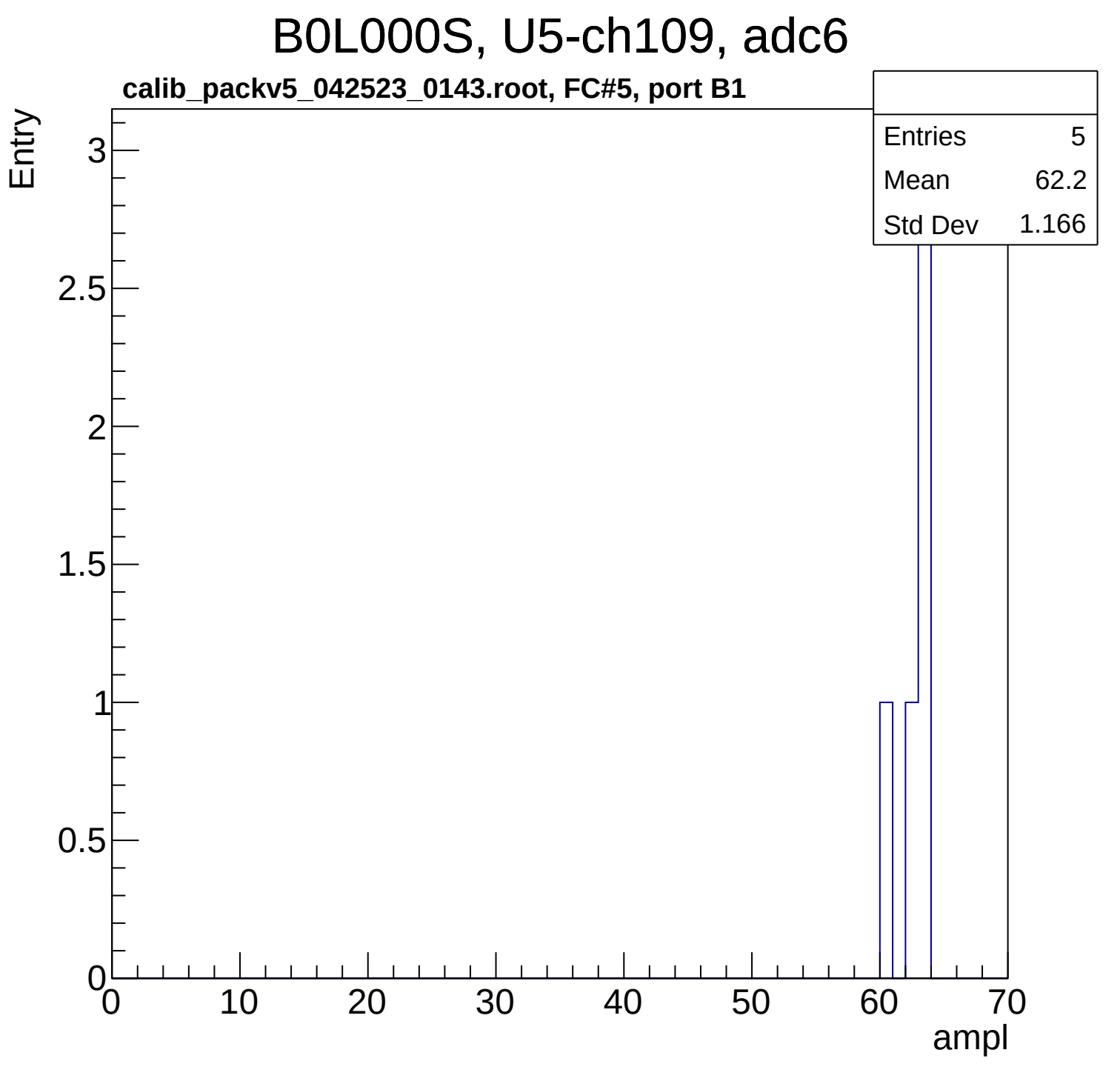
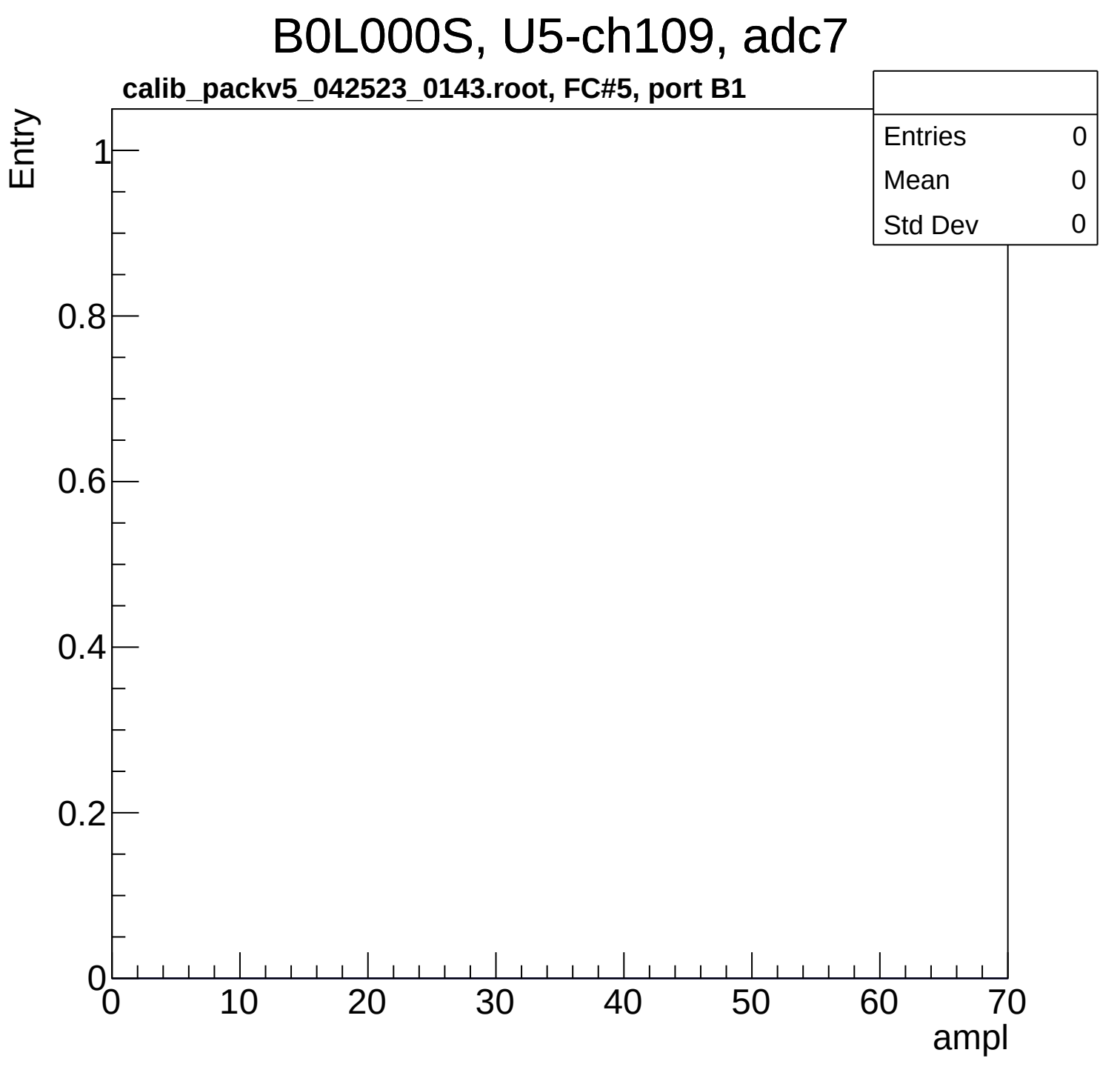
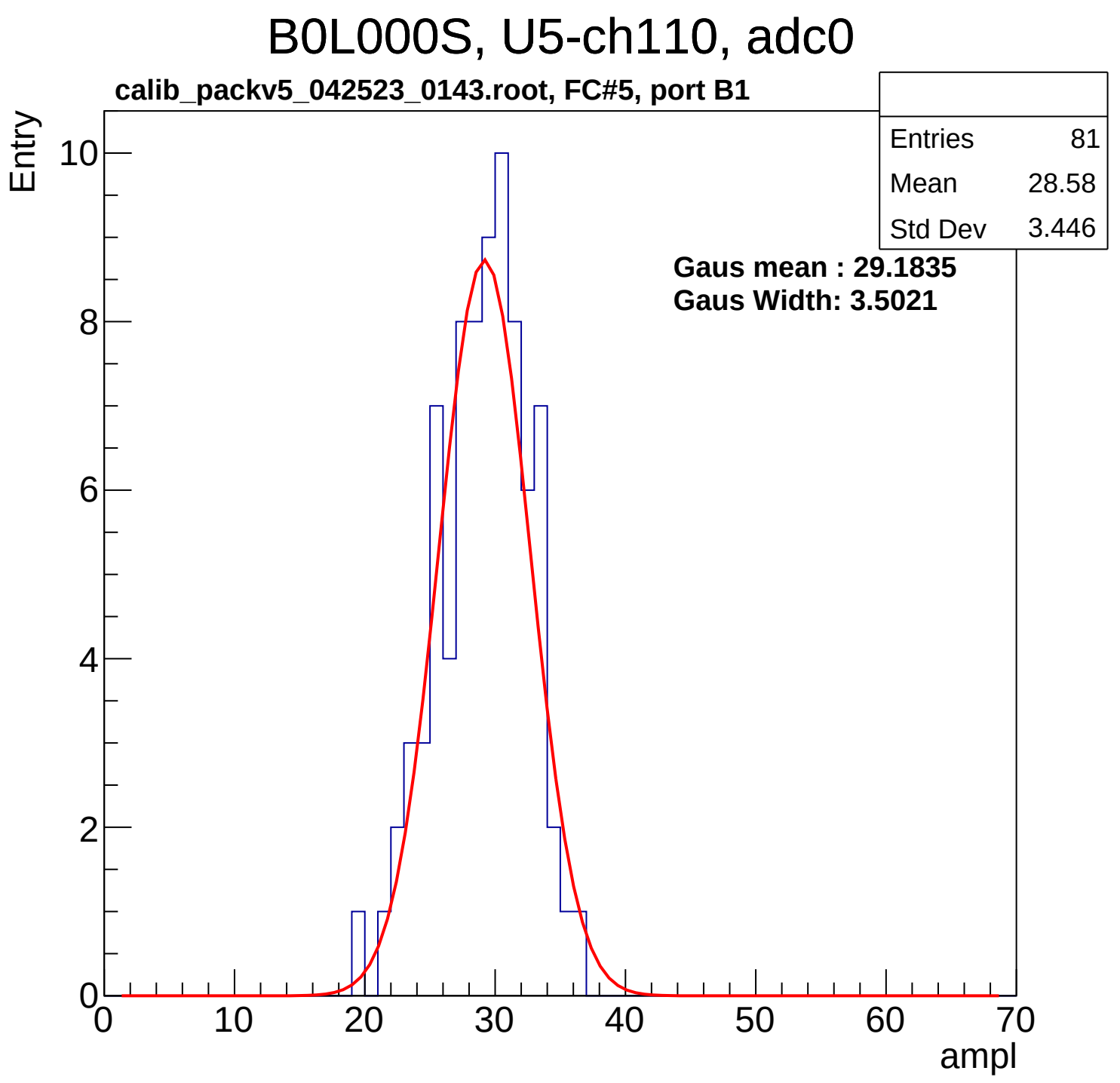
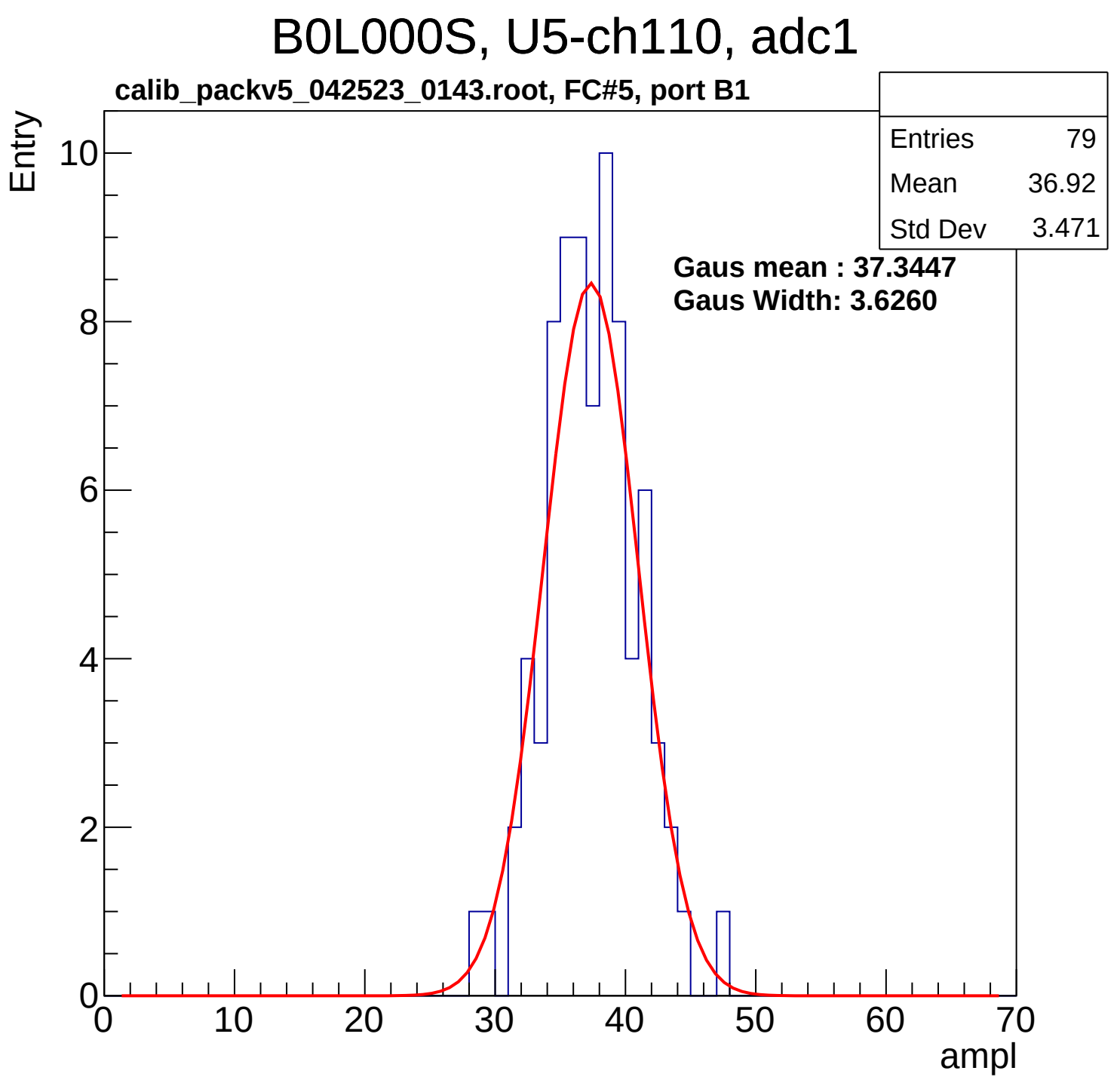
40
50
60
70


B0L000S, U5-ch109, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 5
Mean 62.2
Std Dev 1.166
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch109, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch110, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 28.58
Std Dev 3.446
Gaus mean : 29.1835
Gaus Width: 3.5021
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch110, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 36.92
Std Dev 3.471
Gaus mean : 37.3447
Gaus Width: 3.6260
Entry
ampl
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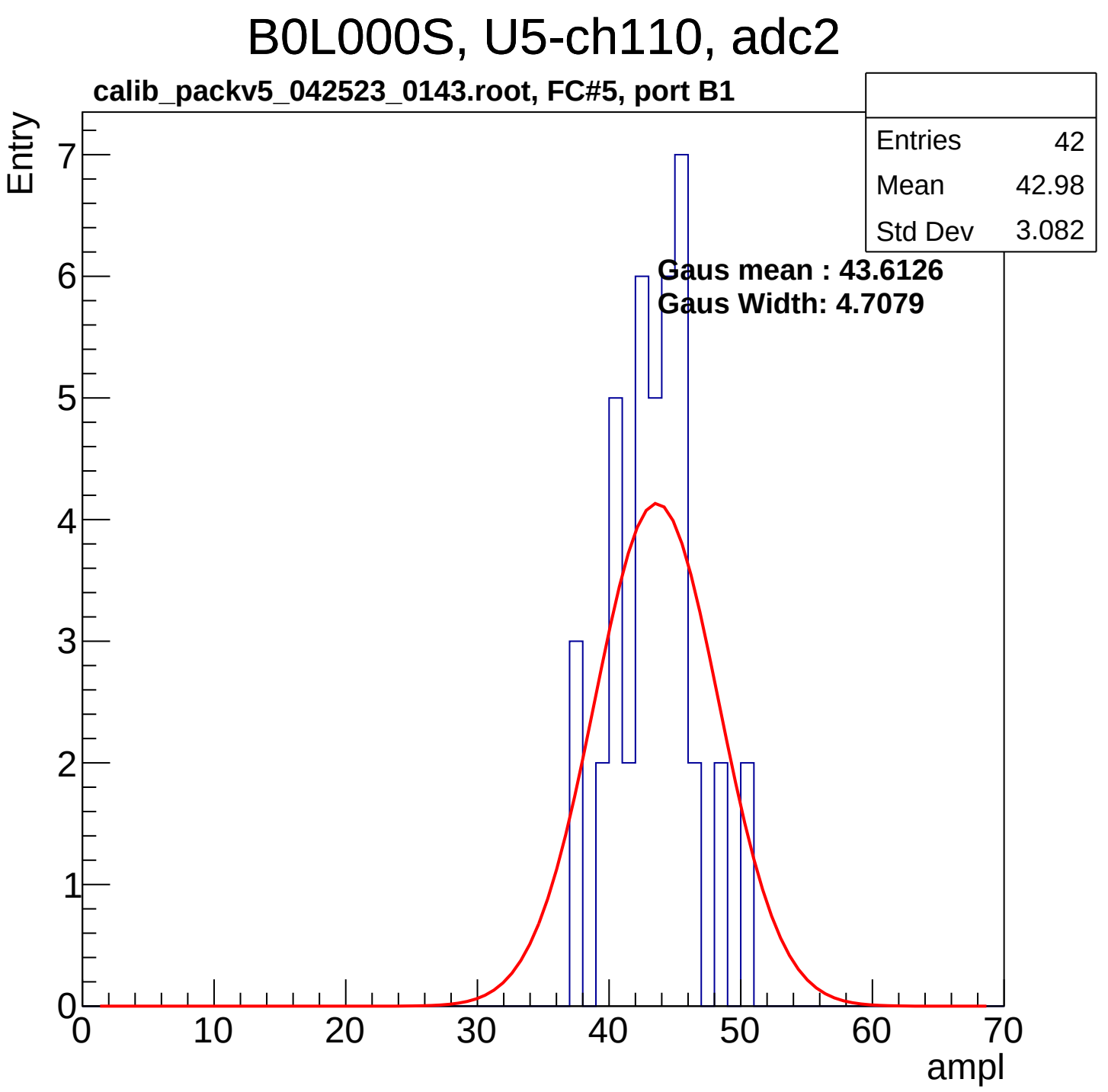
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch110, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 42.98
Std Dev 3.082
Gaus mean : 43.6126
Gaus Width: 4.7079
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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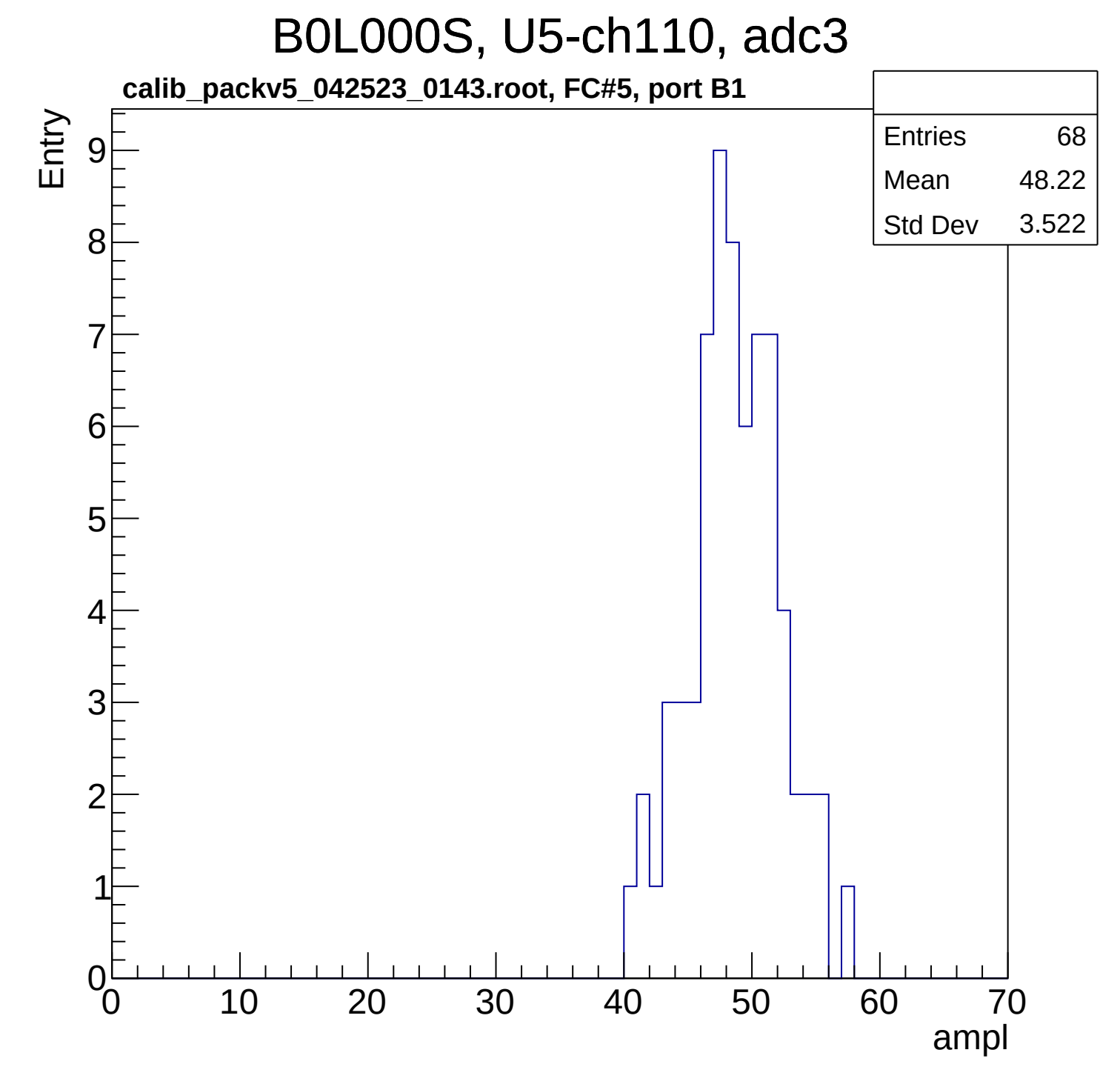
B0L000S, U5-ch110, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 48.22
Std Dev 3.522
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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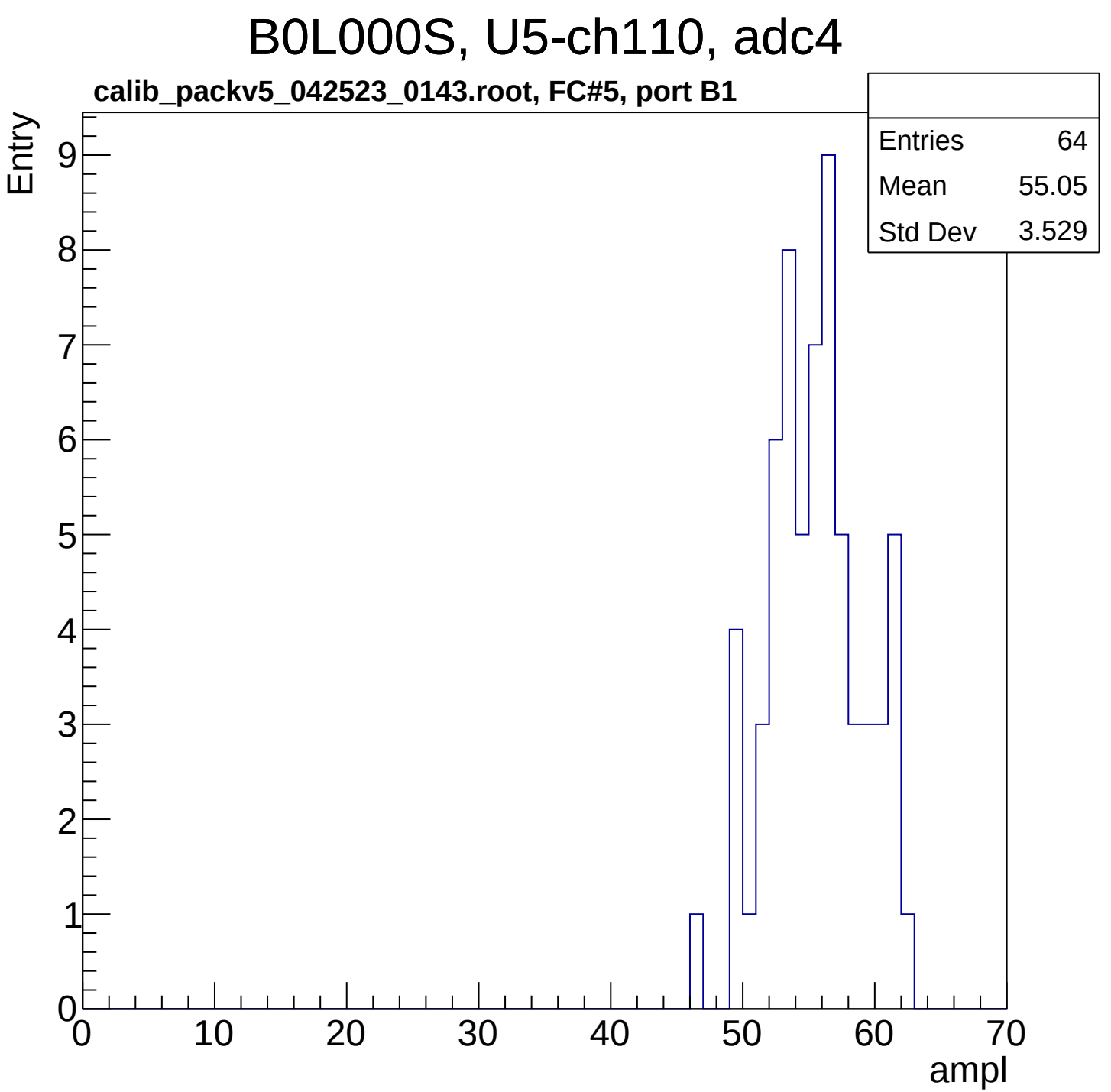
B0L000S, U5-ch110, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 55.05
Std Dev 3.529
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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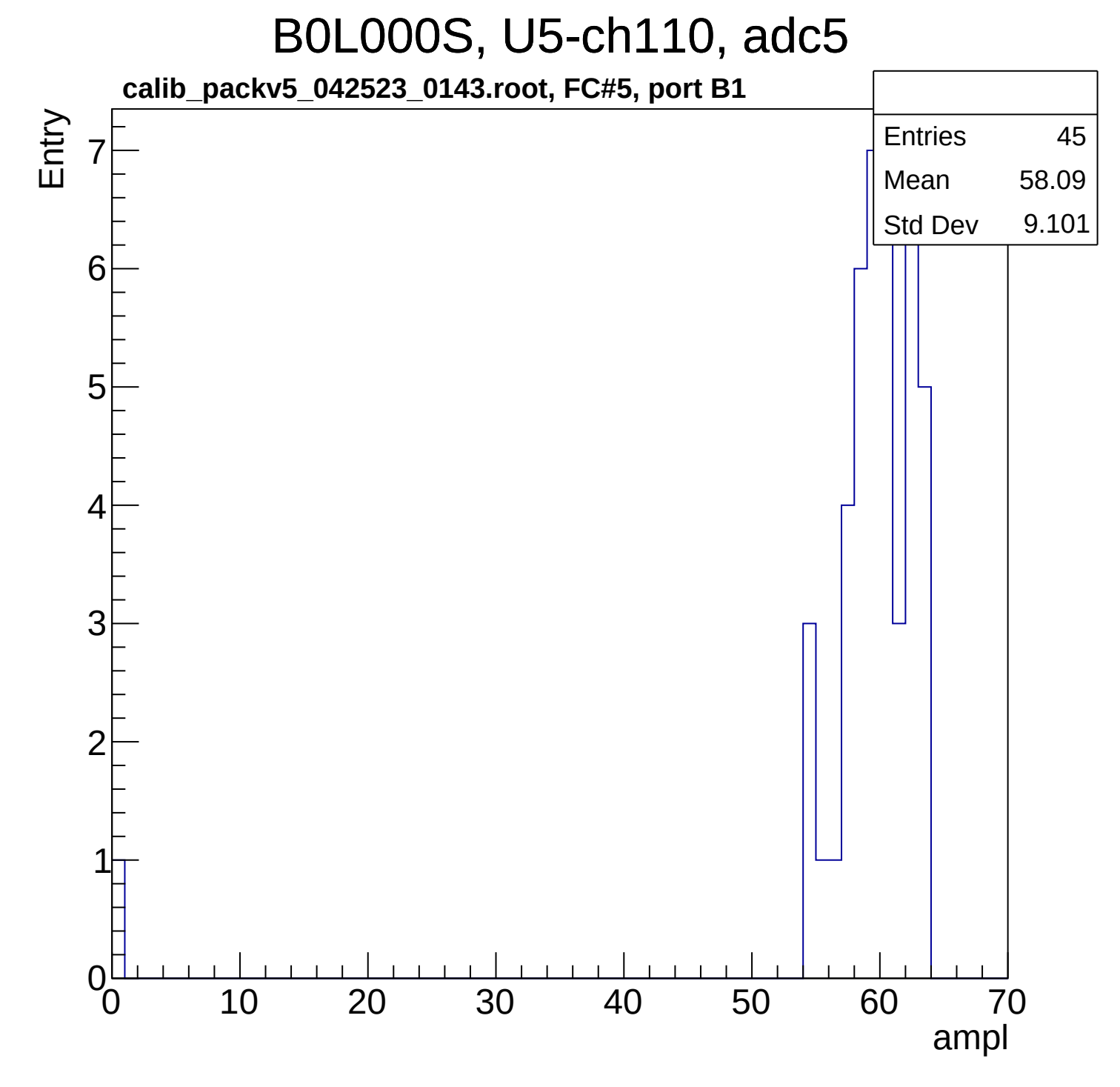
B0L000S, U5-ch110, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 58.09
Std Dev 9.101
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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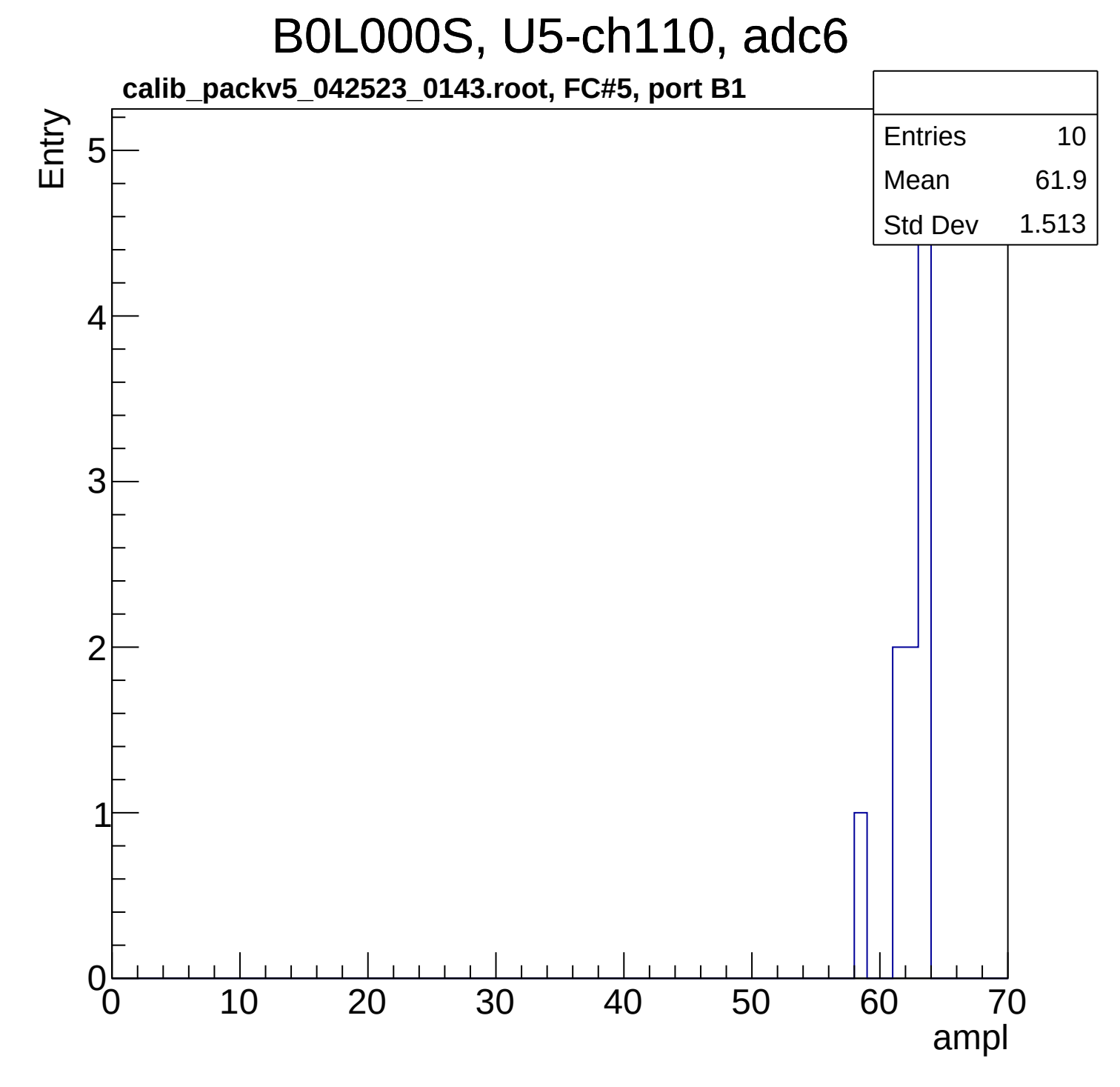
B0L000S, U5-ch110, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 10
Mean 61.9
Std Dev 1.513
Entry
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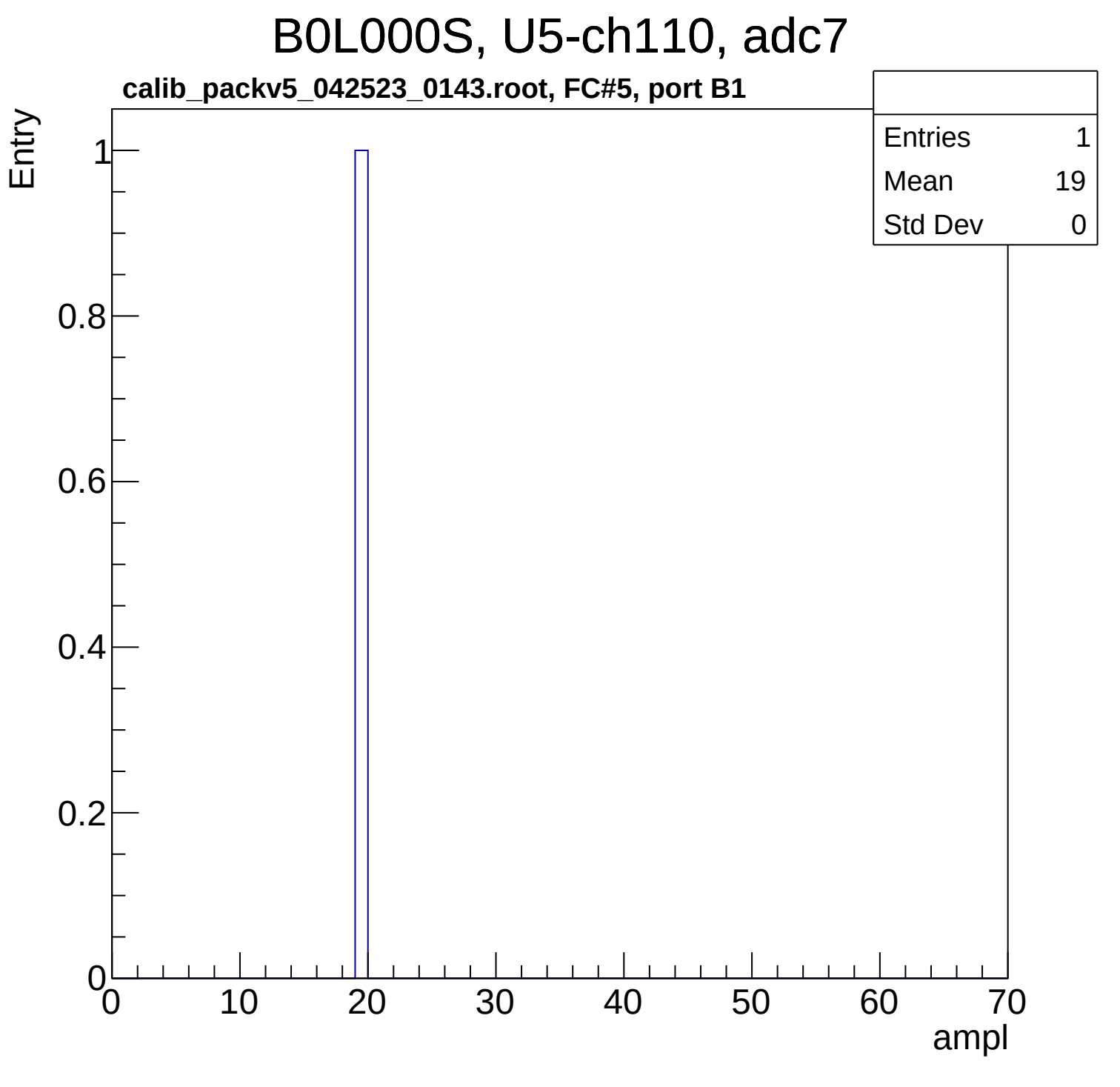
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch110, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 19
Std Dev 0
Entry
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0.8
0.6
0.4
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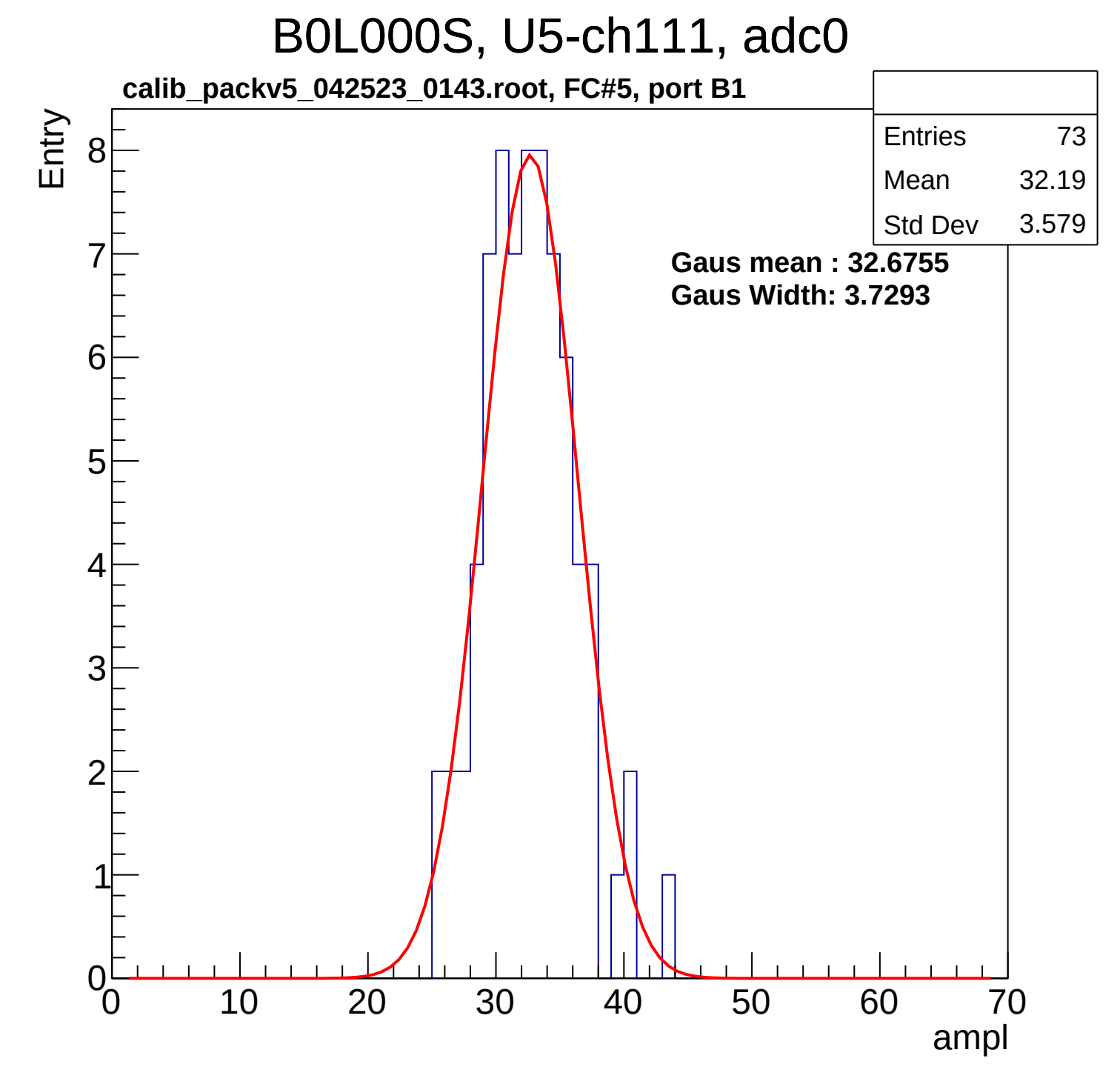
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch111, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 32.19
Std Dev 3.579
Gaus mean : 32.6755
Gaus Width: 3.7293
Entry
ampl
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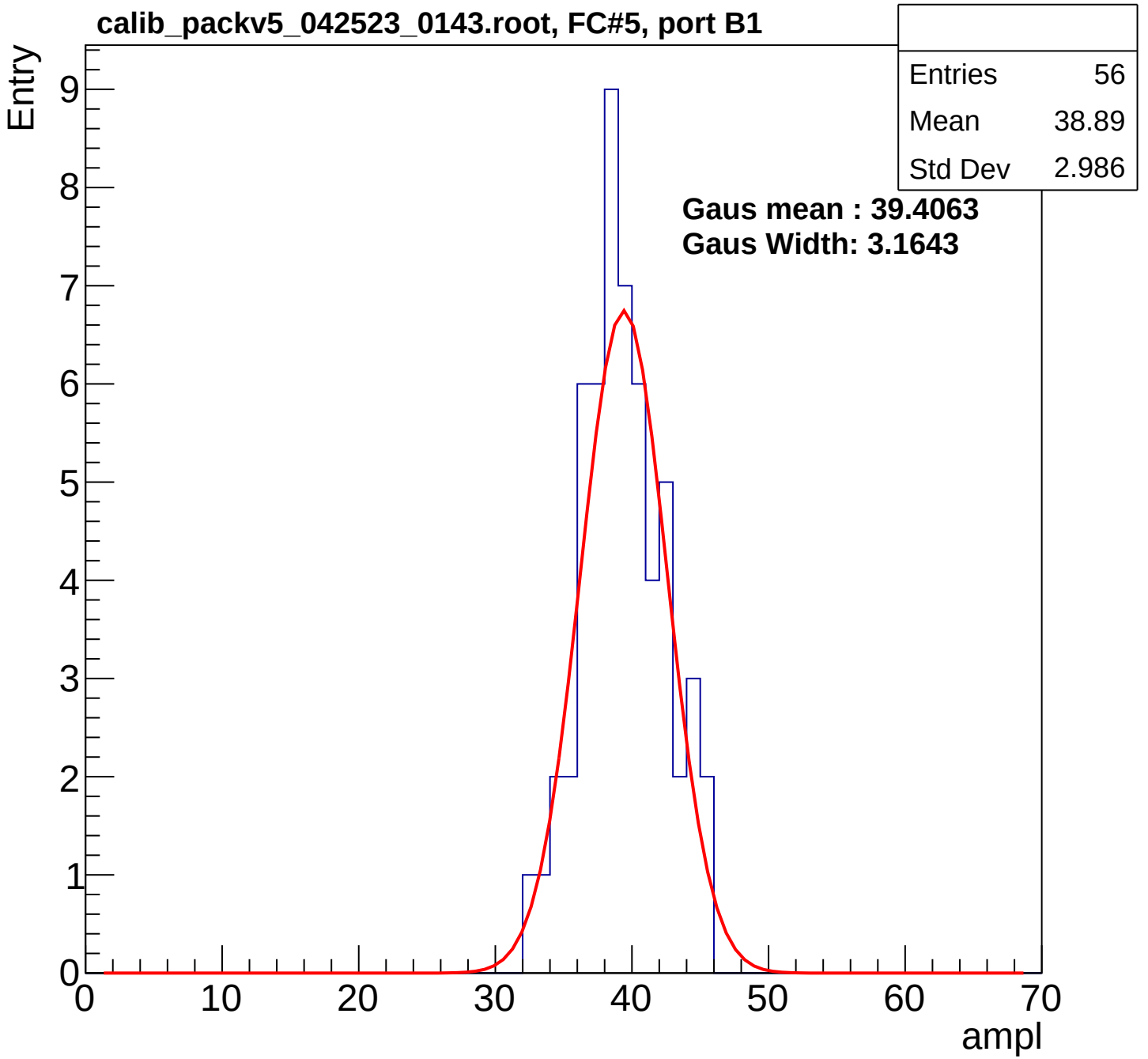
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch111, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 38.89
Std Dev 2.986
Gaus mean : 39.4063
Gaus Width: 3.1643
Entry
ampl
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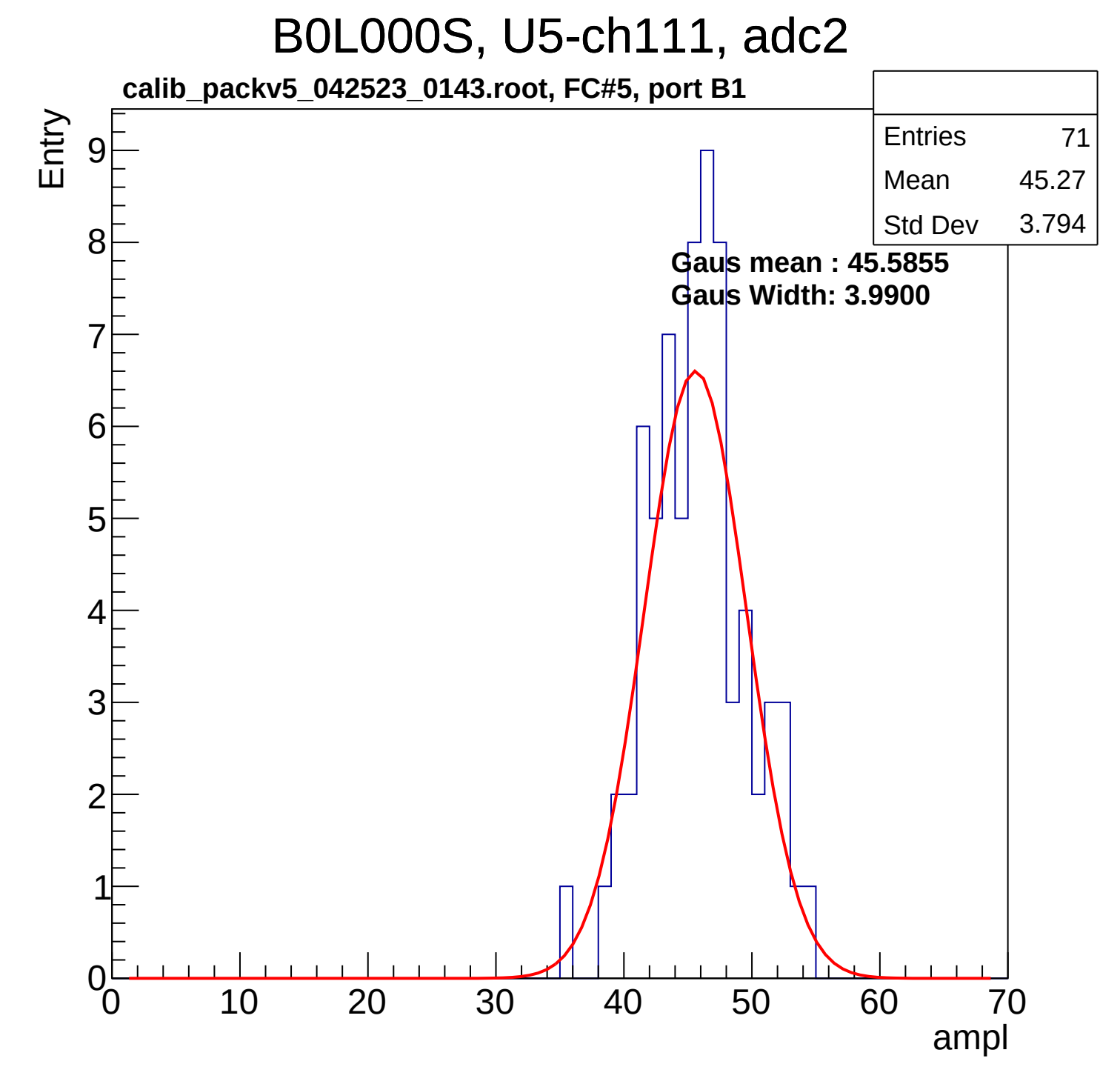
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch111, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 45.27
Std Dev 3.794
Gaus mean : 45.5855
Gaus Width: 3.9900
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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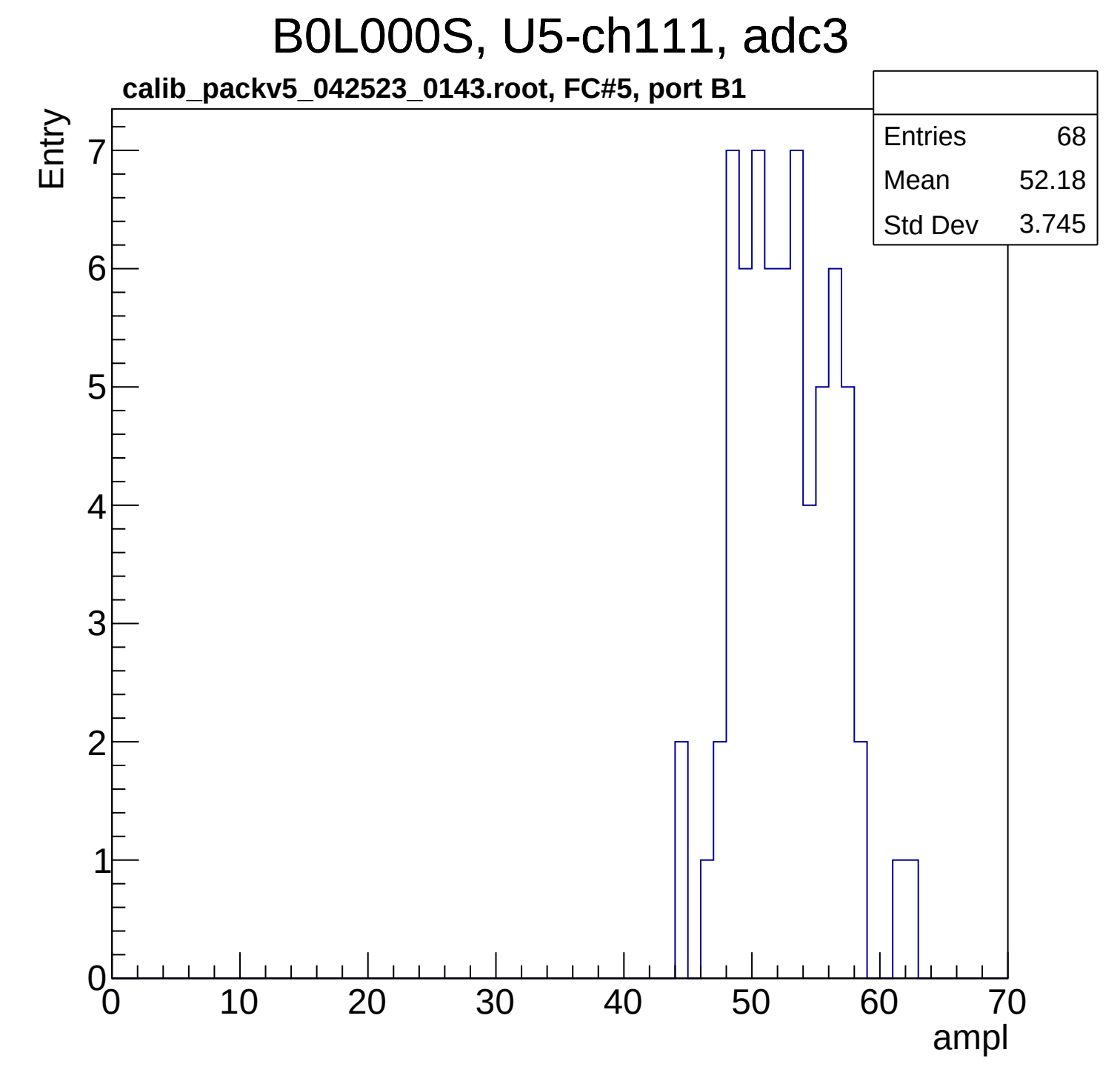
B0L000S, U5-ch111, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 52.18
Std Dev 3.745
Entry
ampl
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2
3
4
5
6
7
0
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60
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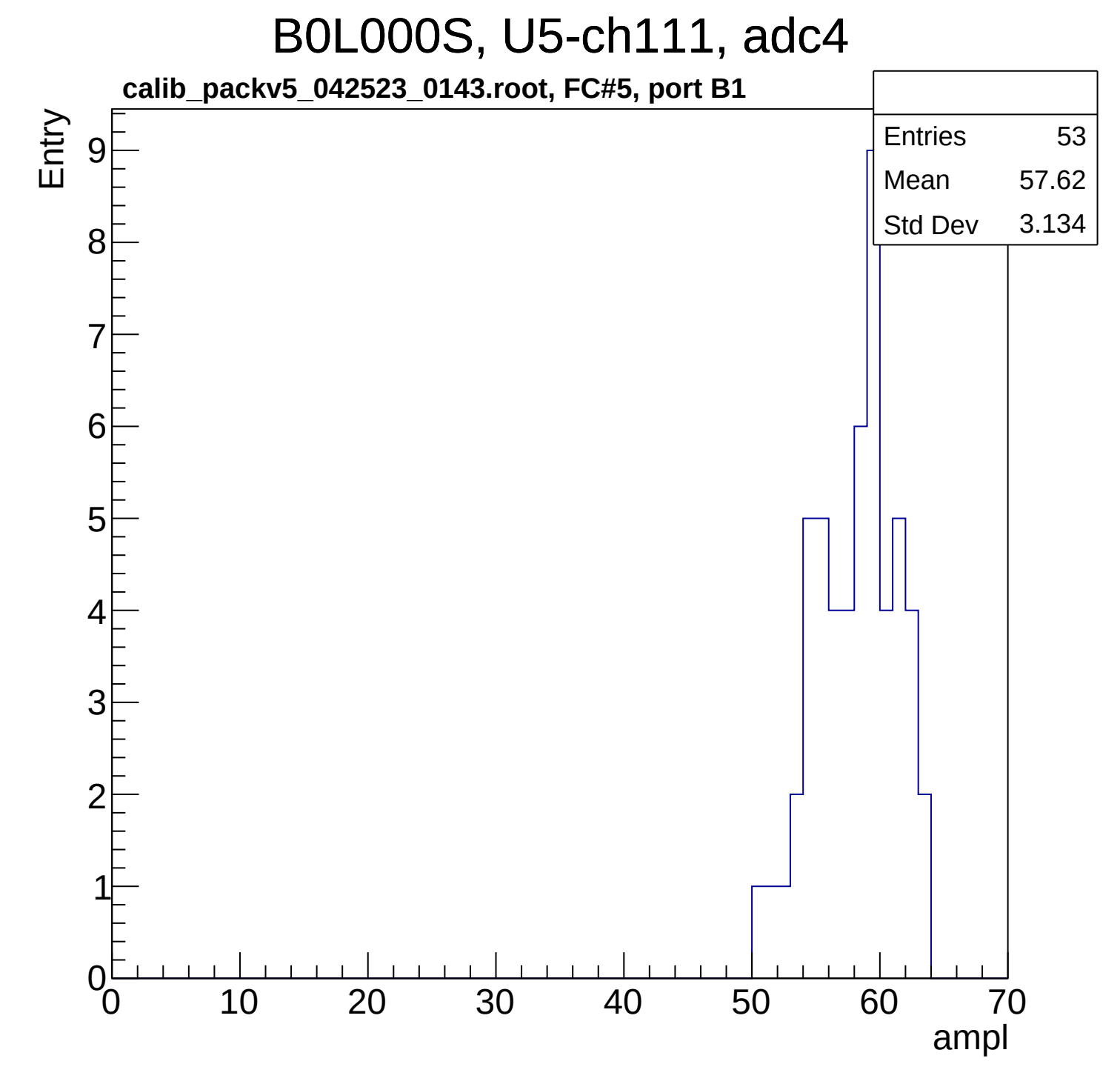
B0L000S, U5-ch111, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 57.62
Std Dev 3.134
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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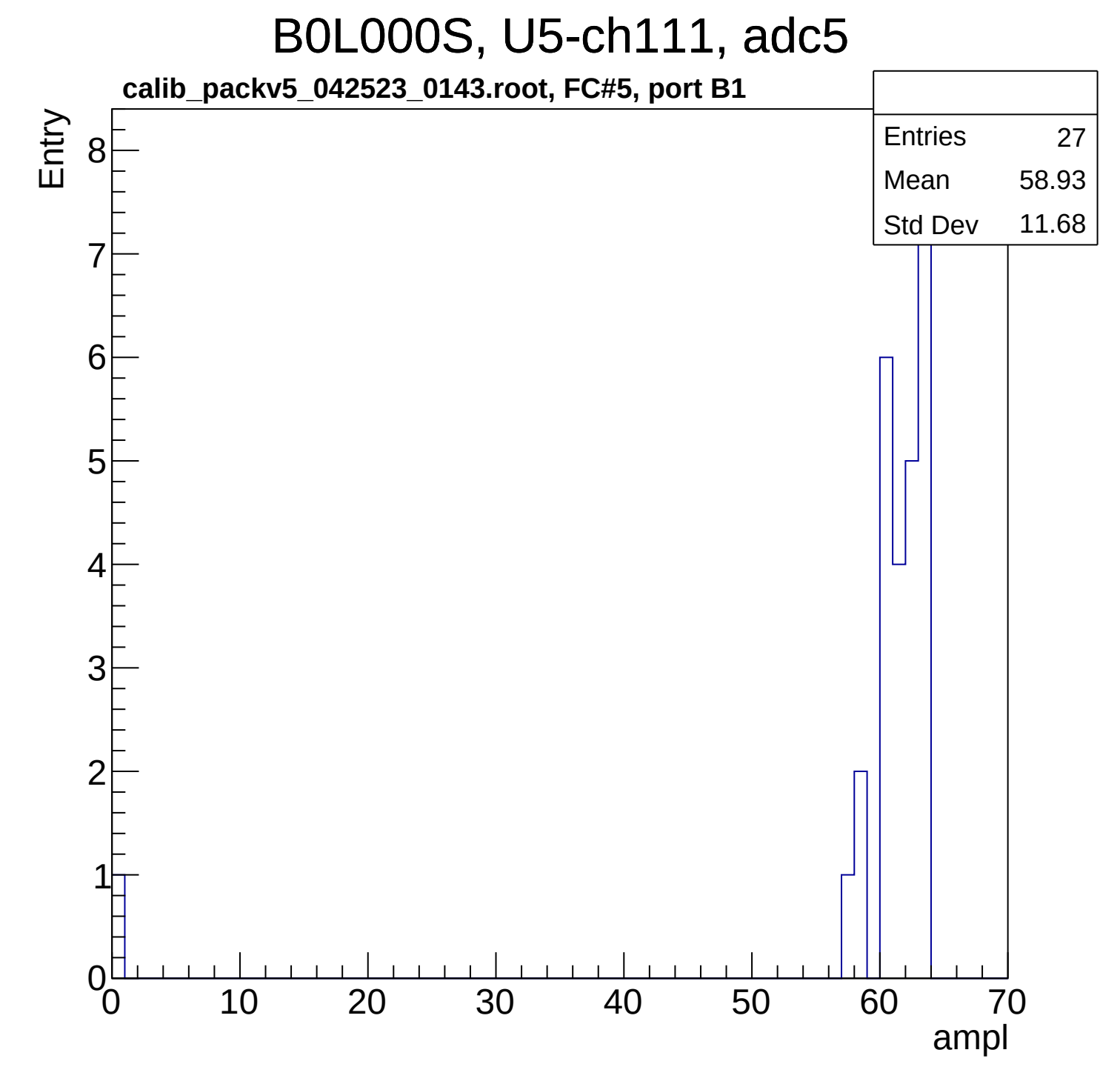
B0L000S, U5-ch111, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 27
Mean 58.93
Std Dev 11.68
Entry
ampl
0
1
2
3
4
5
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7
8
0
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20
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40
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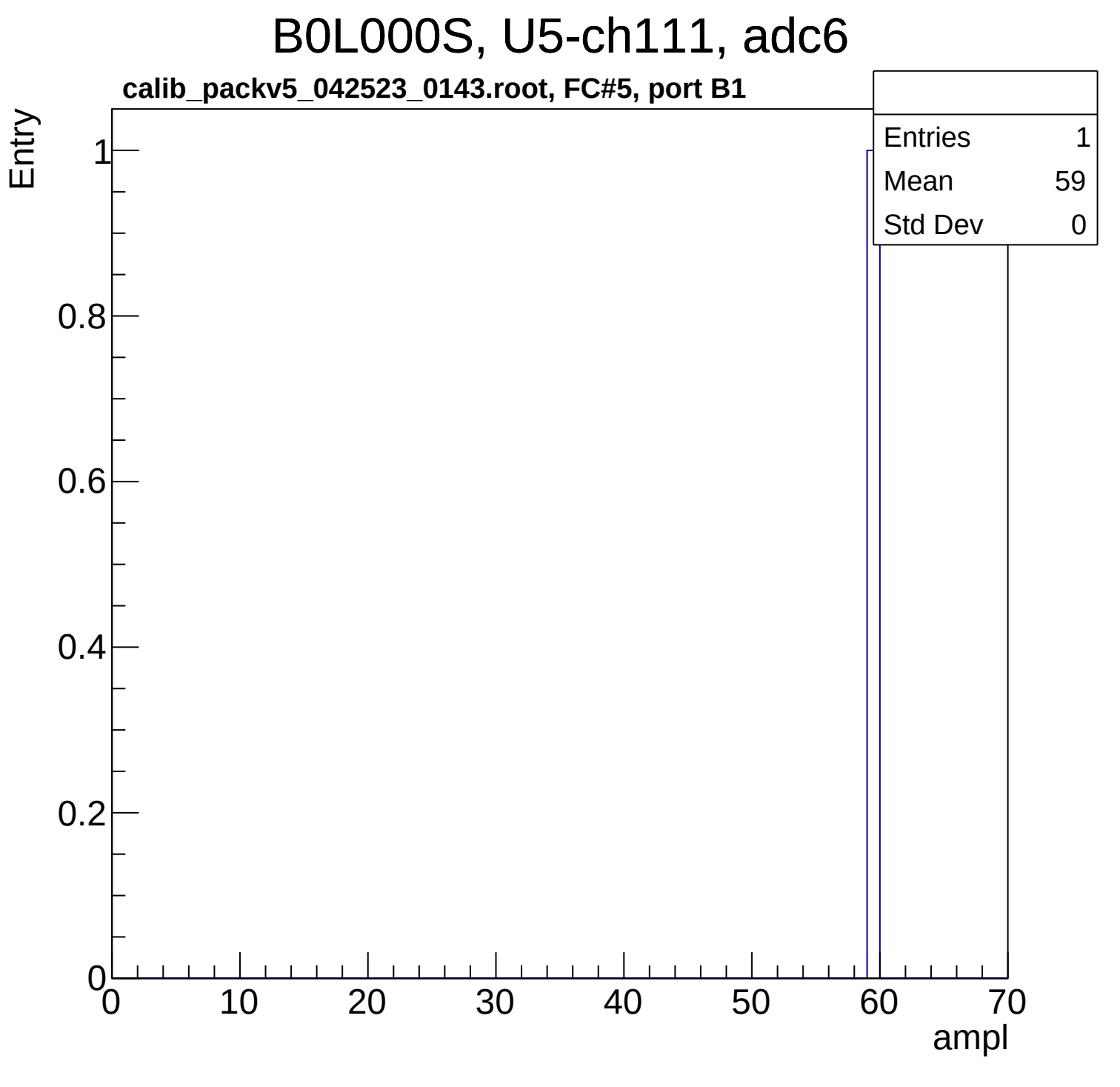
60
70


B0L000S, U5-ch111, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 59
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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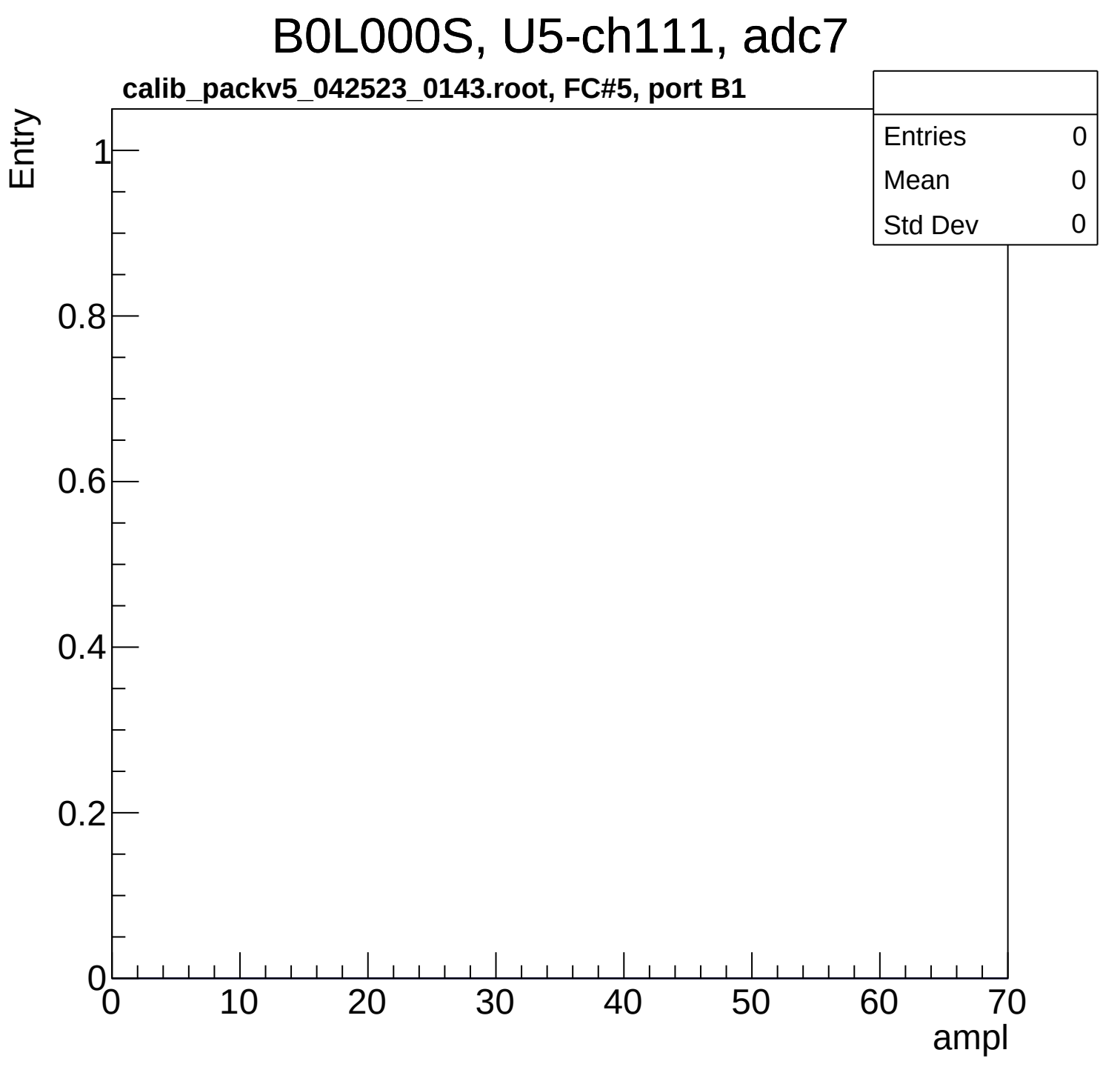
50
60
70
ampl


B0L000S, U5-ch111, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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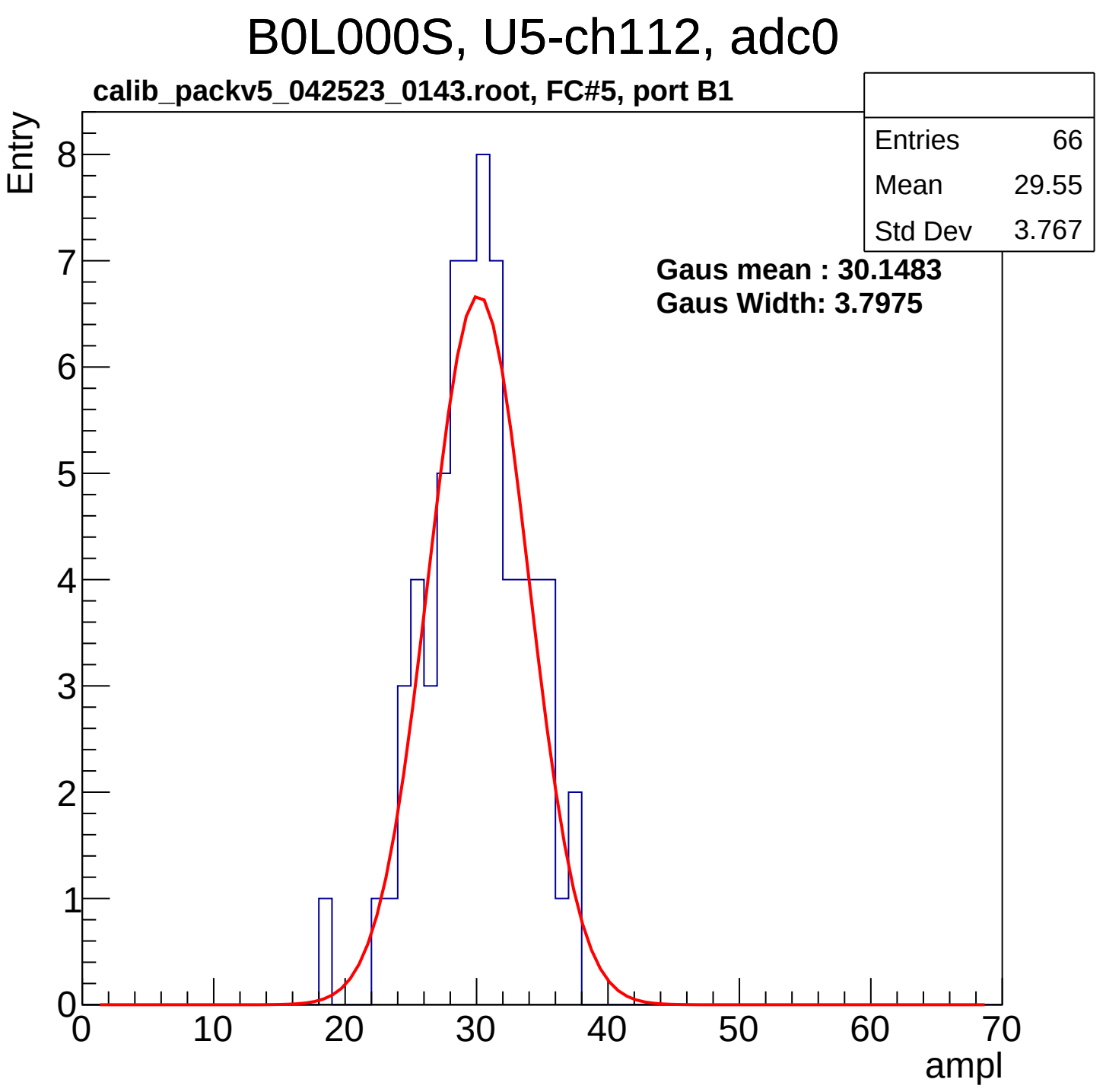
40
50
60
70
ampl


B0L000S, U5-ch112, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.55
Std Dev 3.767
Gaus mean : 30.1483
Gaus Width: 3.7975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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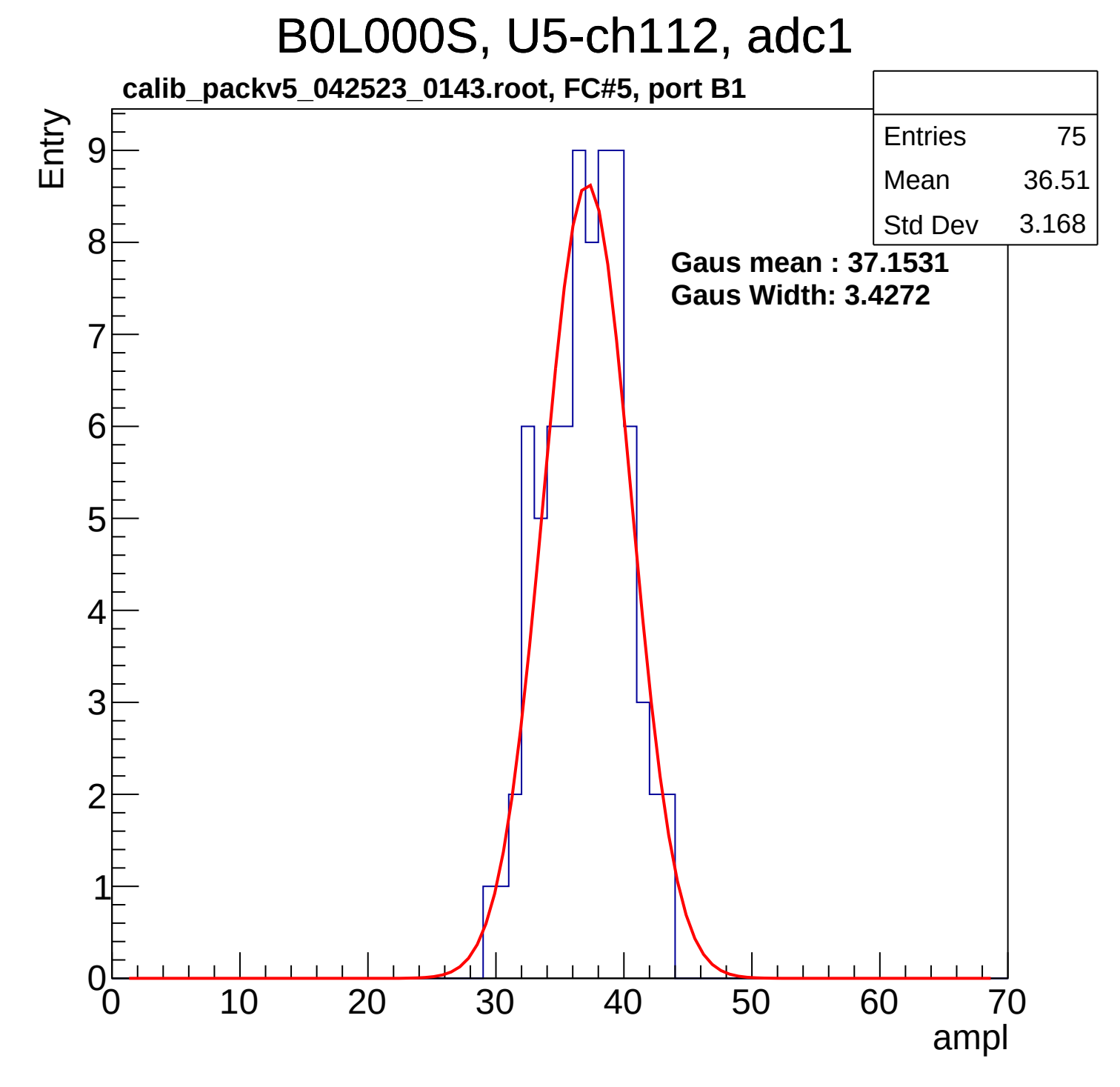
B0L000S, U5-ch112, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.51
Std Dev 3.168
Gaus mean : 37.1531
Gaus Width: 3.4272
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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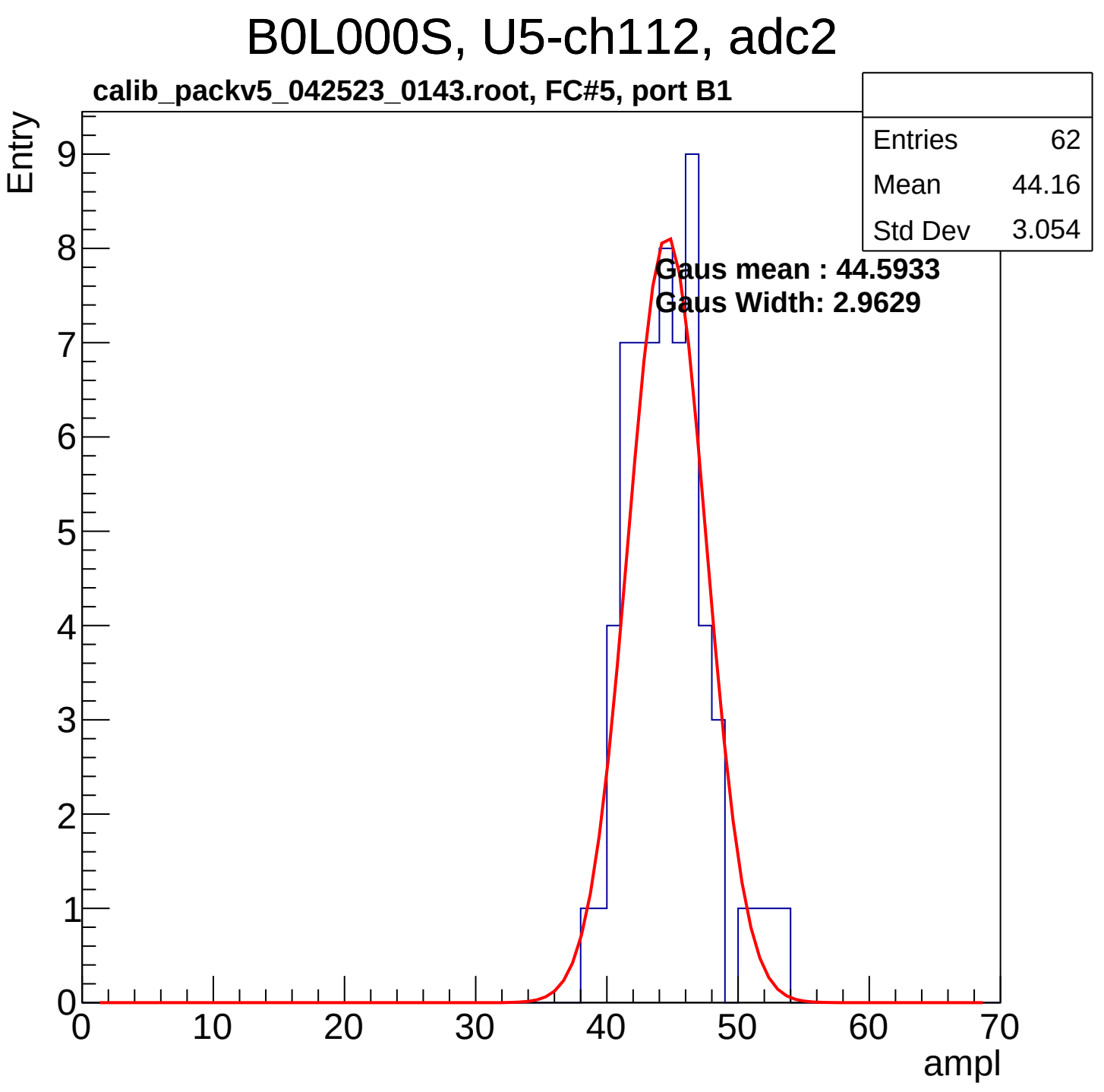
B0L000S, U5-ch112, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 44.16
Std Dev 3.054
Gaus mean : 44.5933
Gaus Width: 2.9629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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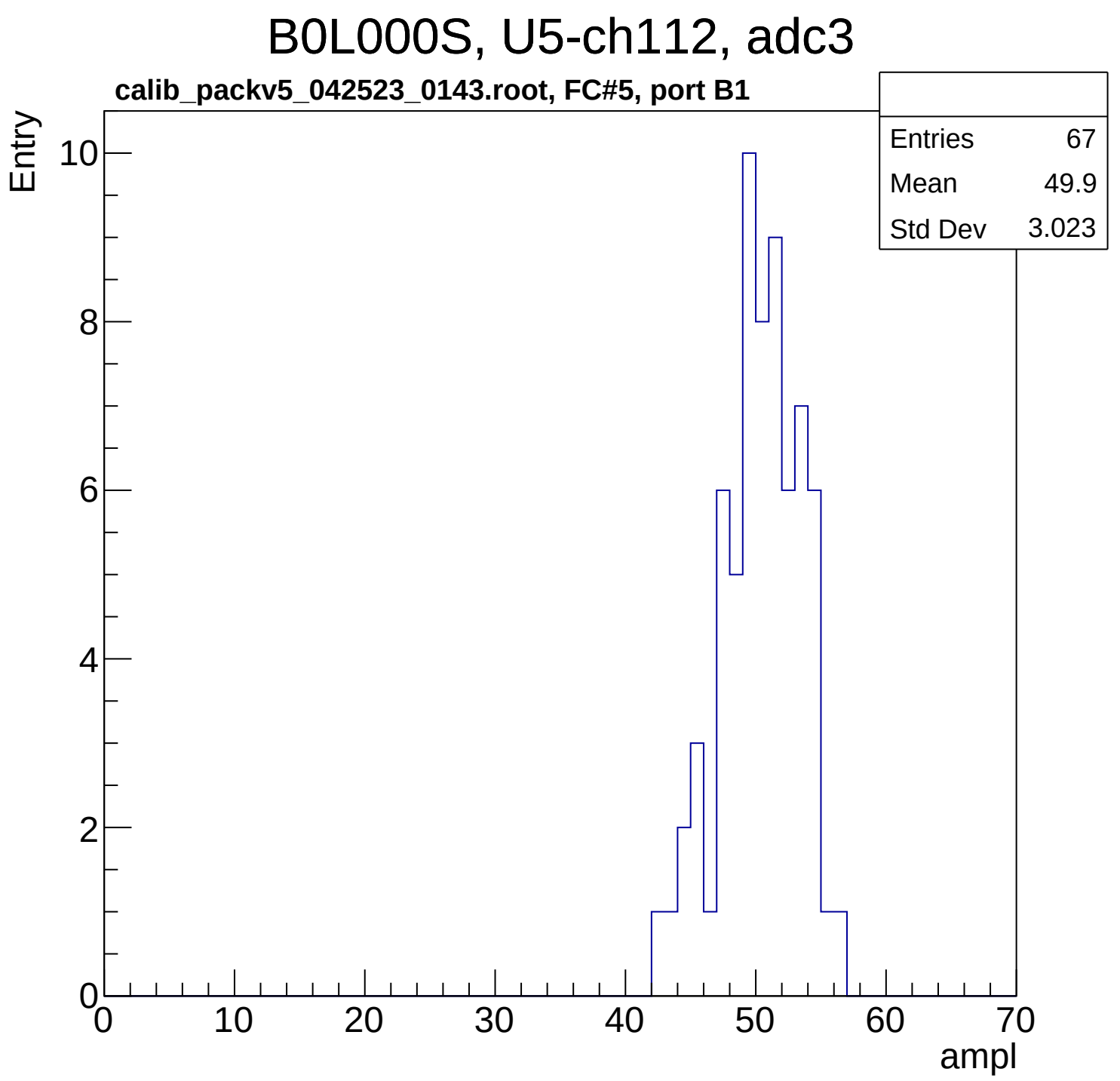
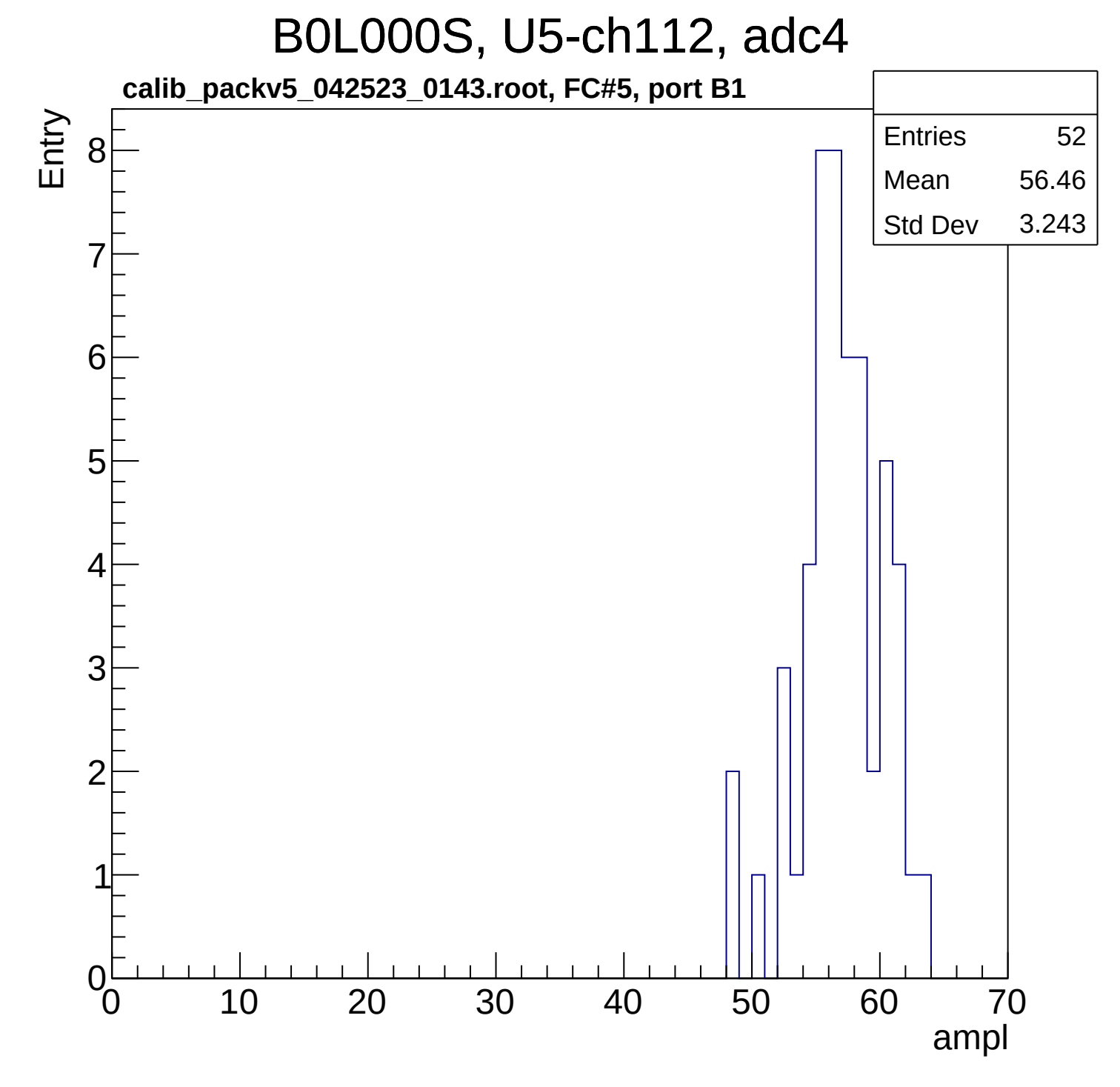
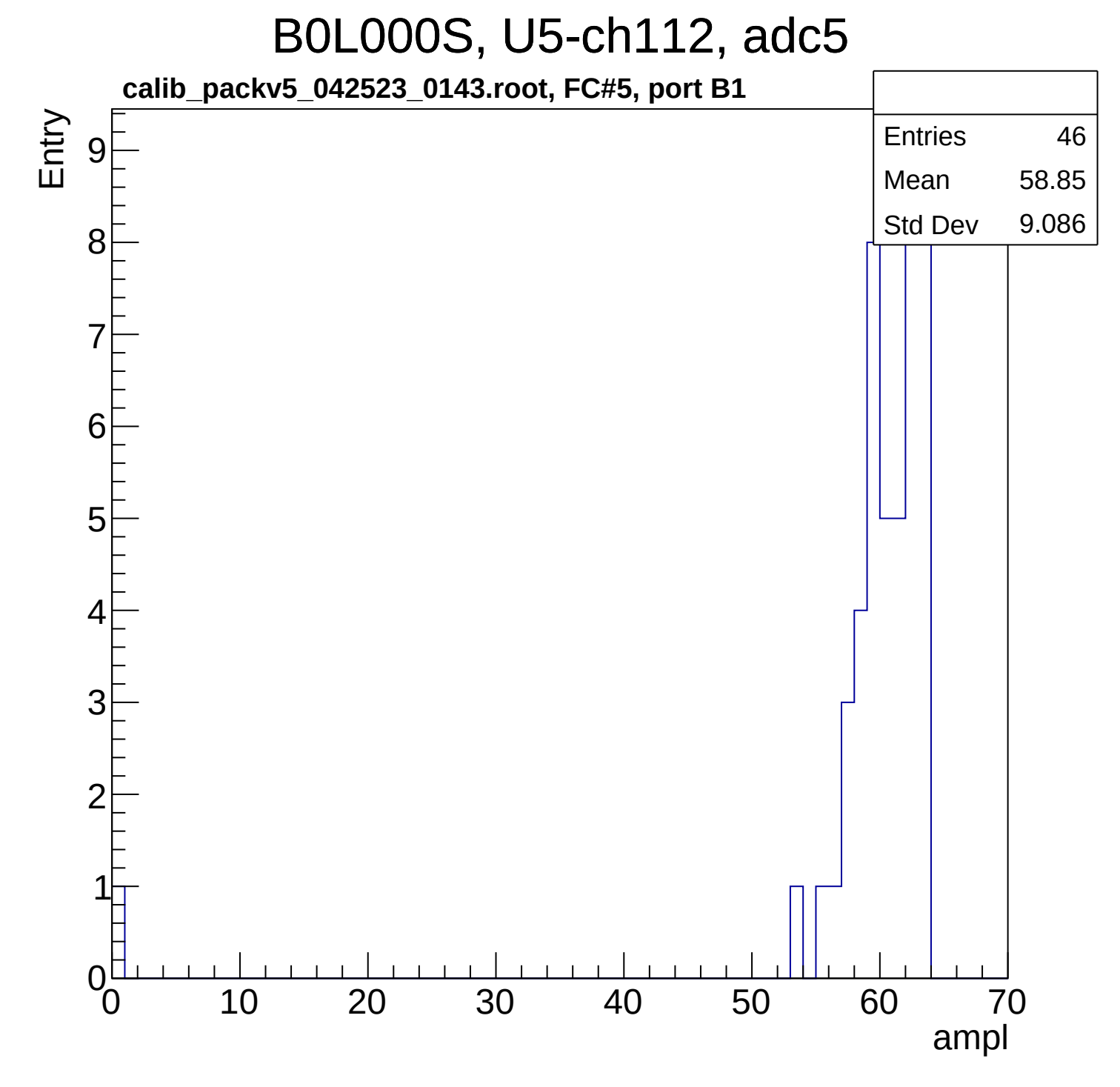

B0L000S, U5-ch112, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 49.9
Std Dev 3.023
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch112, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 52
Mean 56.46
Std Dev 3.243
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch112, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 58.85
Std Dev 9.086
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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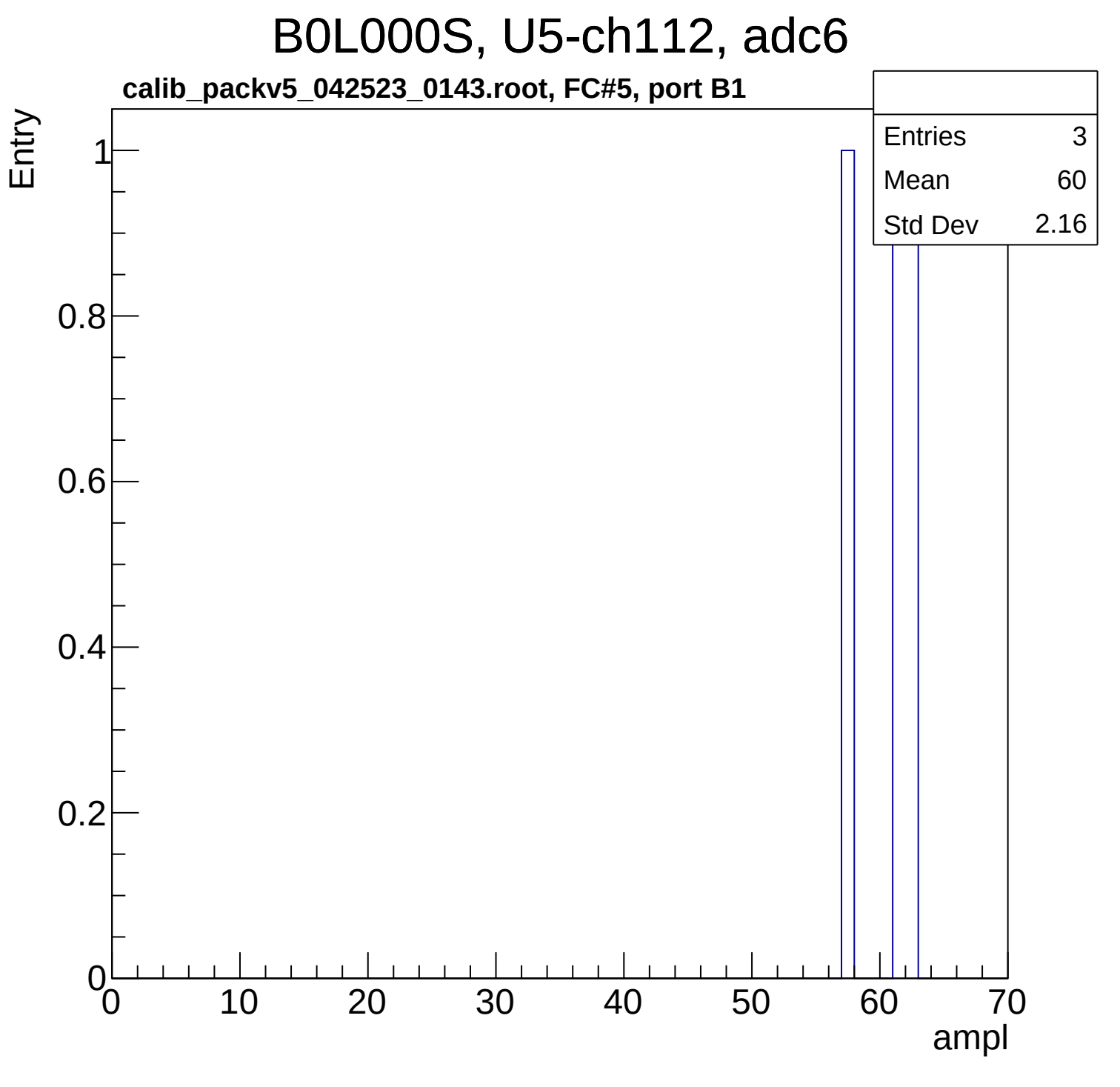
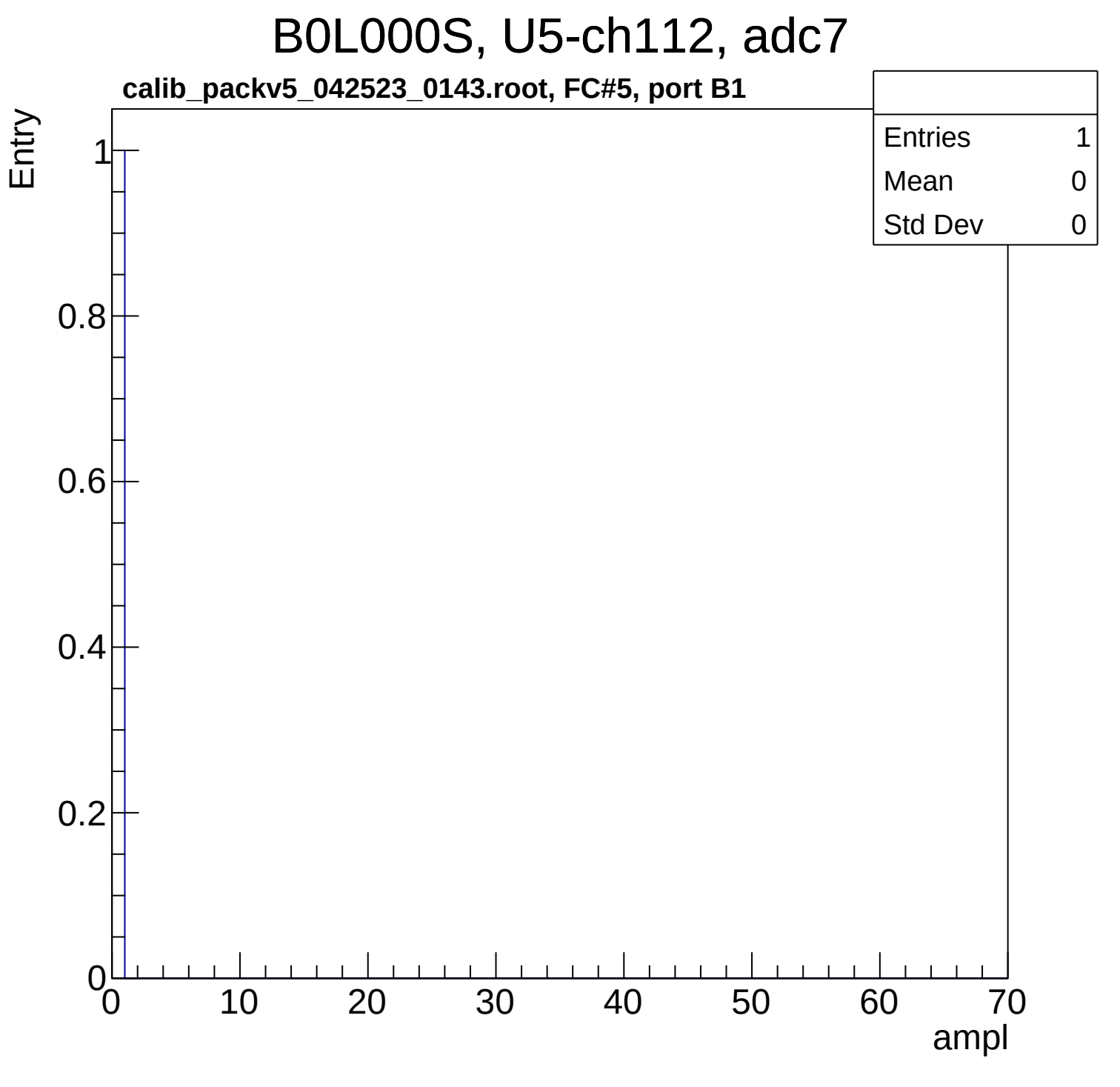
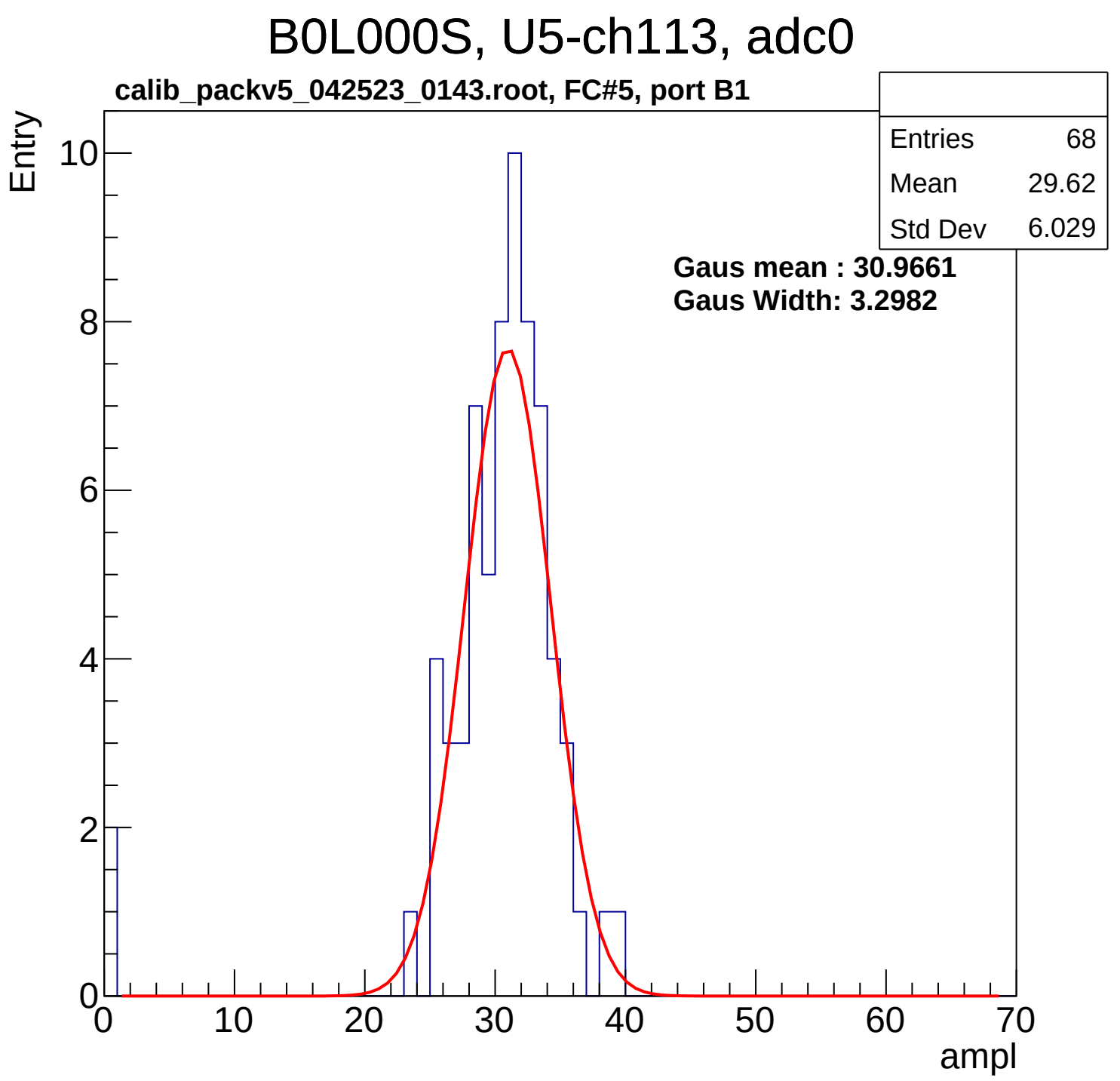

B0L000S, U5-ch112, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60
Std Dev 2.16
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch112, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch113, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 29.62
Std Dev 6.029
Gaus mean : 30.9661
Gaus Width: 3.2982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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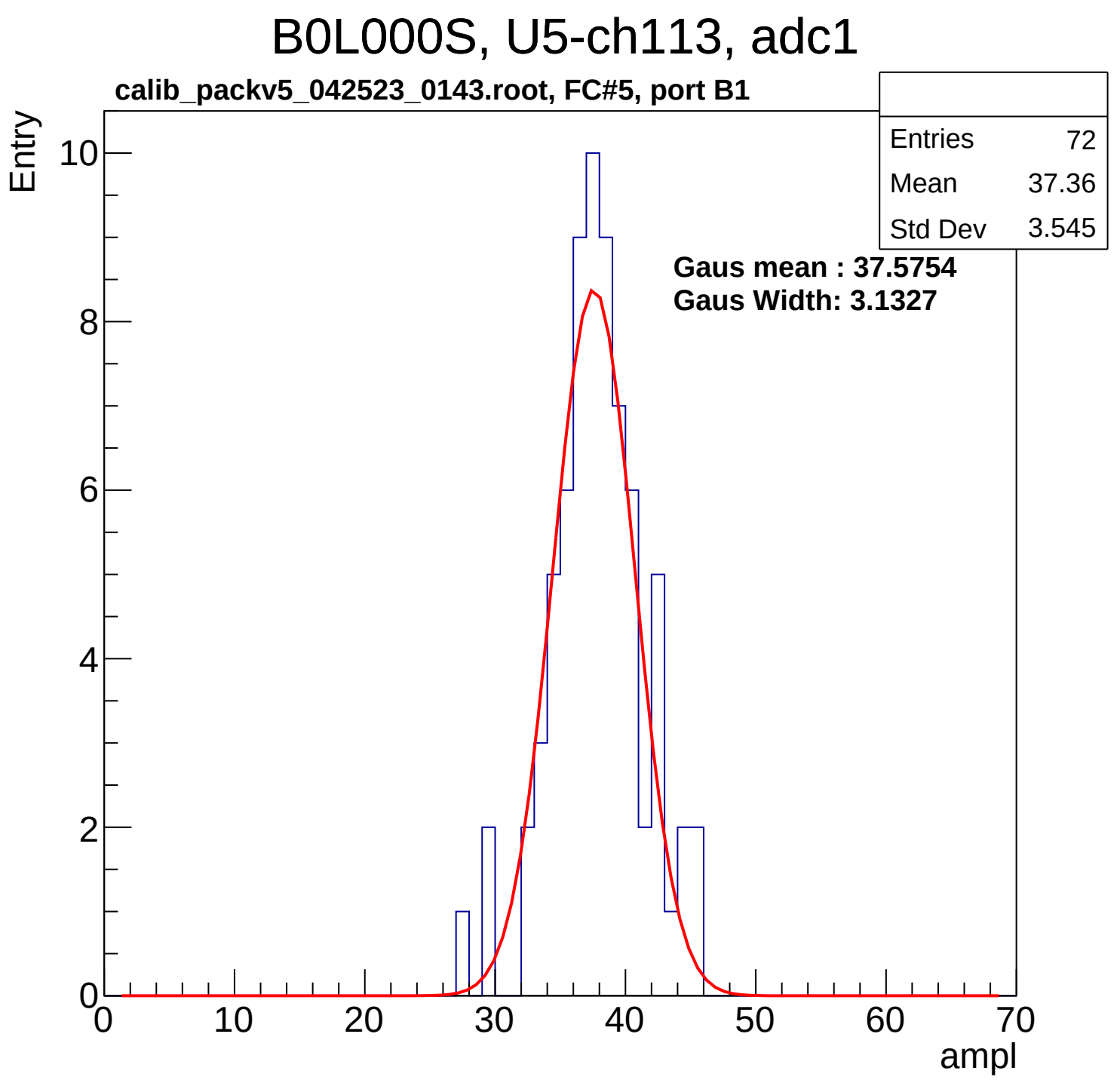
B0L000S, U5-ch113, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.36
Std Dev 3.545
Gaus mean : 37.5754
Gaus Width: 3.1327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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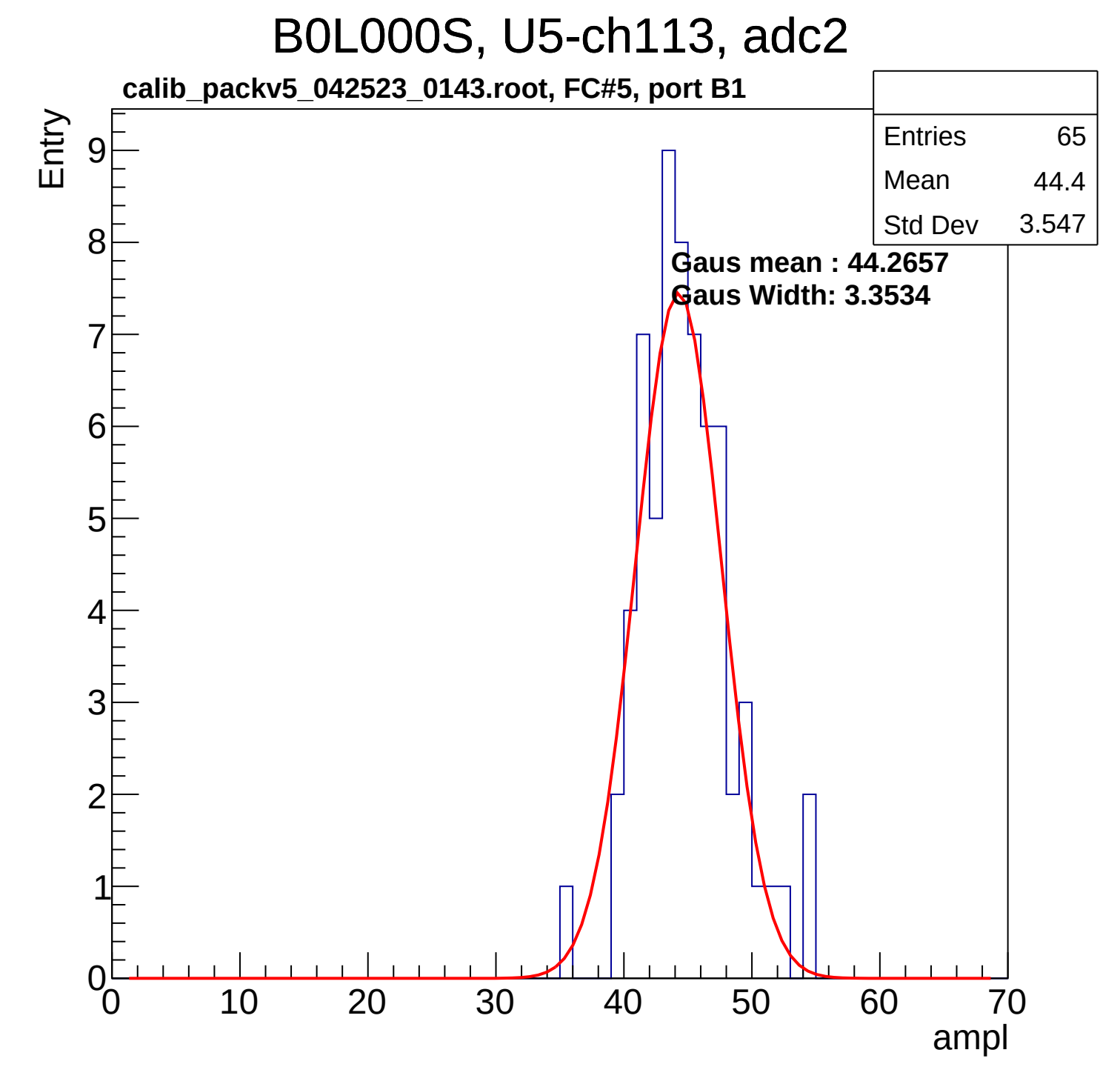
B0L000S, U5-ch113, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 44.4
Std Dev 3.547
Gaus mean : 44.2657
Gaus Width: 3.3534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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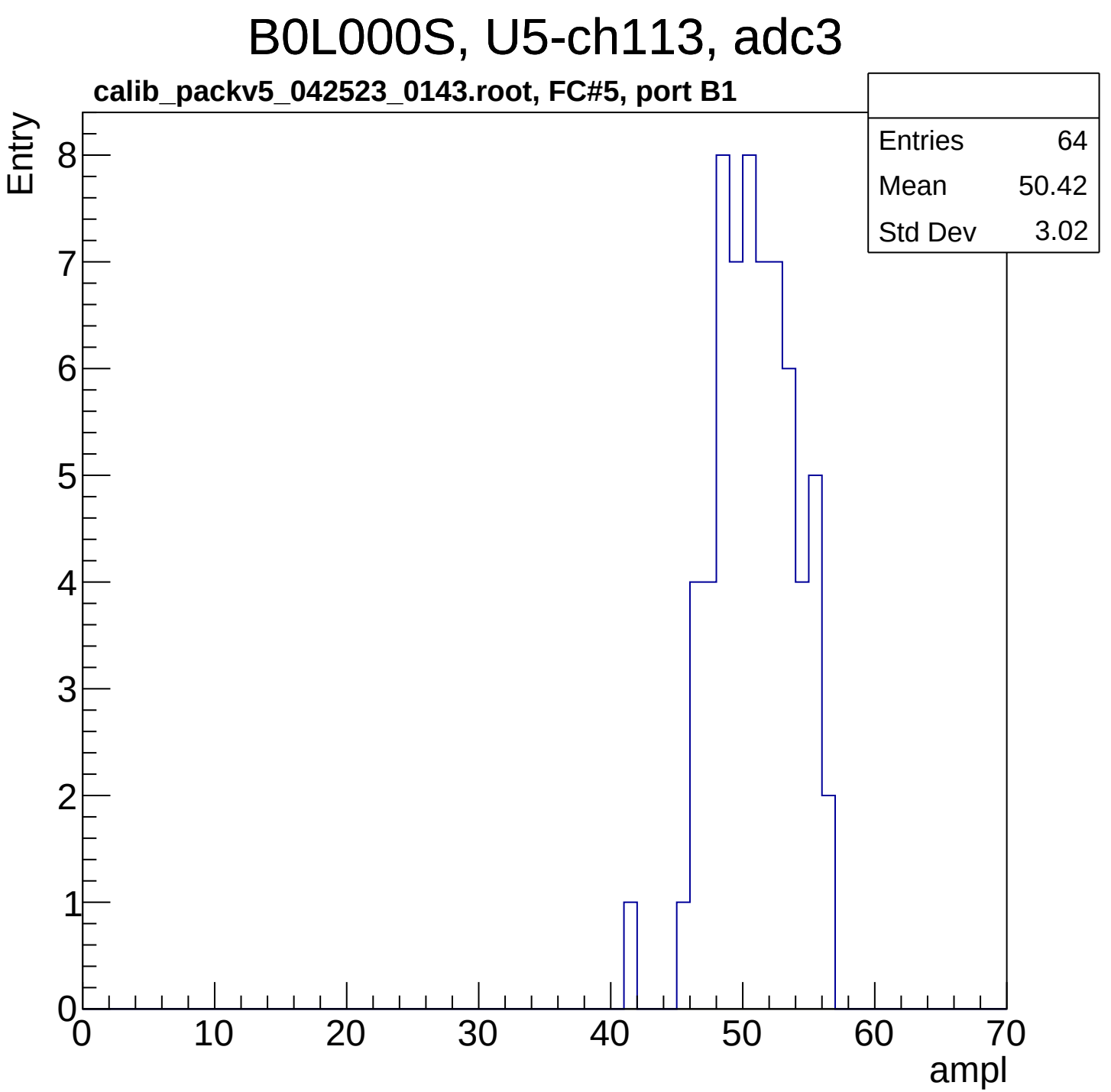
B0L000S, U5-ch113, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 50.42
Std Dev 3.02
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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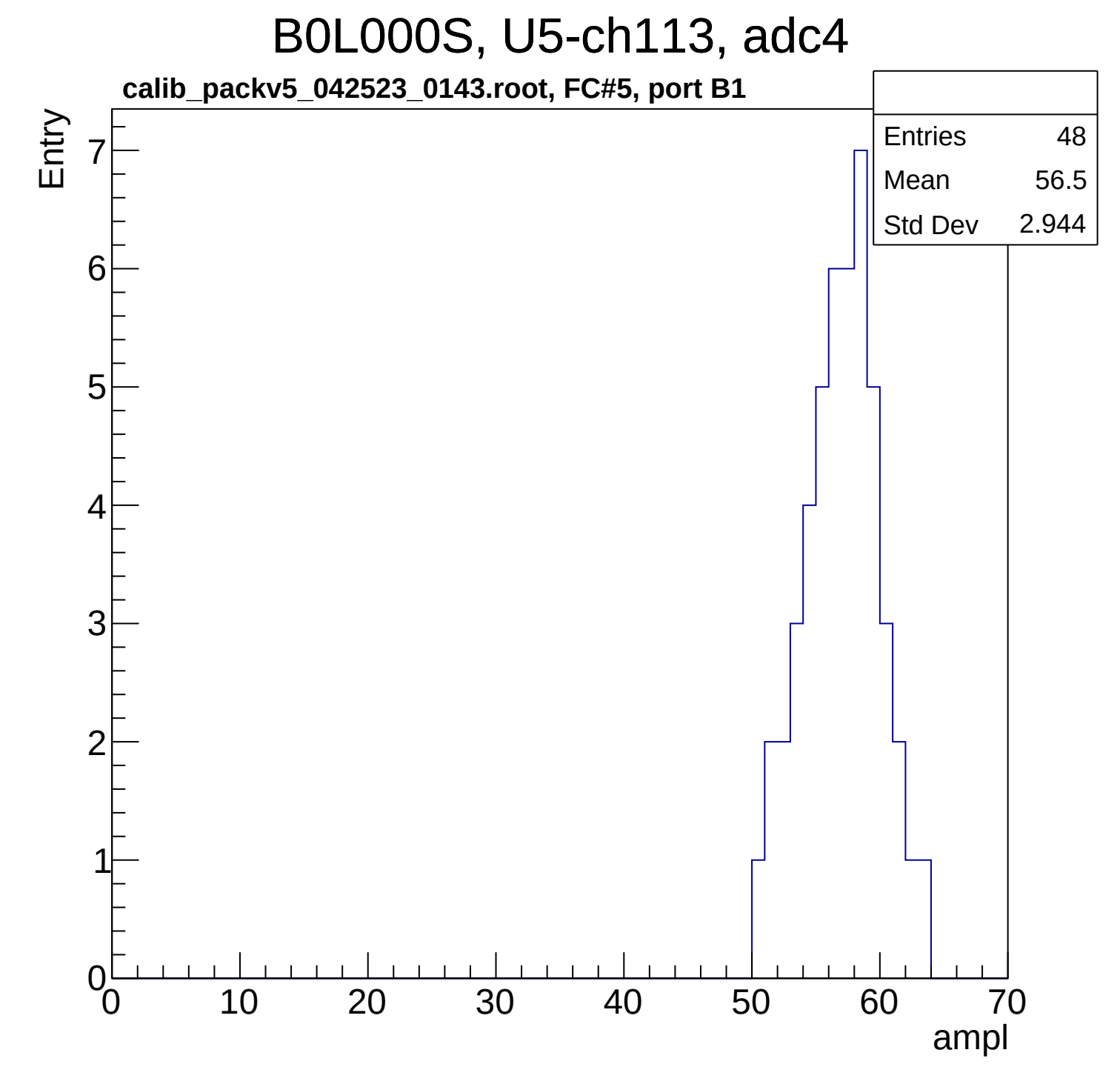
B0L000S, U5-ch113, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 56.5
Std Dev 2.944
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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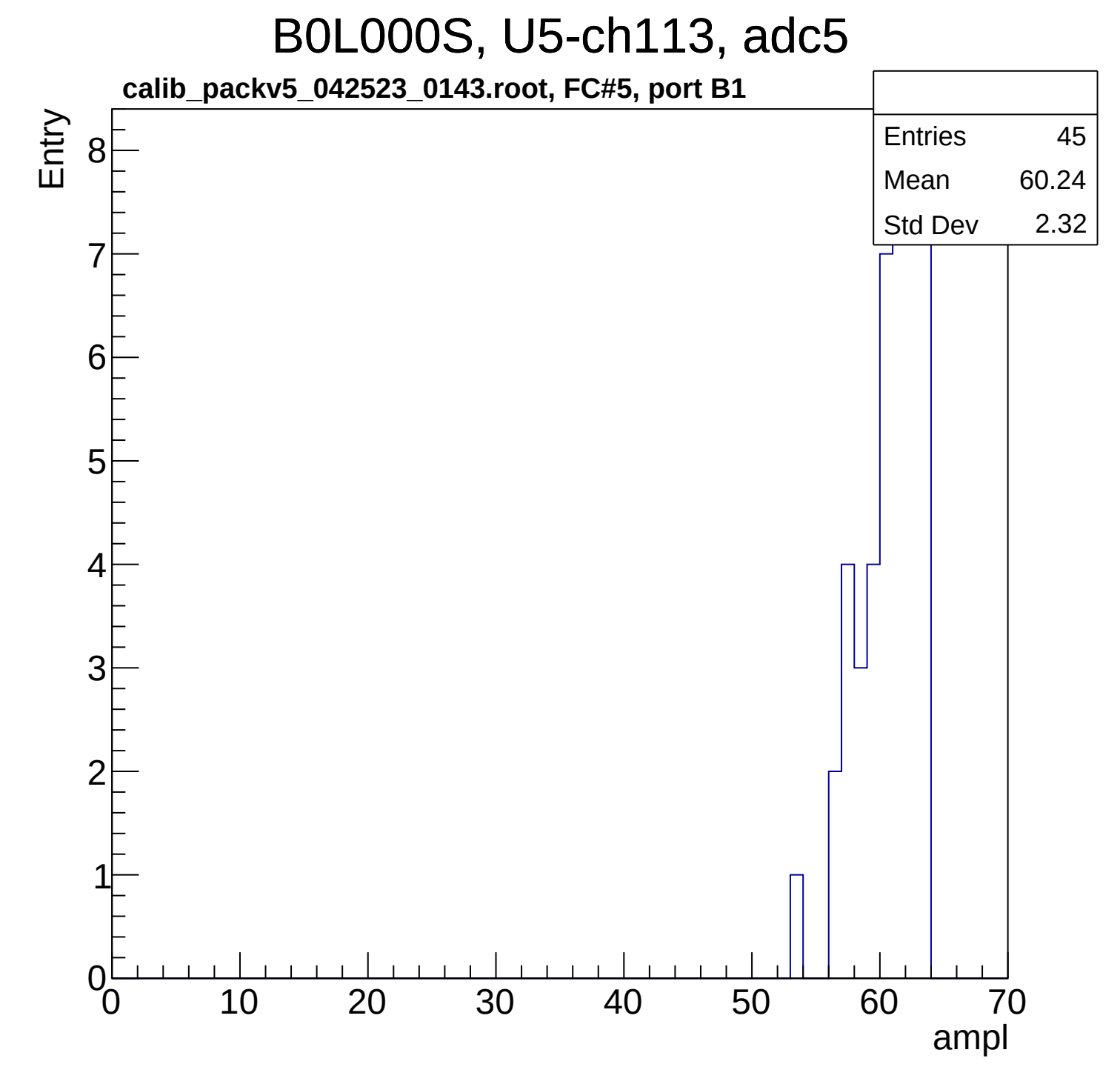
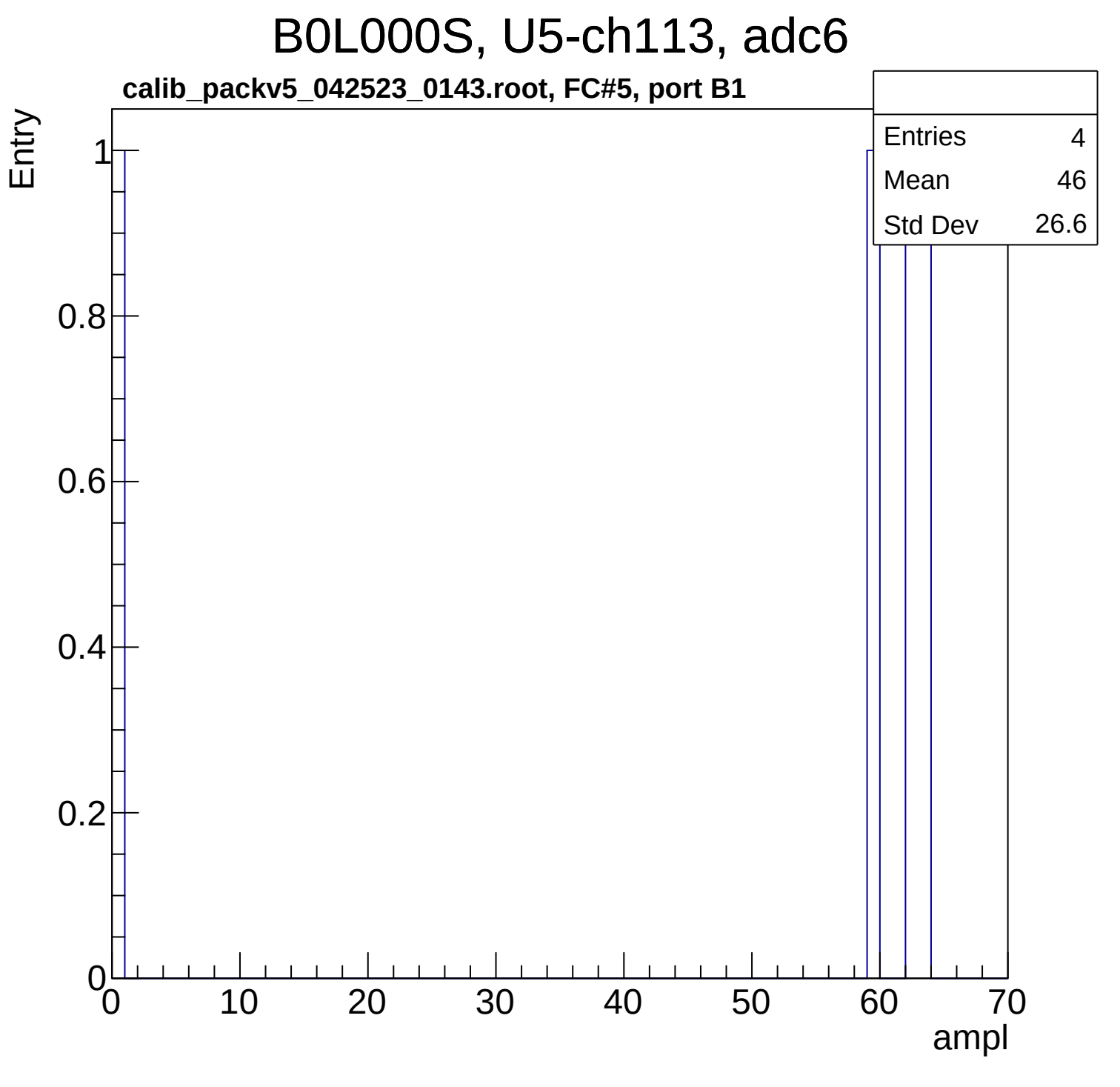
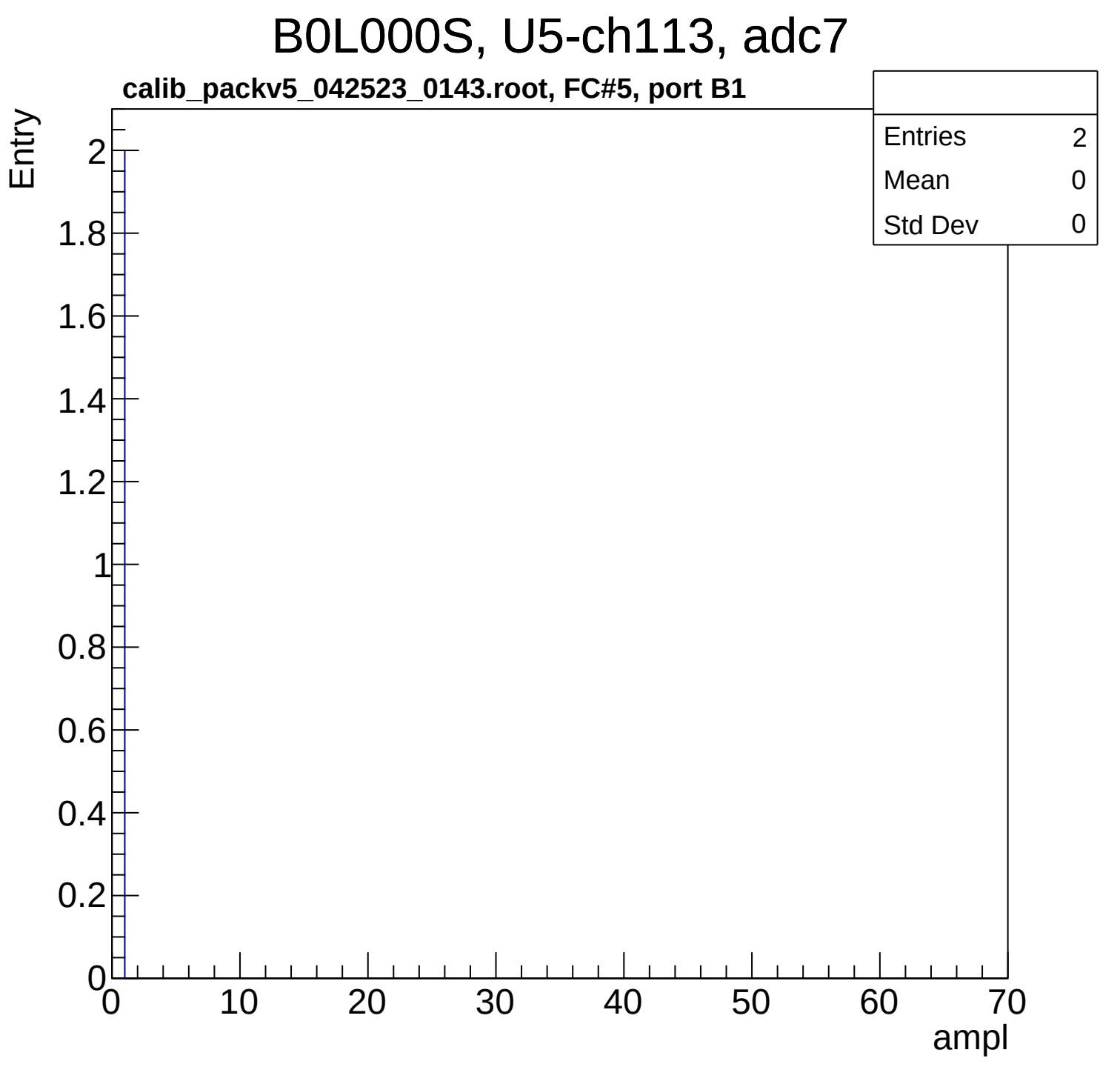
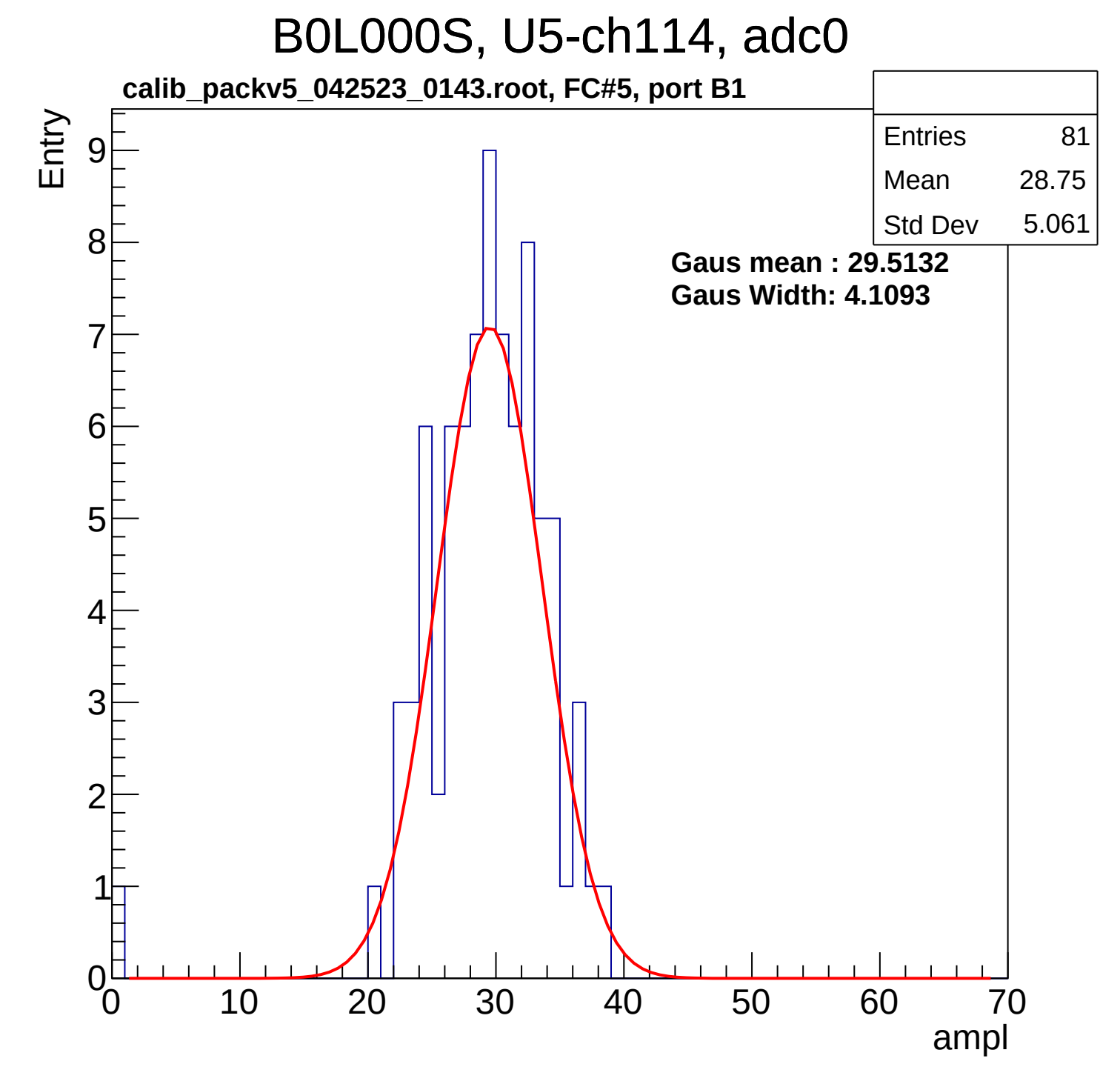

B0L000S, U5-ch113, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 45
Mean 60.24
Std Dev 2.32
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch113, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 46
Std Dev 26.6
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch113, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch114, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 81
Mean 28.75
Std Dev 5.061
Gaus mean : 29.5132
Gaus Width: 4.1093
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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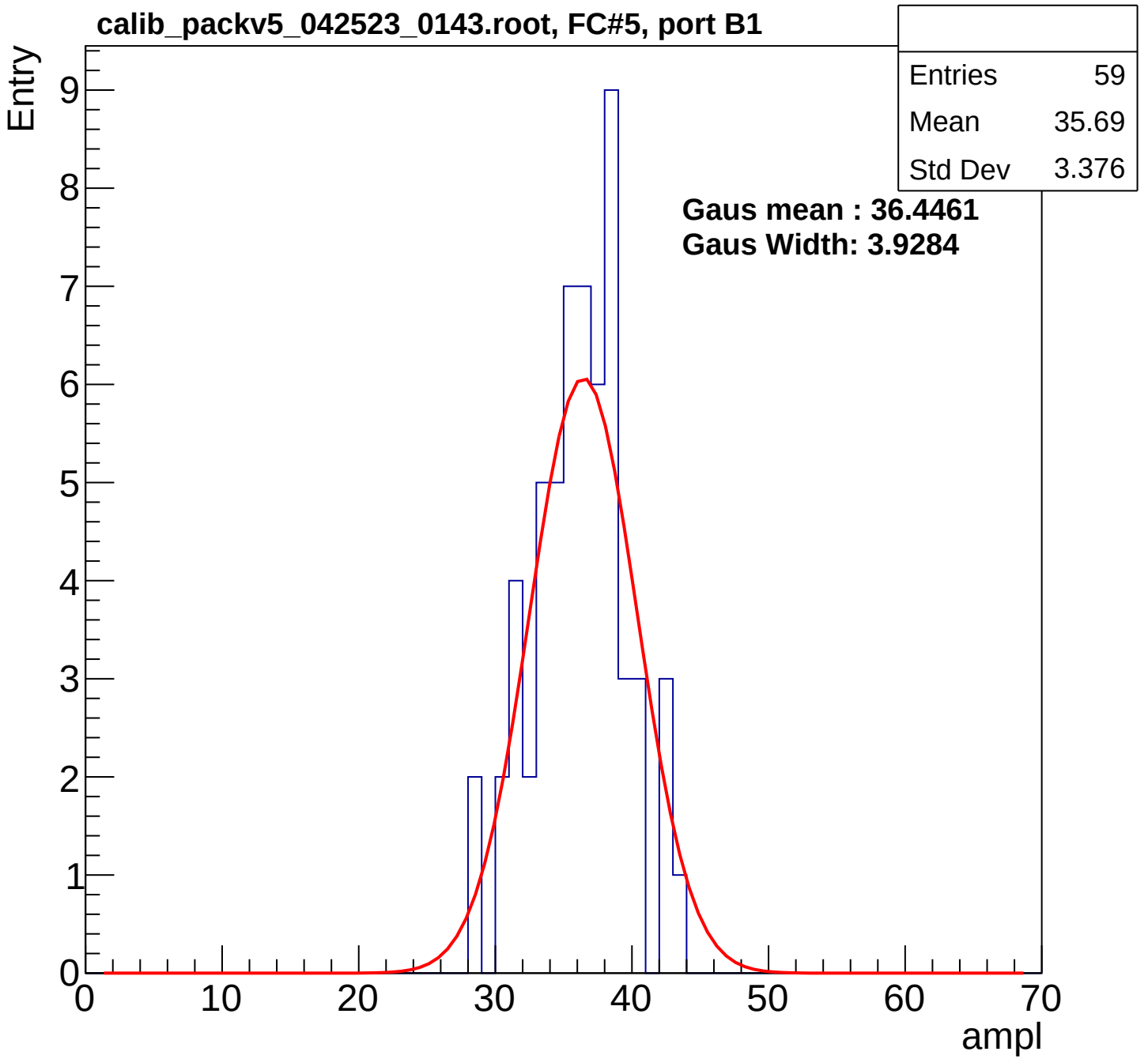
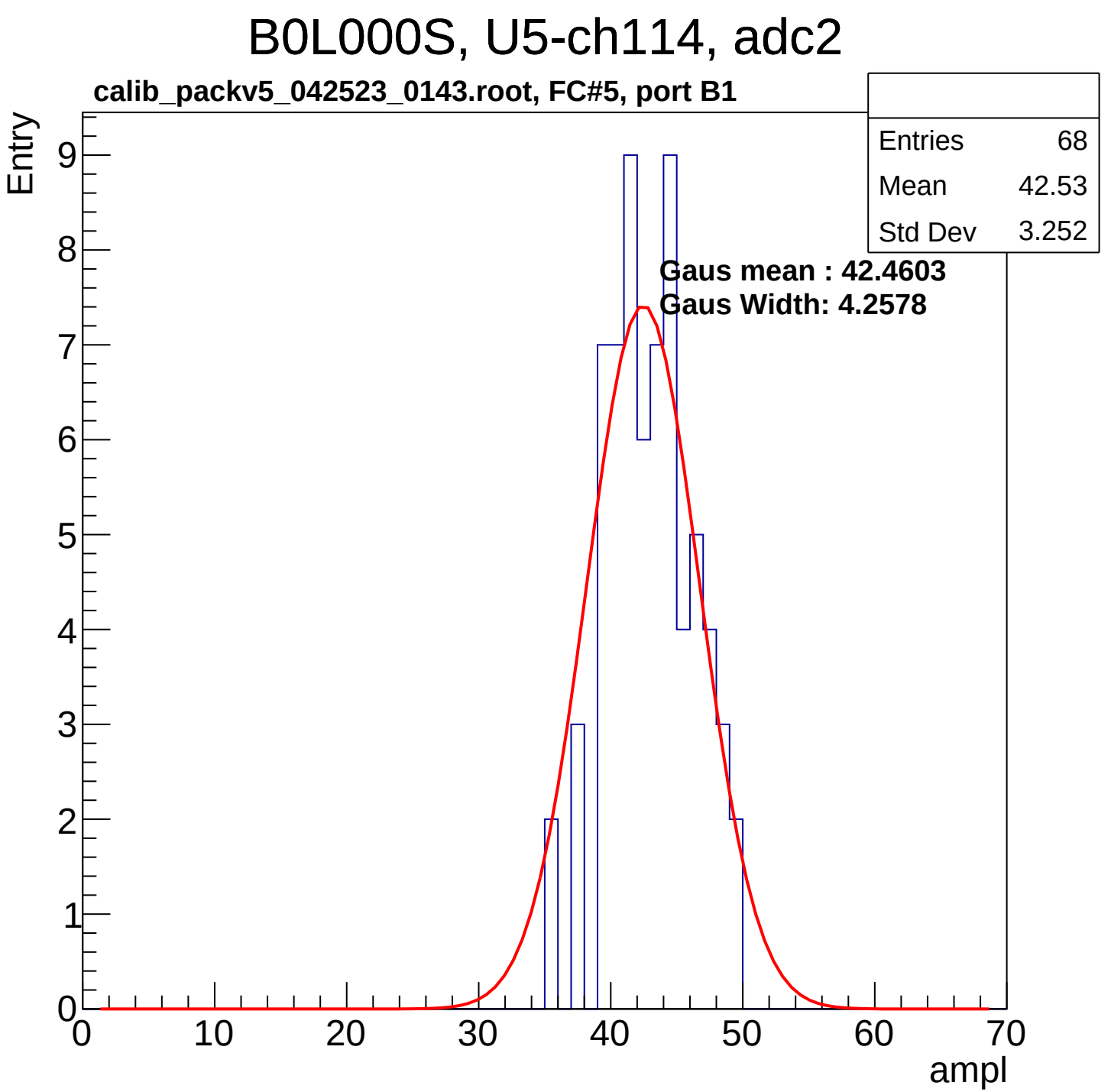
60
70


B0L000S, U5-ch114, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 35.69
Std Dev 3.376
Gaus mean : 36.4461
Gaus Width: 3.9284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch114, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 42.53
Std Dev 3.252
Gaus mean : 42.4603
Gaus Width: 4.2578
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
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60
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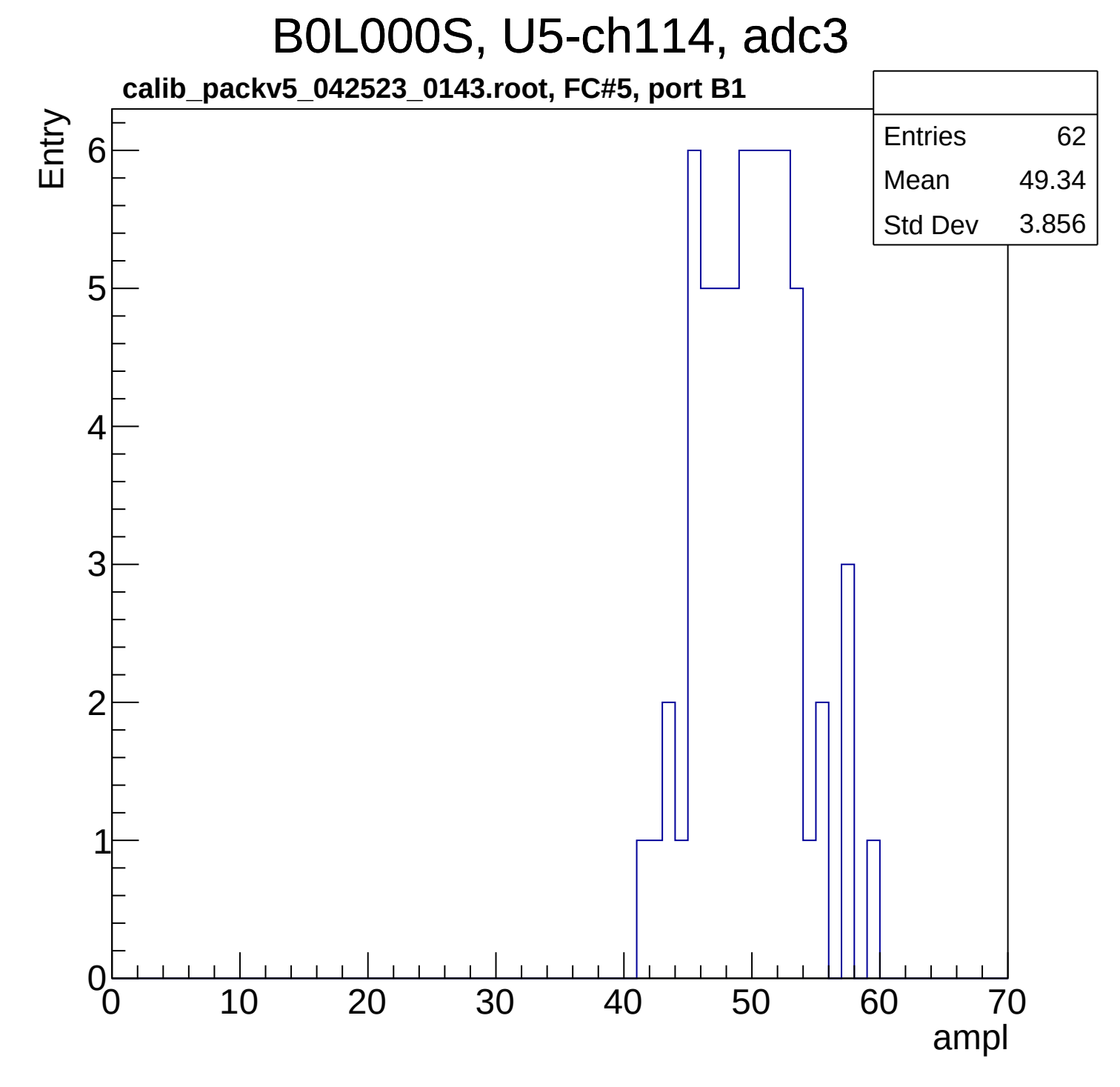
B0L000S, U5-ch114, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 49.34
Std Dev 3.856
Entry
ampl
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1
2
3
4
5
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0
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20
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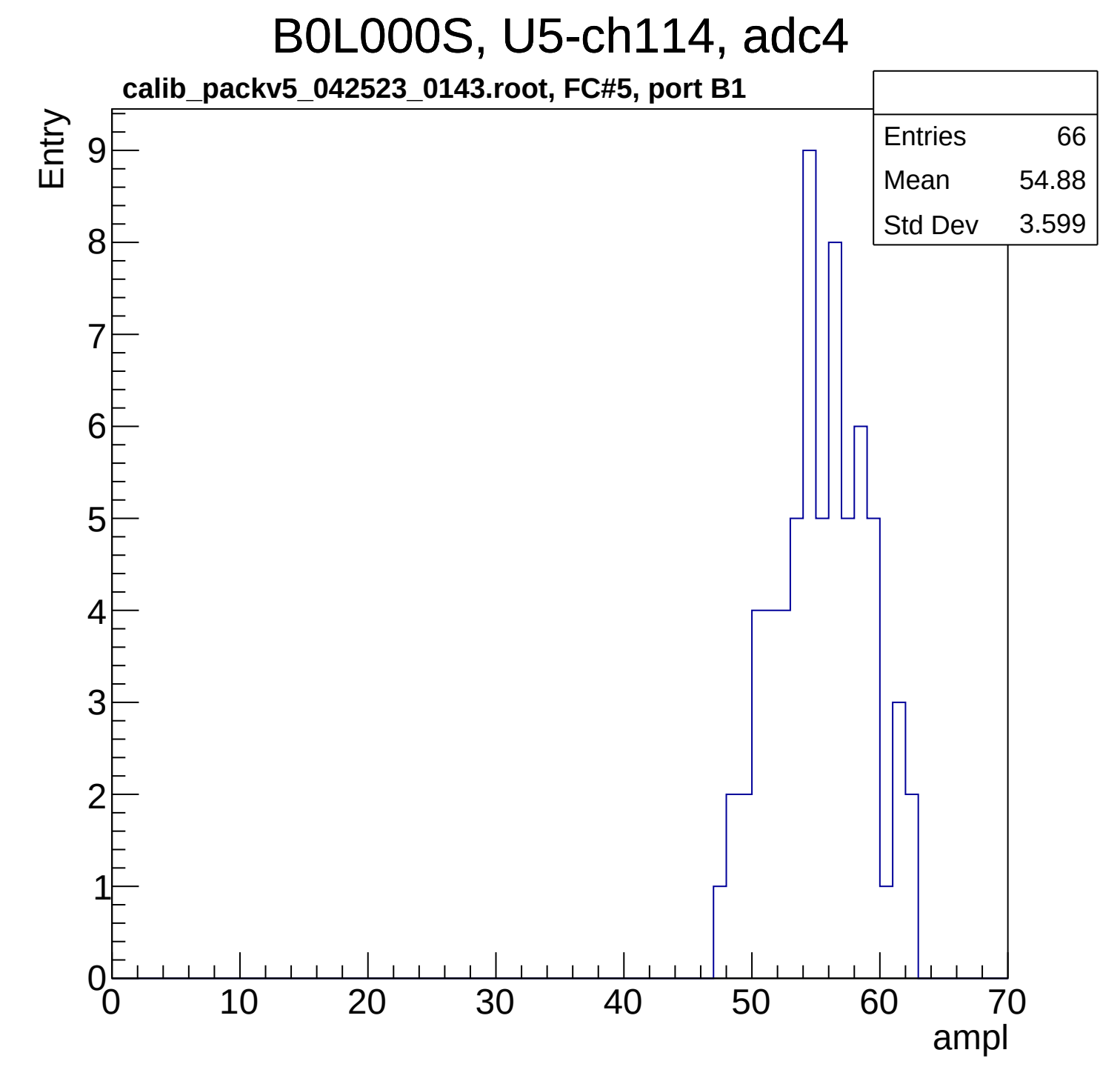
40
50
60
70


B0L000S, U5-ch114, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 54.88
Std Dev 3.599
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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60
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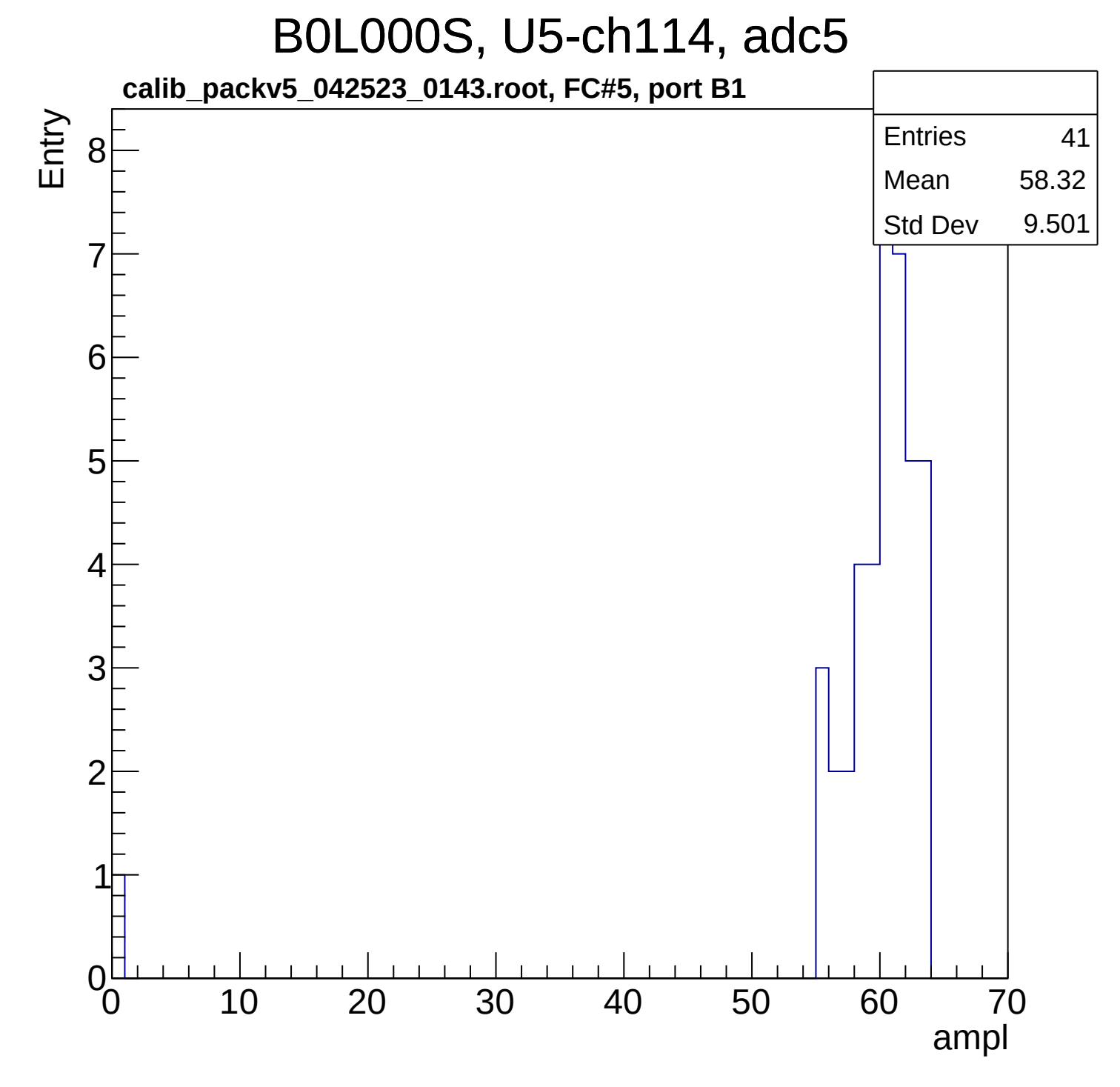
B0L000S, U5-ch114, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 58.32
Std Dev 9.501
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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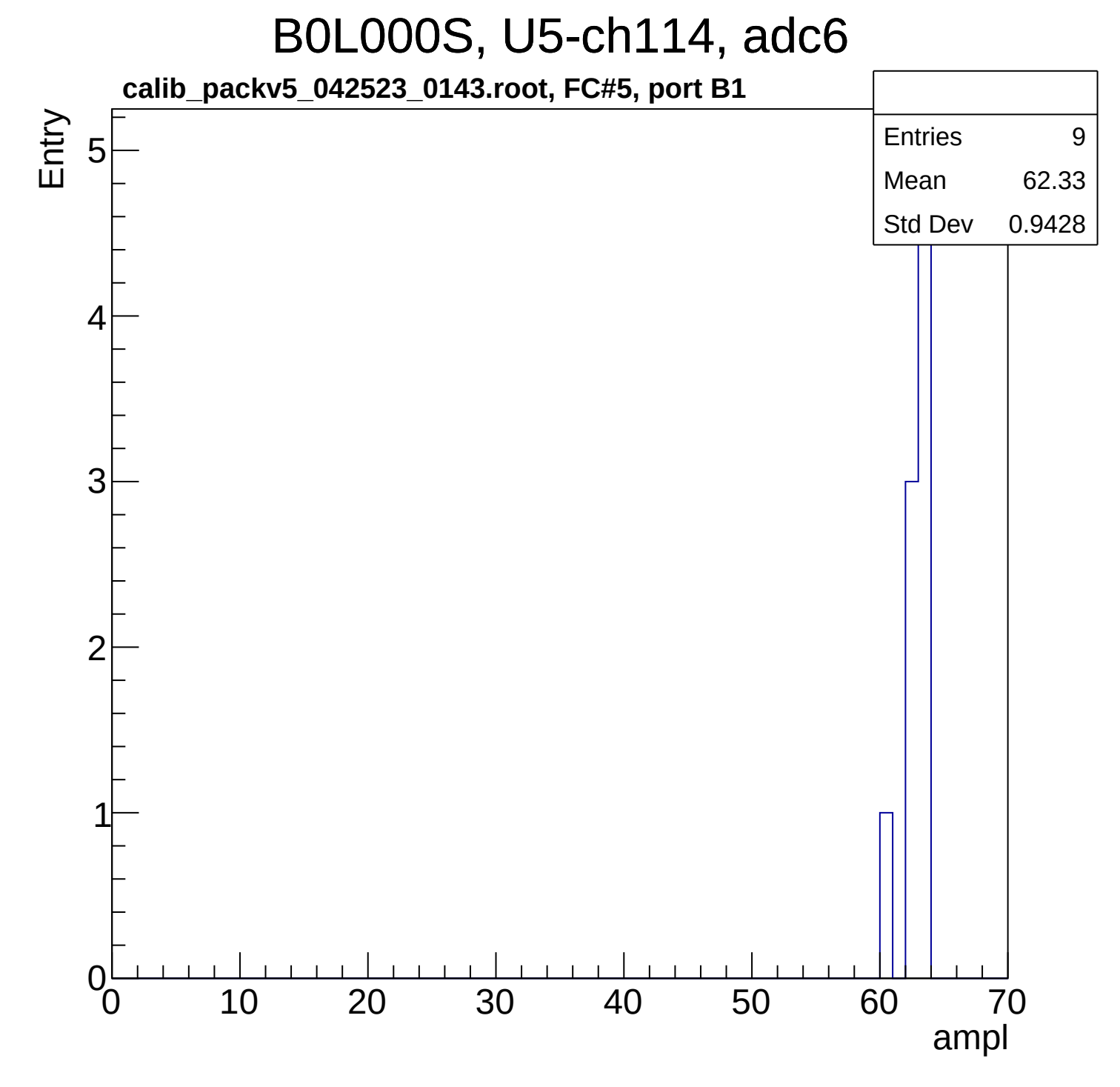
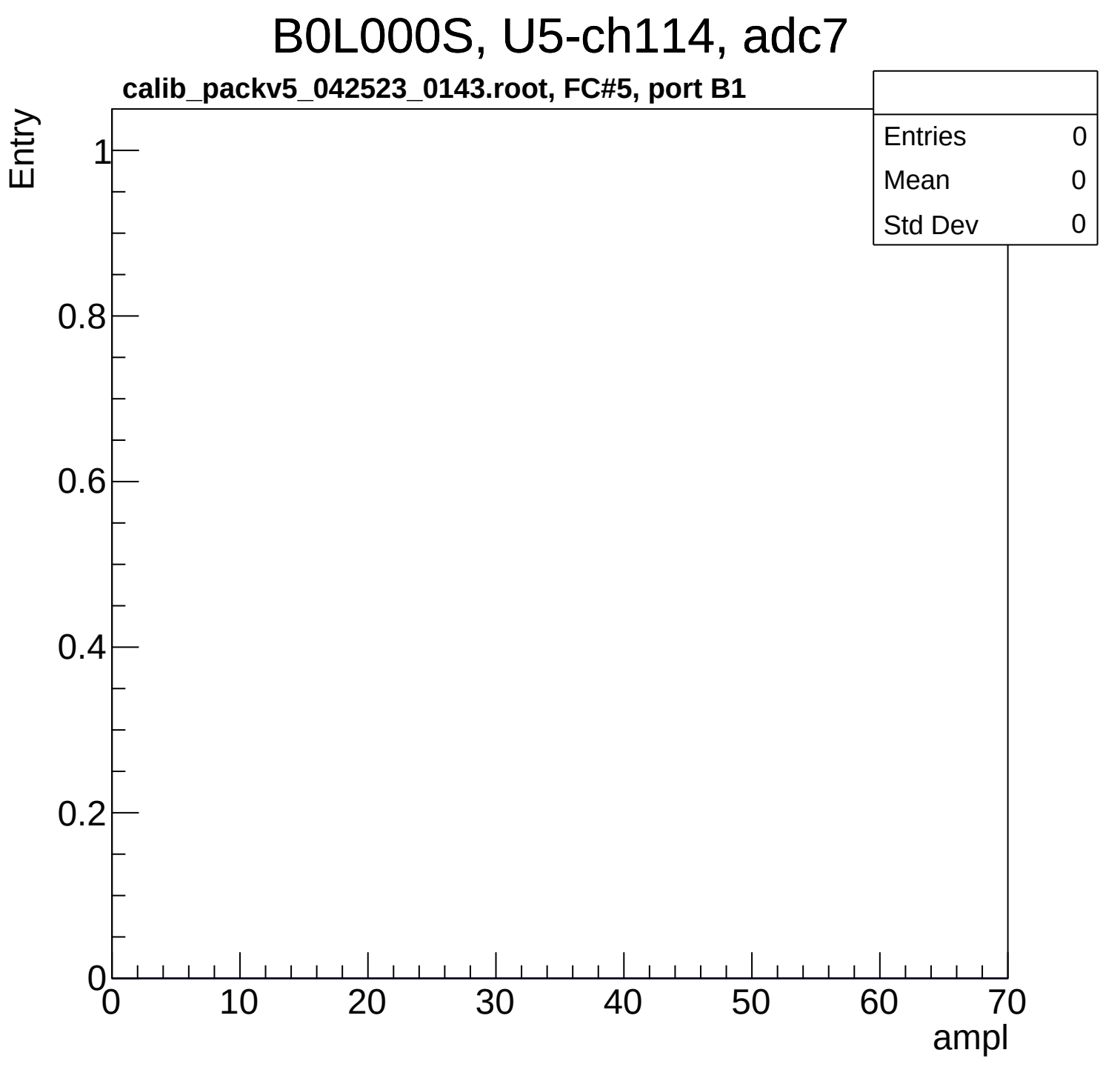

B0L000S, U5-ch114, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L000S, U5-ch114, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
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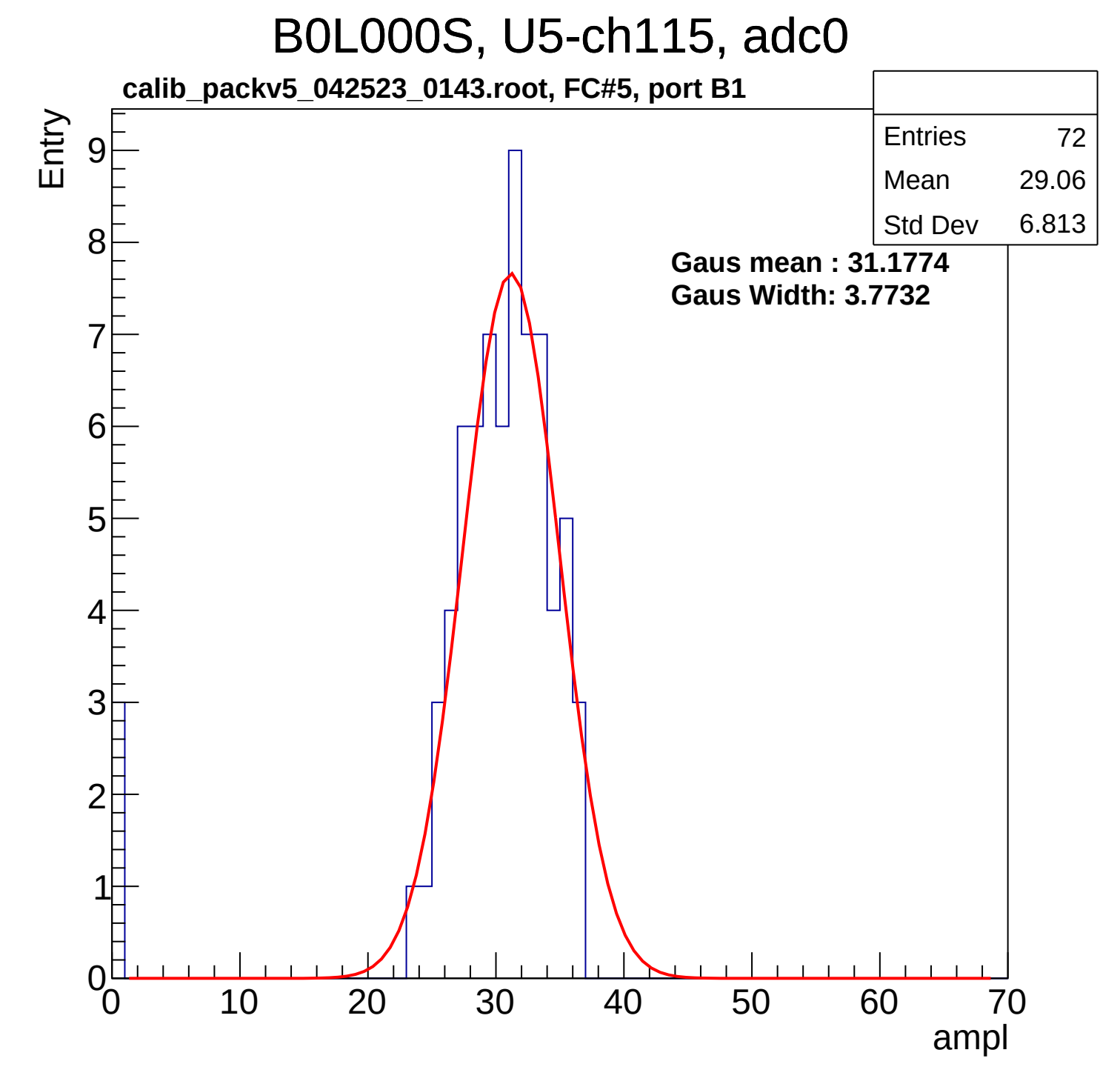
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch115, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 29.06
Std Dev 6.813
Gaus mean : 31.1774
Gaus Width: 3.7732
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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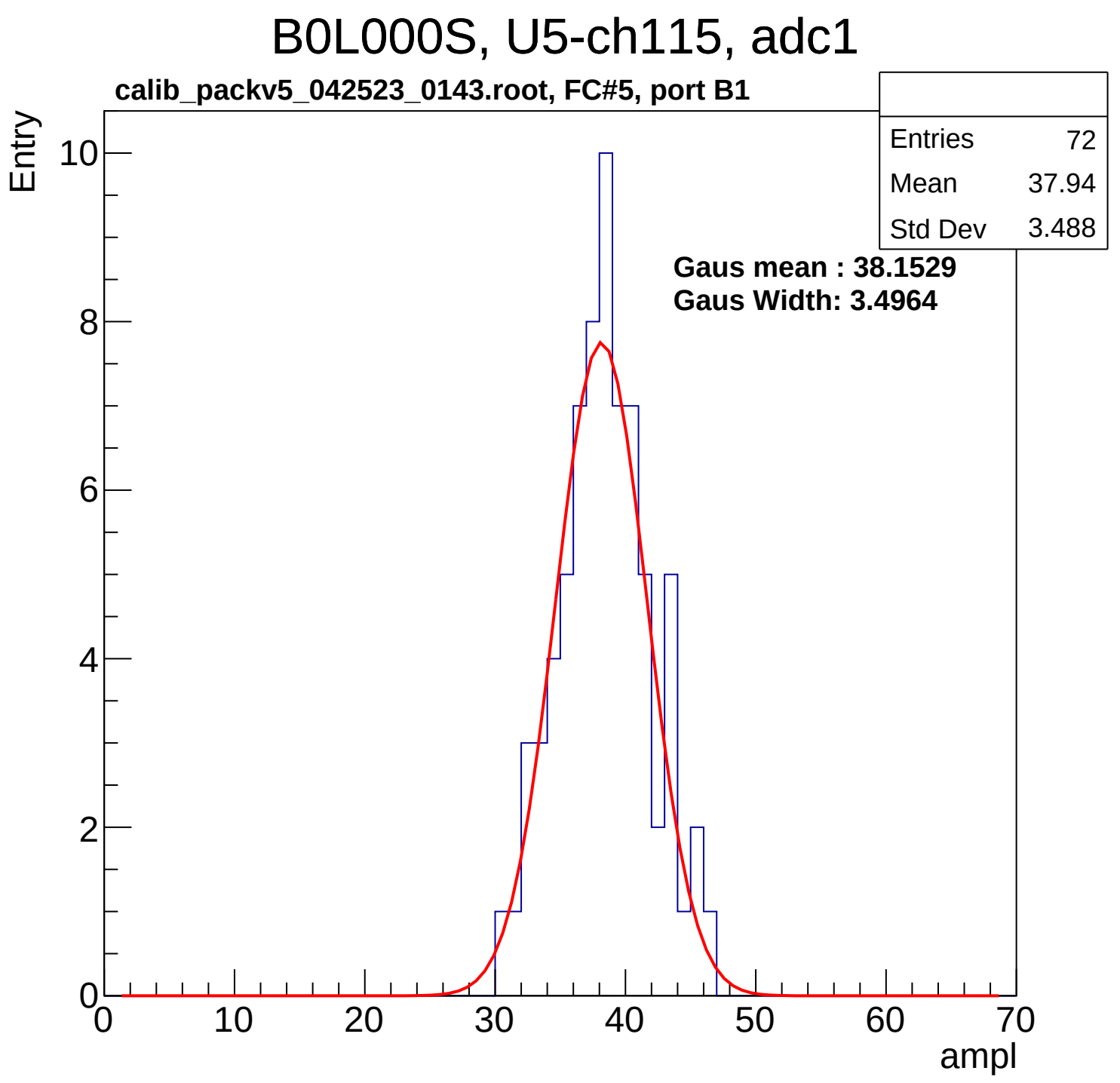
B0L000S, U5-ch115, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 72
Mean 37.94
Std Dev 3.488
Gaus mean : 38.1529
Gaus Width: 3.4964
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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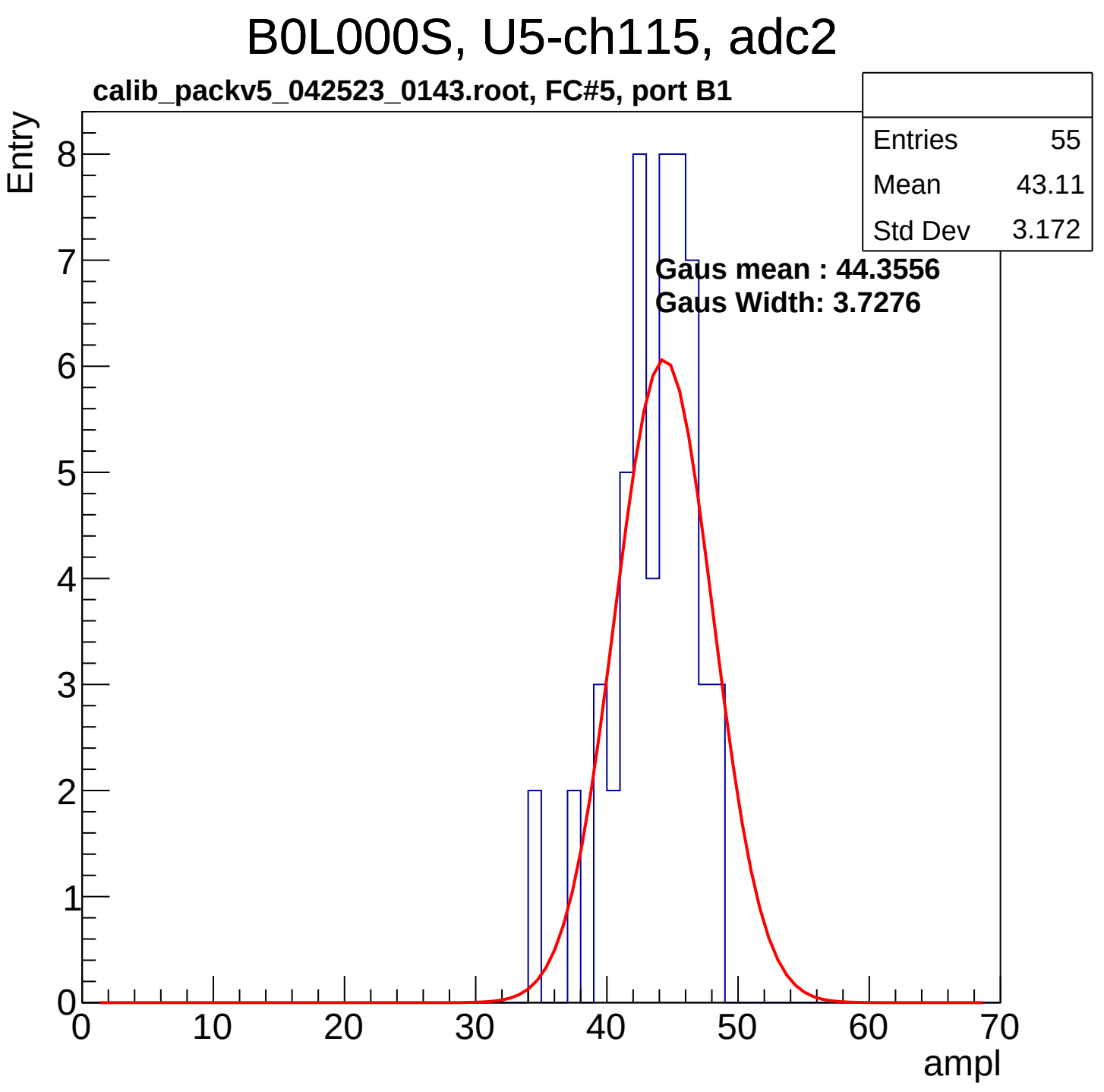
B0L000S, U5-ch115, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 43.11
Std Dev 3.172
Gaus mean : 44.3556
Gaus Width: 3.7276
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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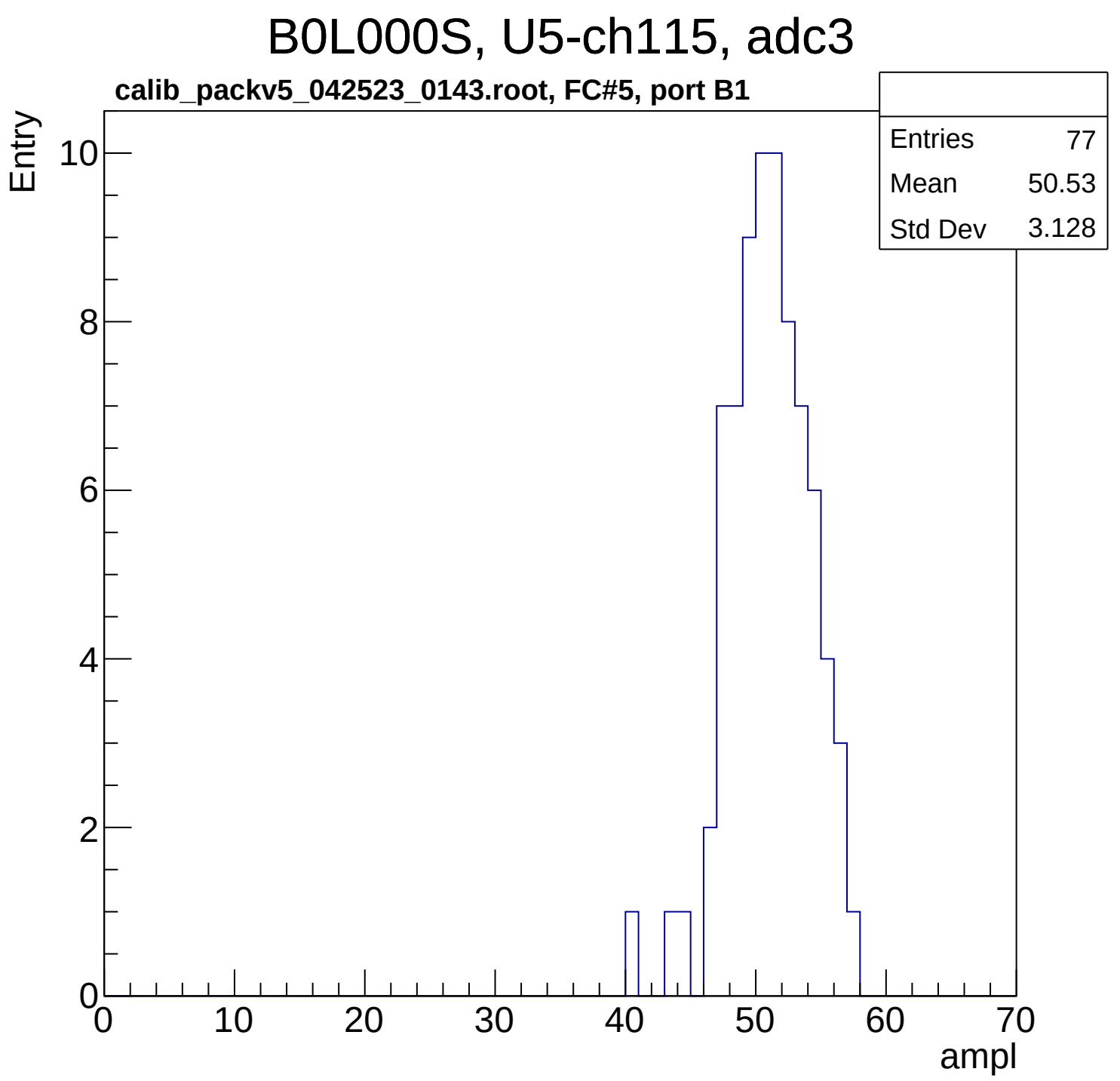
B0L000S, U5-ch115, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 50.53
Std Dev 3.128
Entry
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8
10
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10
20
30
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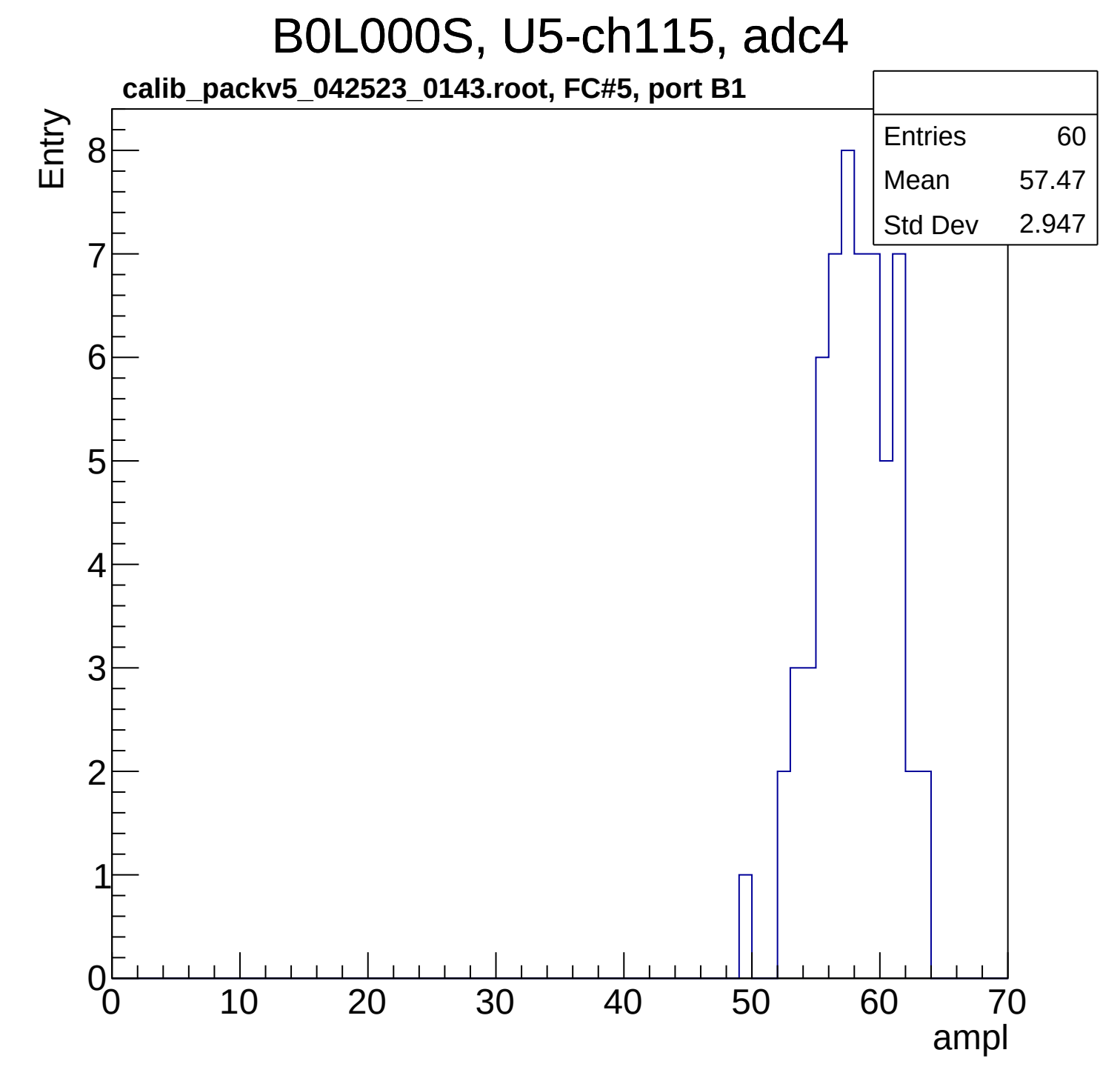
50
60
70
ampl


B0L000S, U5-ch115, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 60
Mean 57.47
Std Dev 2.947
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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60
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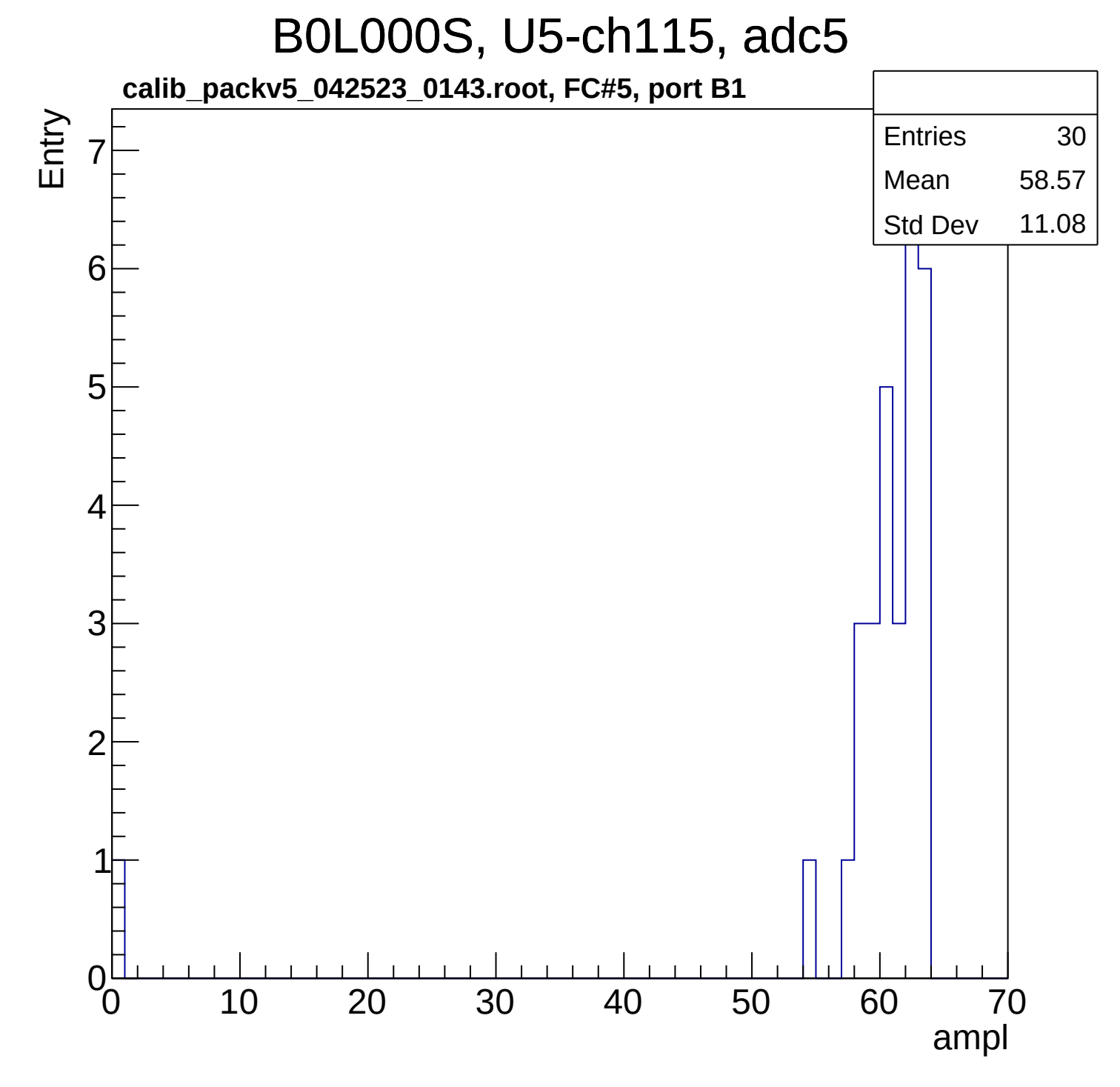
B0L000S, U5-ch115, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 58.57
Std Dev 11.08
Entry
ampl
0
1
2
3
4
5
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0
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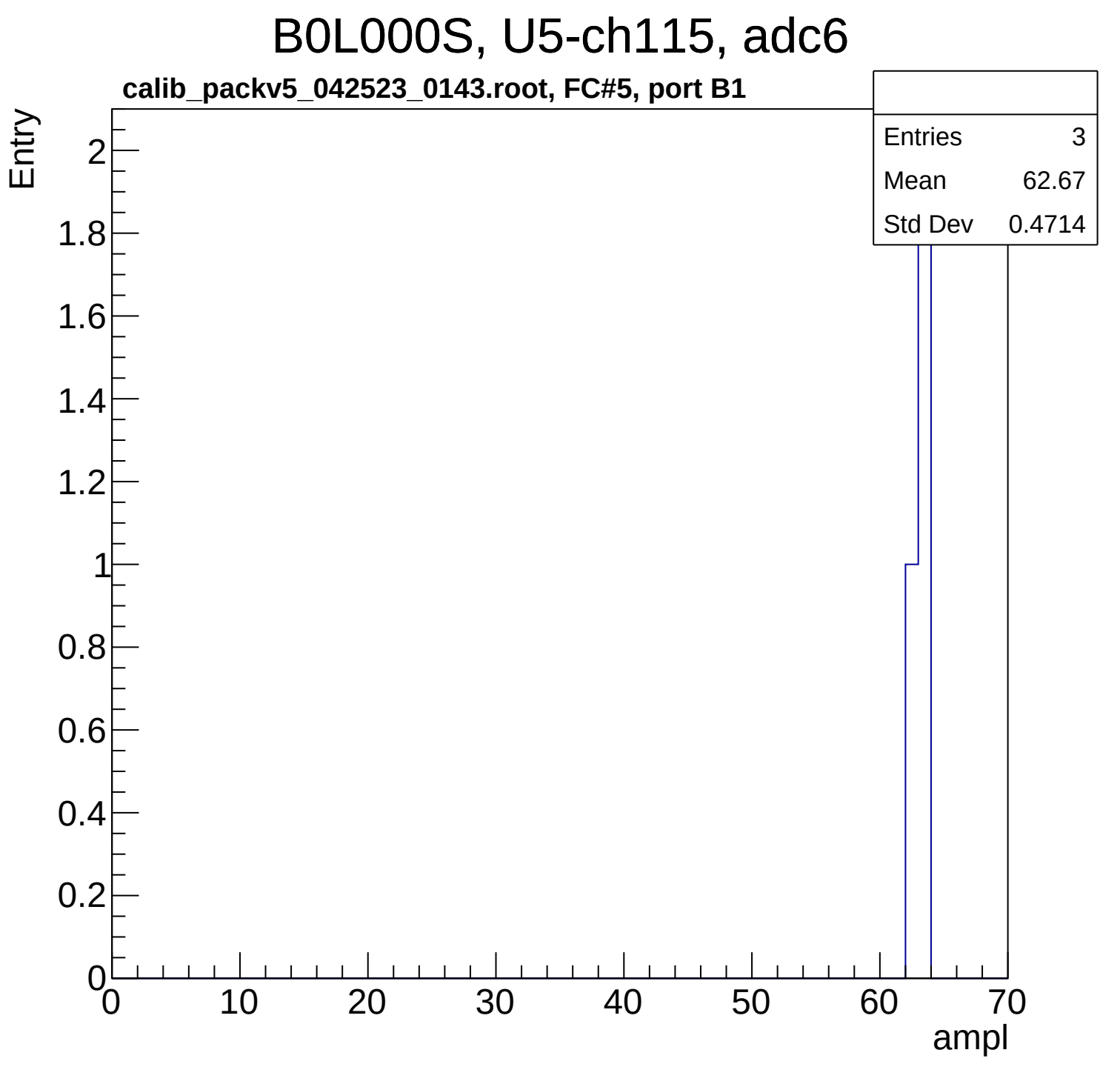
B0L000S, U5-ch115, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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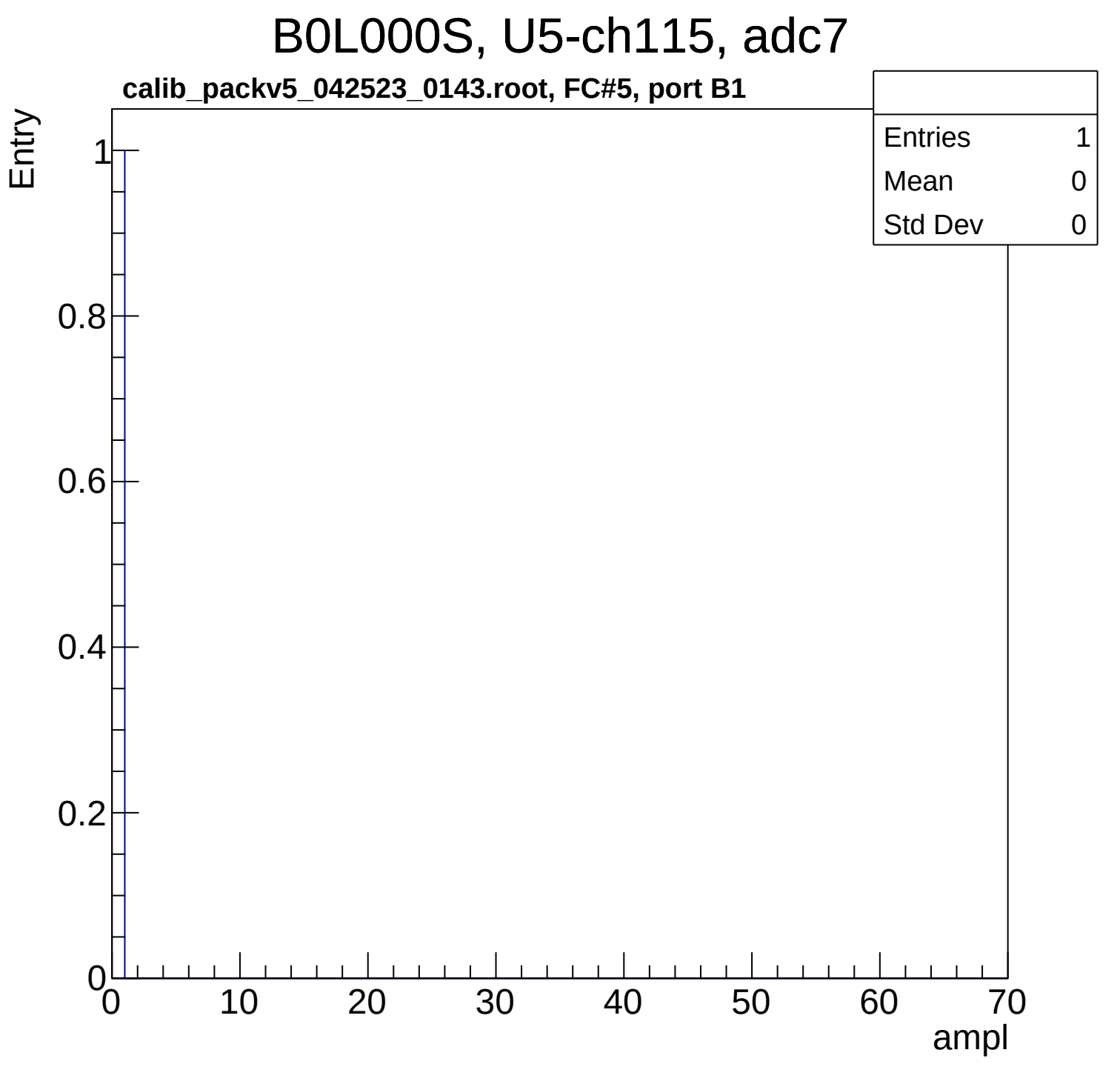
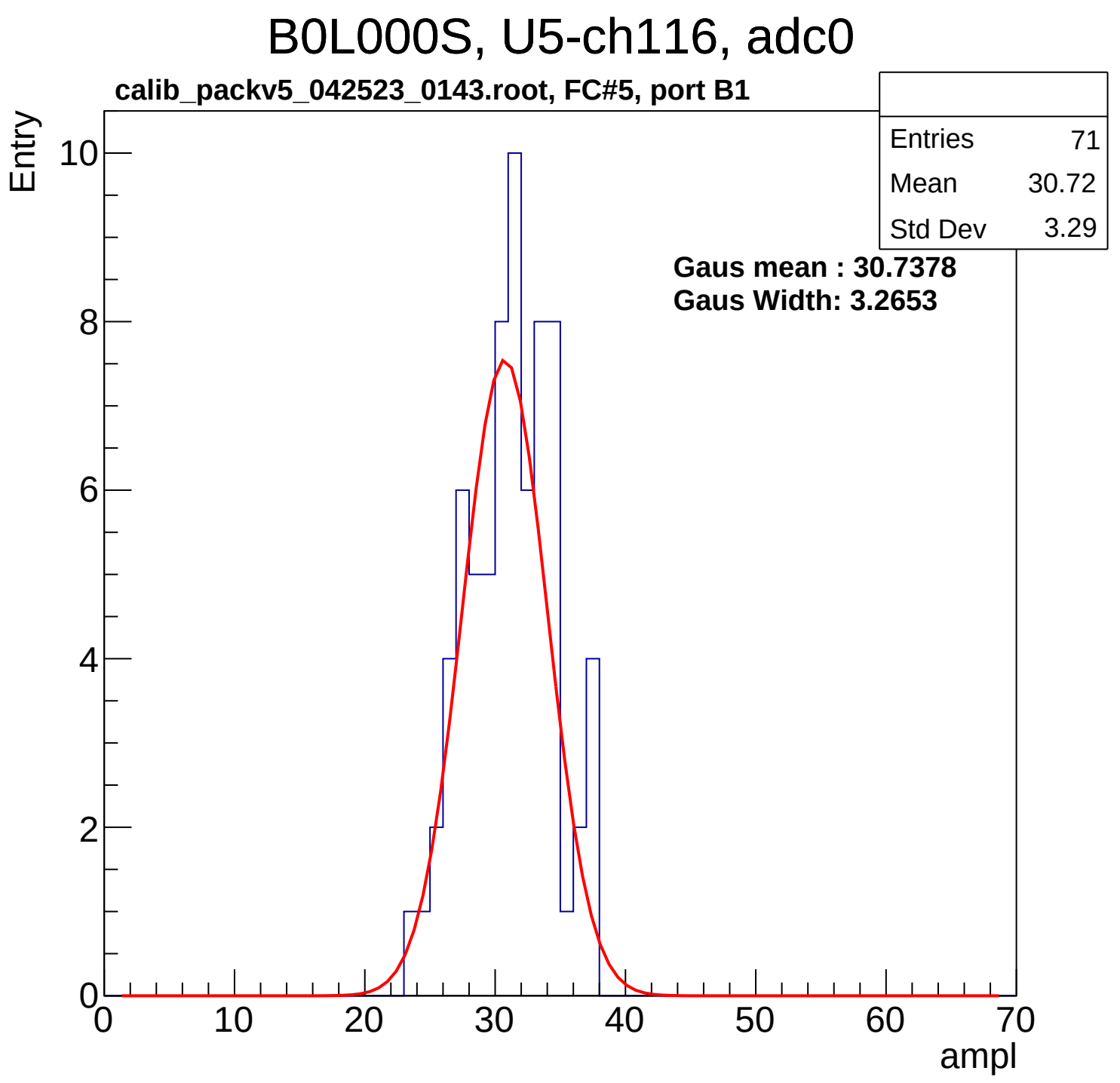

B0L000S, U5-ch115, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch116, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.72
Std Dev 3.29
Gaus mean : 30.7378
Gaus Width: 3.2653
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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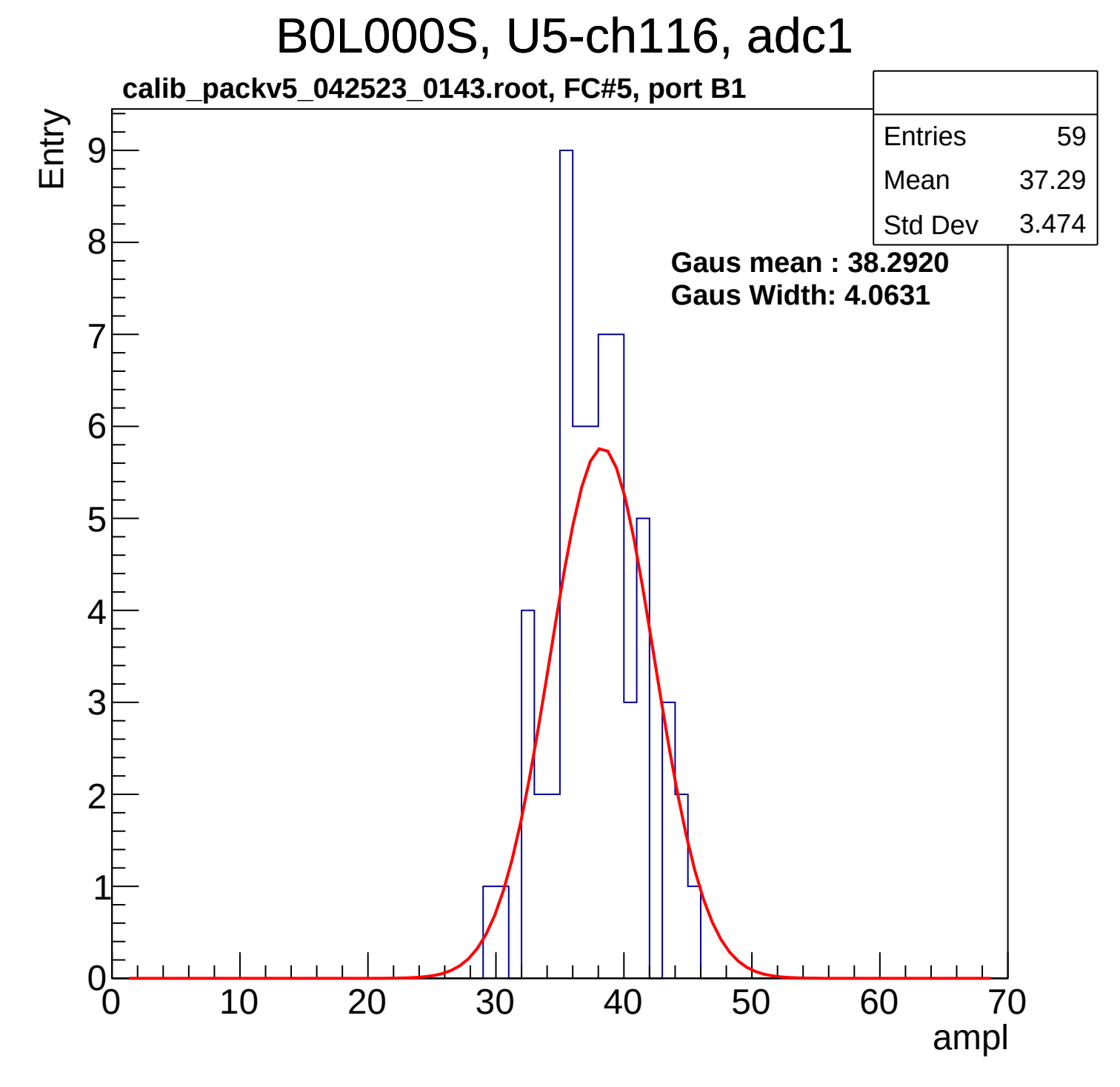
B0L000S, U5-ch116, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 37.29
Std Dev 3.474
Gaus mean : 38.2920
Gaus Width: 4.0631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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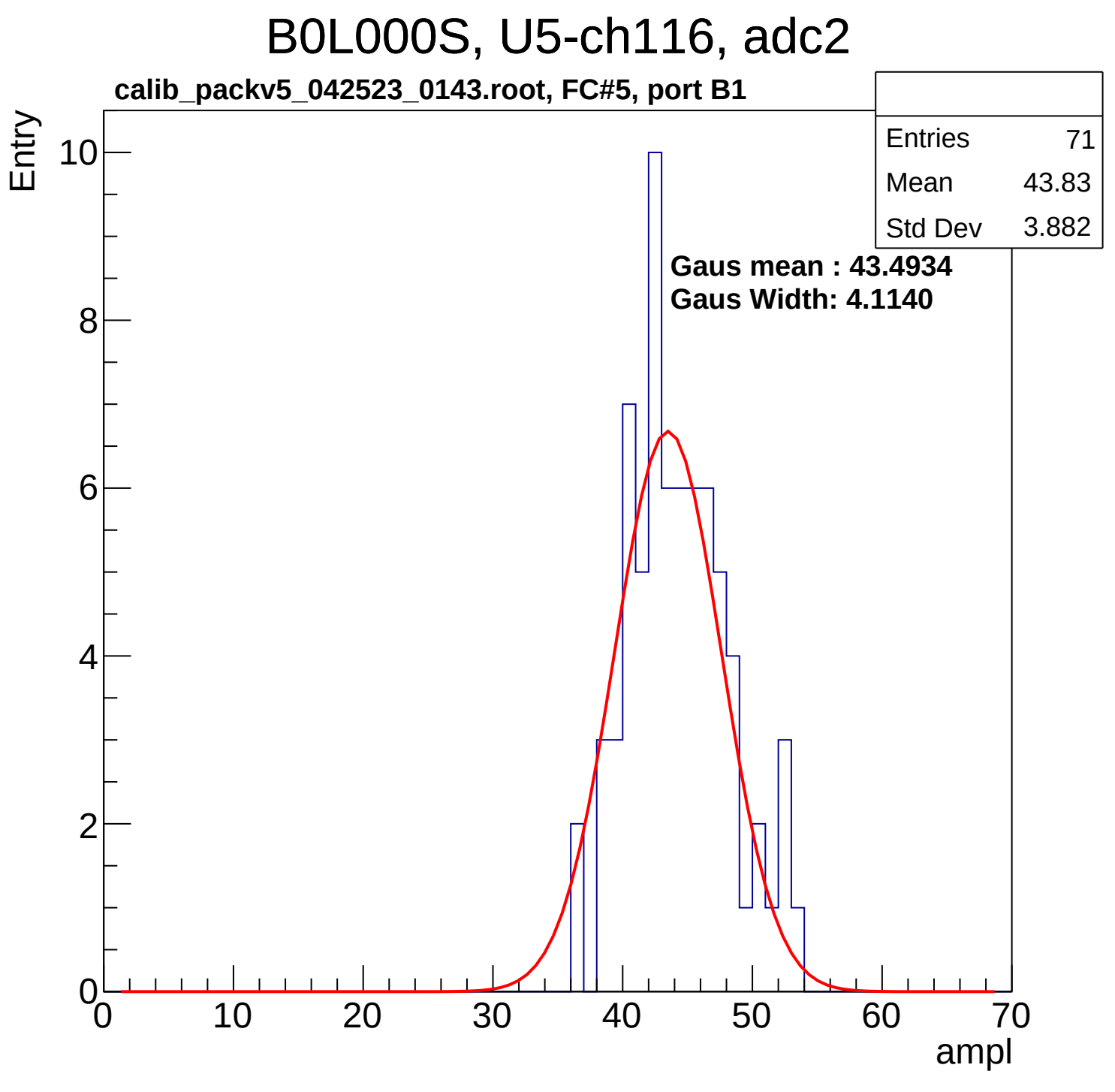
B0L000S, U5-ch116, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 43.83
Std Dev 3.882
Gaus mean : 43.4934
Gaus Width: 4.1140
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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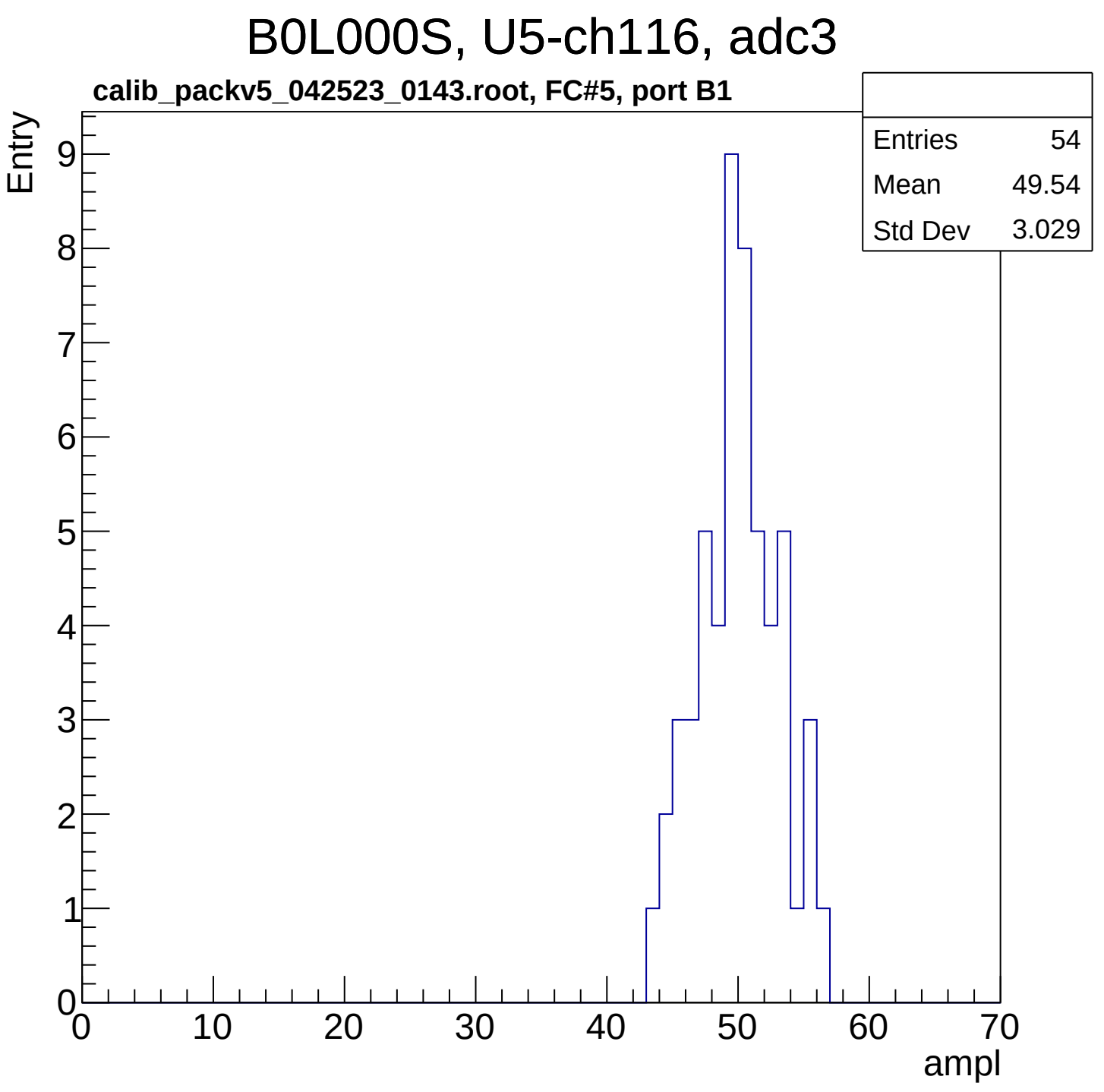
B0L000S, U5-ch116, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 49.54
Std Dev 3.029
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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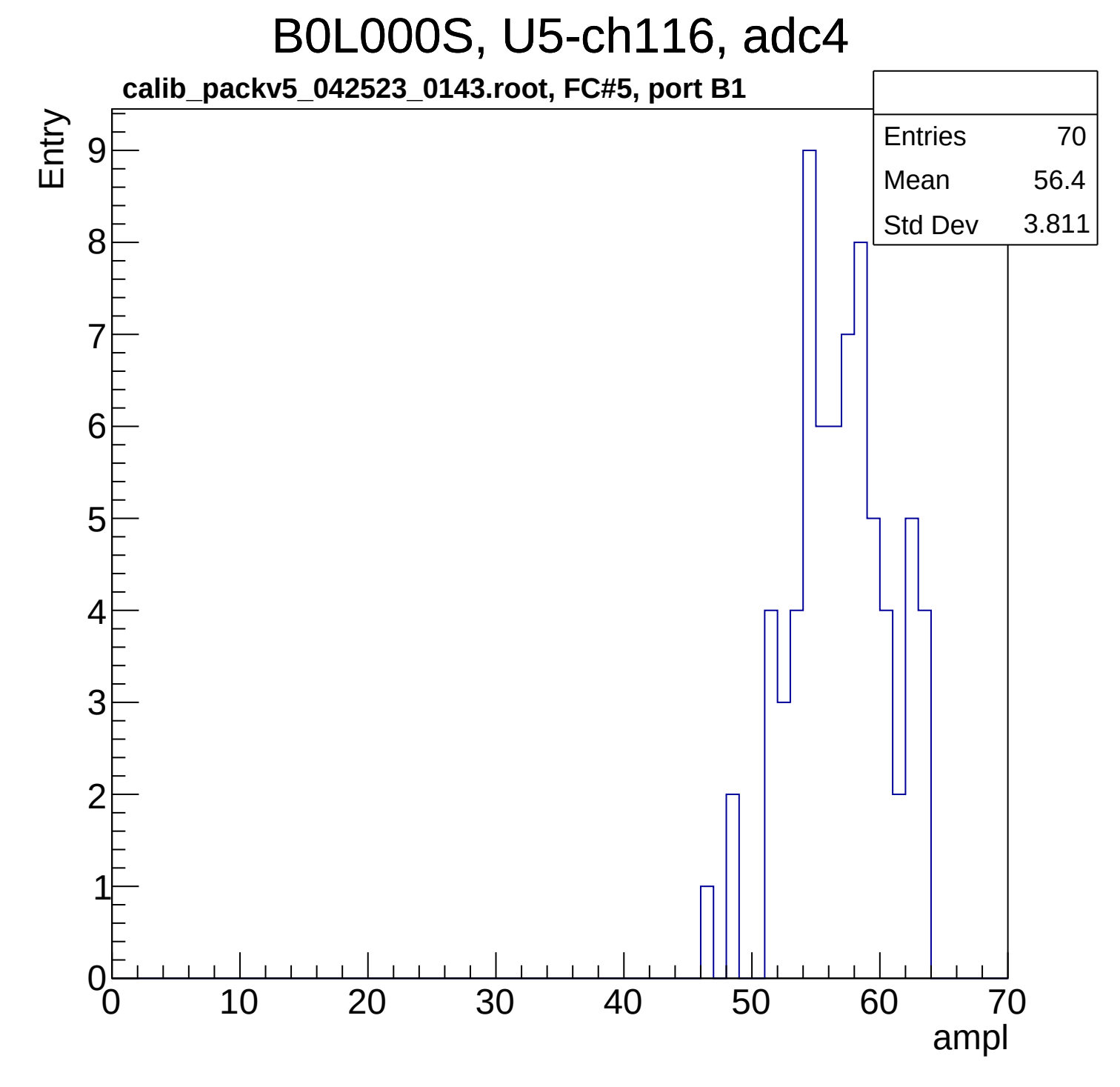
B0L000S, U5-ch116, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 56.4
Std Dev 3.811
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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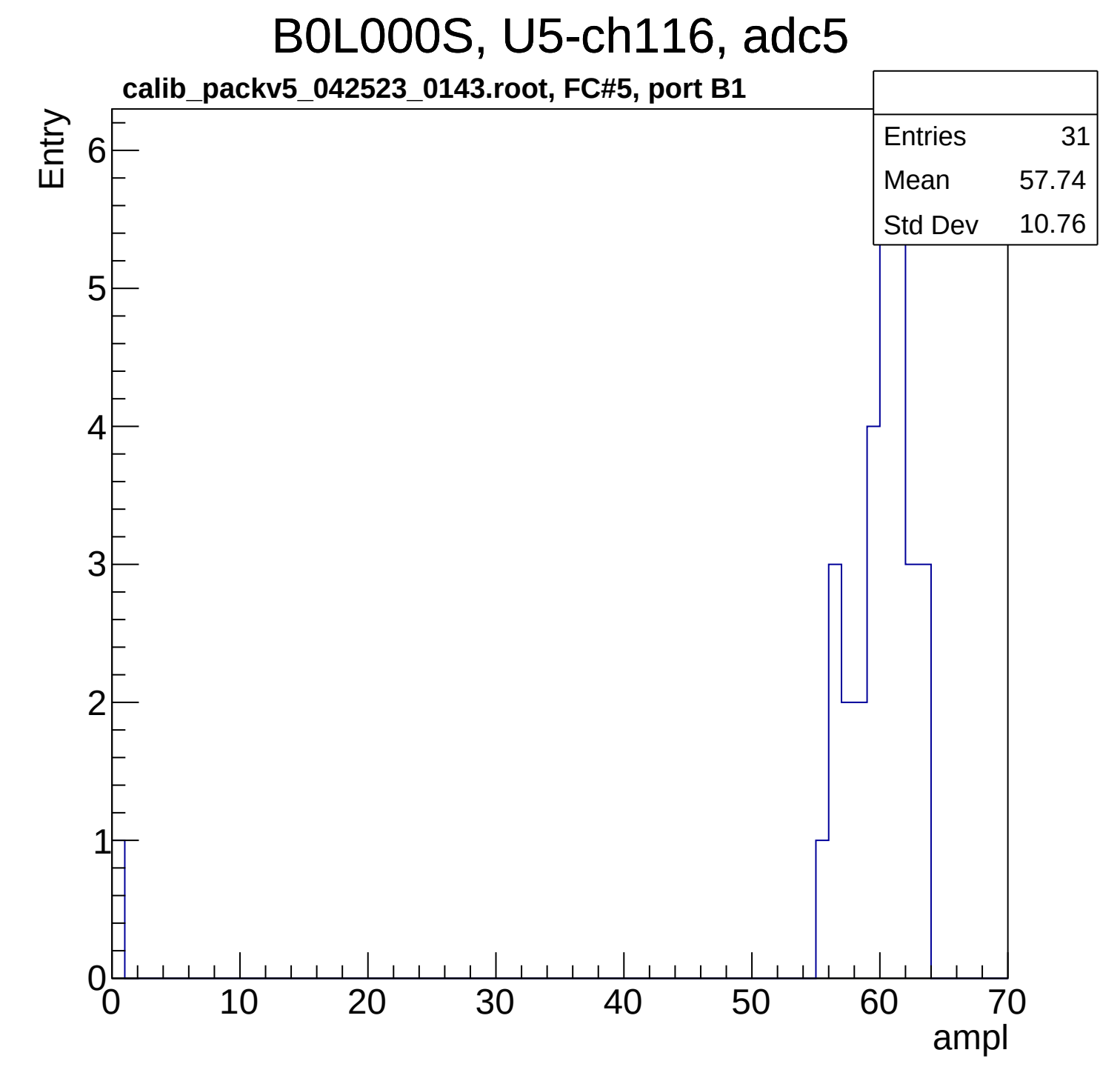
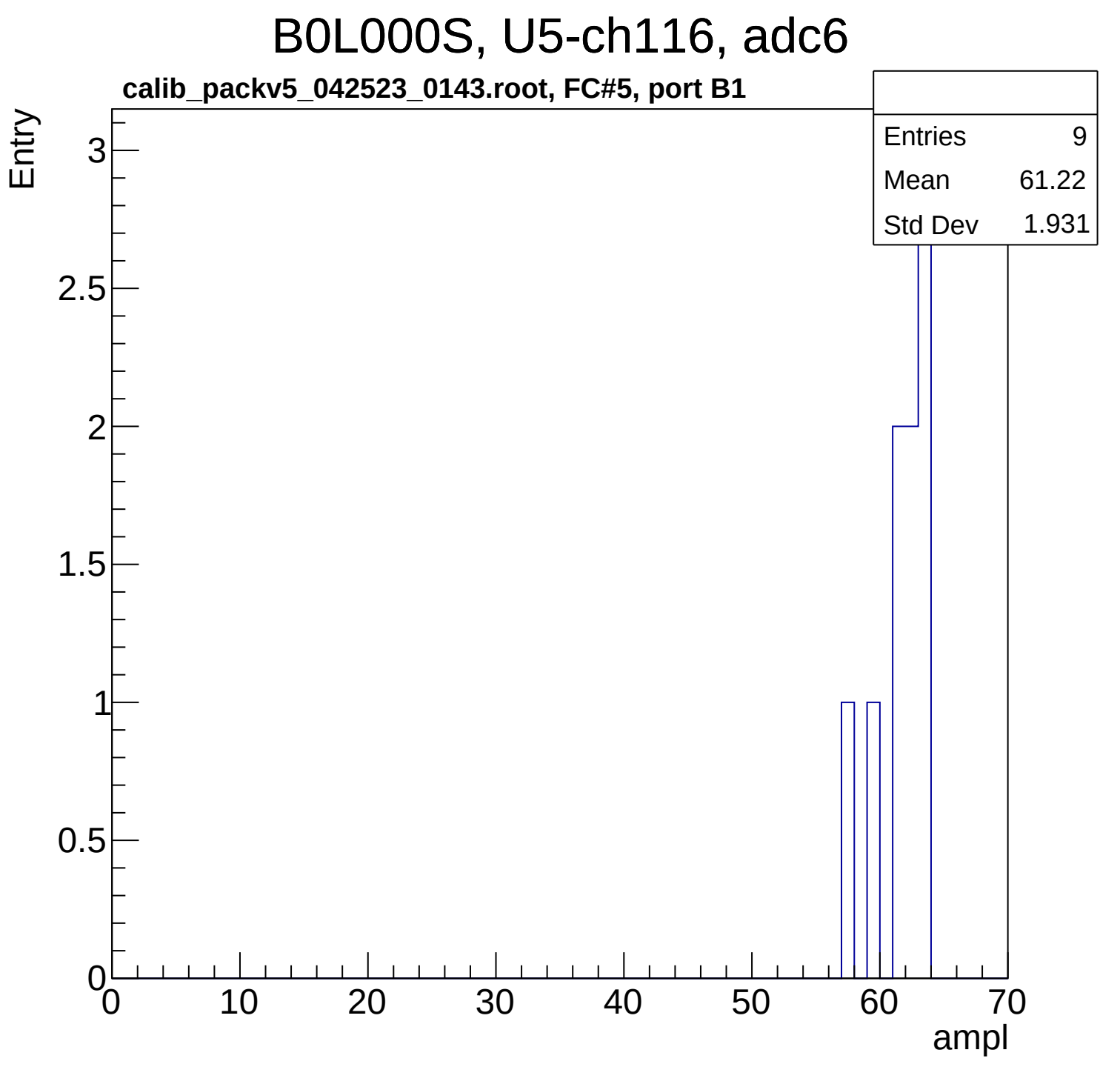
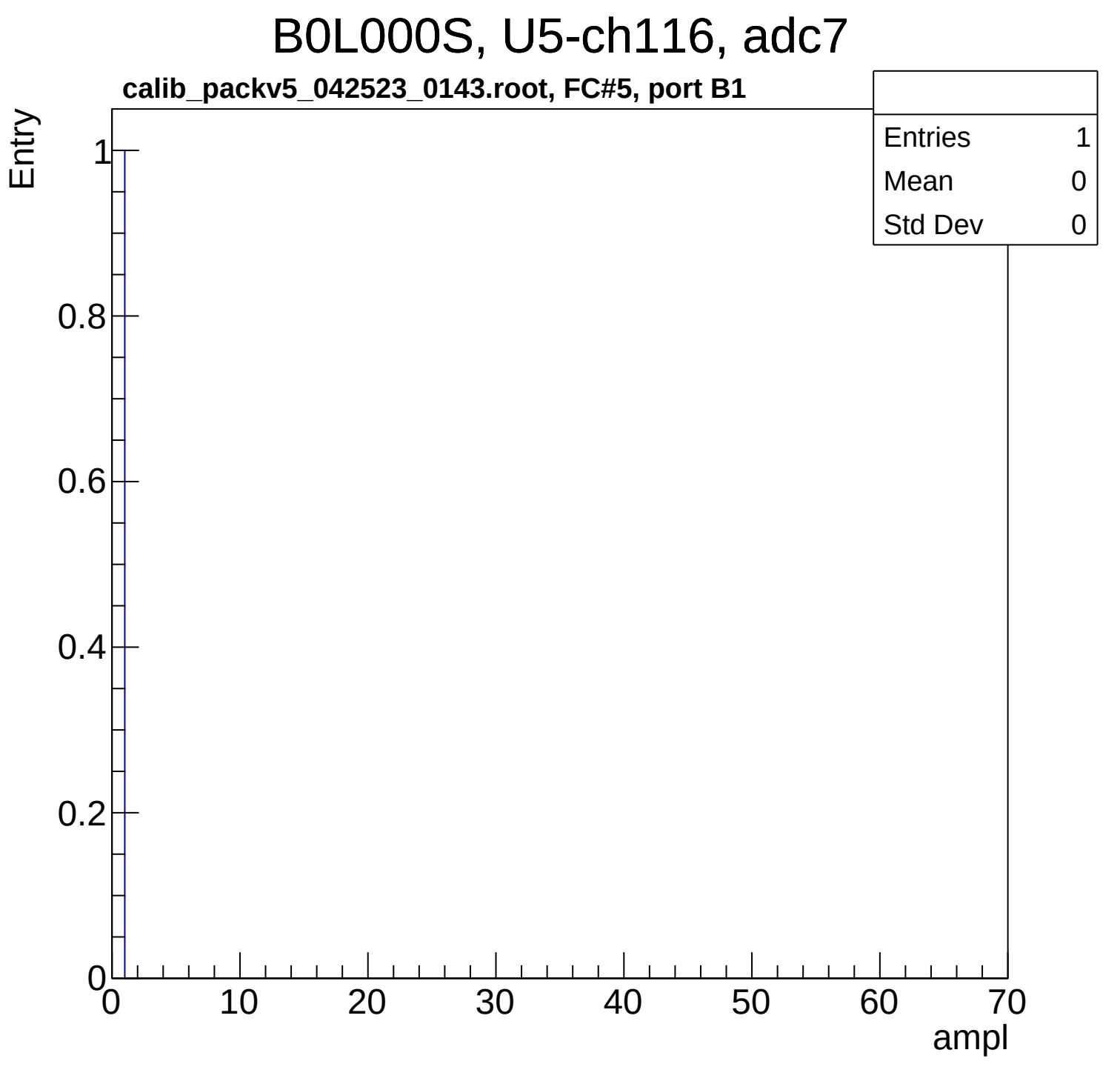
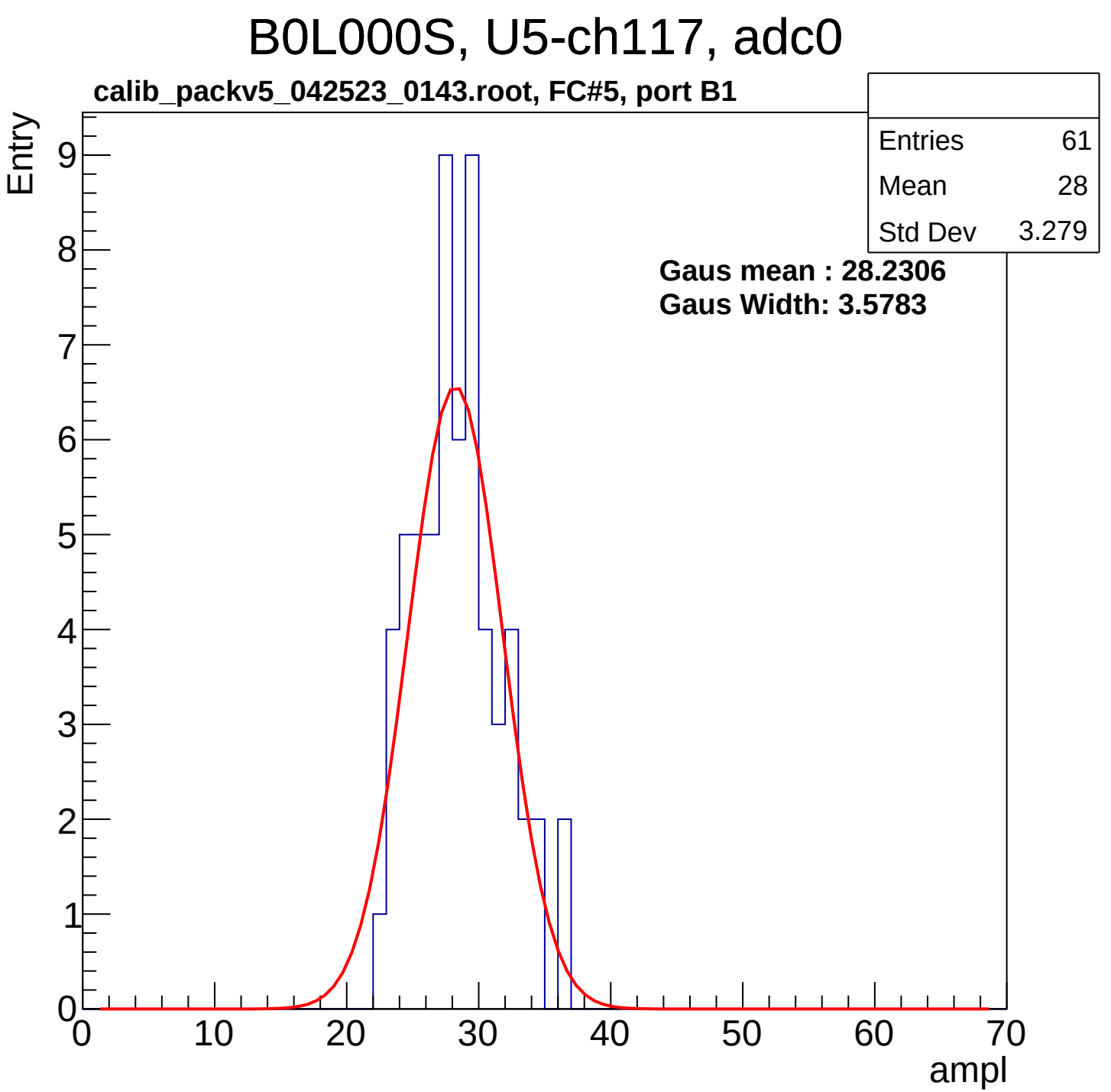

B0L000S, U5-ch116, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 31
Mean 57.74
Std Dev 10.76
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch116, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 9
Mean 61.22
Std Dev 1.931
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch116, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch117, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 28
Std Dev 3.279
Gaus mean : 28.2306
Gaus Width: 3.5783
Entry
ampl
0
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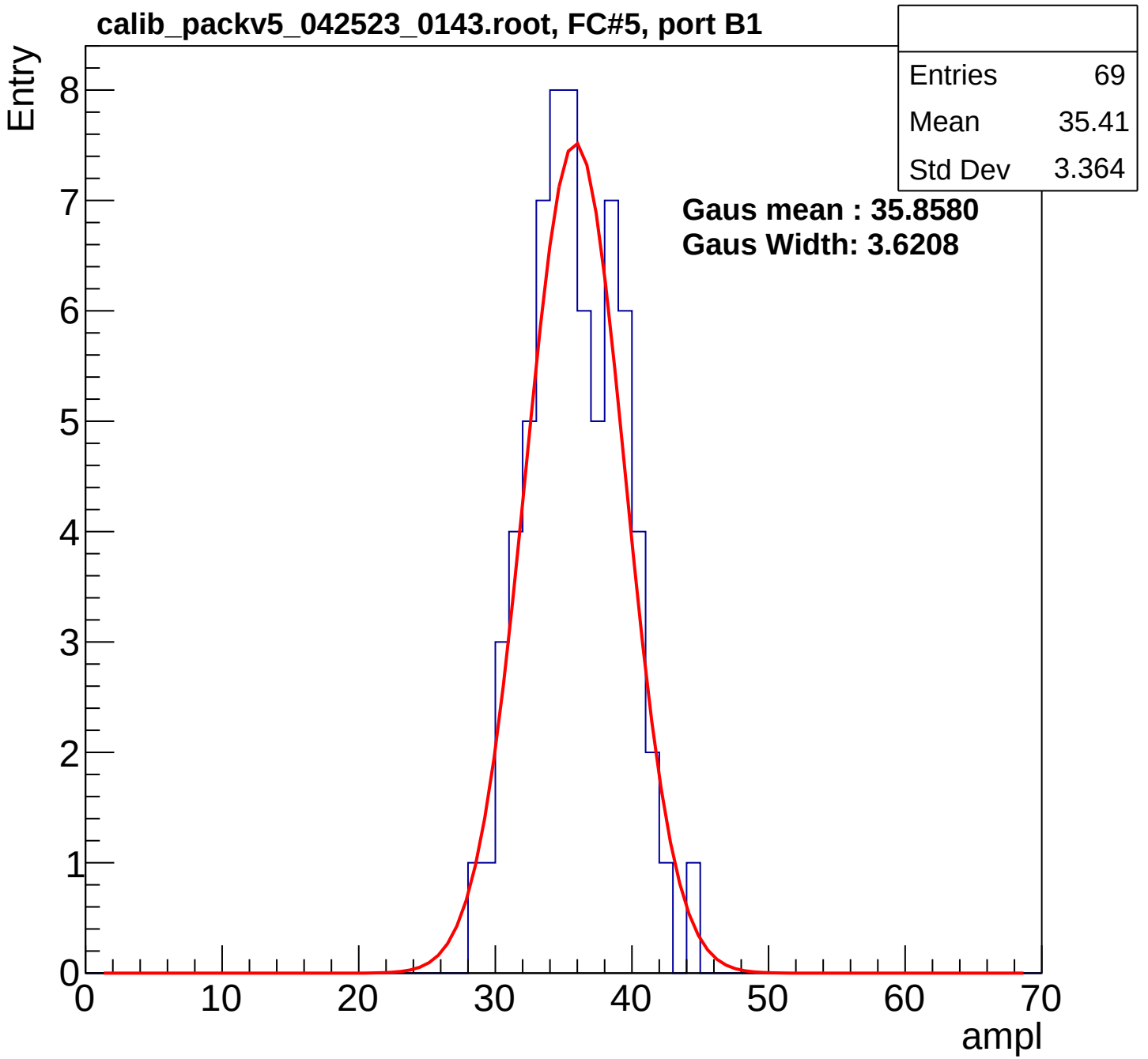
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch117, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 35.41
Std Dev 3.364
Gaus mean : 35.8580
Gaus Width: 3.6208
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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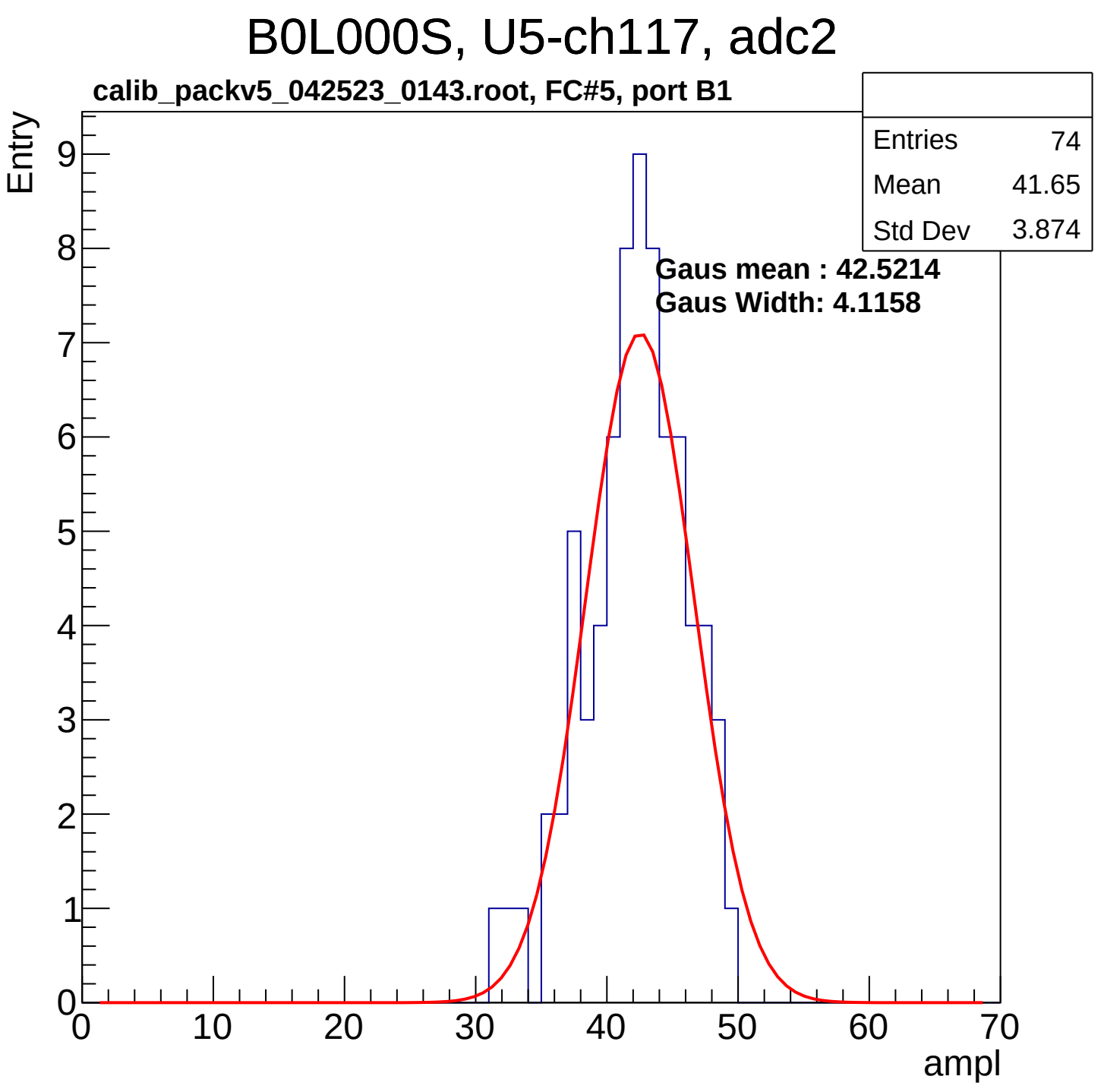
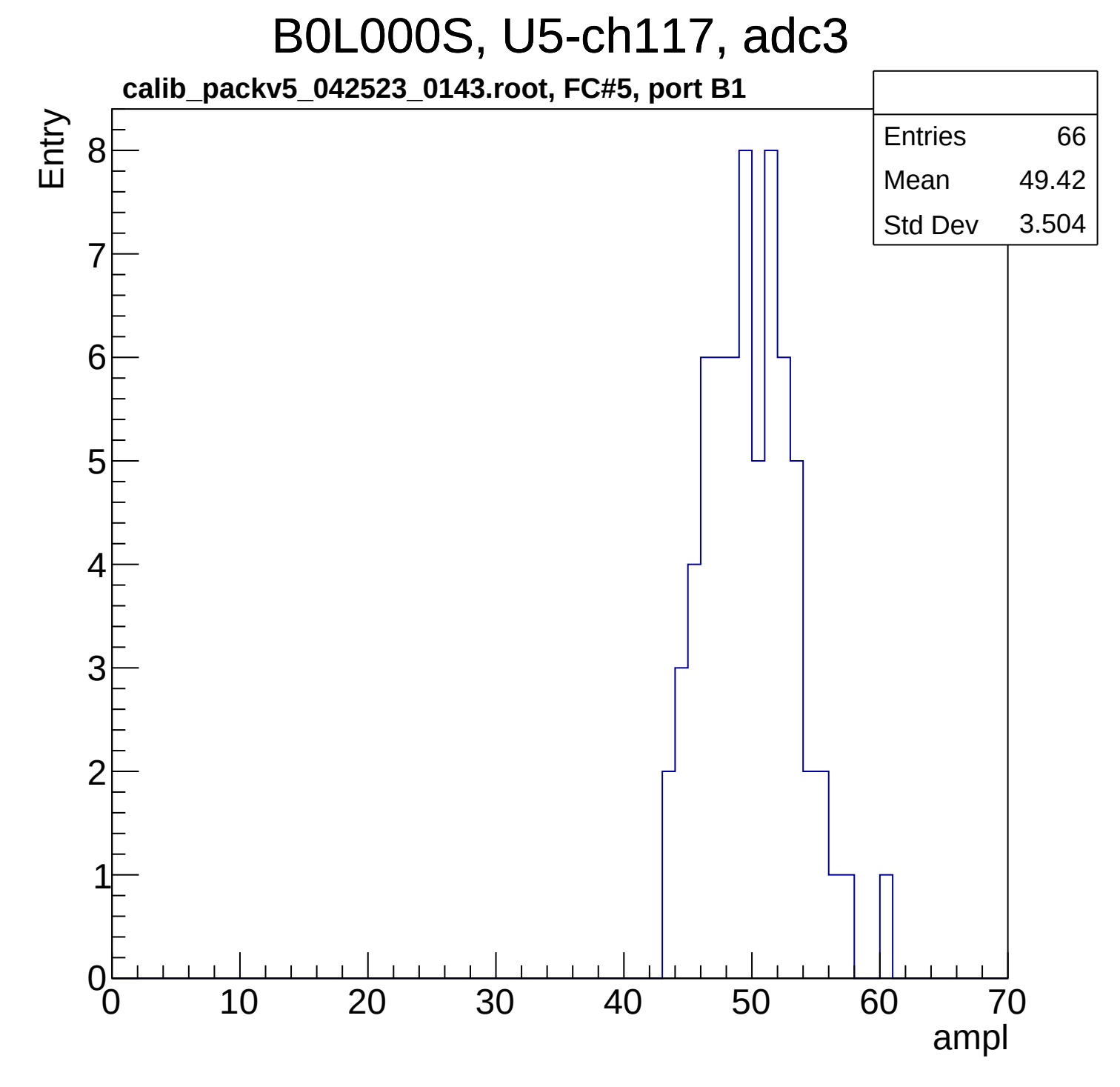
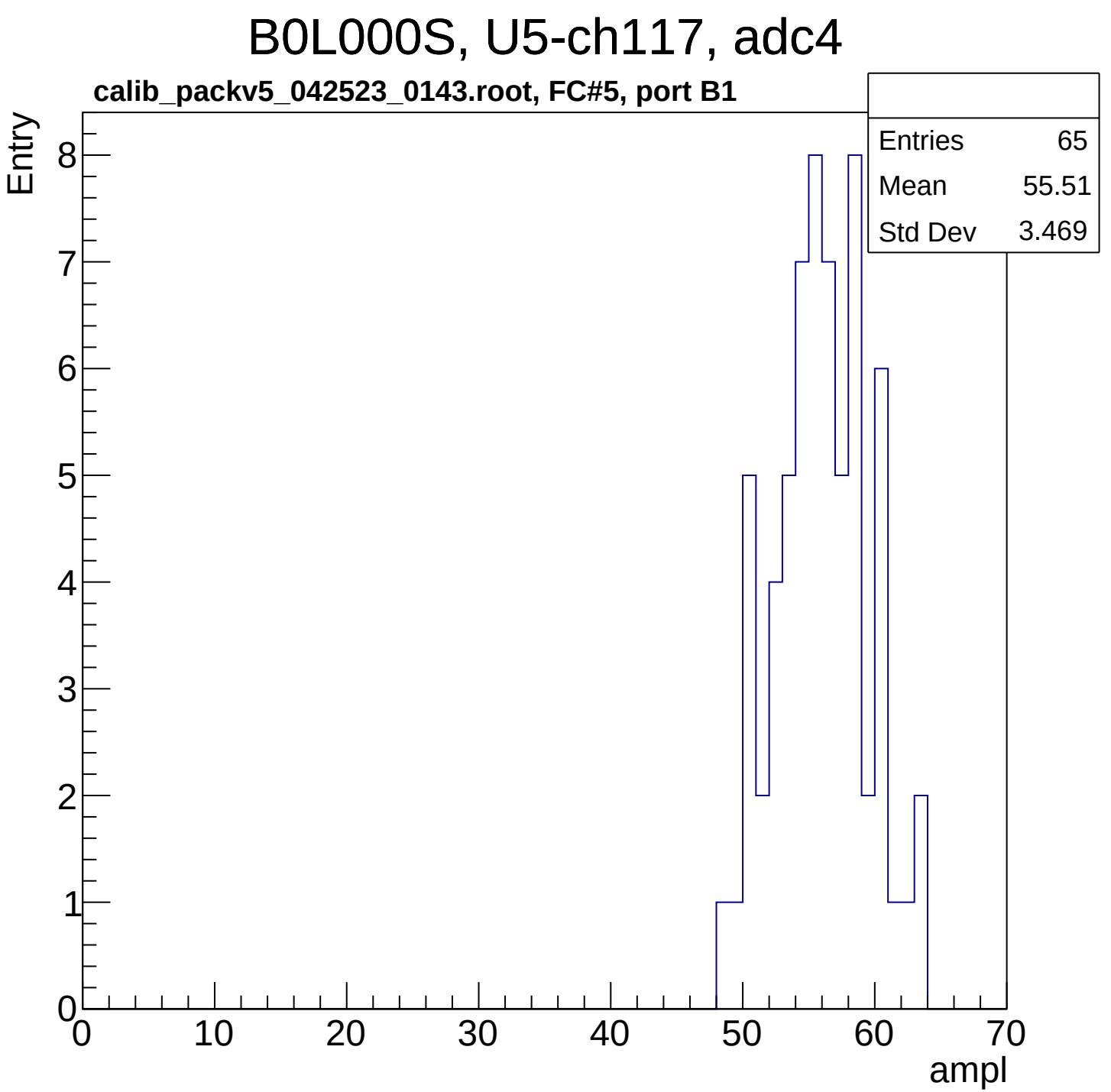

B0L000S, U5-ch117, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 41.65
Std Dev 3.874
Gaus mean : 42.5214
Gaus Width: 4.1158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch117, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 49.42
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch117, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 65
Mean 55.51
Std Dev 3.469
Entry
ampl
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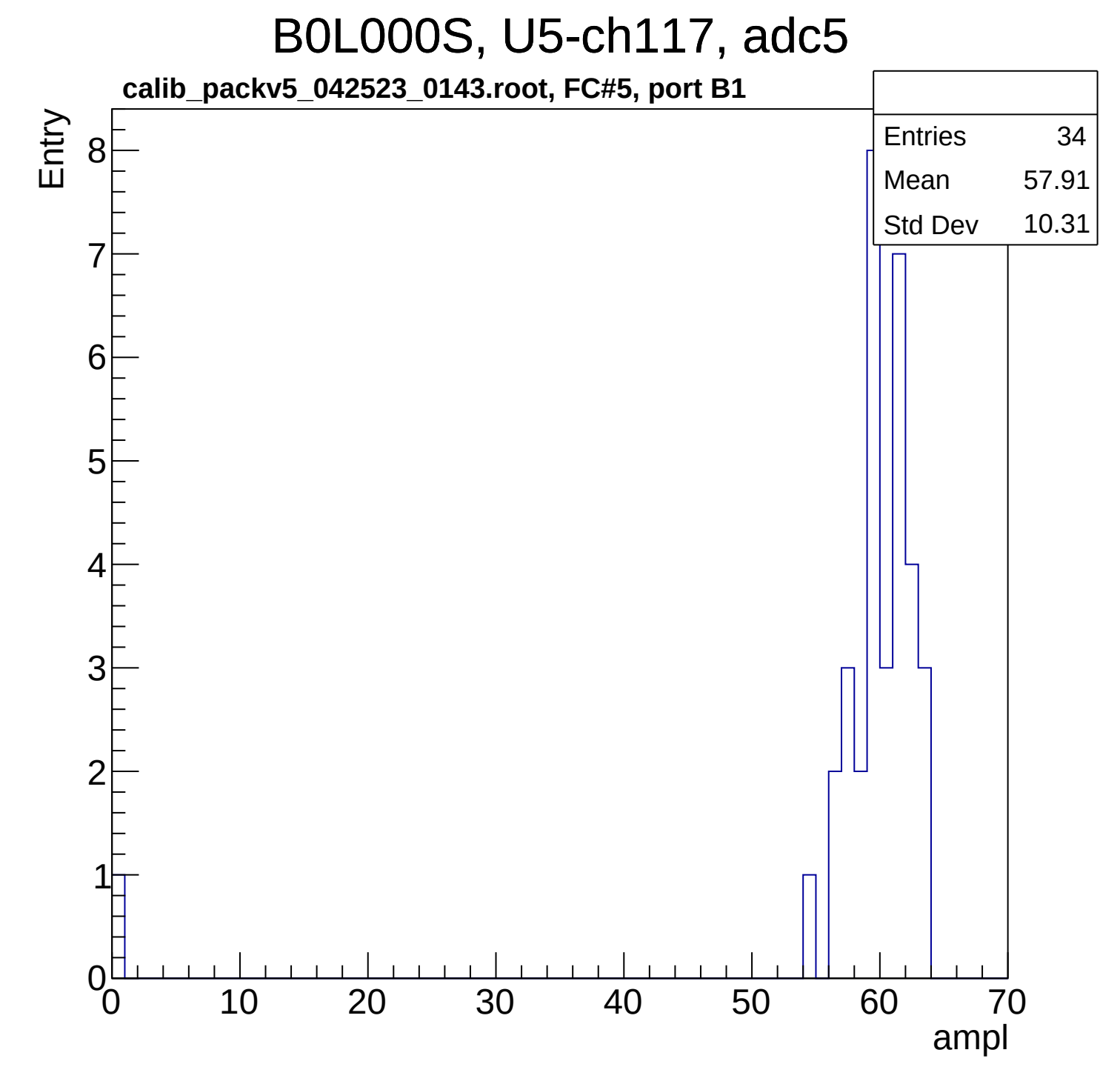
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch117, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 34
Mean 57.91
Std Dev 10.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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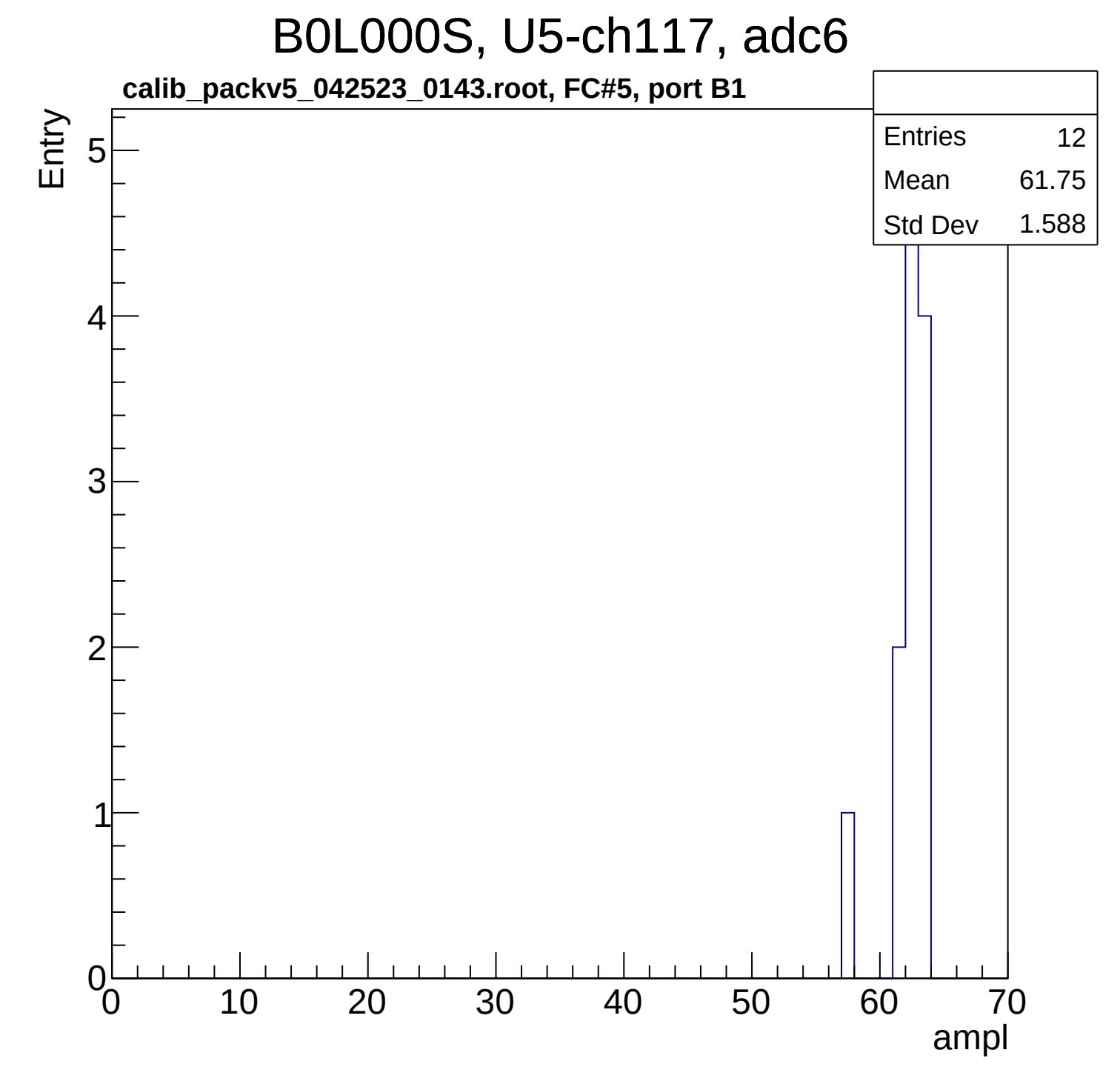
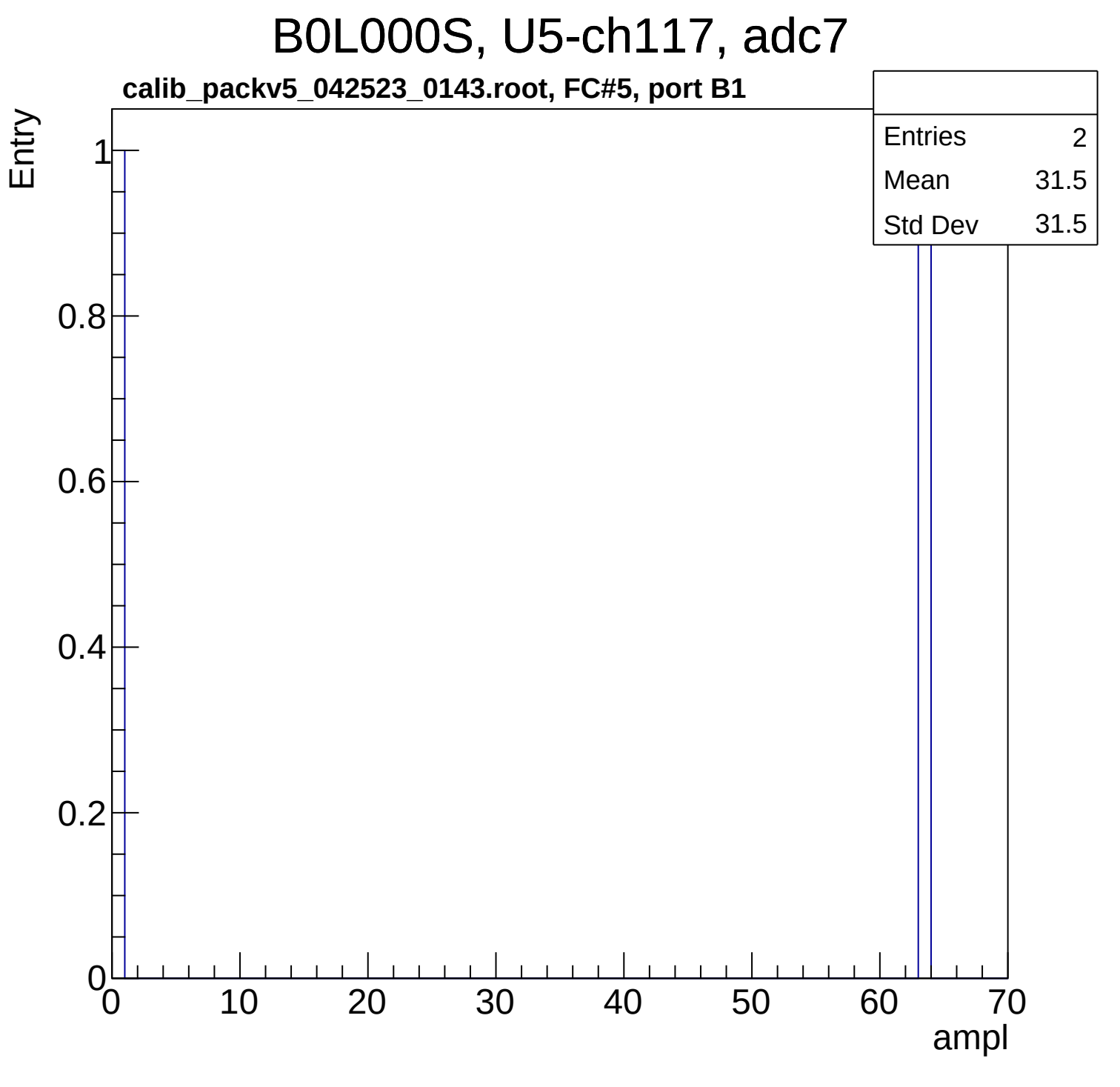
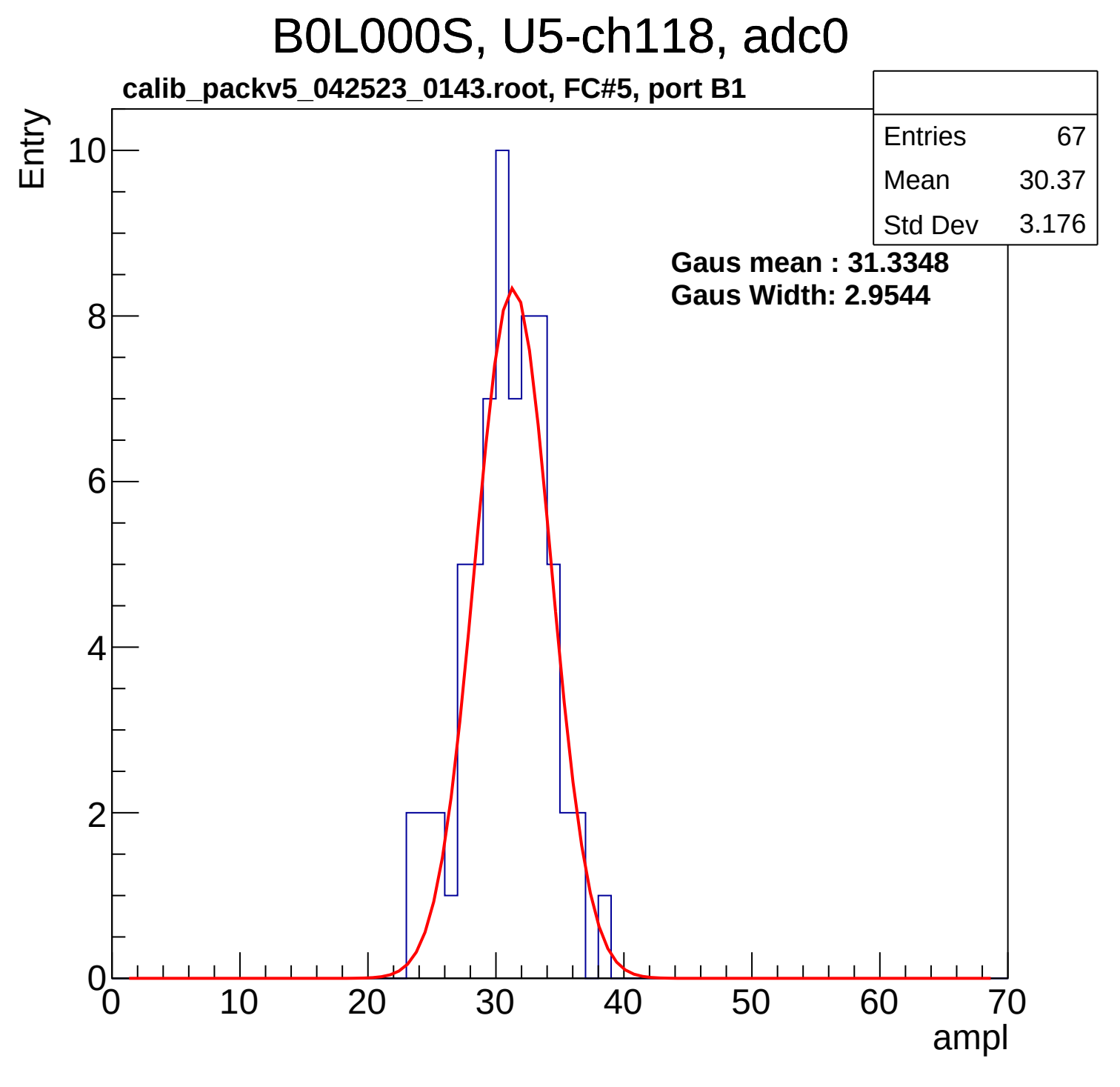
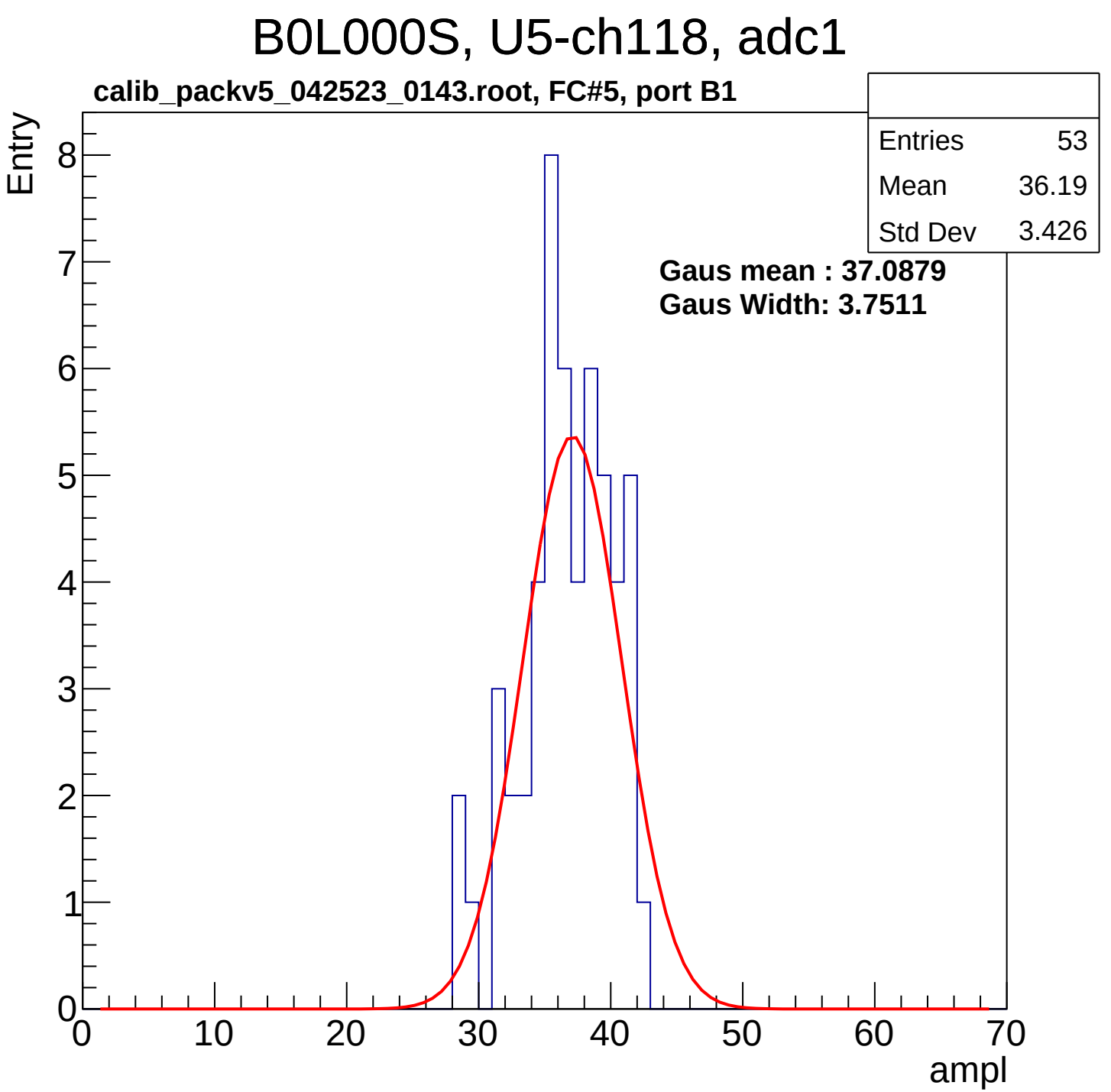

B0L000S, U5-ch117, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.75
Std Dev 1.588
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch117, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch118, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 67
Mean 30.37
Std Dev 3.176
Gaus mean : 31.3348
Gaus Width: 2.9544
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch118, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 36.19
Std Dev 3.426
Gaus mean : 37.0879
Gaus Width: 3.7511
Entry
ampl
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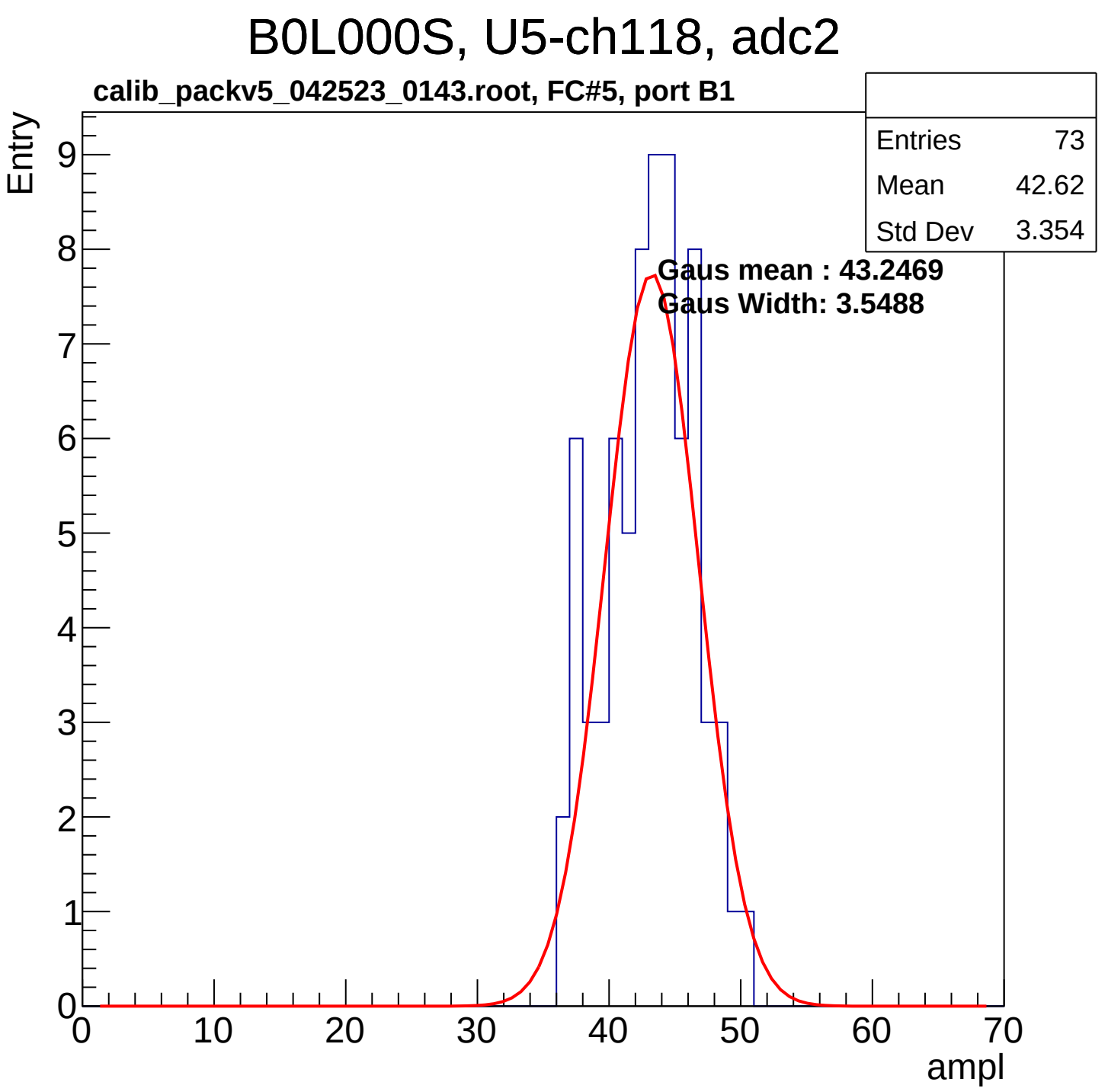
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch118, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 42.62
Std Dev 3.354
Gaus mean : 43.2469
Gaus Width: 3.5488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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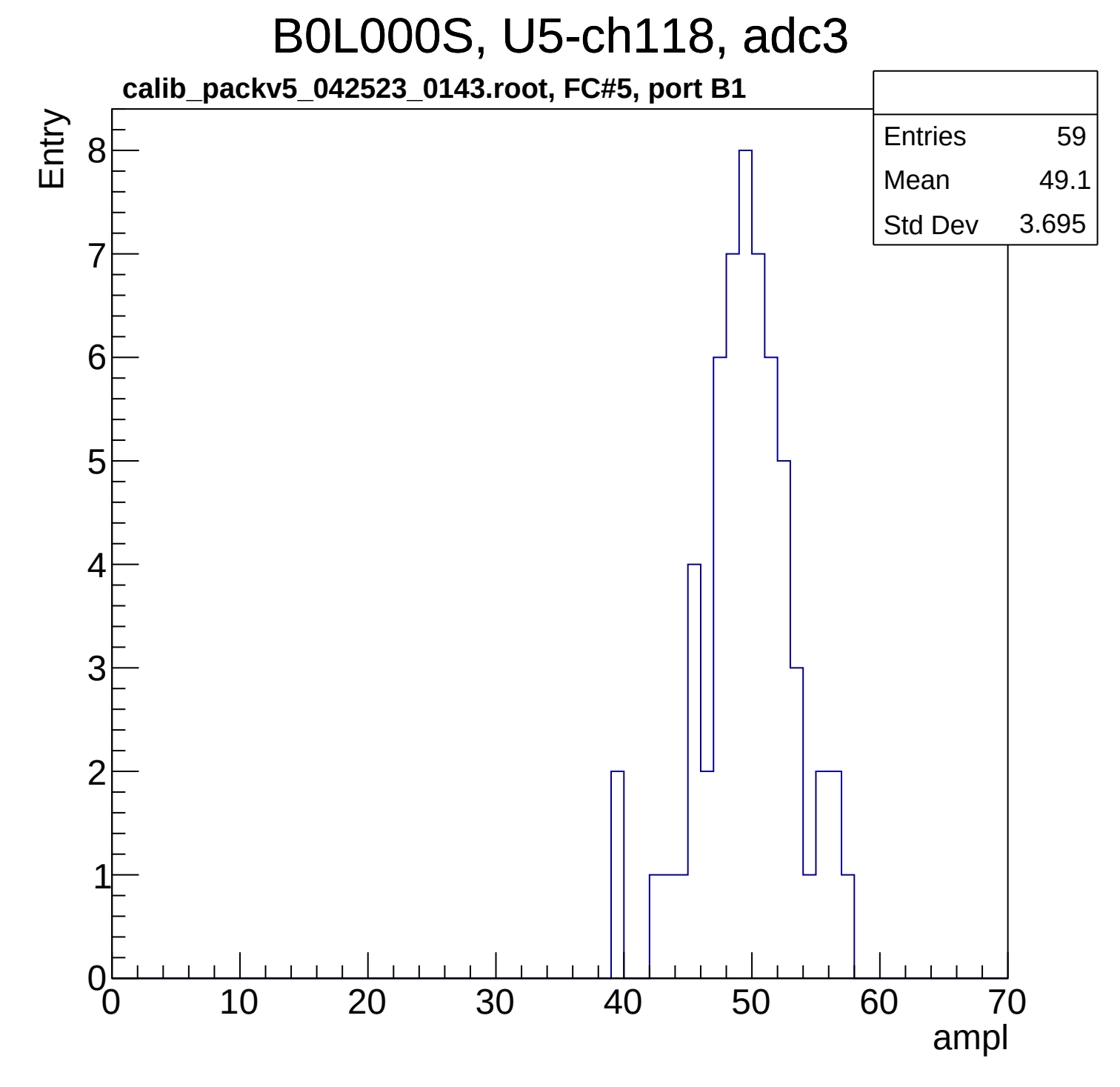
B0L000S, U5-ch118, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 59
Mean 49.1
Std Dev 3.695
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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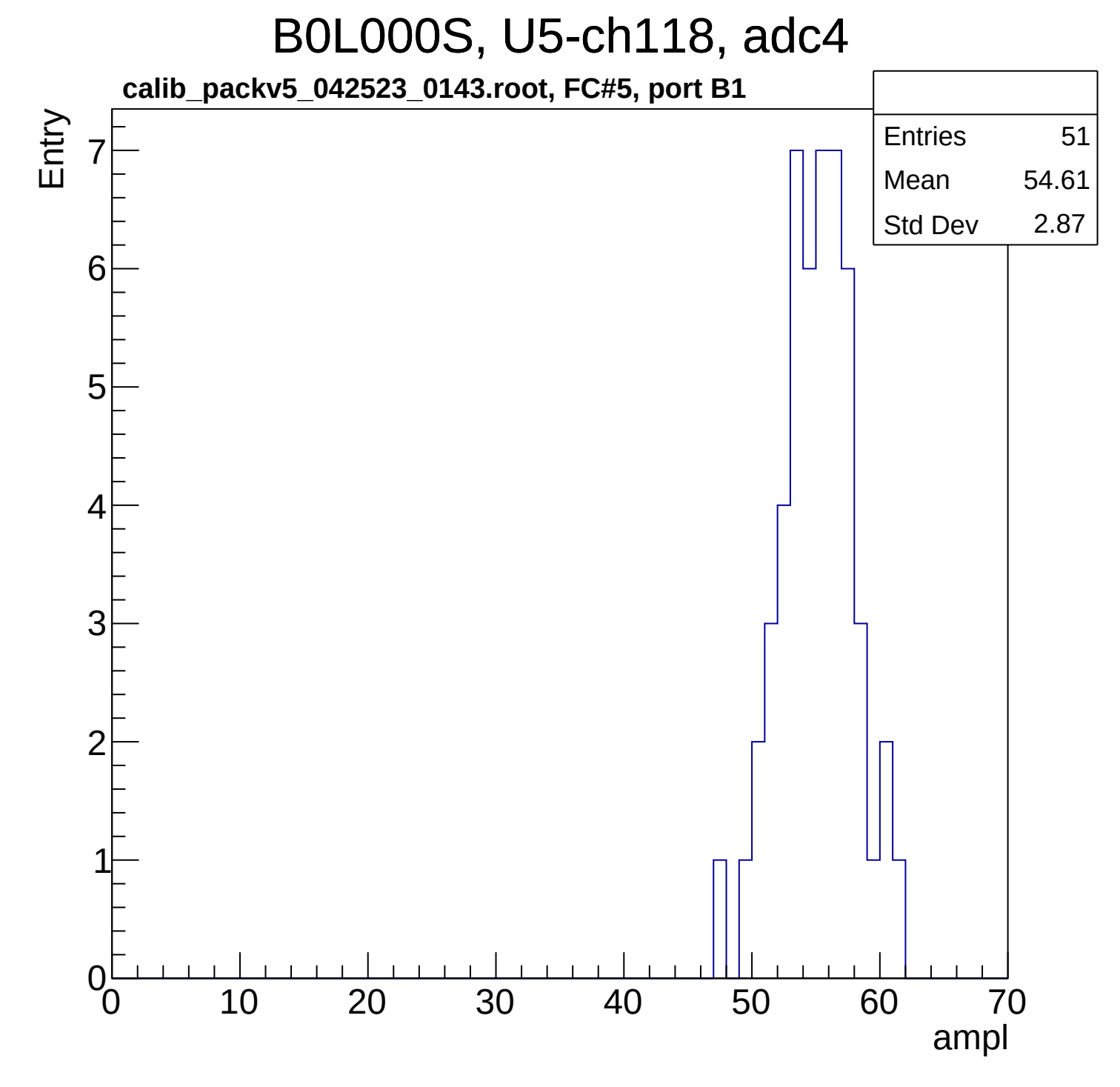
B0L000S, U5-ch118, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 54.61
Std Dev 2.87
Entry
ampl
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4
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6
7
0
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40
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60
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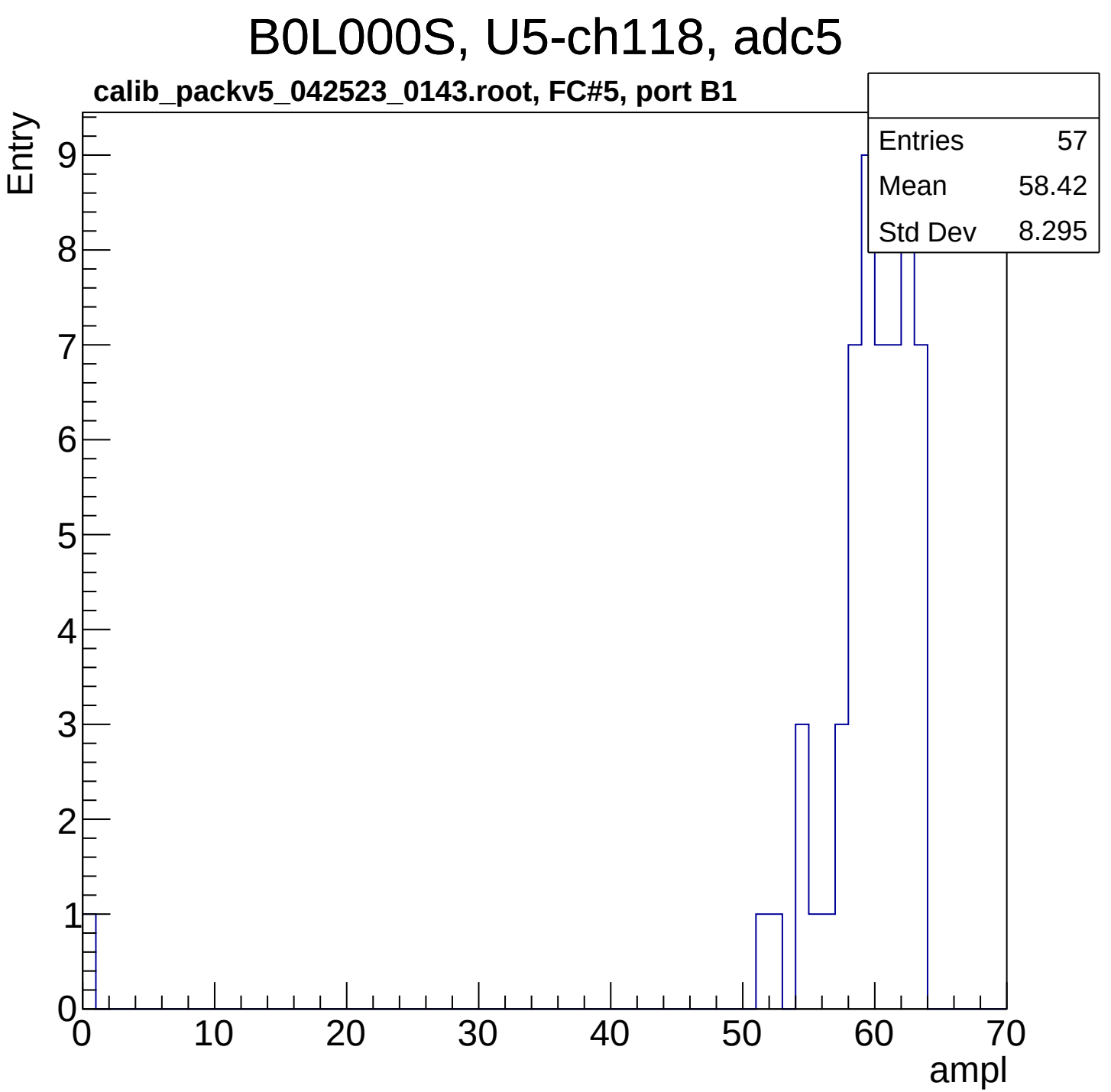
B0L000S, U5-ch118, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 58.42
Std Dev 8.295
Entry
ampl
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3
4
5
6
7
8
9
0
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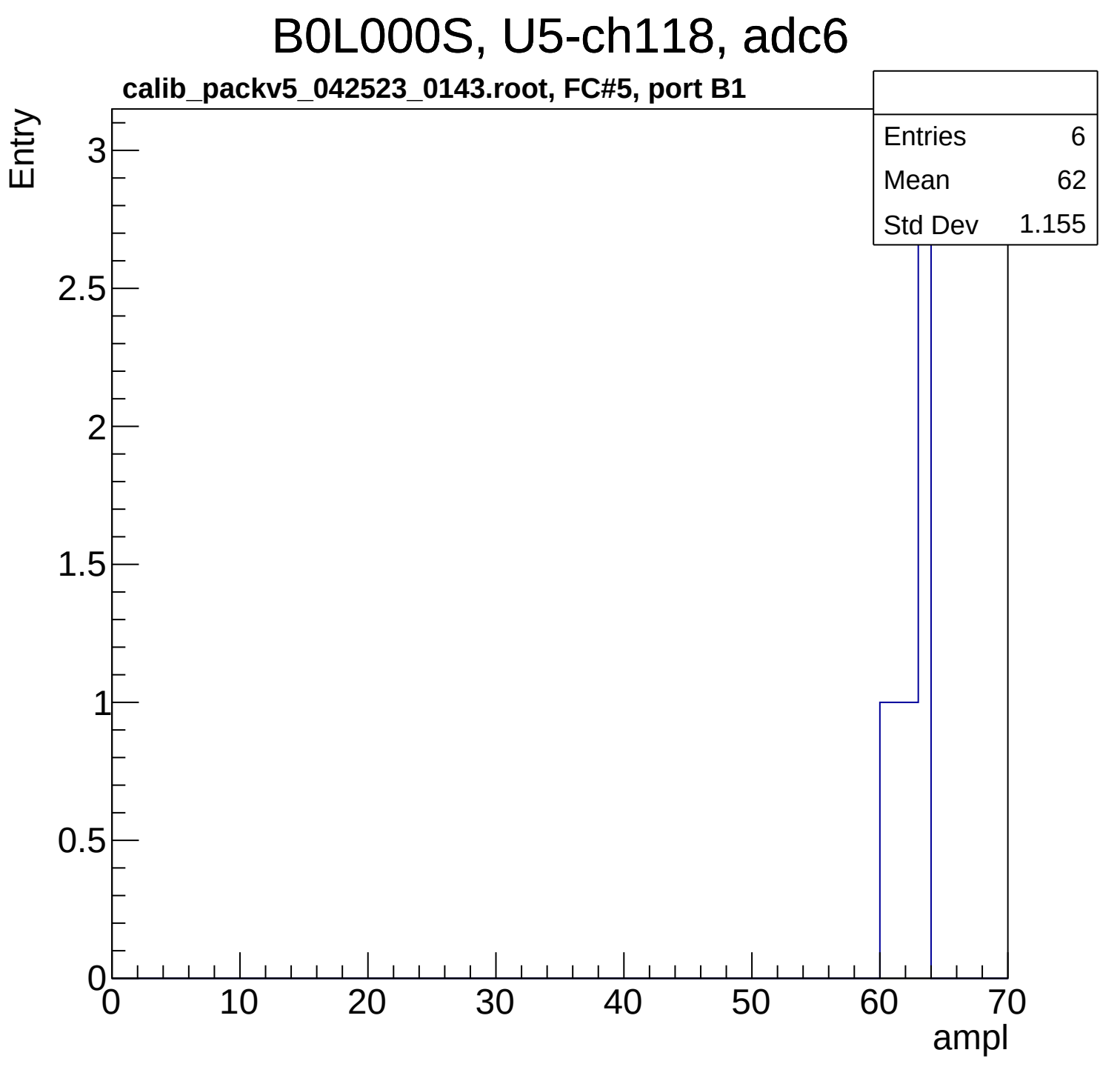
B0L000S, U5-ch118, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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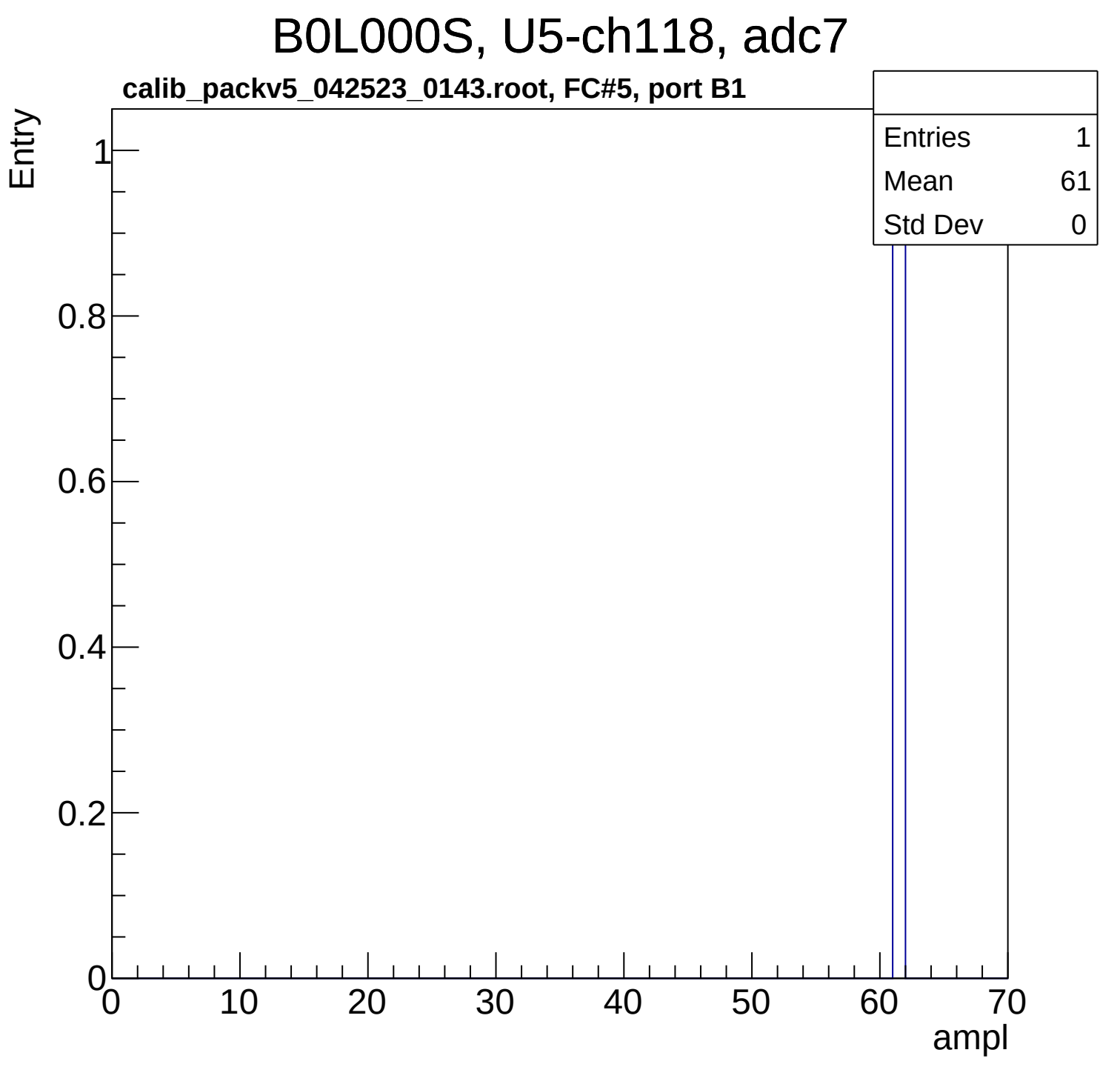
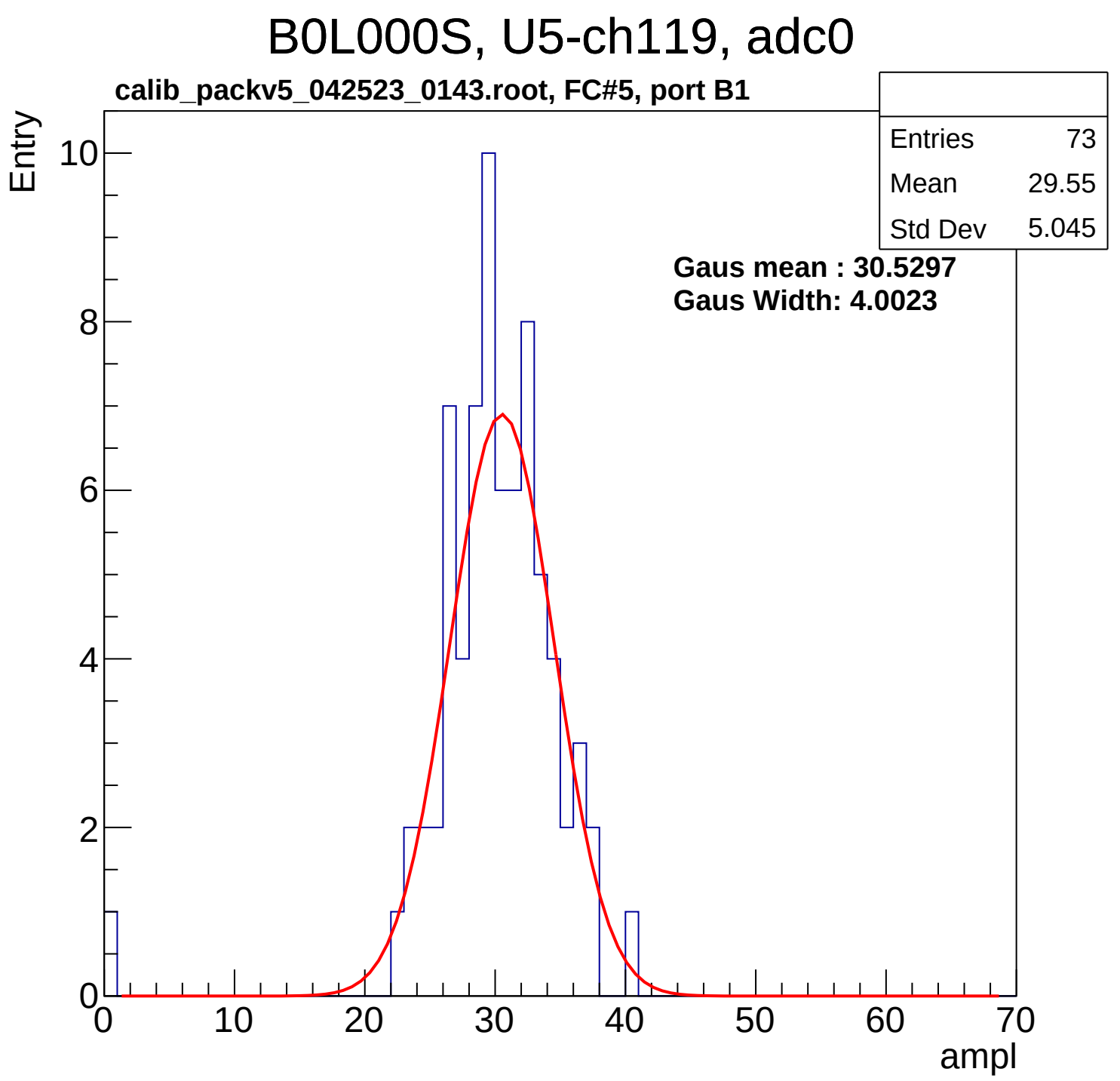
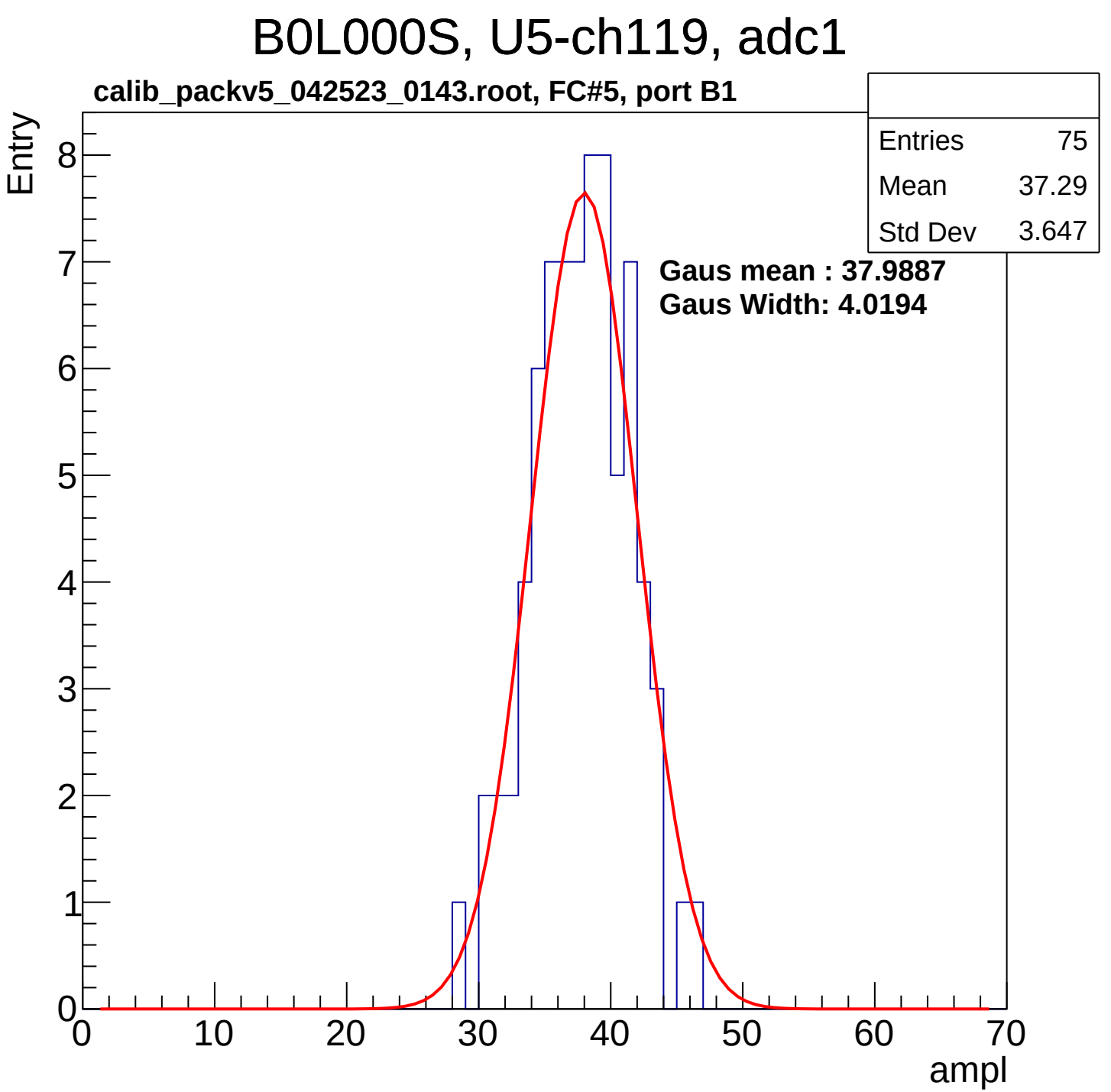
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch118, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch119, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.55
Std Dev 5.045
Gaus mean : 30.5297
Gaus Width: 4.0023
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch119, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 37.29
Std Dev 3.647
Gaus mean : 37.9887
Gaus Width: 4.0194
Entry
ampl
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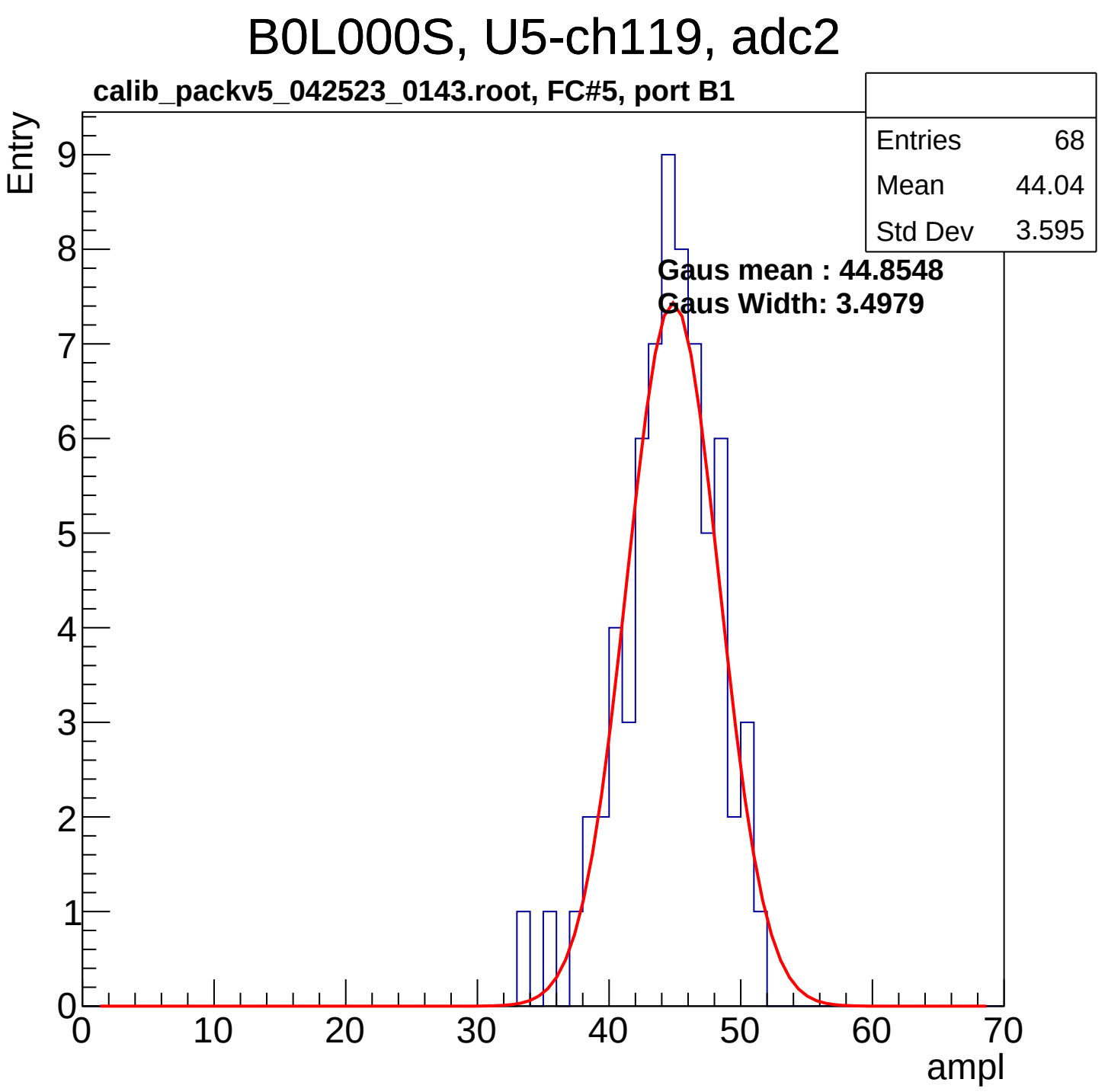
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch119, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 44.04
Std Dev 3.595
Gaus mean : 44.8548
Gaus Width: 3.4979
Entry
ampl
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2
3
4
5
6
7
8
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0
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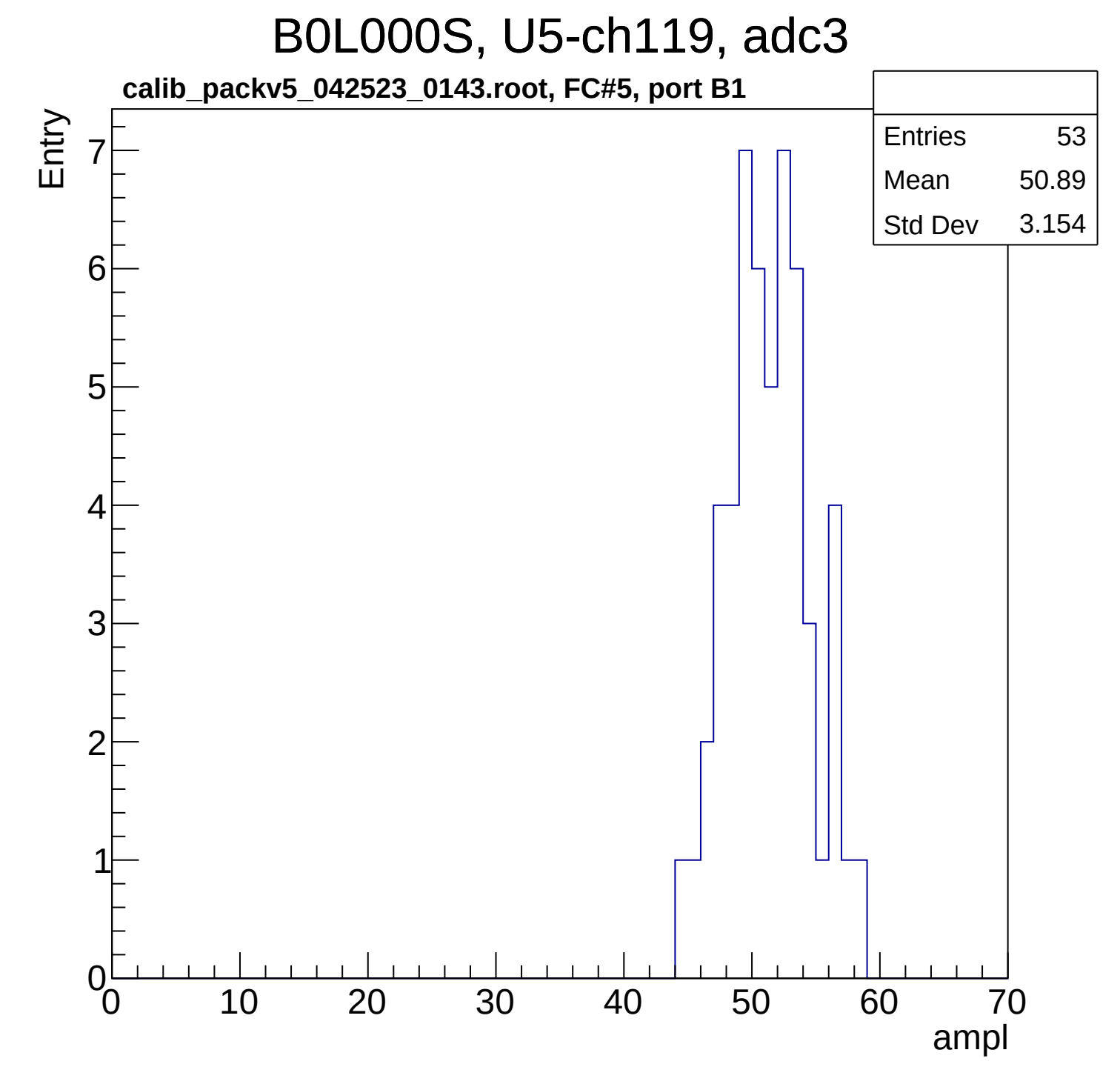
B0L000S, U5-ch119, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 50.89
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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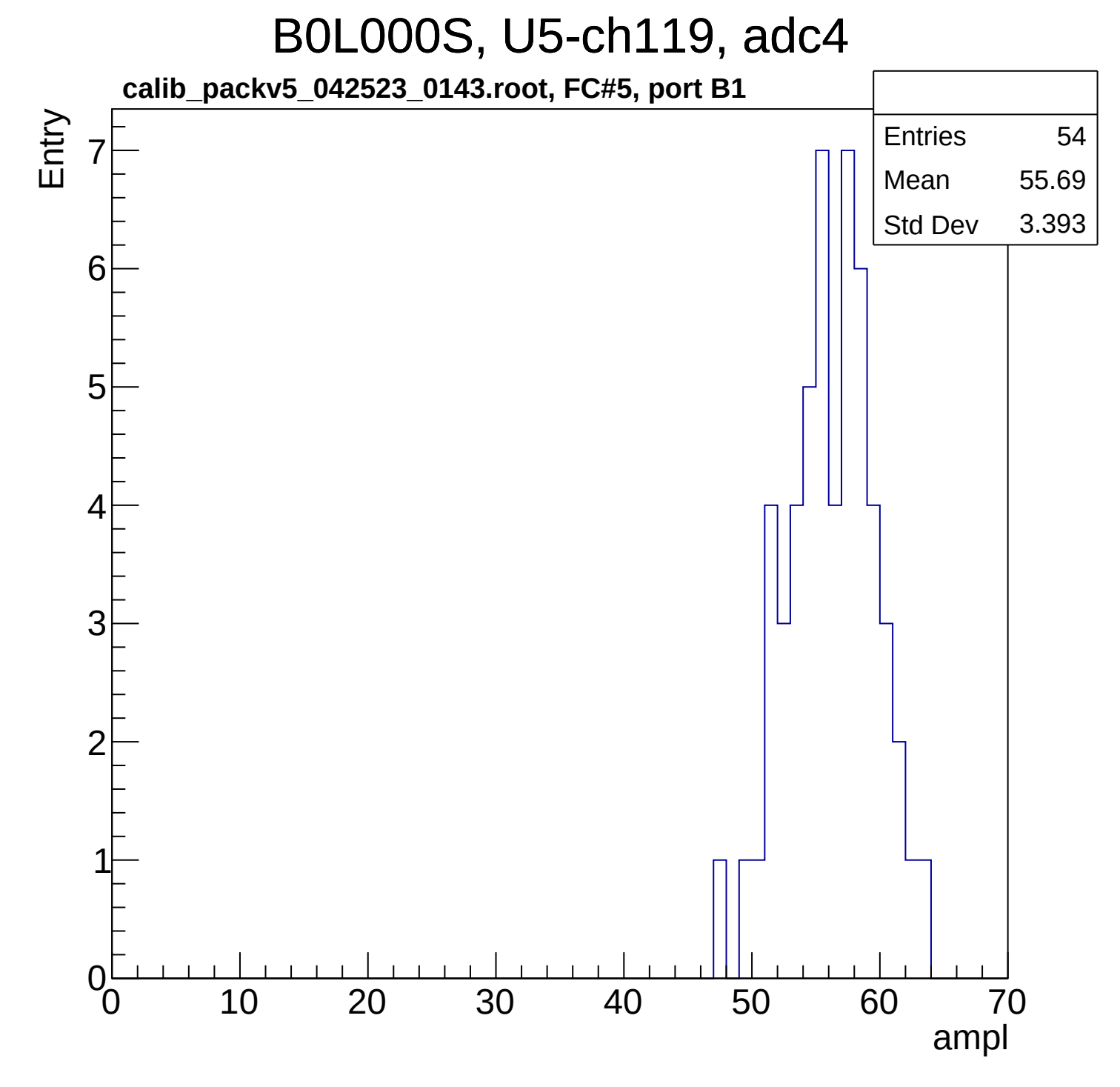
B0L000S, U5-ch119, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 55.69
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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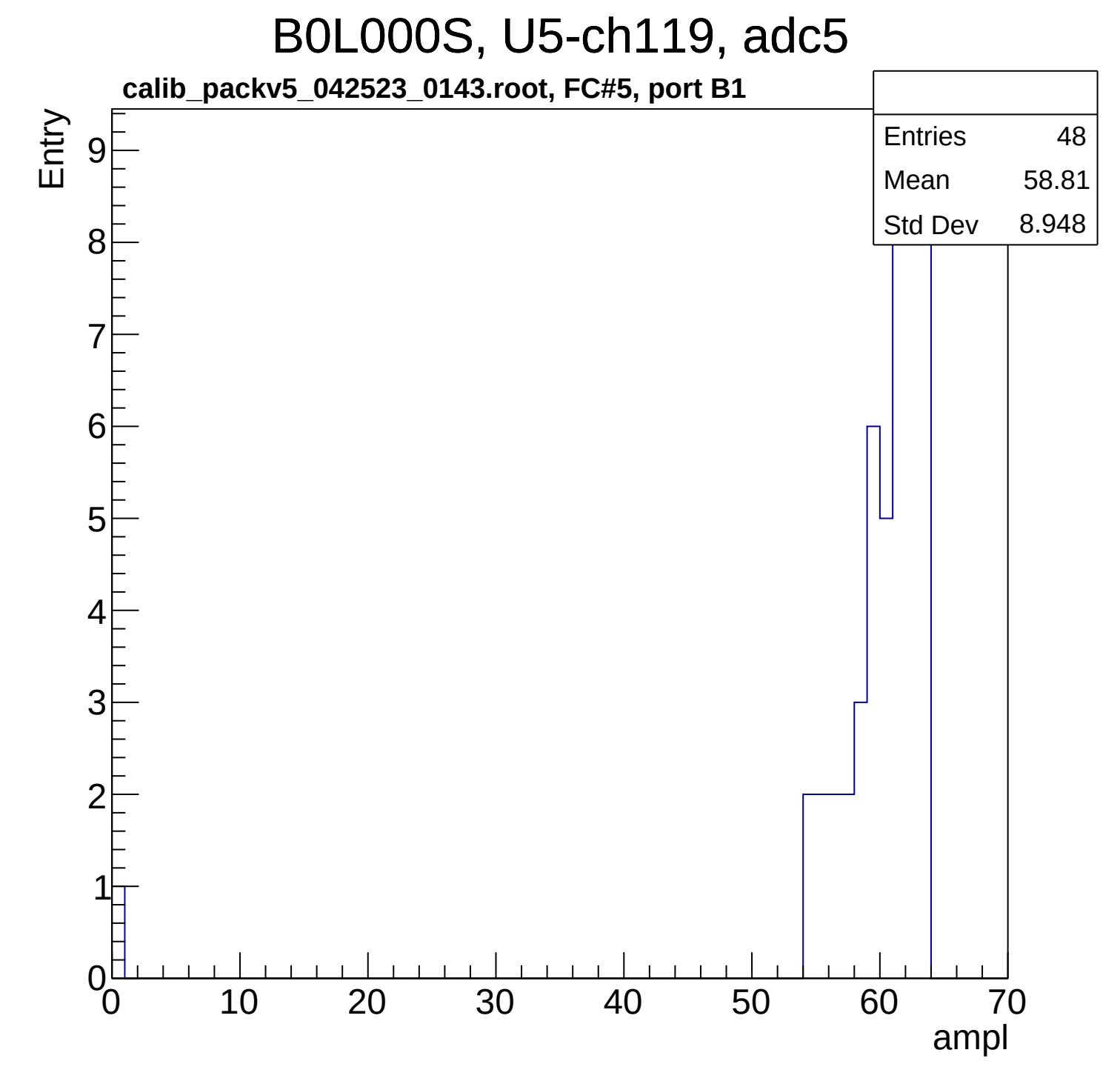
B0L000S, U5-ch119, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 58.81
Std Dev 8.948
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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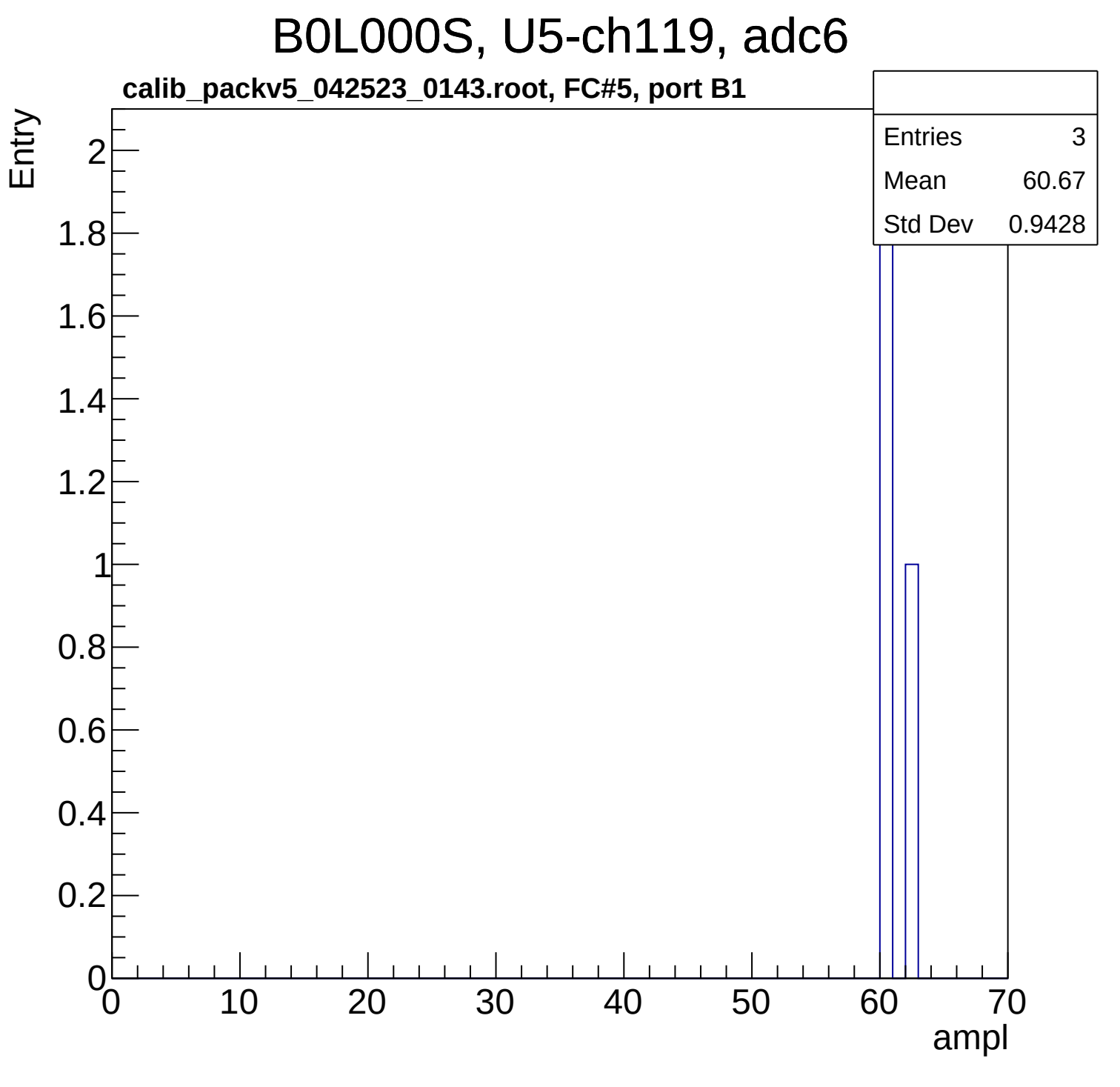
B0L000S, U5-ch119, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 60.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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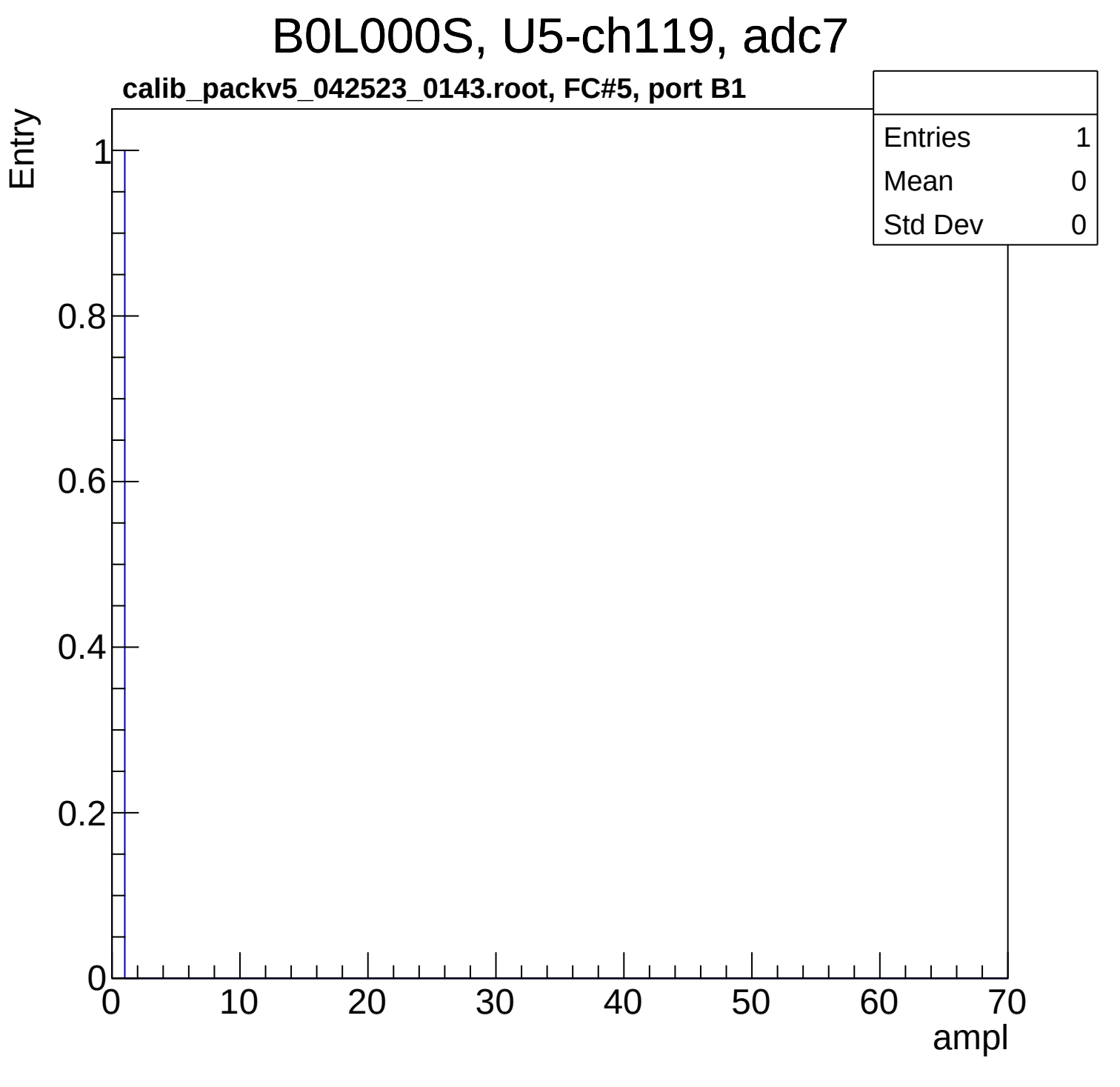
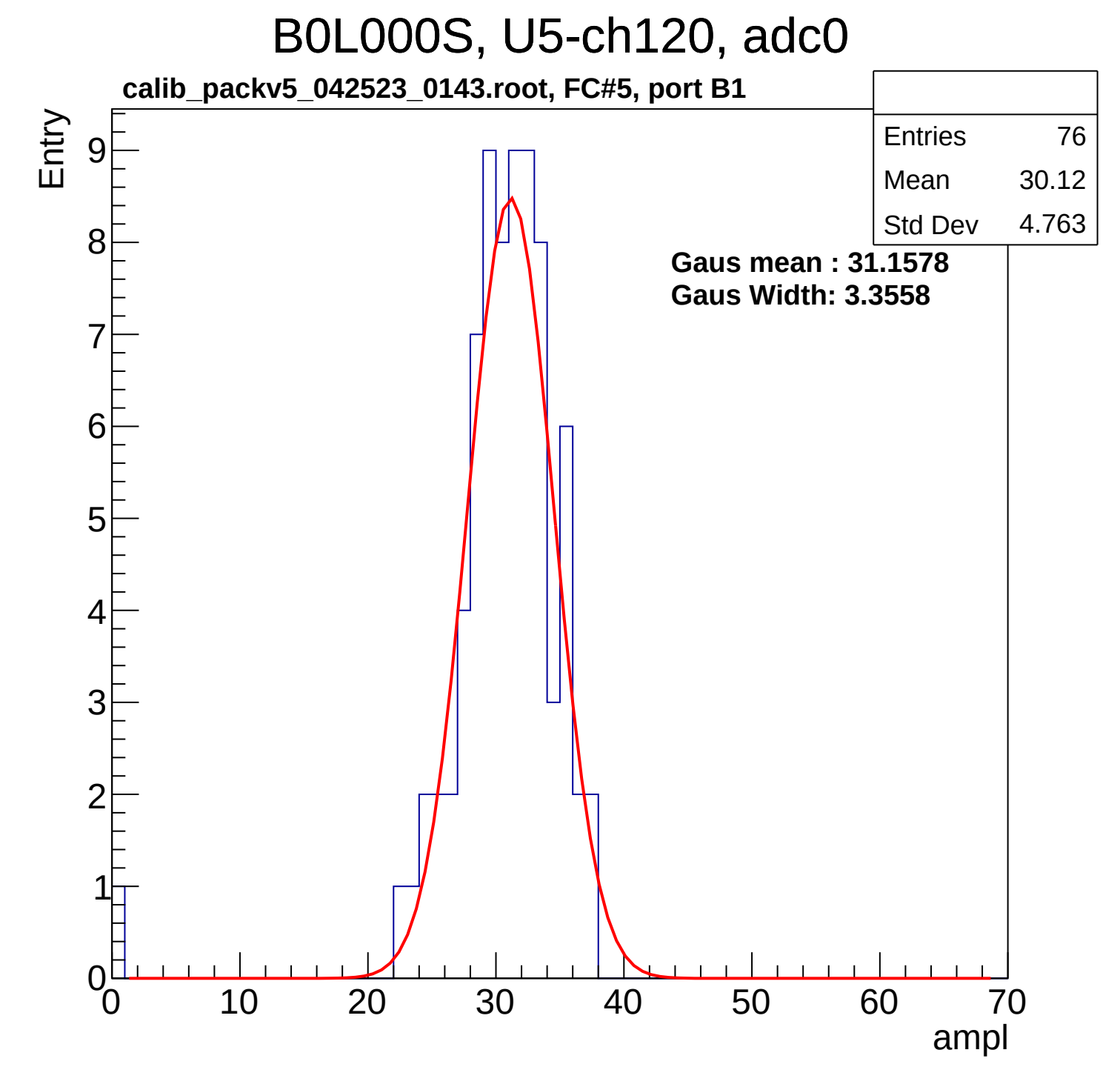

B0L000S, U5-ch119, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch120, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 76
Mean 30.12
Std Dev 4.763
Gaus mean : 31.1578
Gaus Width: 3.3558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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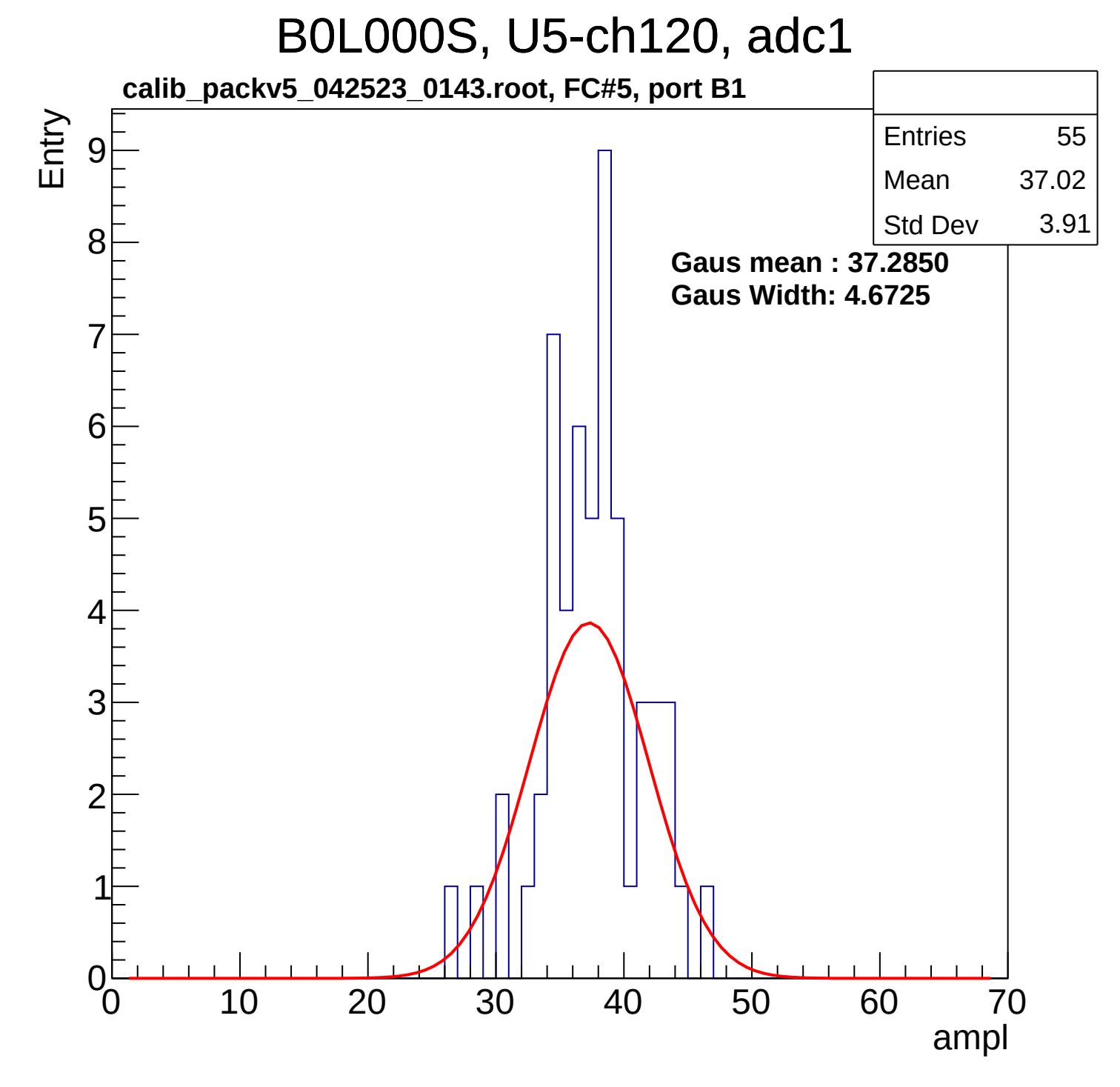
B0L000S, U5-ch120, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 55
Mean 37.02
Std Dev 3.91
Gaus mean : 37.2850
Gaus Width: 4.6725
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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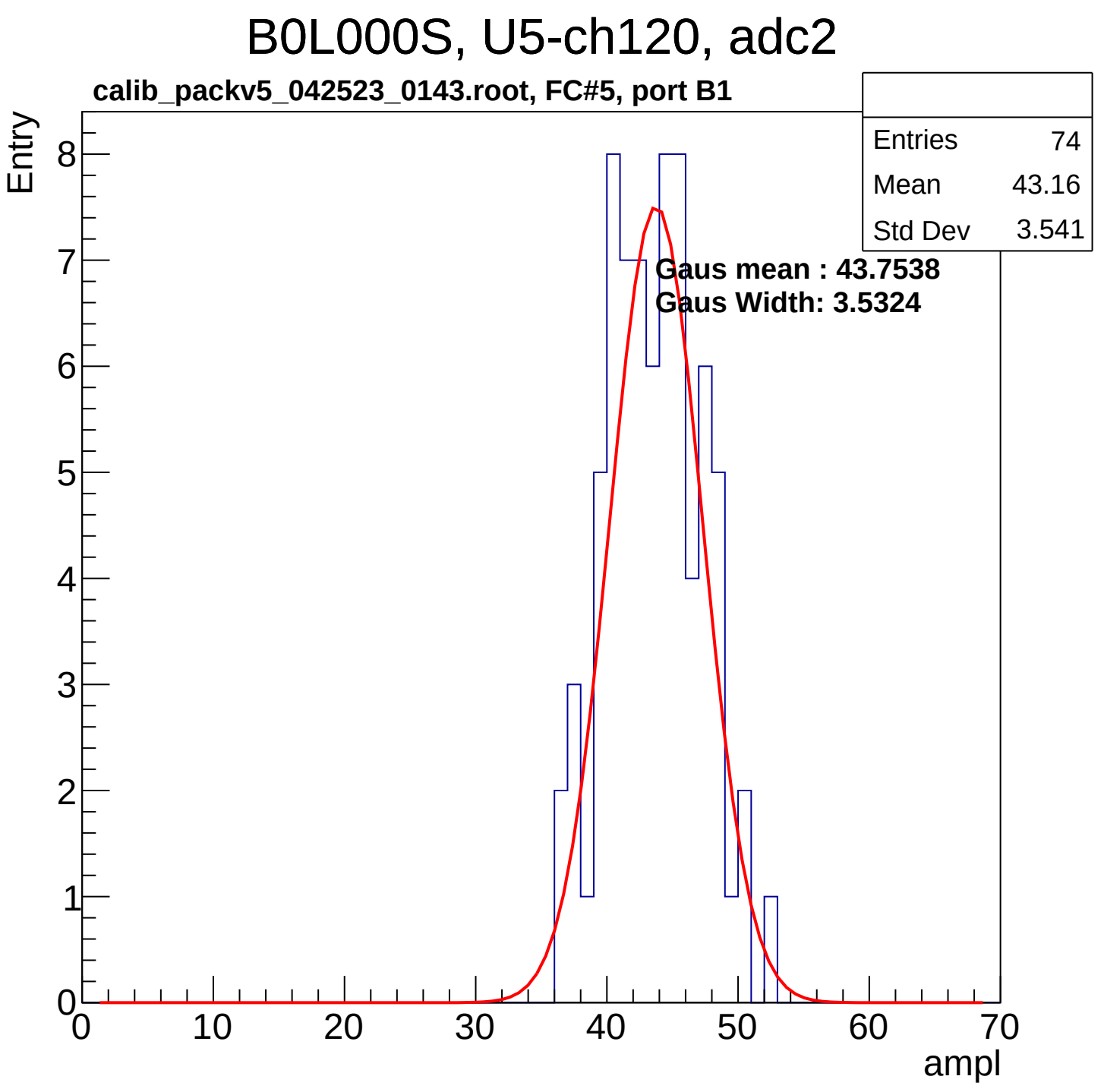
B0L000S, U5-ch120, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 43.16
Std Dev 3.541
Gaus mean : 43.7538
Gaus Width: 3.5324
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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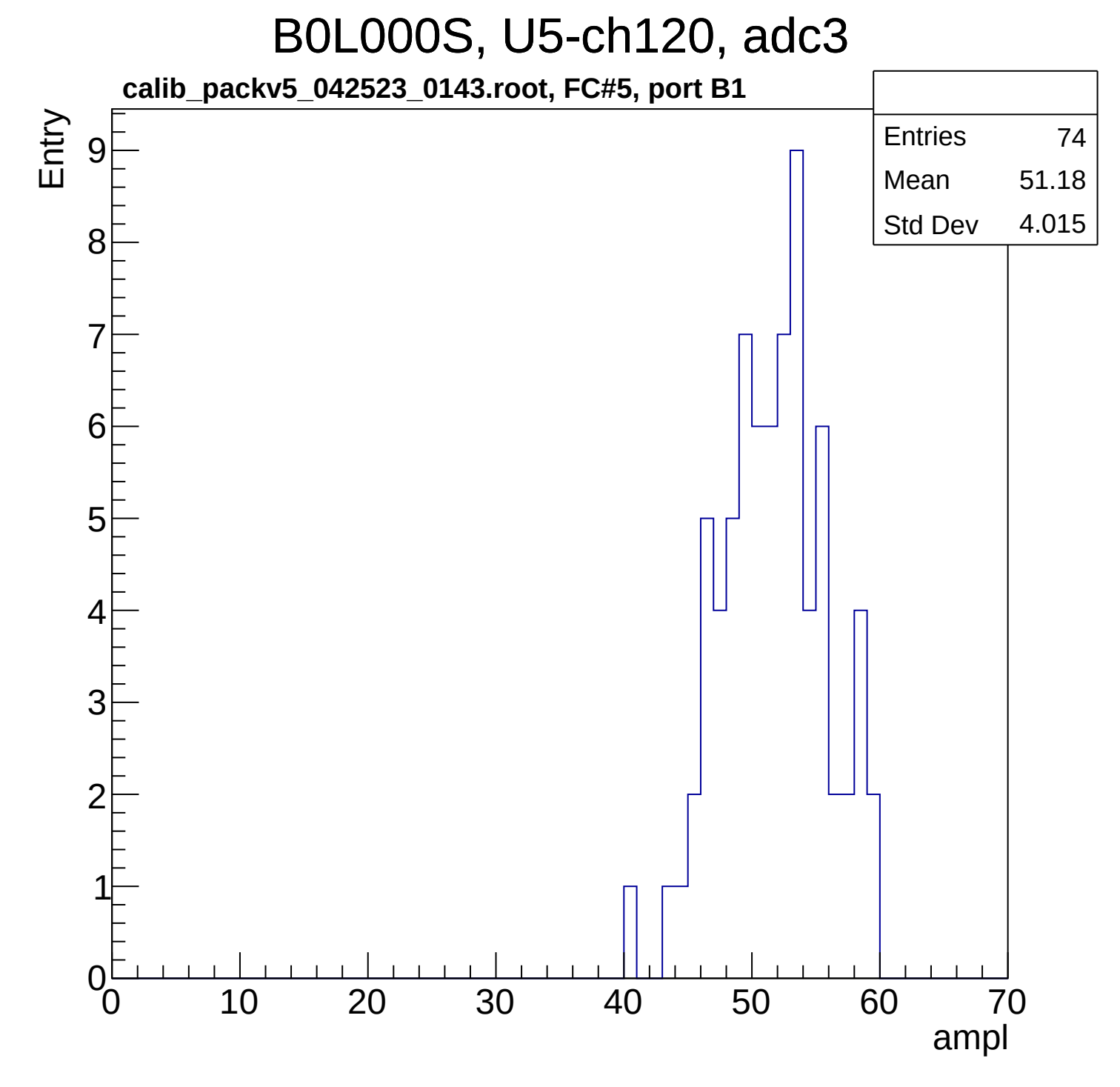
B0L000S, U5-ch120, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 51.18
Std Dev 4.015
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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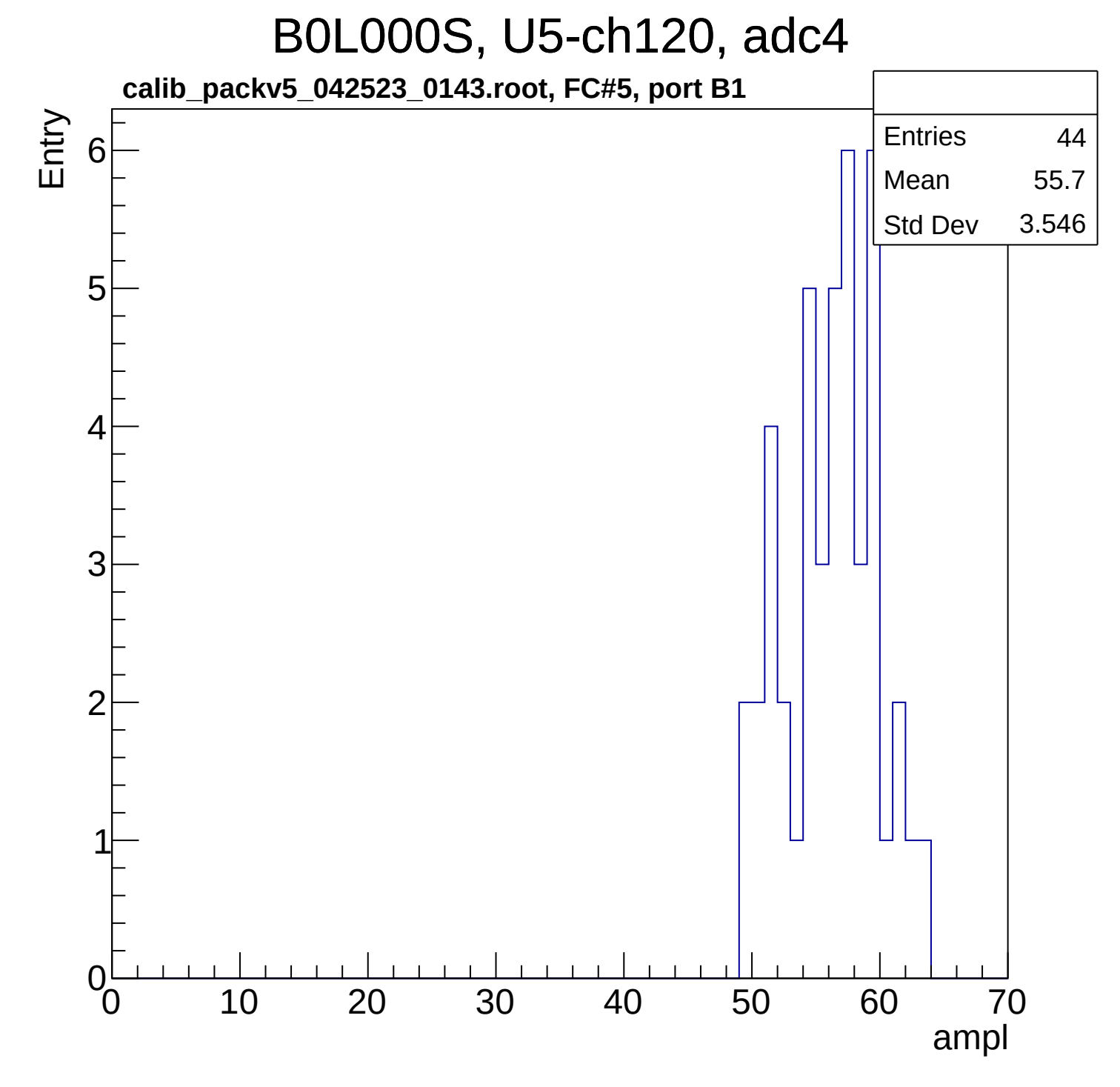
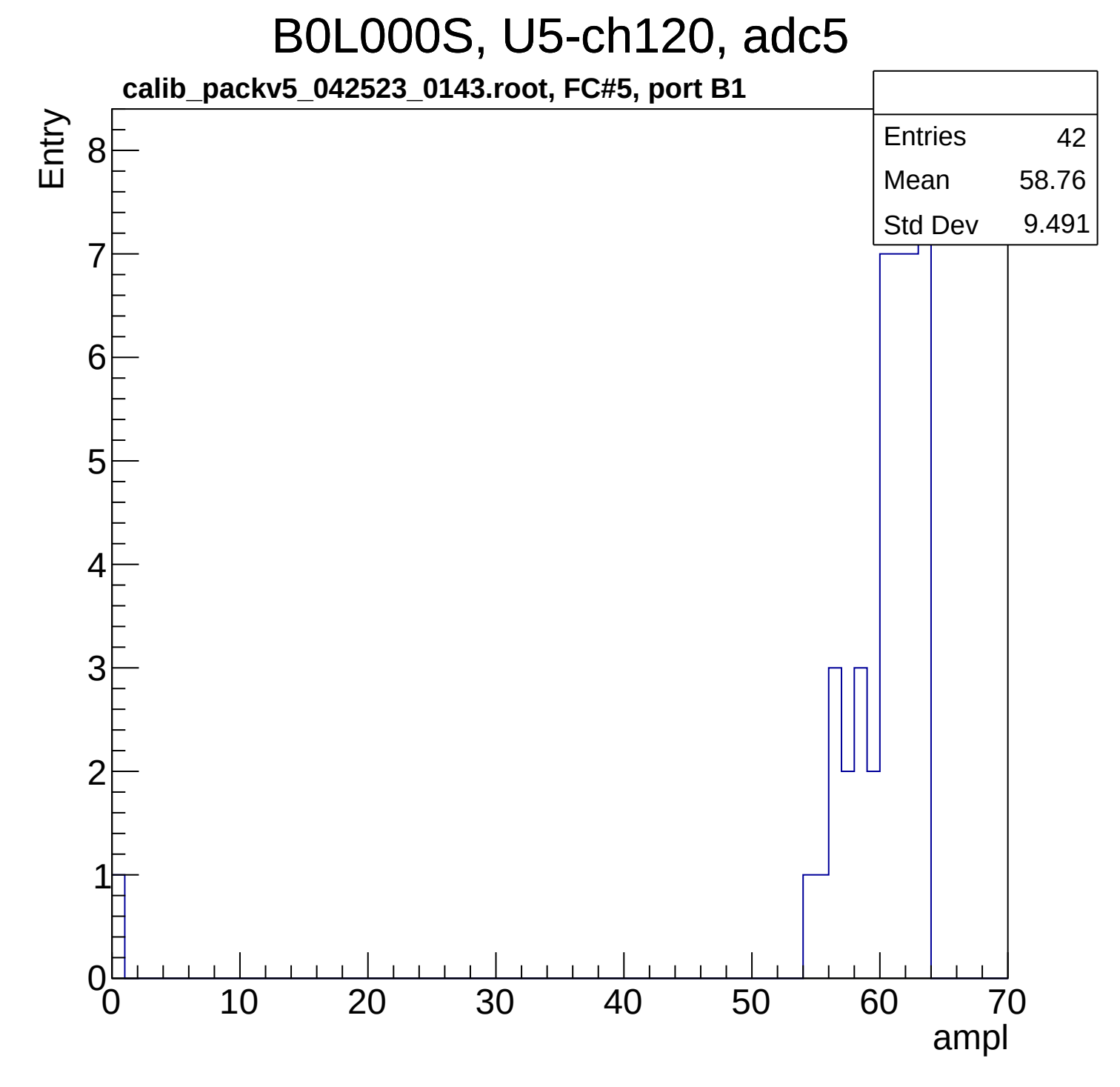

B0L000S, U5-ch120, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 44
Mean 55.7
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L000S, U5-ch120, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 58.76
Std Dev 9.491
Entry
ampl
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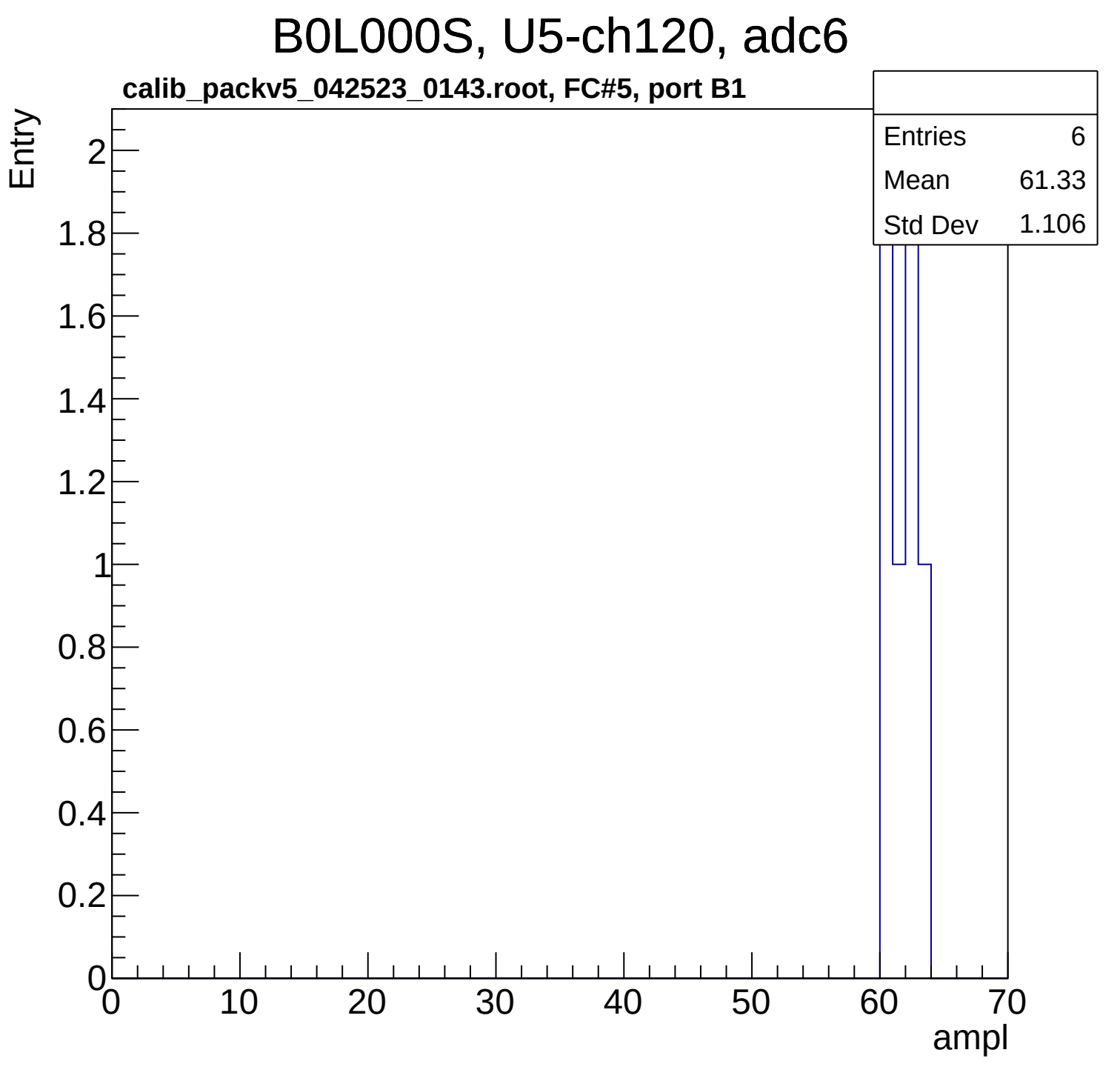
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch120, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 6
Mean 61.33
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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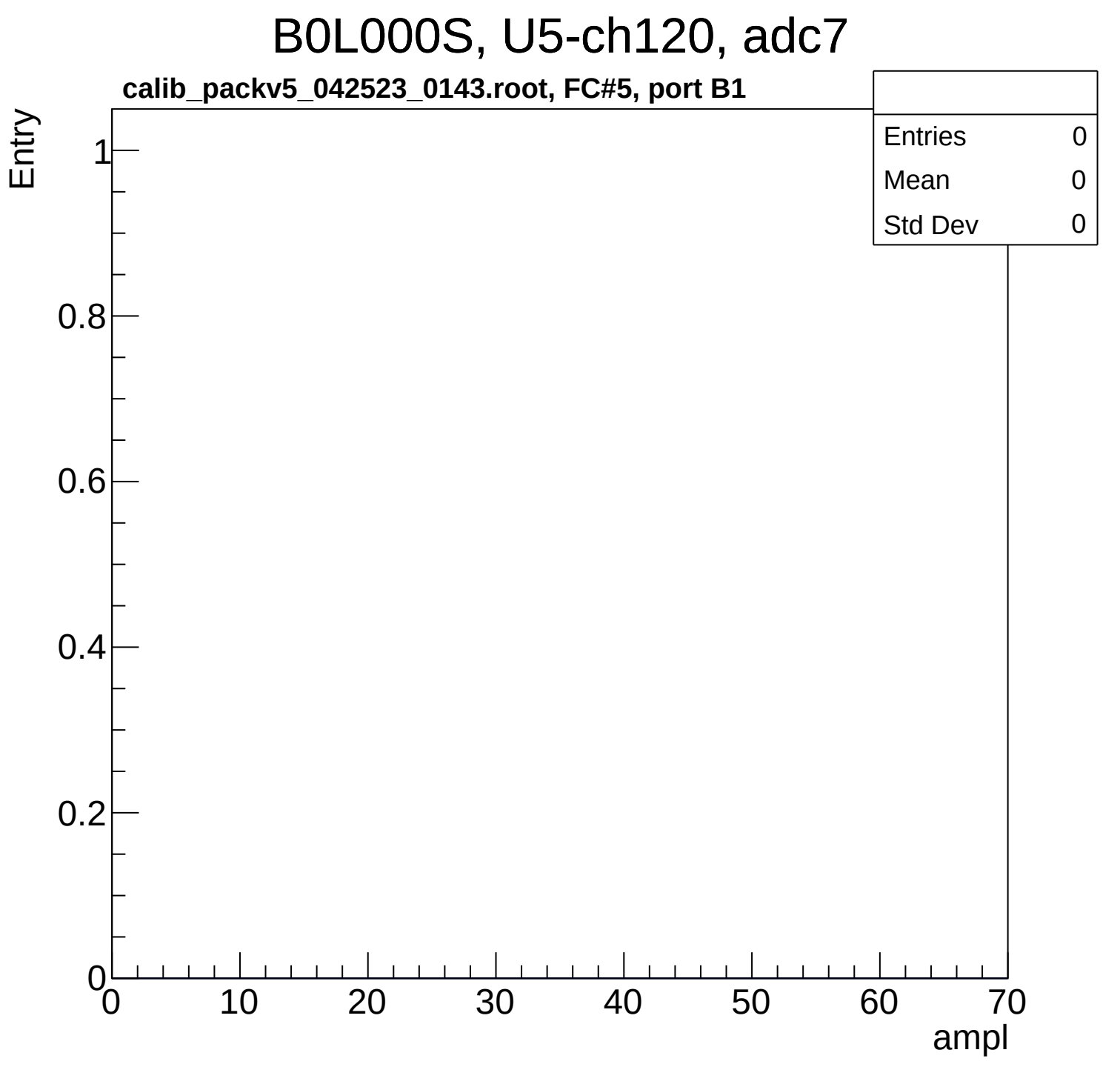
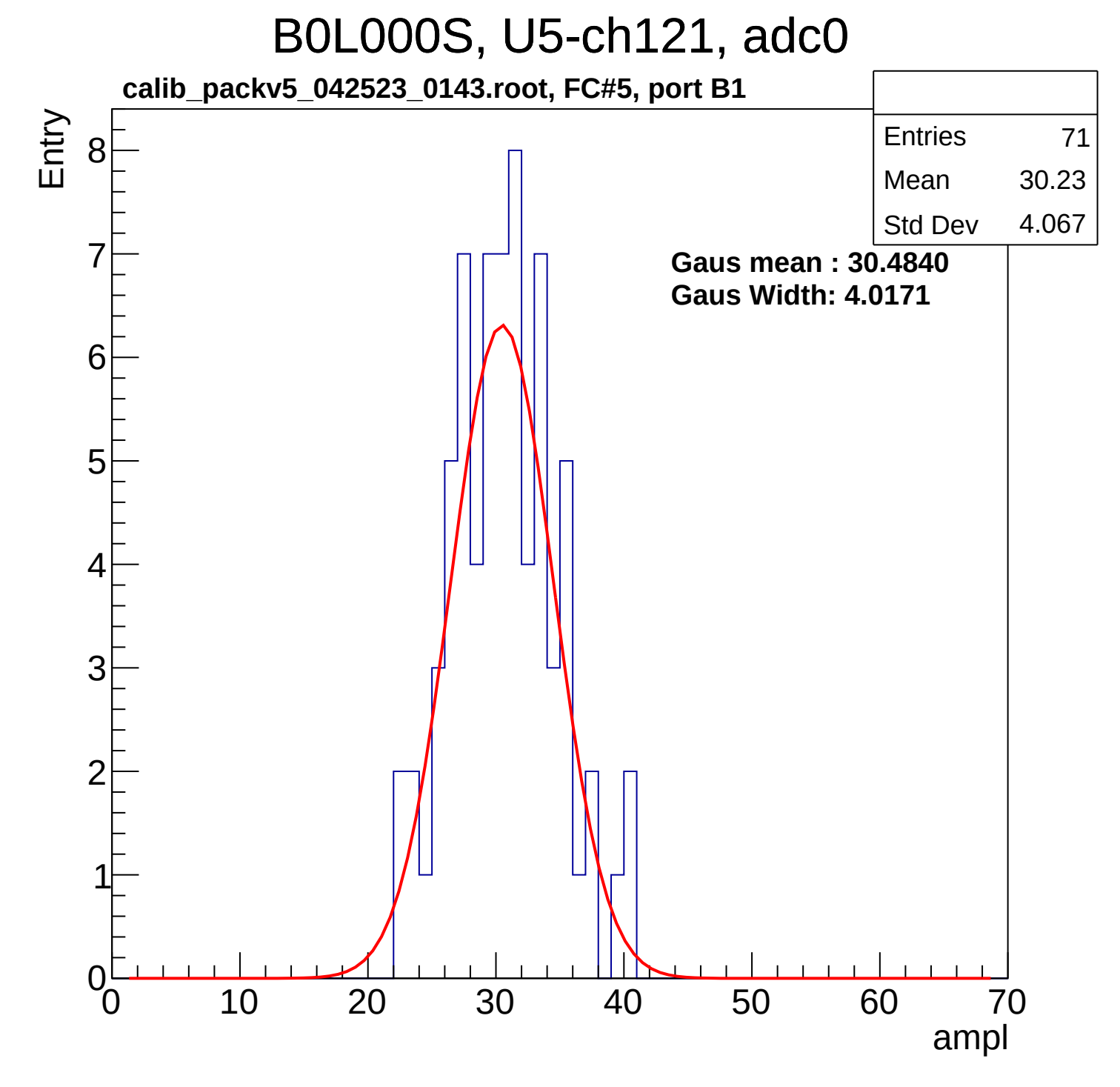
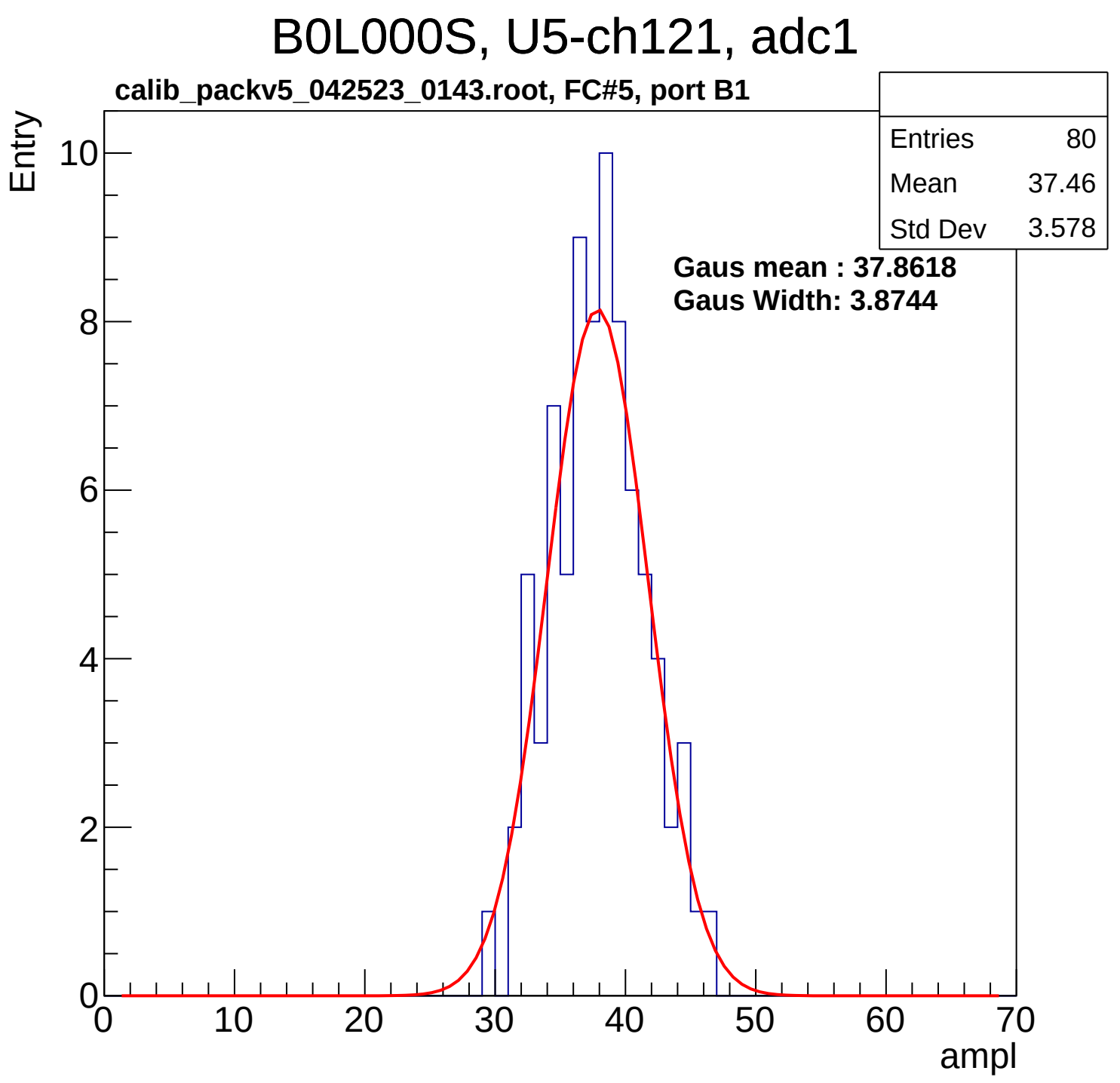
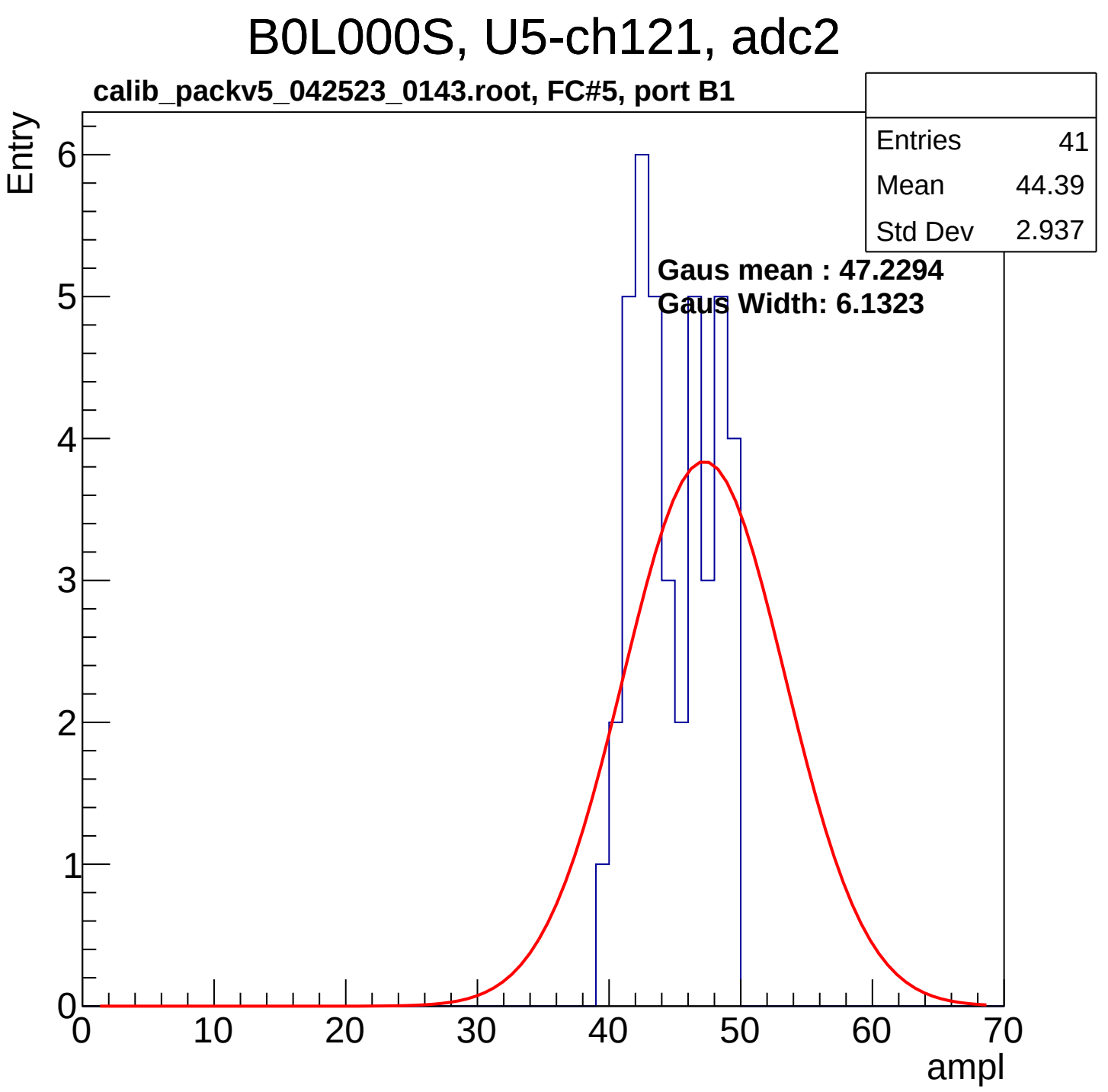

B0L000S, U5-ch120, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch121, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 30.23
Std Dev 4.067
Gaus mean : 30.4840
Gaus Width: 4.0171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch121, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 80
Mean 37.46
Std Dev 3.578
Gaus mean : 37.8618
Gaus Width: 3.8744
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch121, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 41
Mean 44.39
Std Dev 2.937
Gaus mean : 47.2294
Gaus Width: 6.1323
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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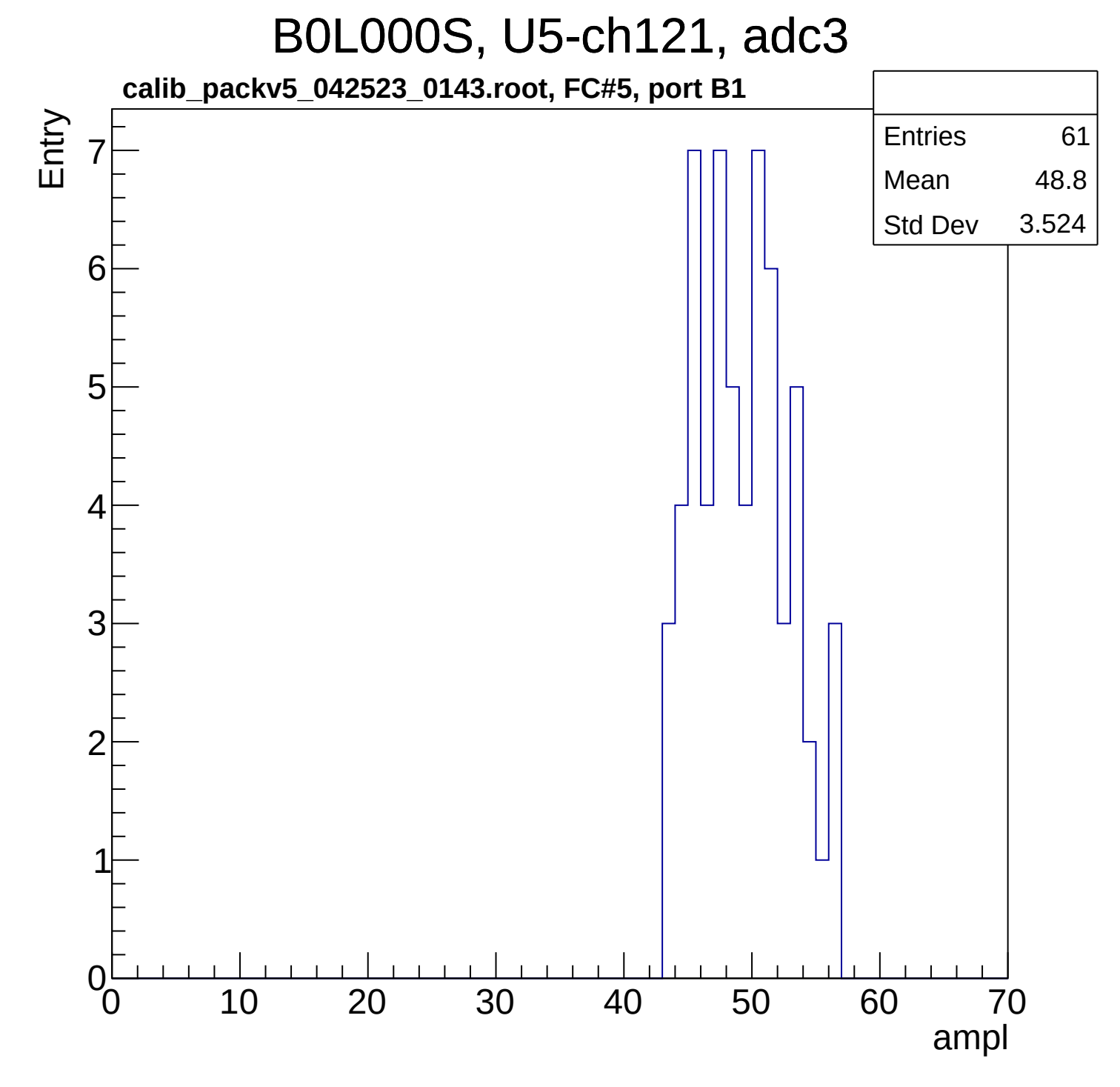
B0L000S, U5-ch121, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 48.8
Std Dev 3.524
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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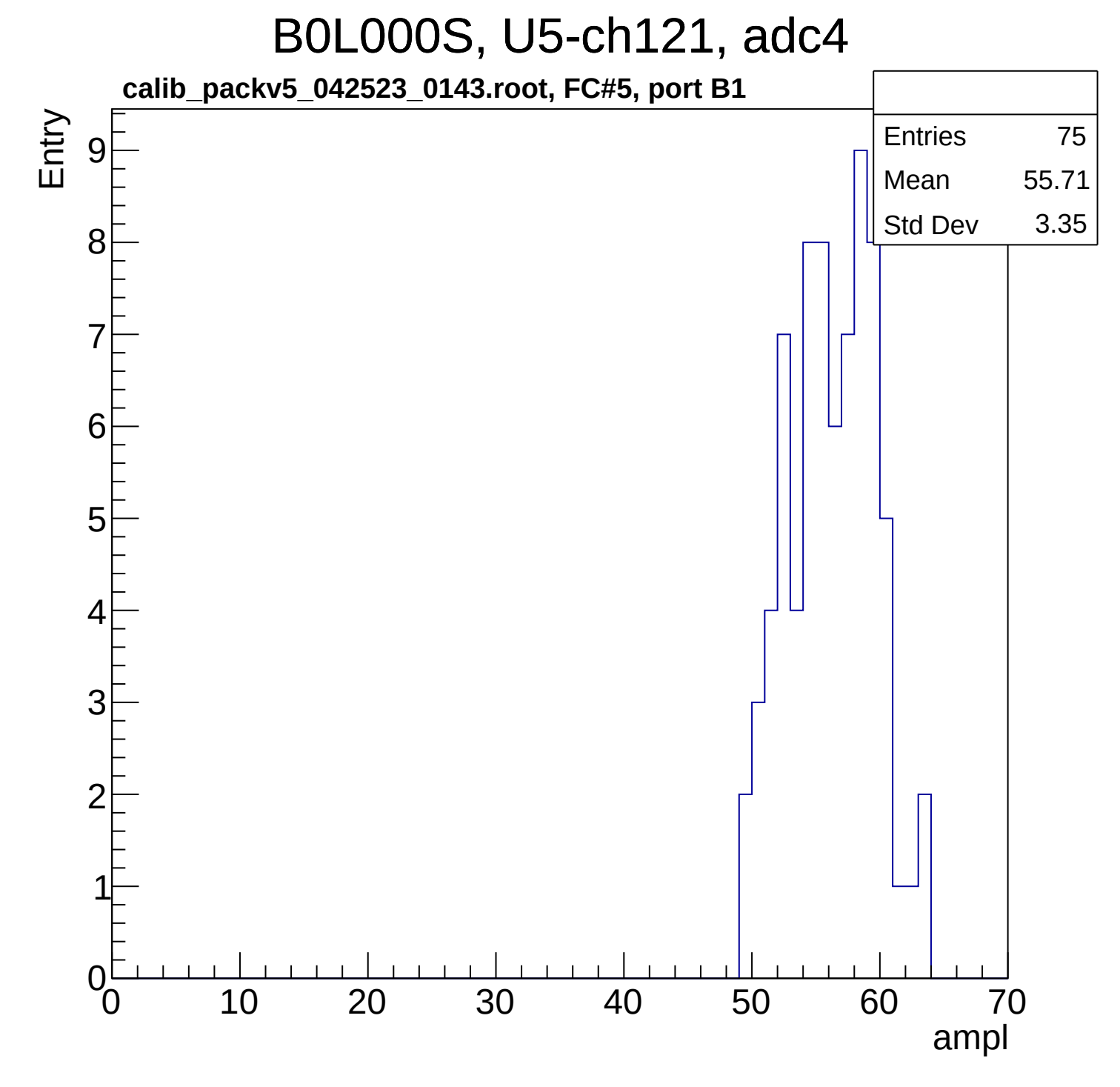
60
70


B0L000S, U5-ch121, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 55.71
Std Dev 3.35
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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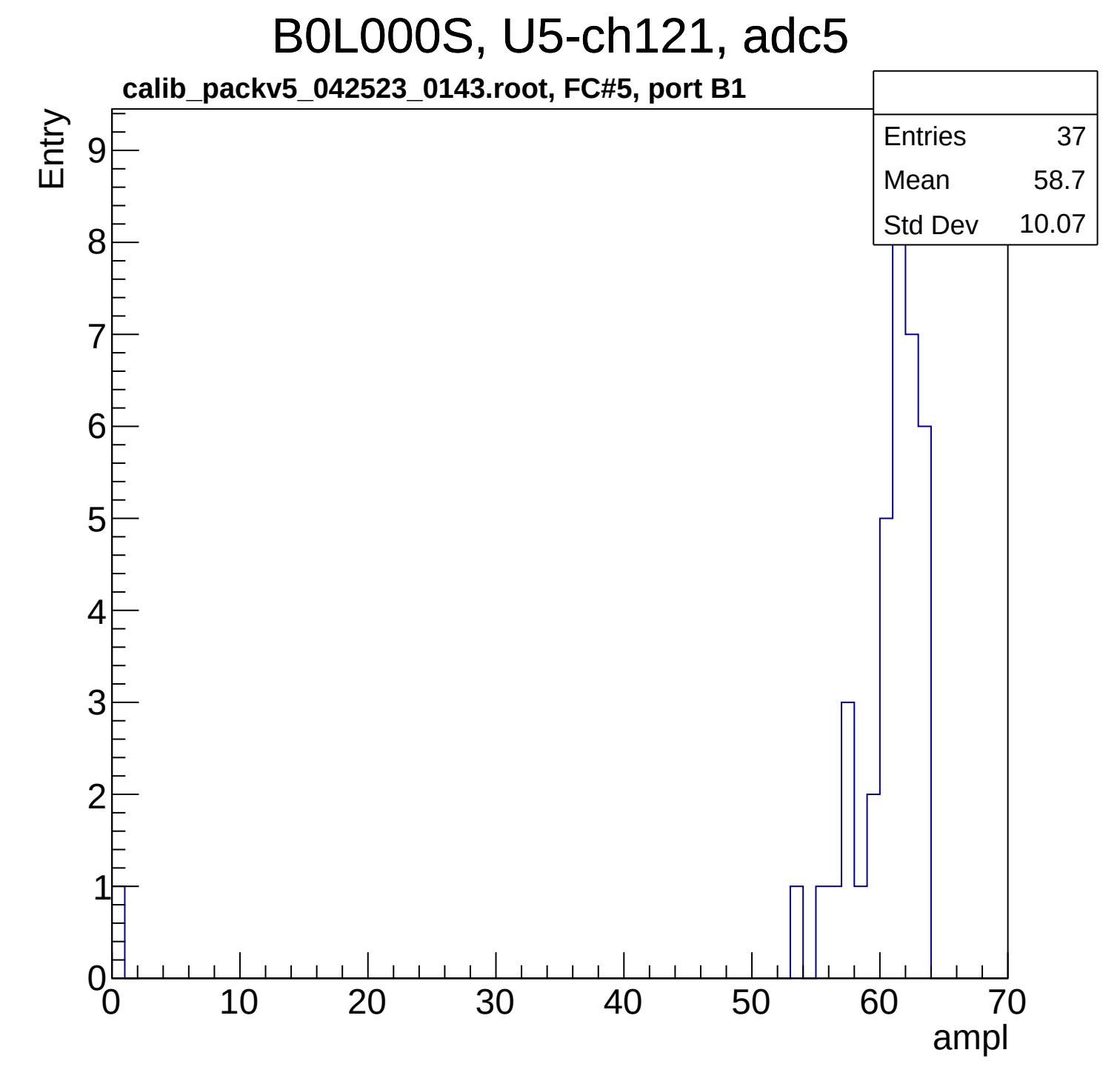
60
70


B0L000S, U5-ch121, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.7
Std Dev 10.07
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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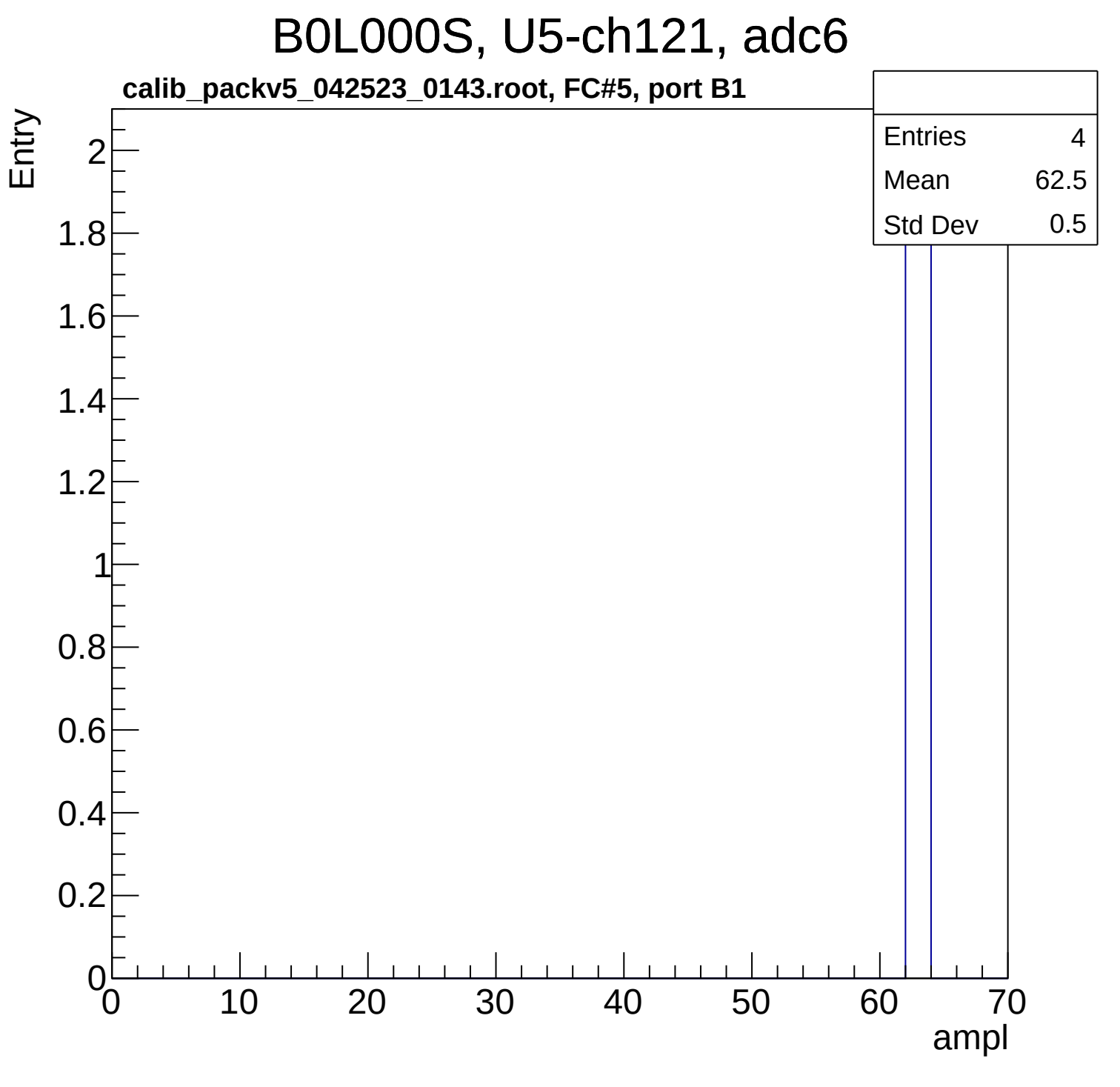
B0L000S, U5-ch121, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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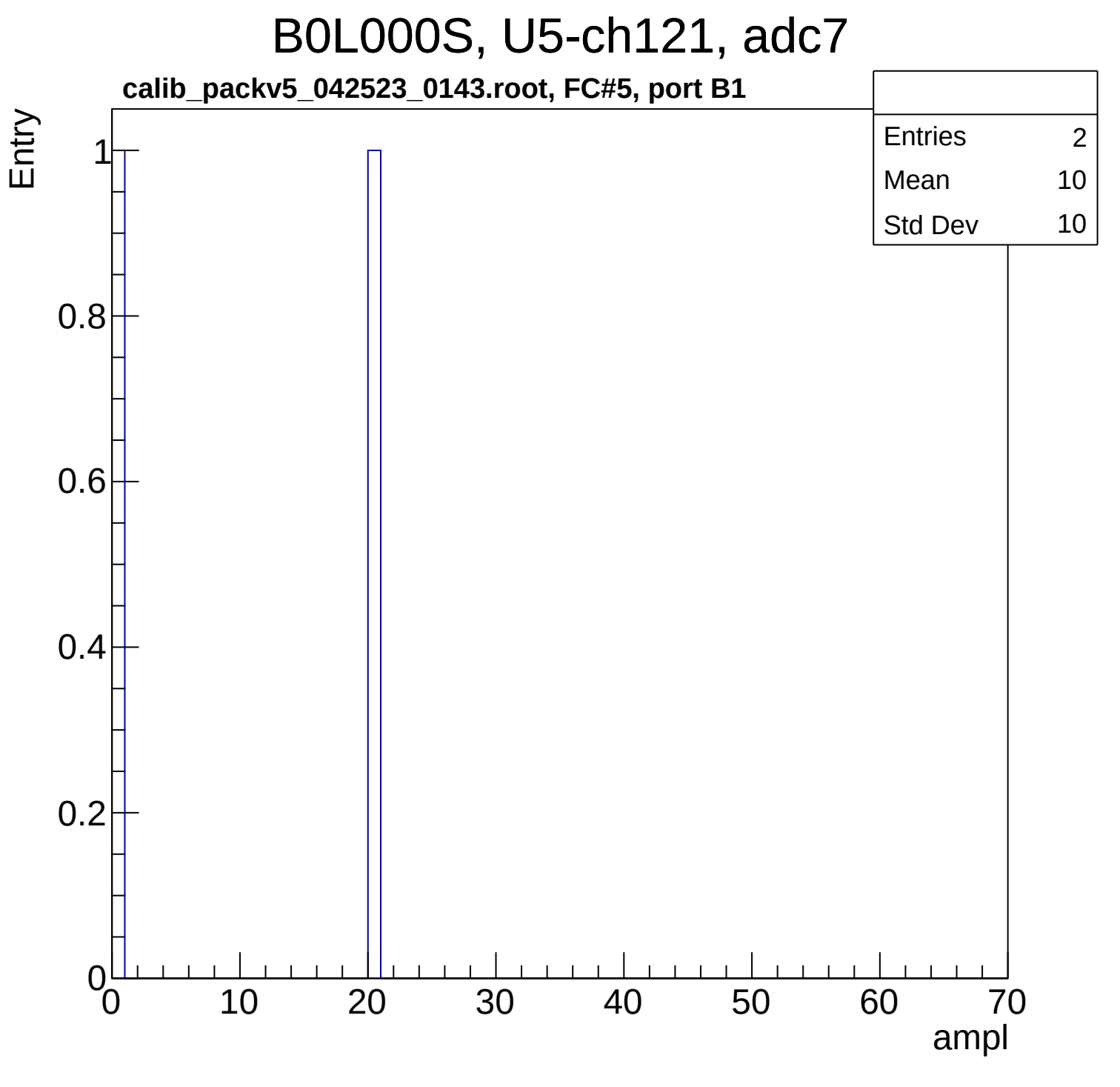
60
70


B0L000S, U5-ch121, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 10
Std Dev 10
Entry
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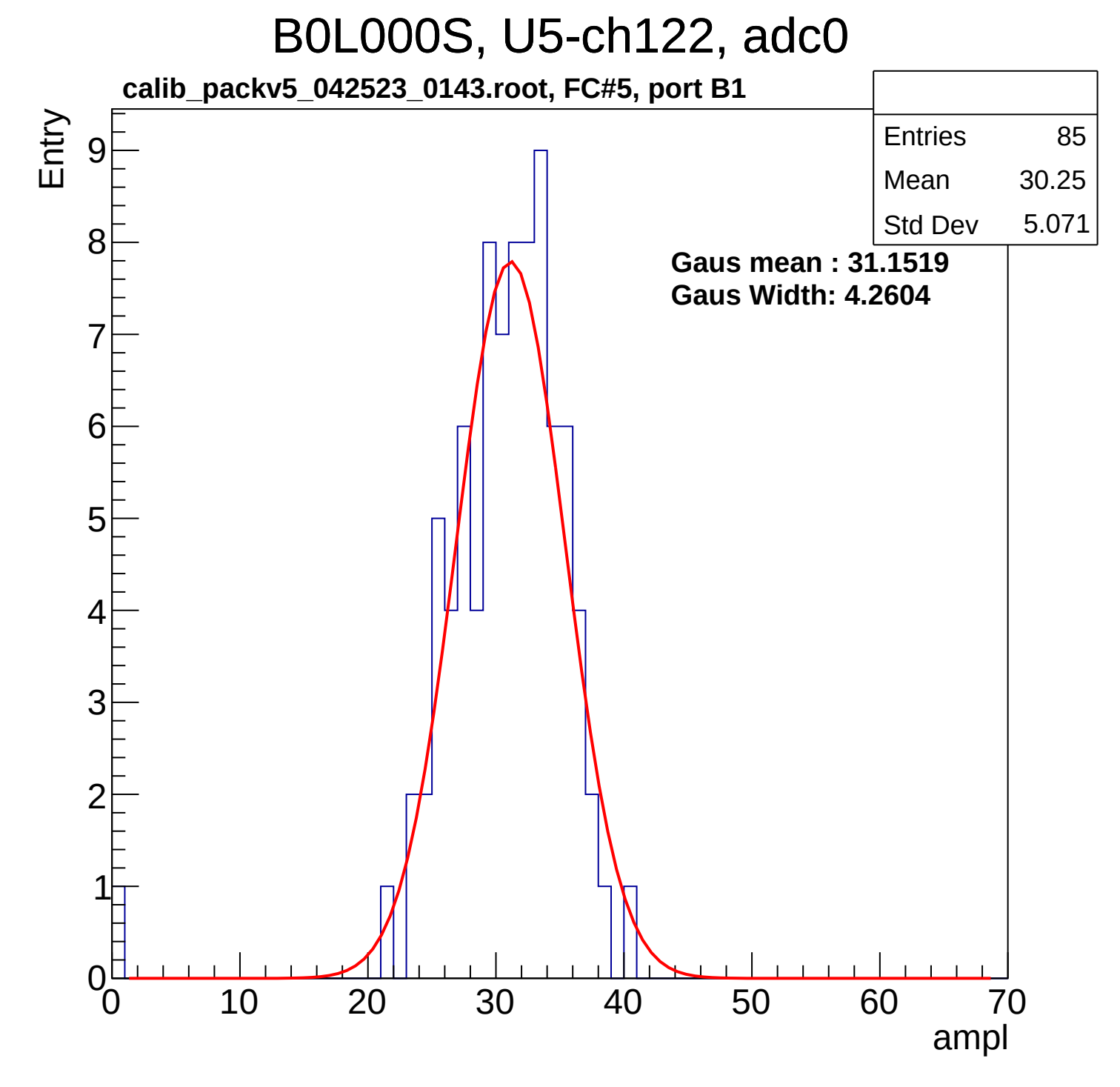
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch122, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 85
Mean 30.25
Std Dev 5.071
Gaus mean : 31.1519
Gaus Width: 4.2604
Entry
ampl
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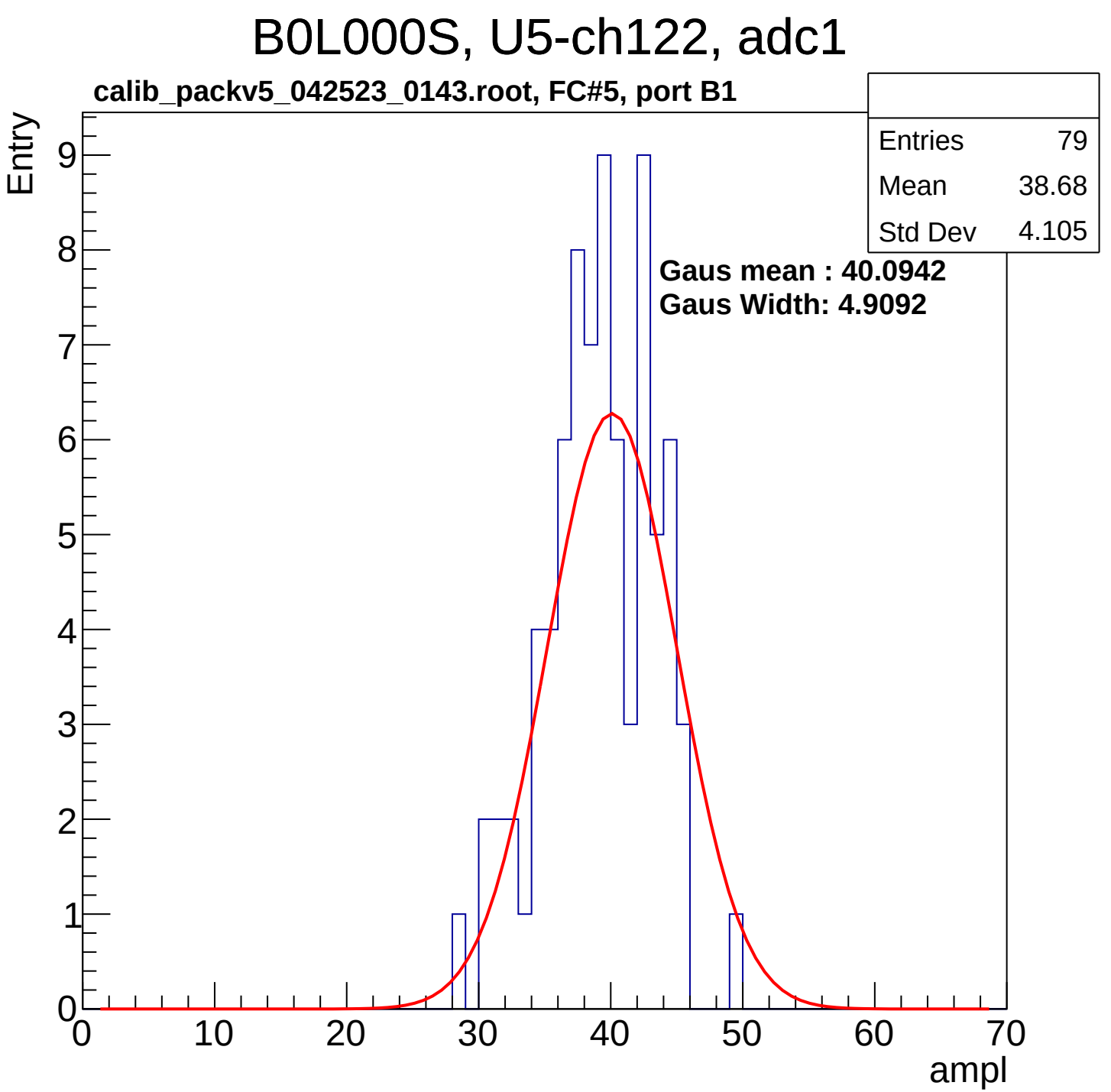
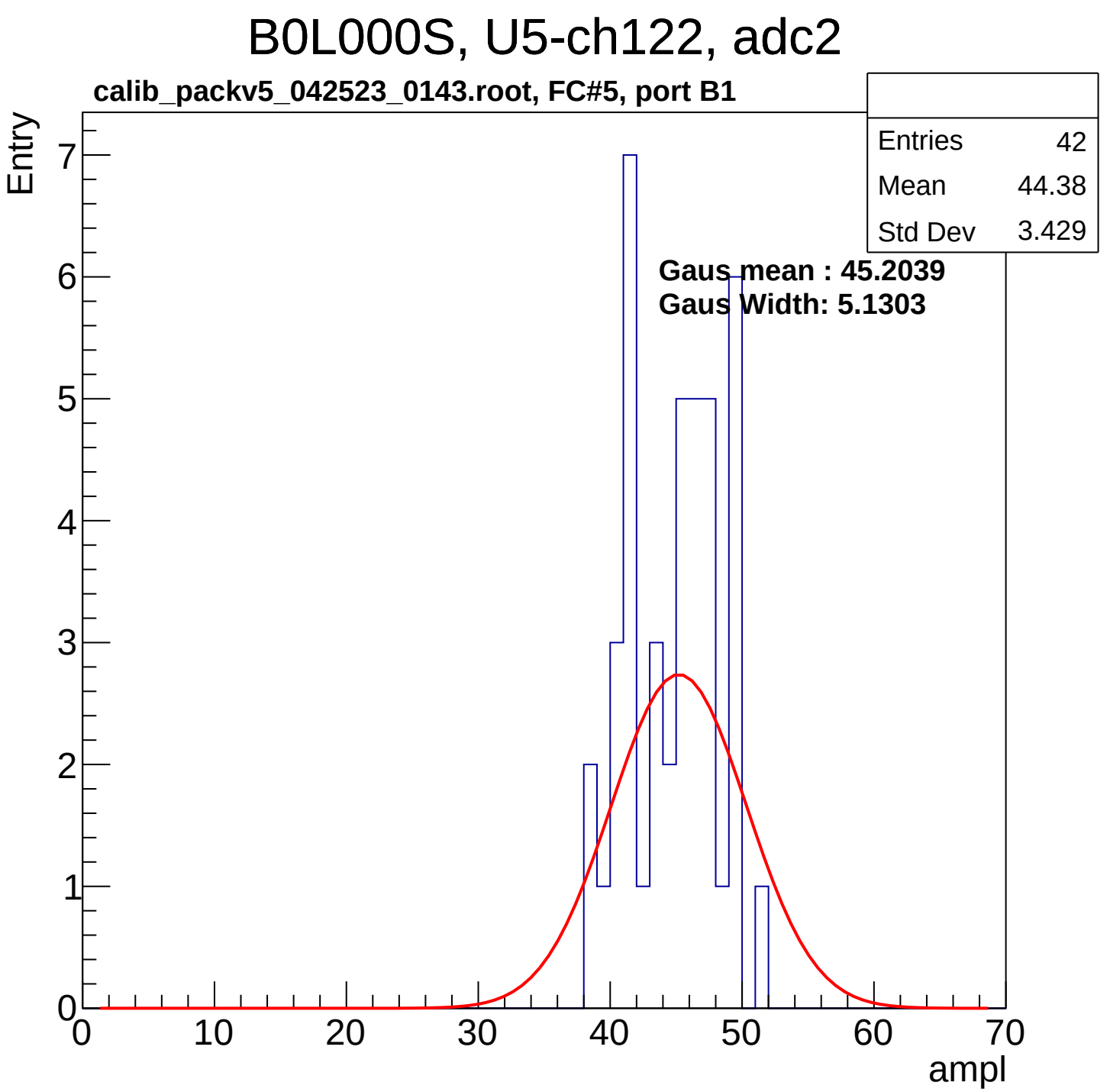
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch122, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 38.68
Std Dev 4.105
Gaus mean : 40.0942
Gaus Width: 4.9092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch122, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 42
Mean 44.38
Std Dev 3.429
Gaus mean : 45.2039
Gaus Width: 5.1303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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50
60
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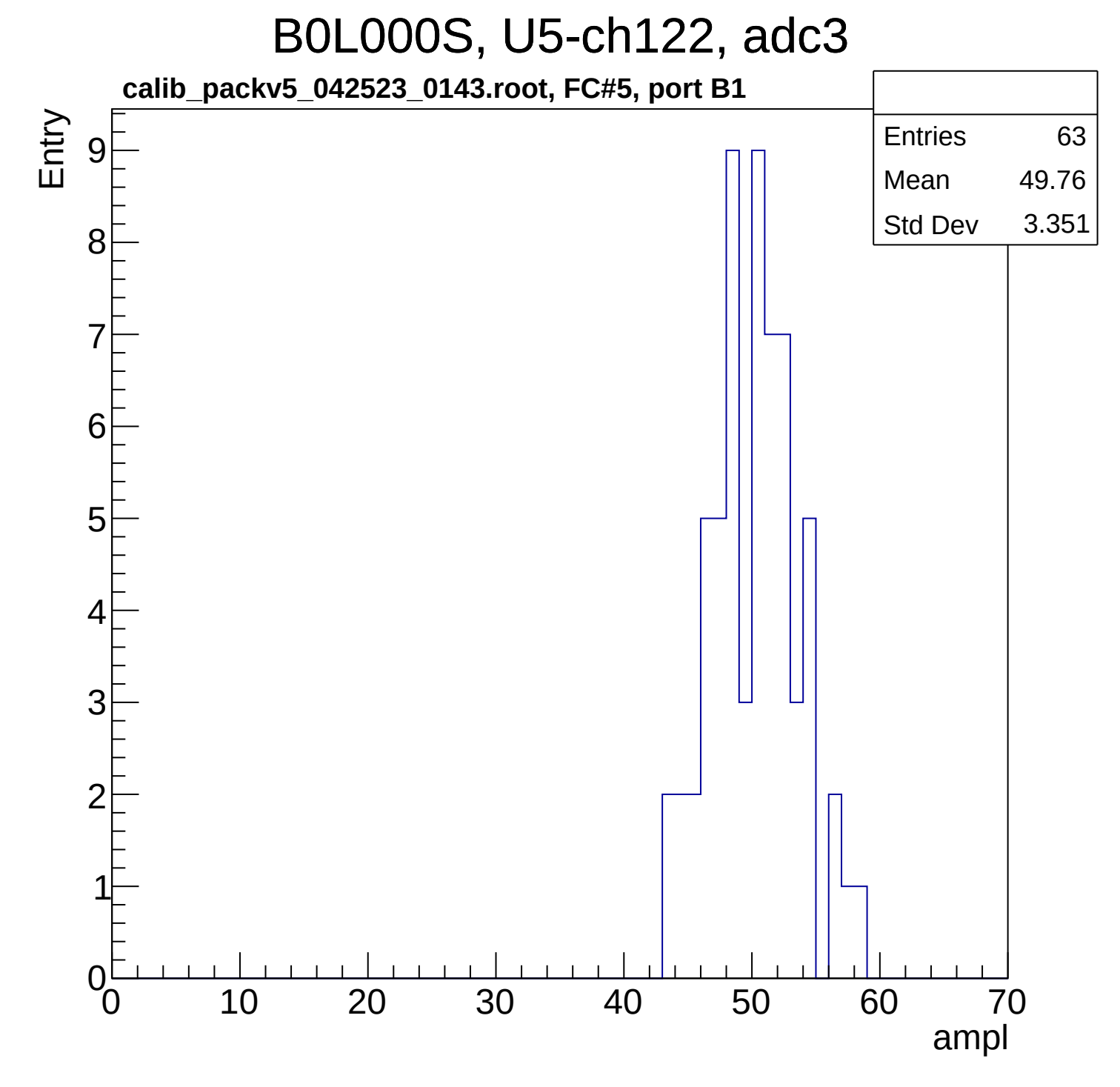
B0L000S, U5-ch122, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 49.76
Std Dev 3.351
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
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60
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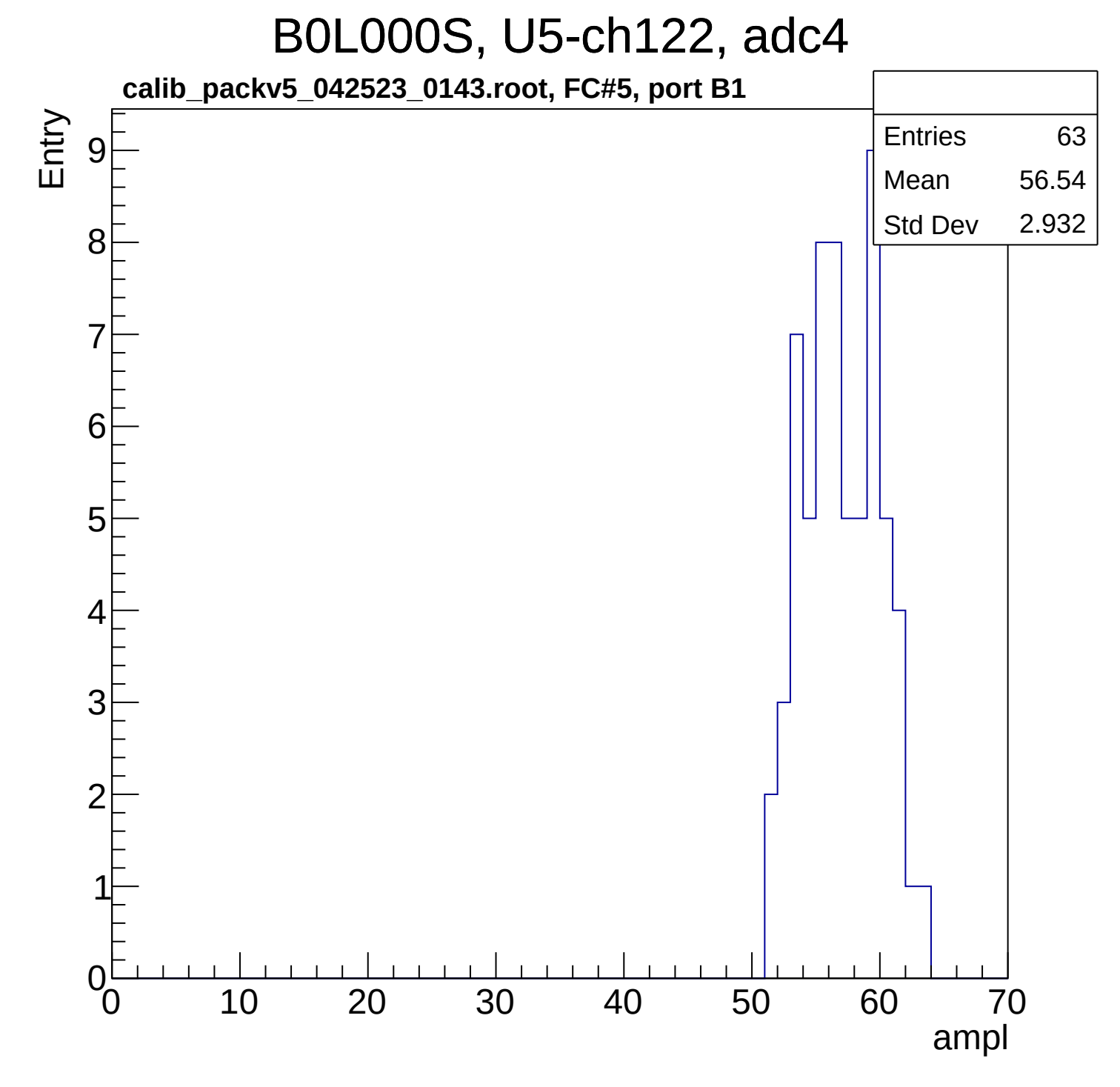
B0L000S, U5-ch122, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 63
Mean 56.54
Std Dev 2.932
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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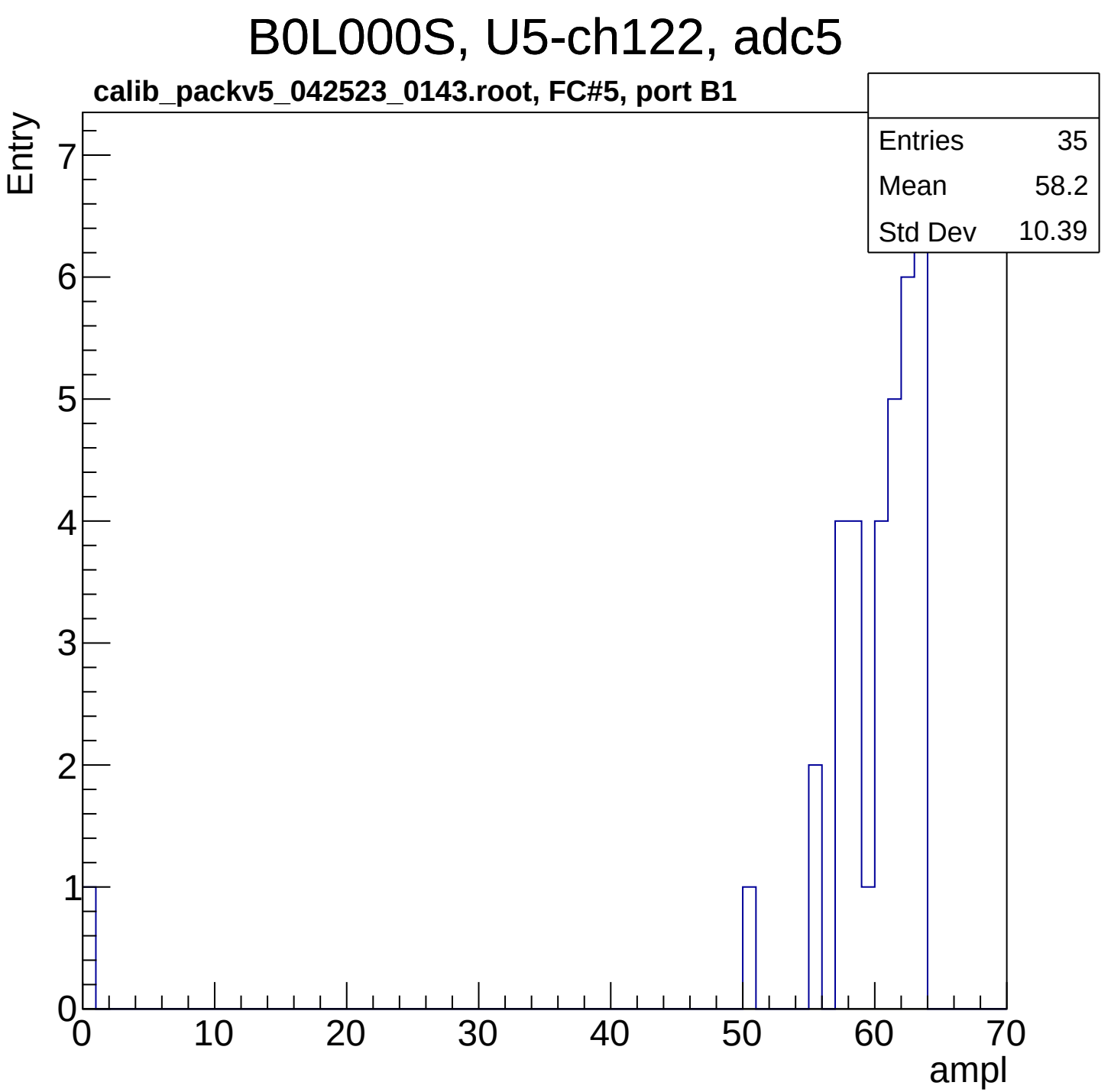
B0L000S, U5-ch122, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 35
Mean 58.2
Std Dev 10.39
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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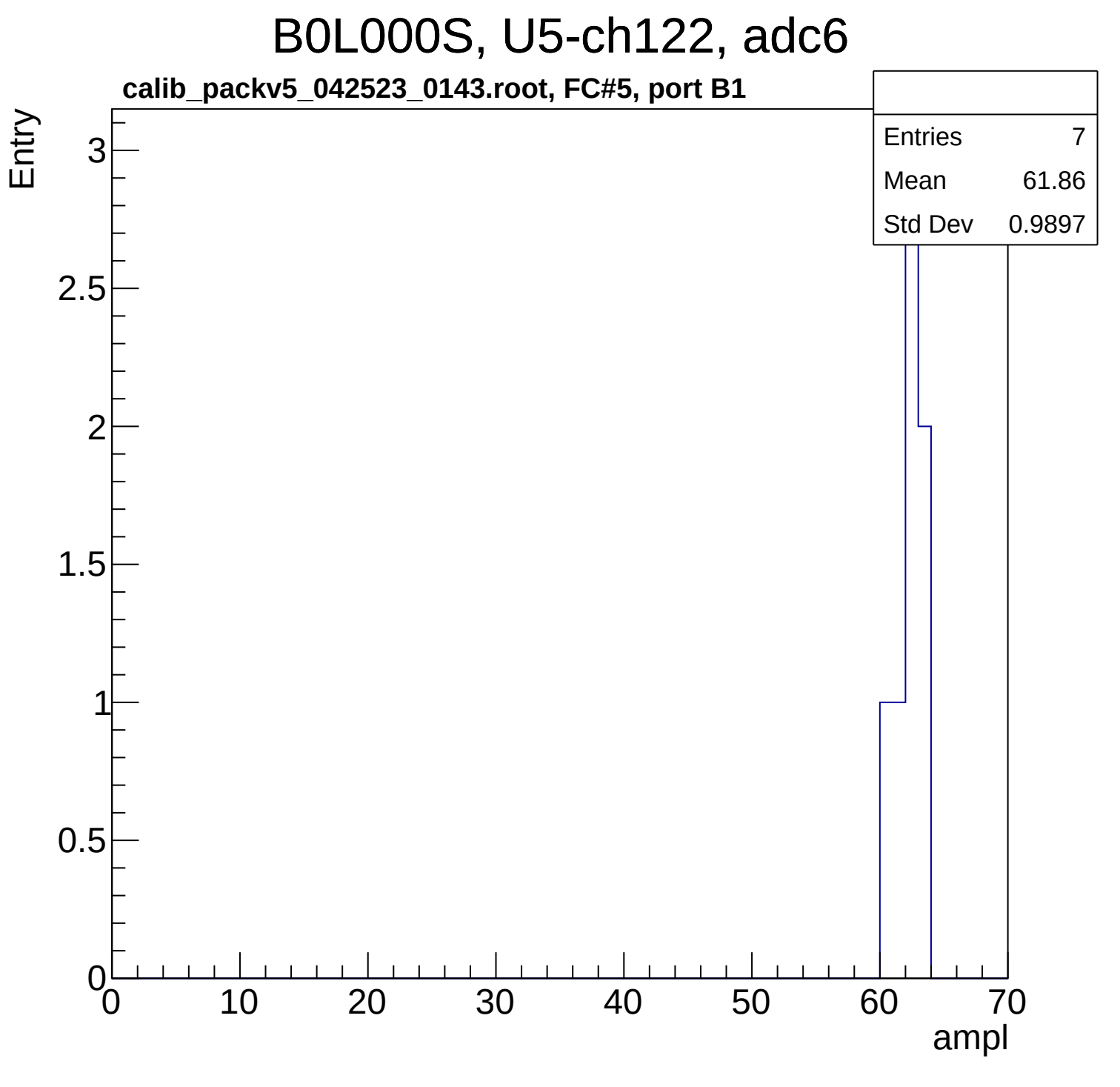
B0L000S, U5-ch122, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
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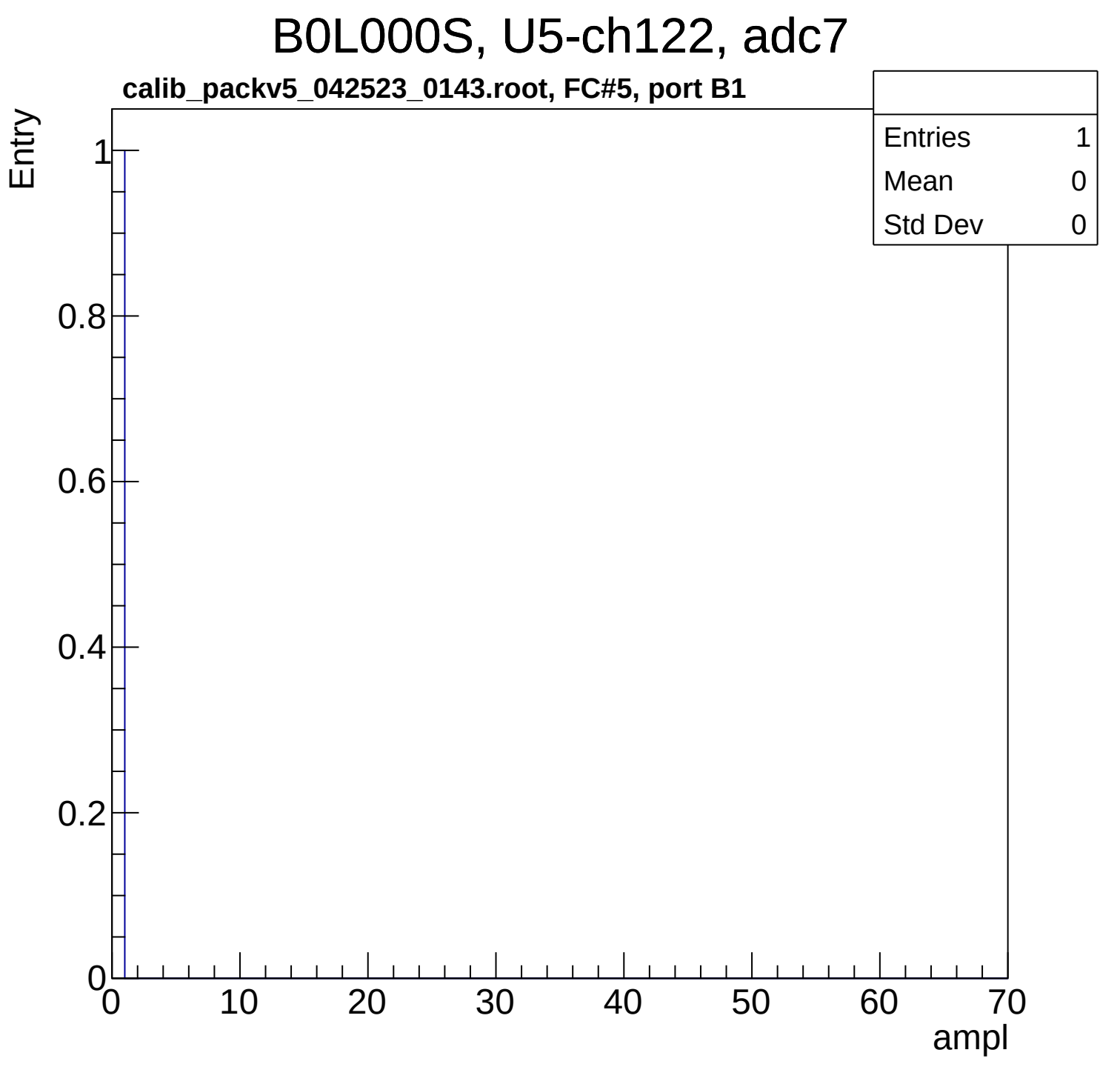
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L000S, U5-ch122, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
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0.4
0.2
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10
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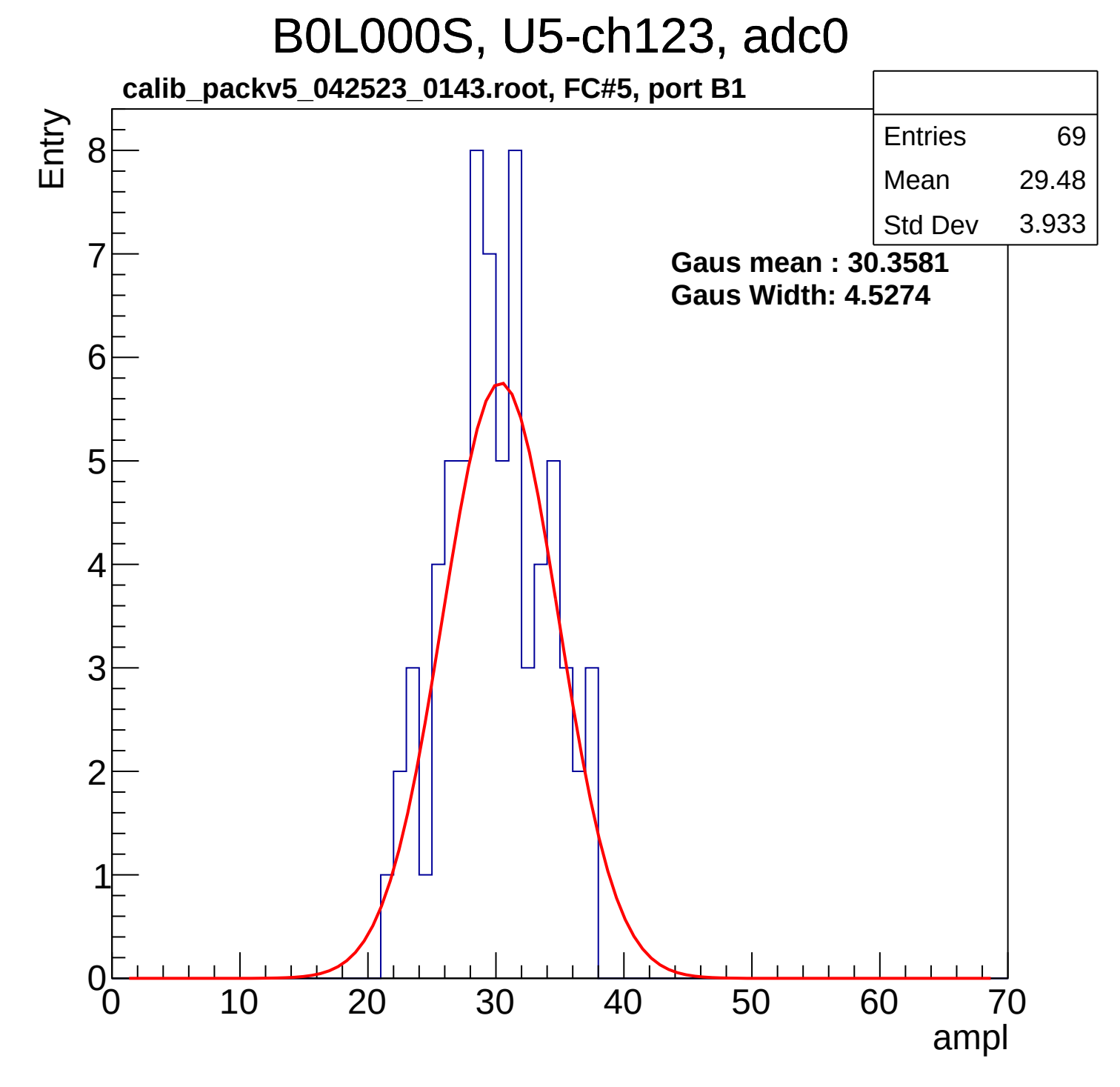
50
60
70
ampl


B0L000S, U5-ch123, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 29.48
Std Dev 3.933
Gaus mean : 30.3581
Gaus Width: 4.5274
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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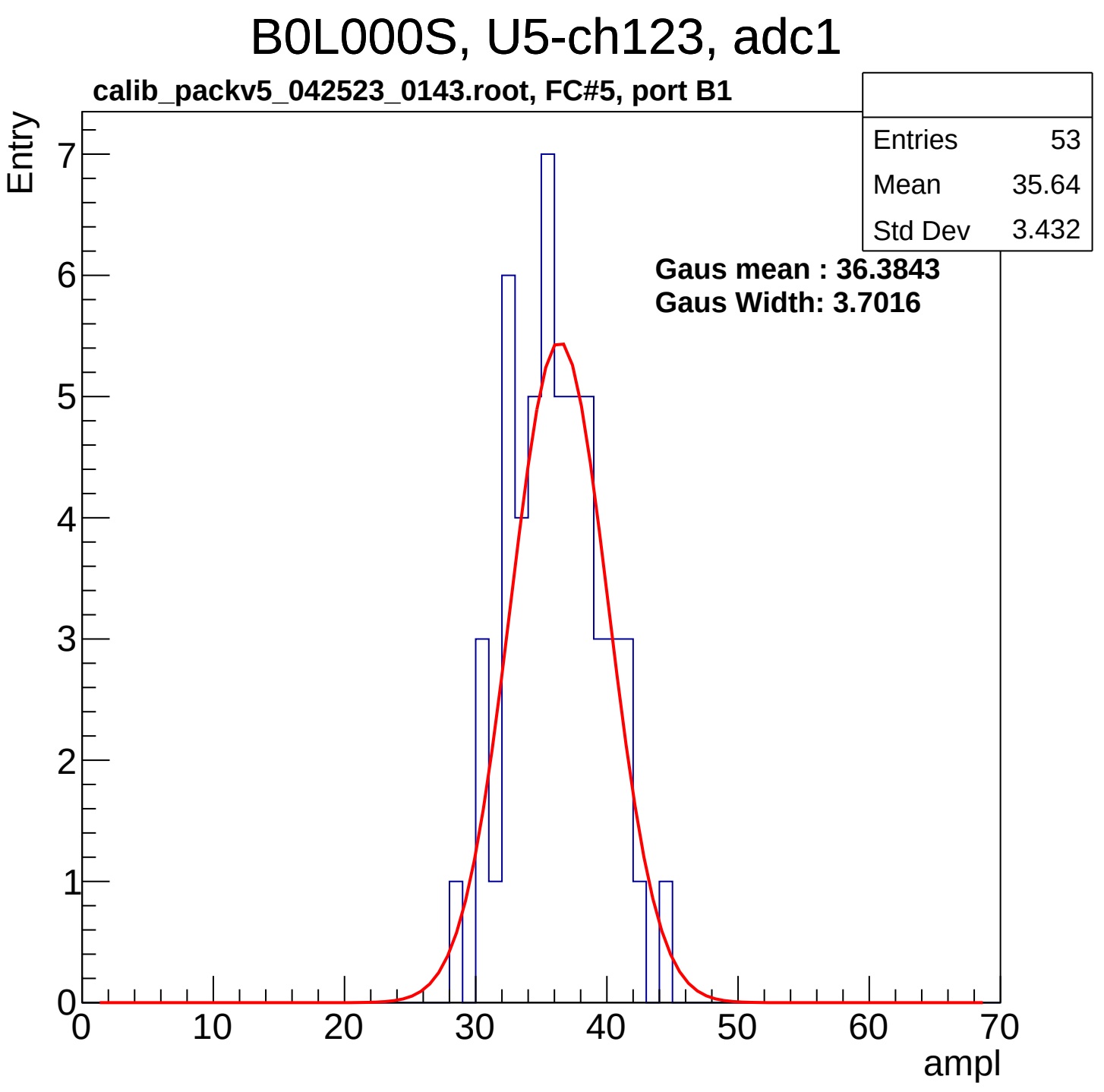
B0L000S, U5-ch123, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 35.64
Std Dev 3.432
Gaus mean : 36.3843
Gaus Width: 3.7016
Entry
ampl
0
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2
3
4
5
6
7
0
10
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60
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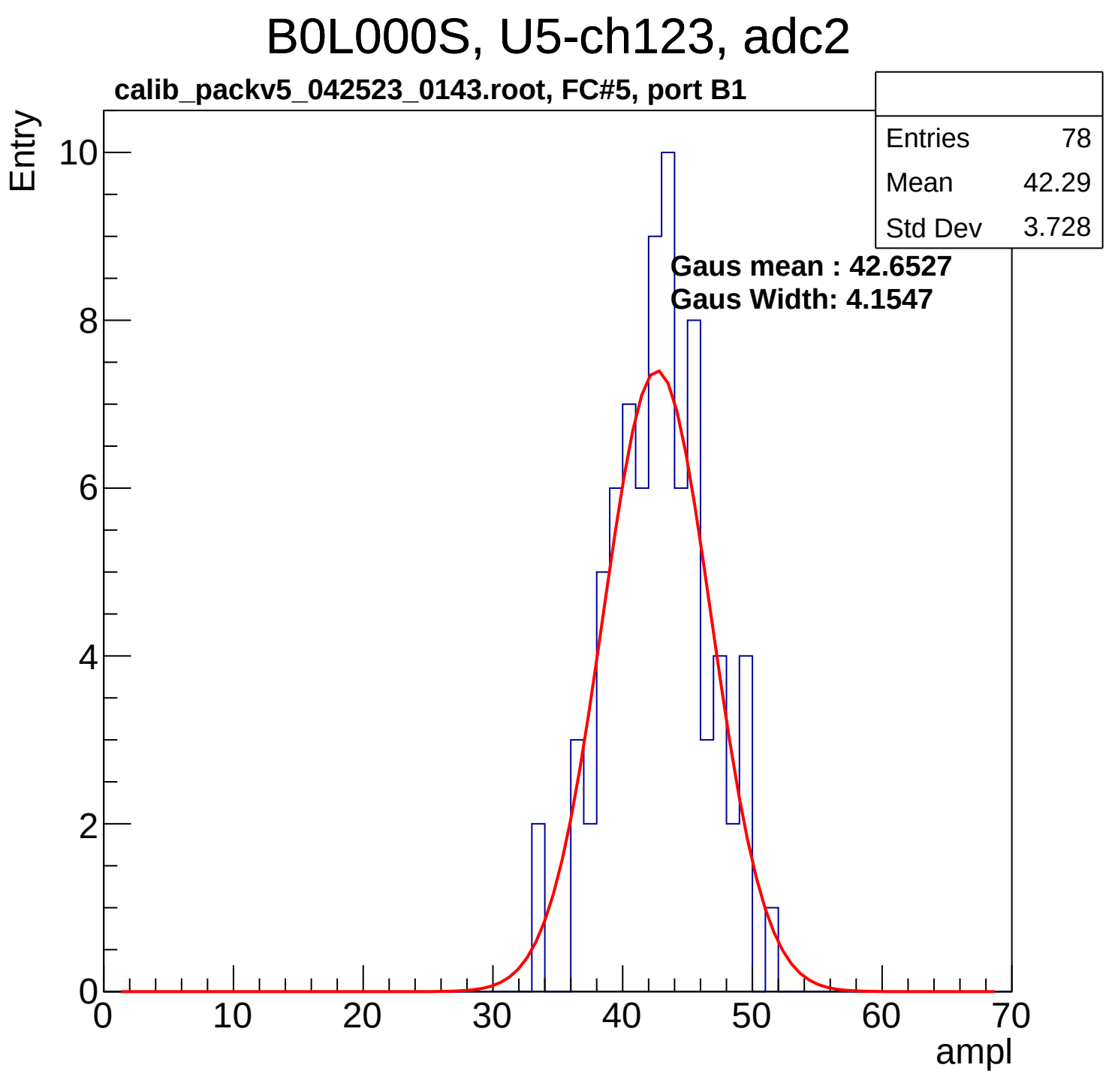
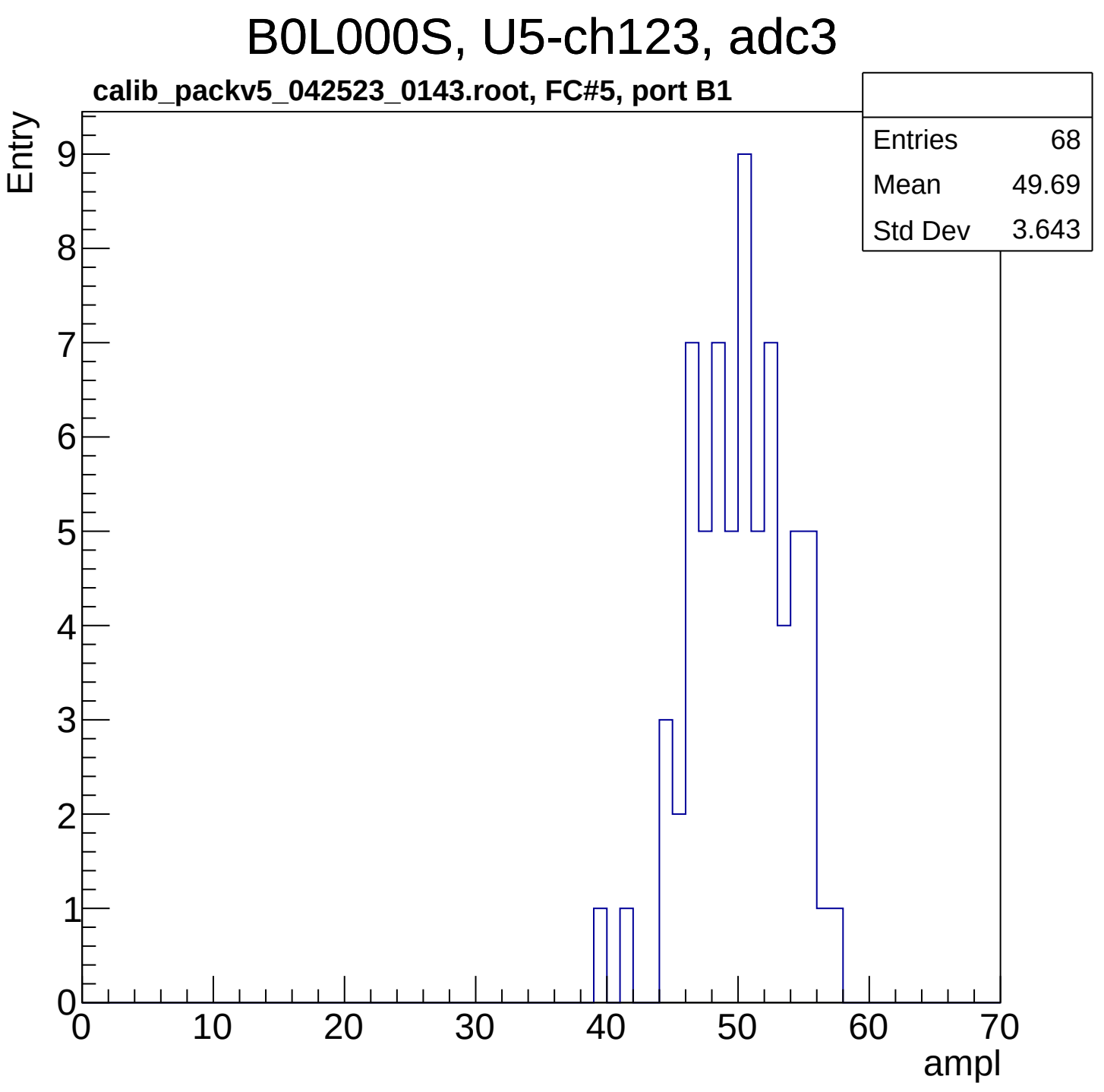

B0L000S, U5-ch123, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 78
Mean 42.29
Std Dev 3.728
Gaus mean : 42.6527
Gaus Width: 4.1547
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch123, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 68
Mean 49.69
Std Dev 3.643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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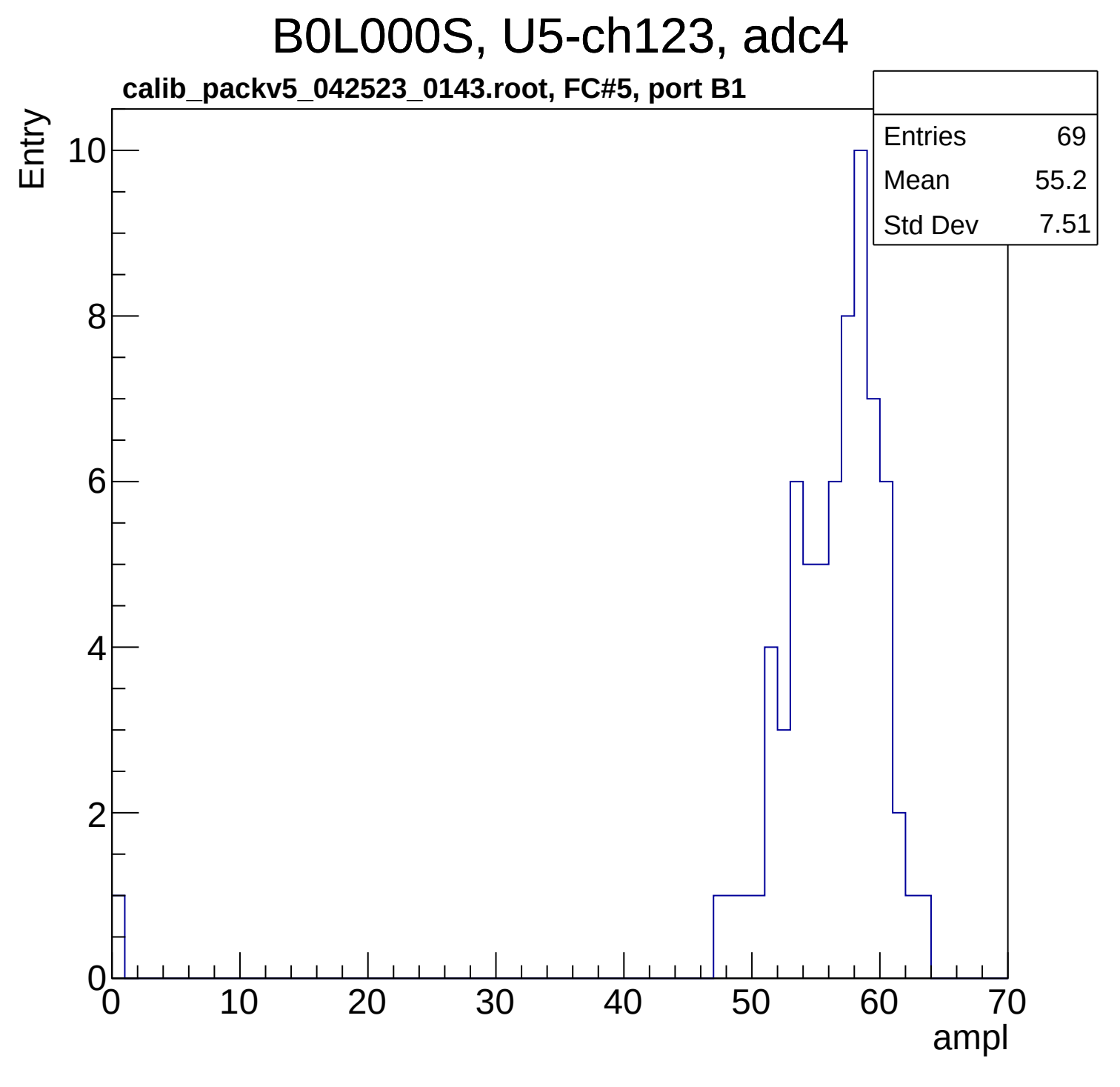
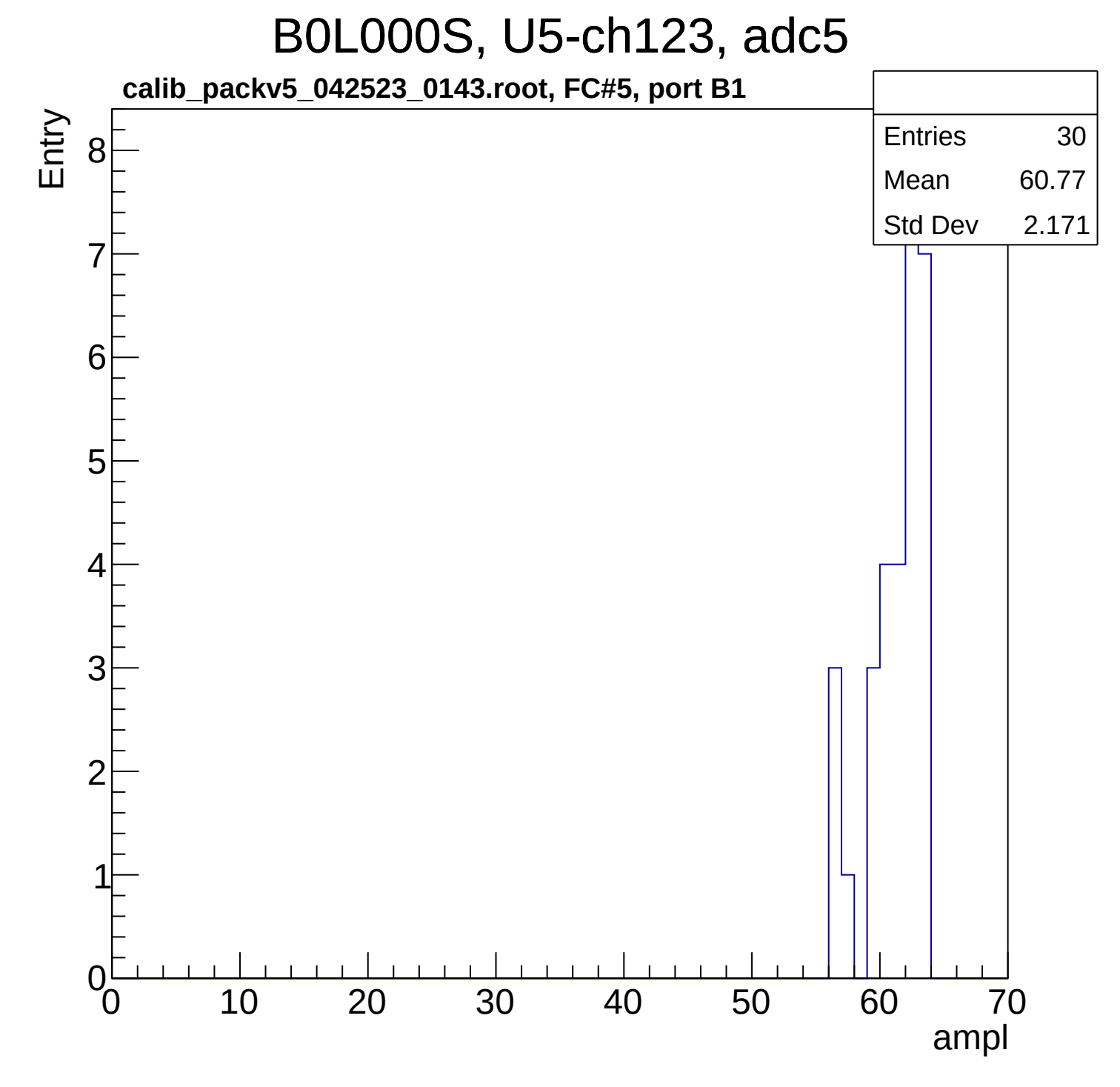
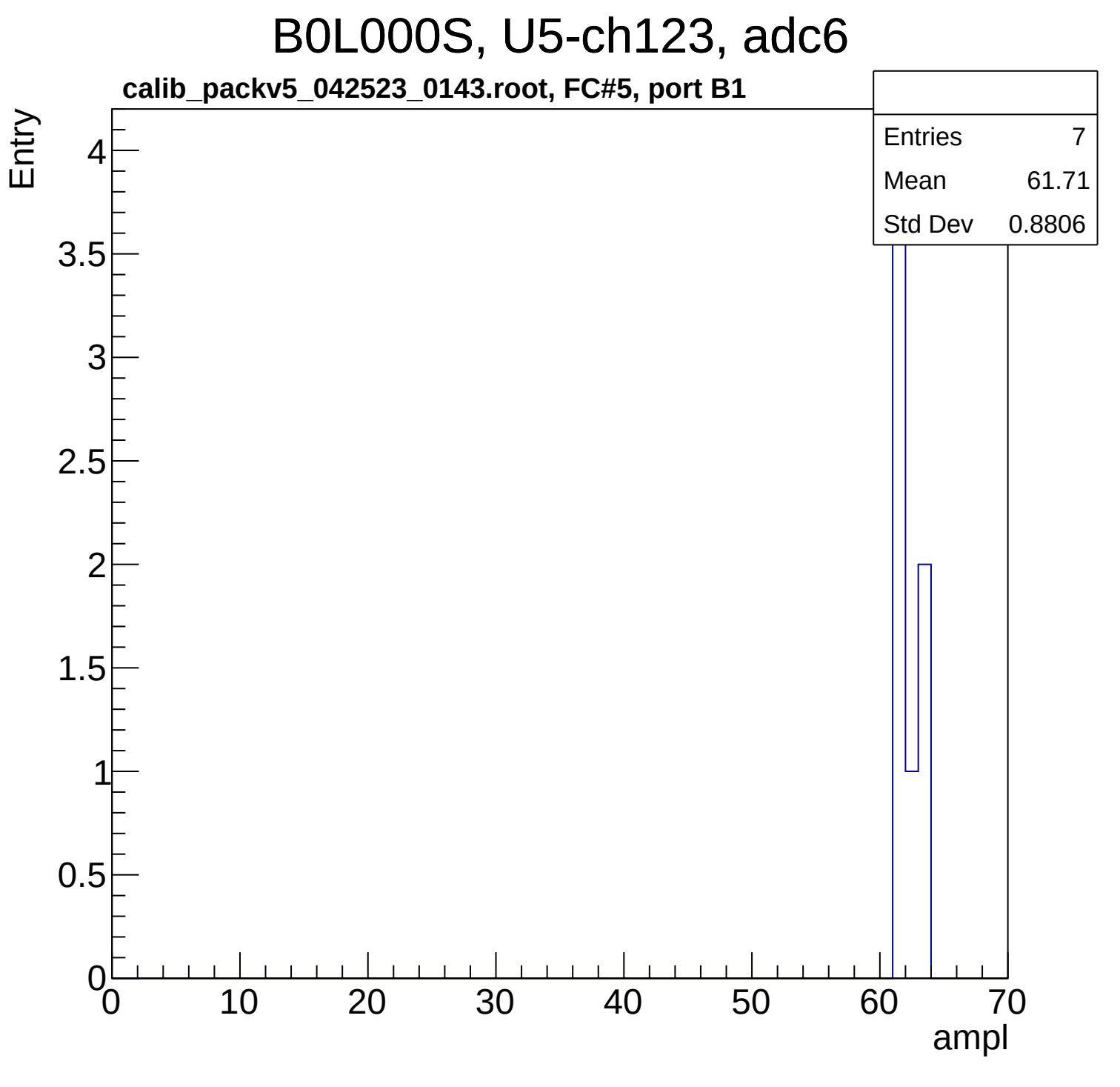
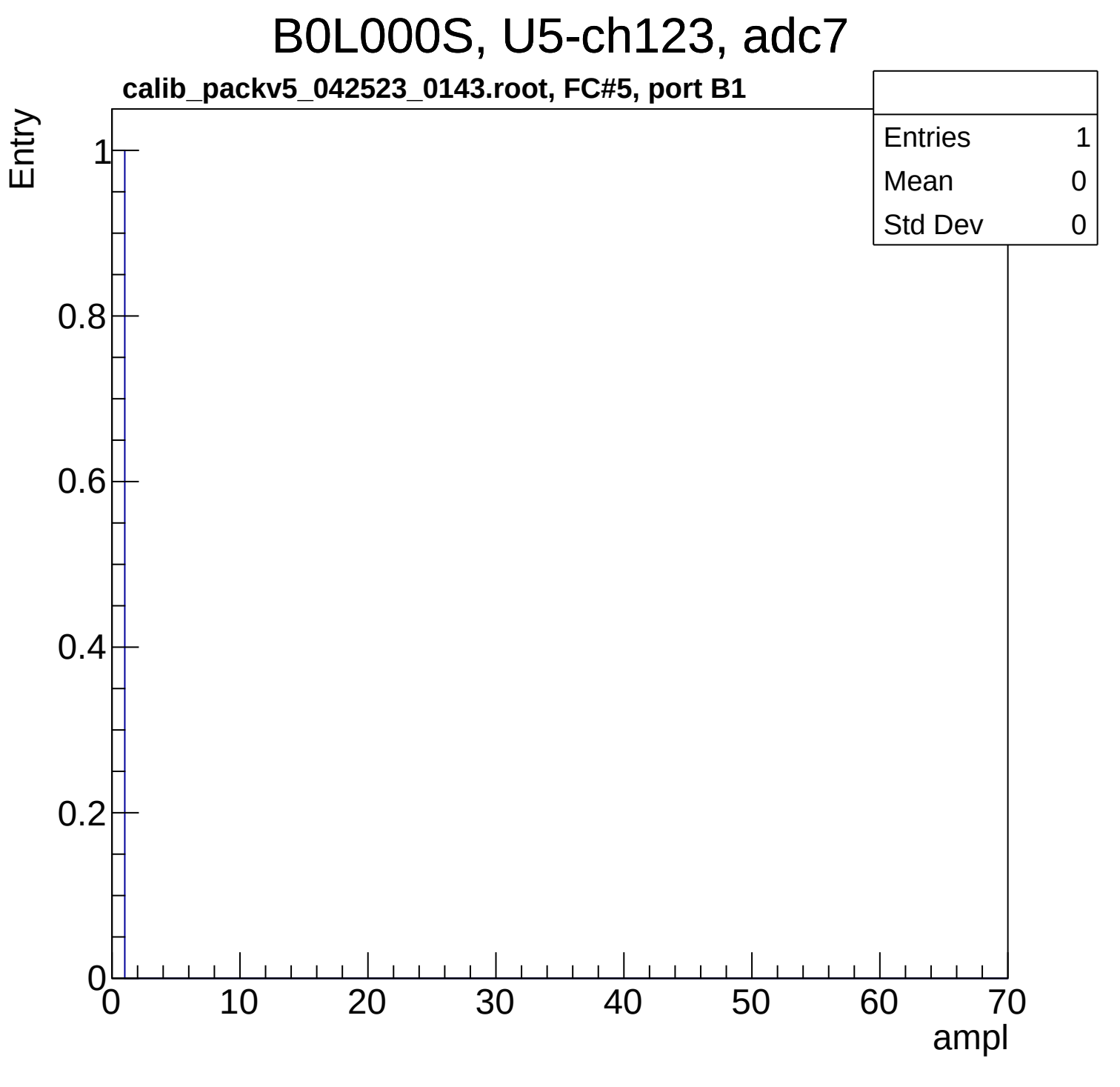
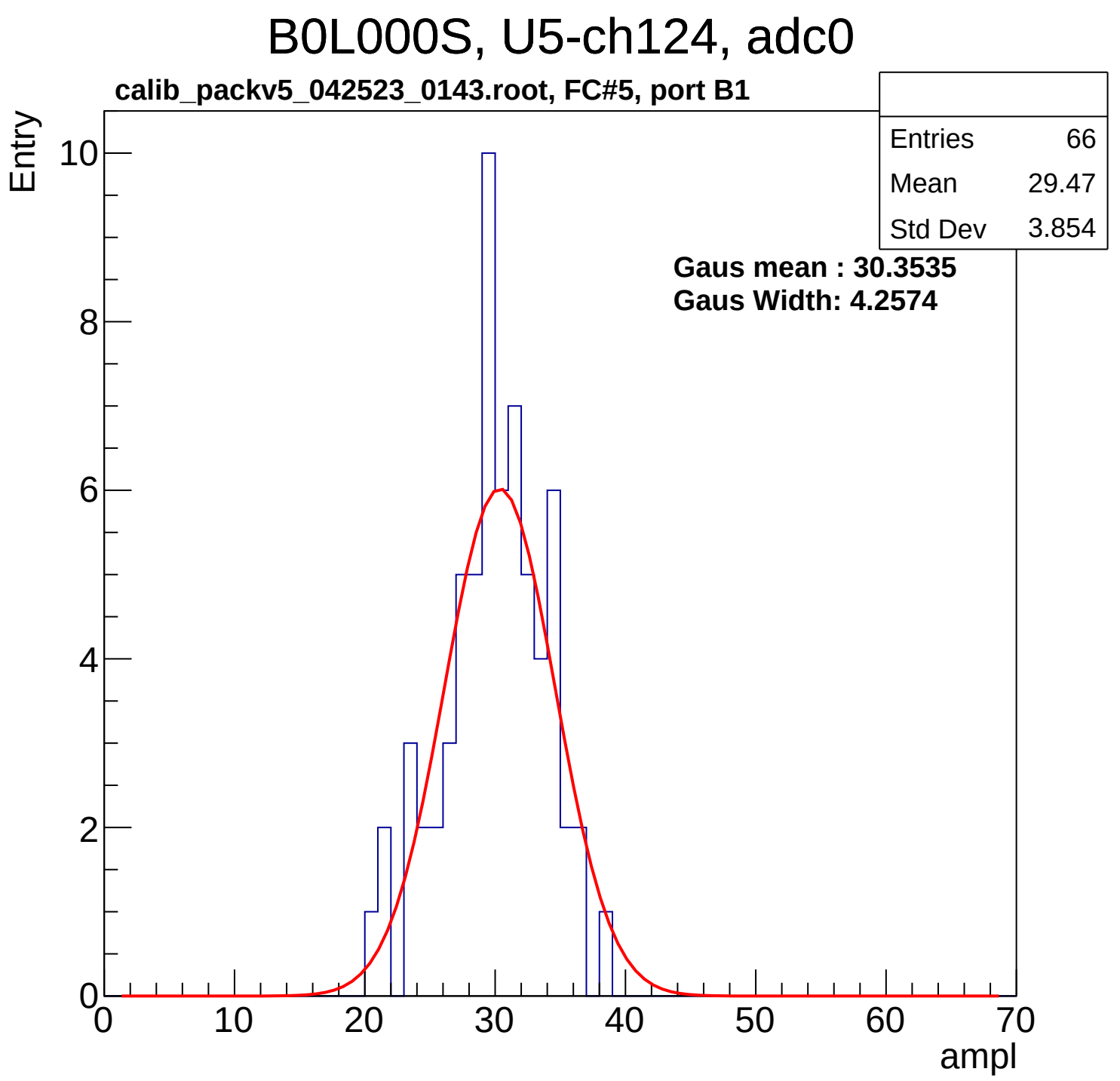
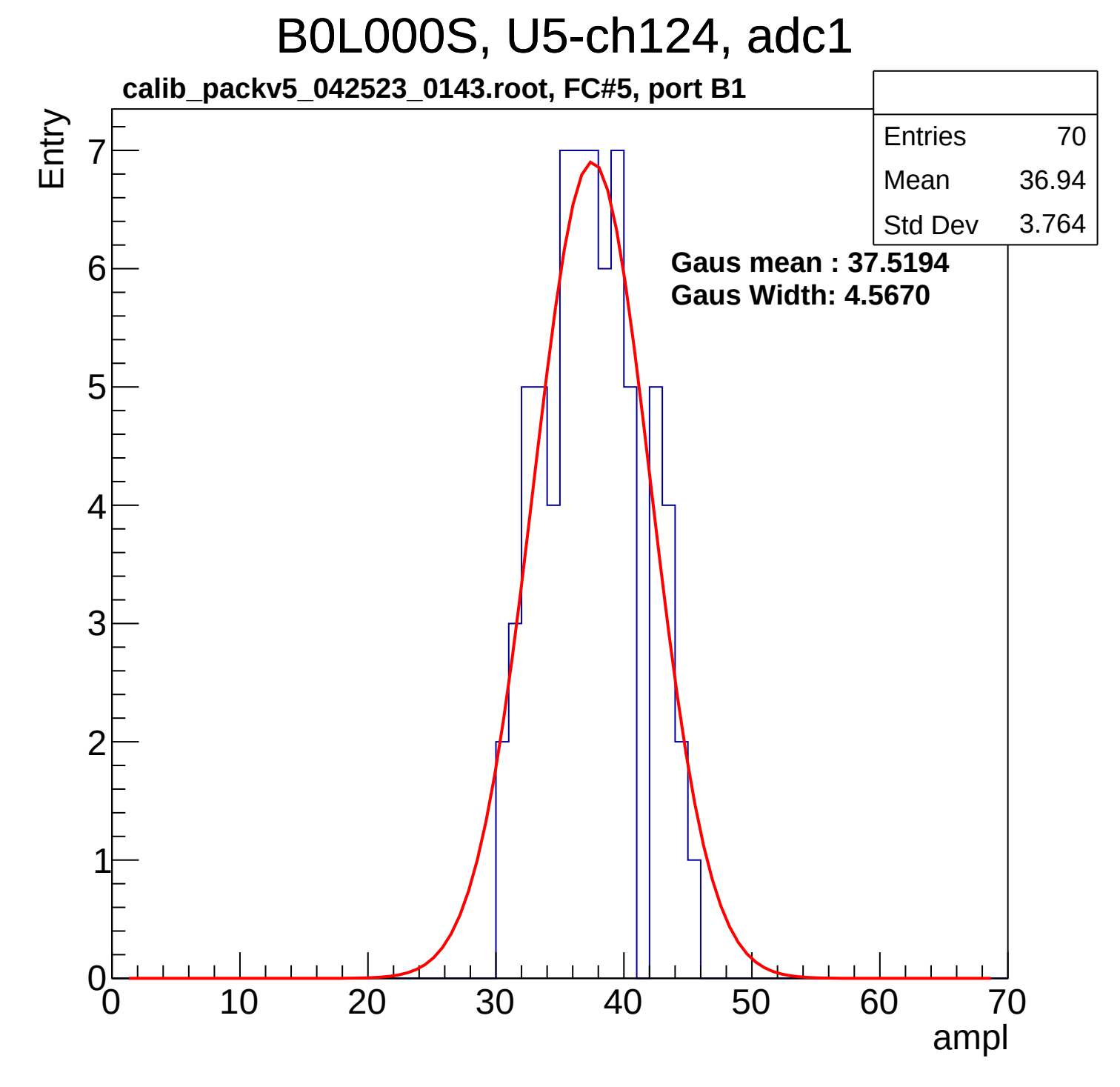

B0L000S, U5-ch123, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 69
Mean 55.2
Std Dev 7.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch123, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 30
Mean 60.77
Std Dev 2.171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch123, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 7
Mean 61.71
Std Dev 0.8806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L000S, U5-ch123, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch124, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 66
Mean 29.47
Std Dev 3.854
Gaus mean : 30.3535
Gaus Width: 4.2574
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch124, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 70
Mean 36.94
Std Dev 3.764
Gaus mean : 37.5194
Gaus Width: 4.5670
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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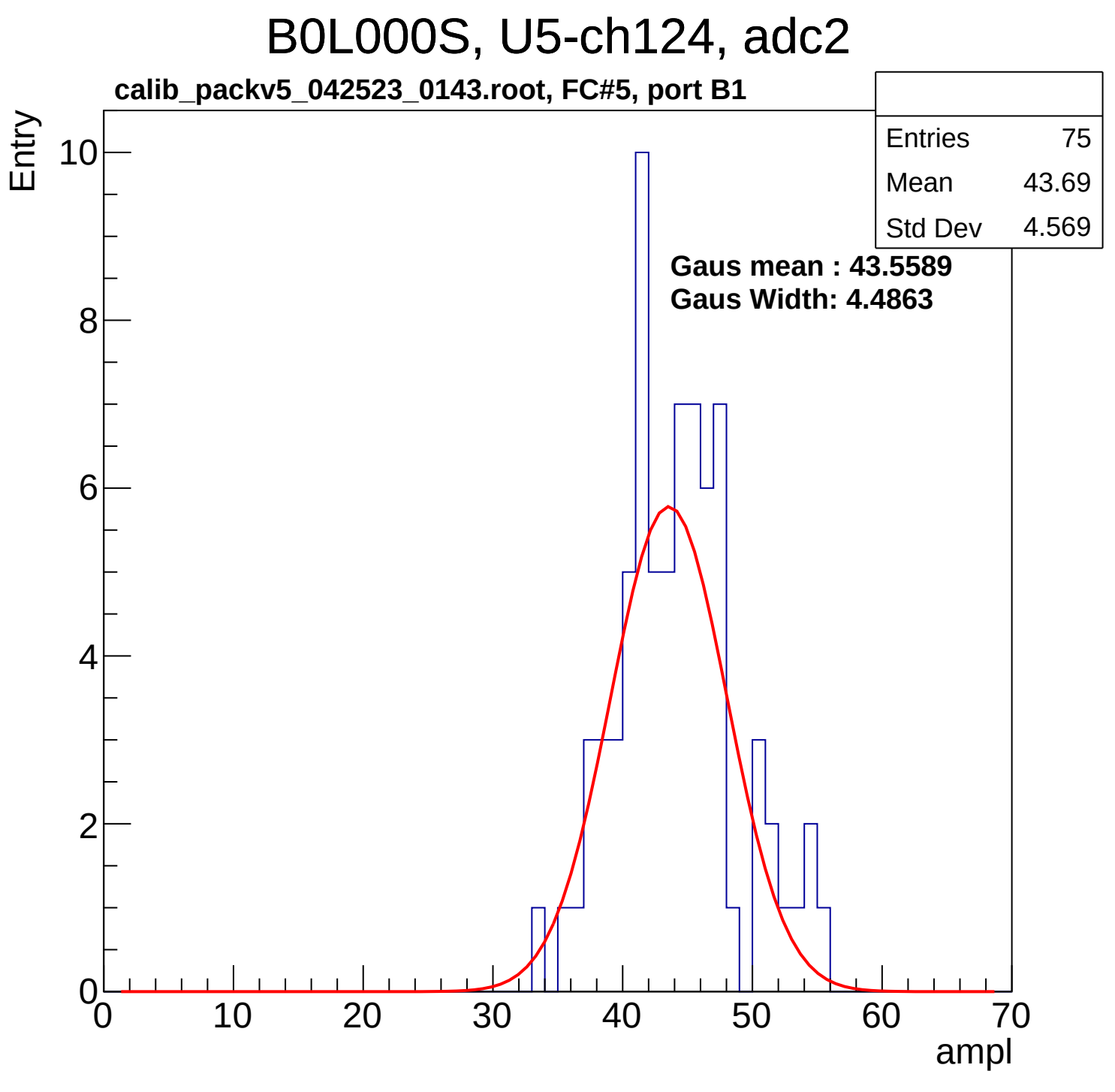
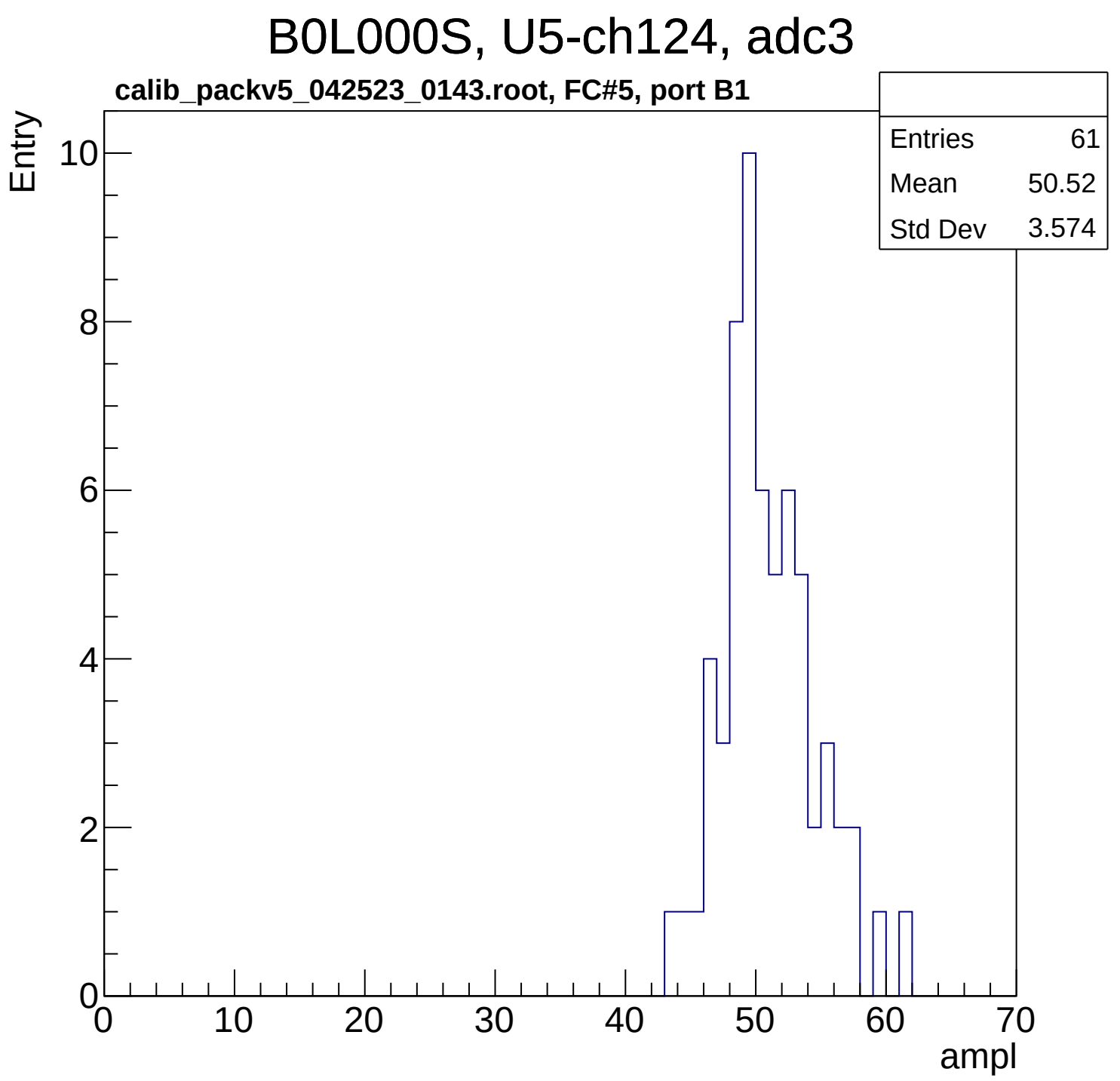
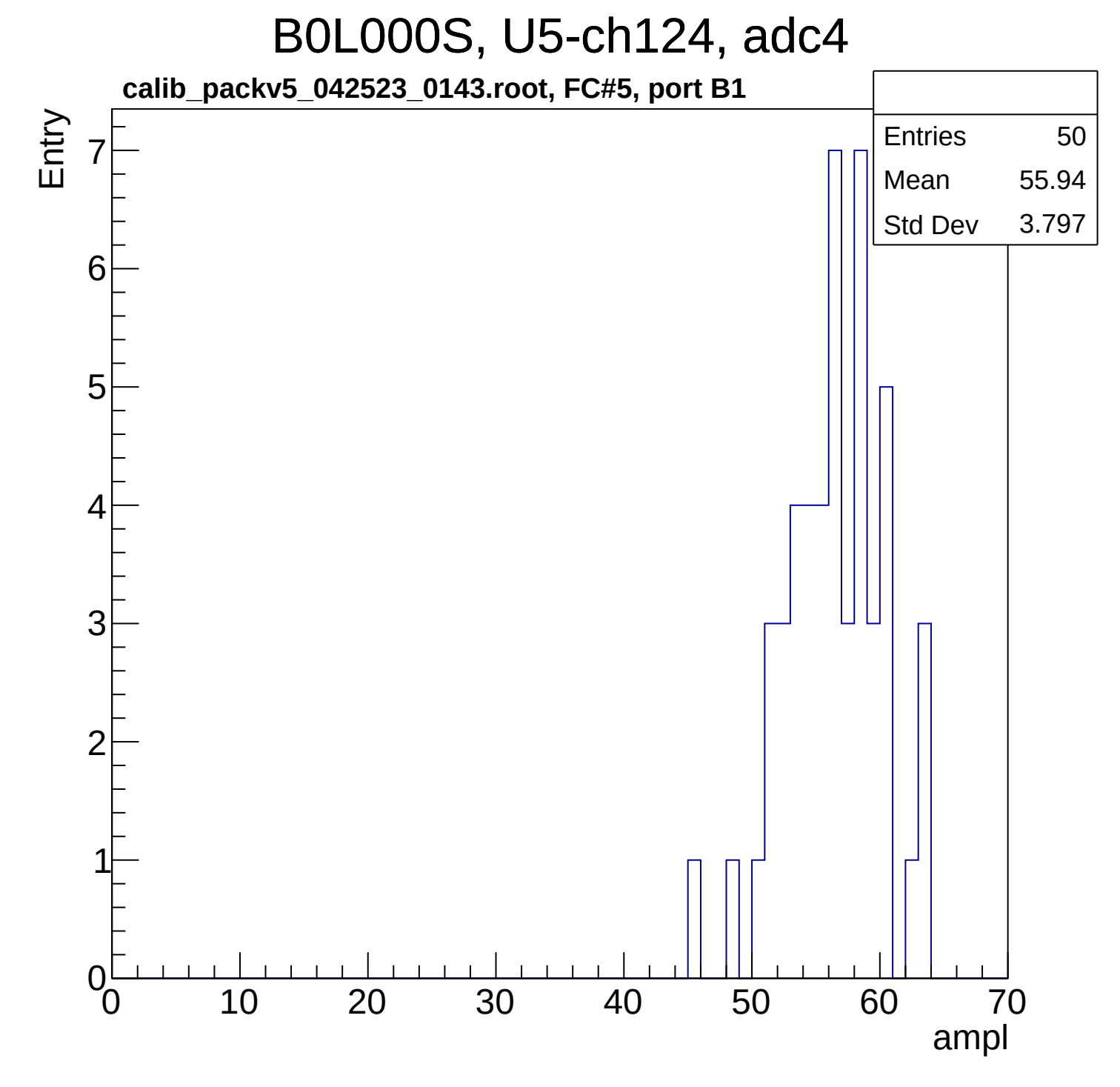
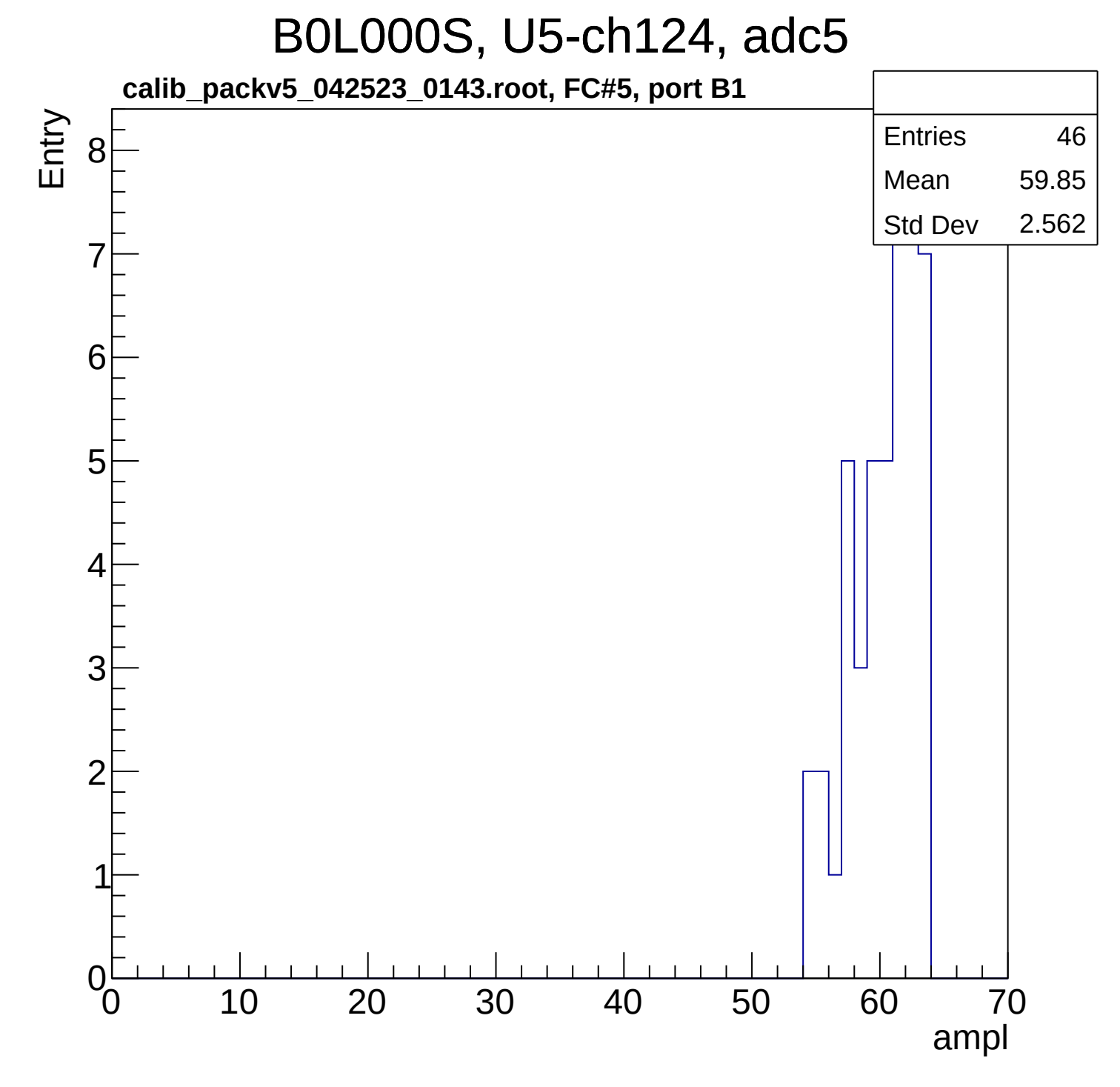
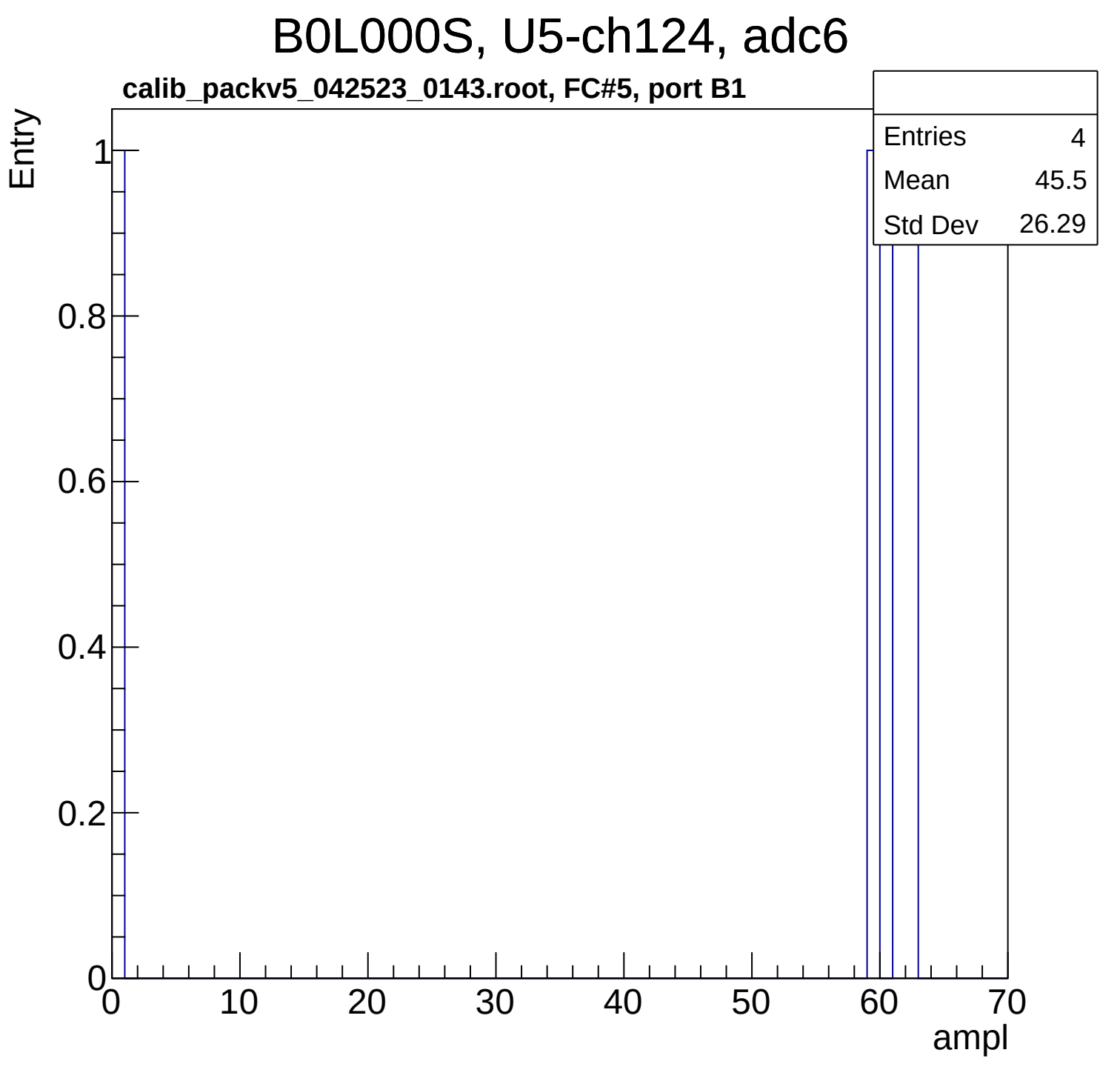
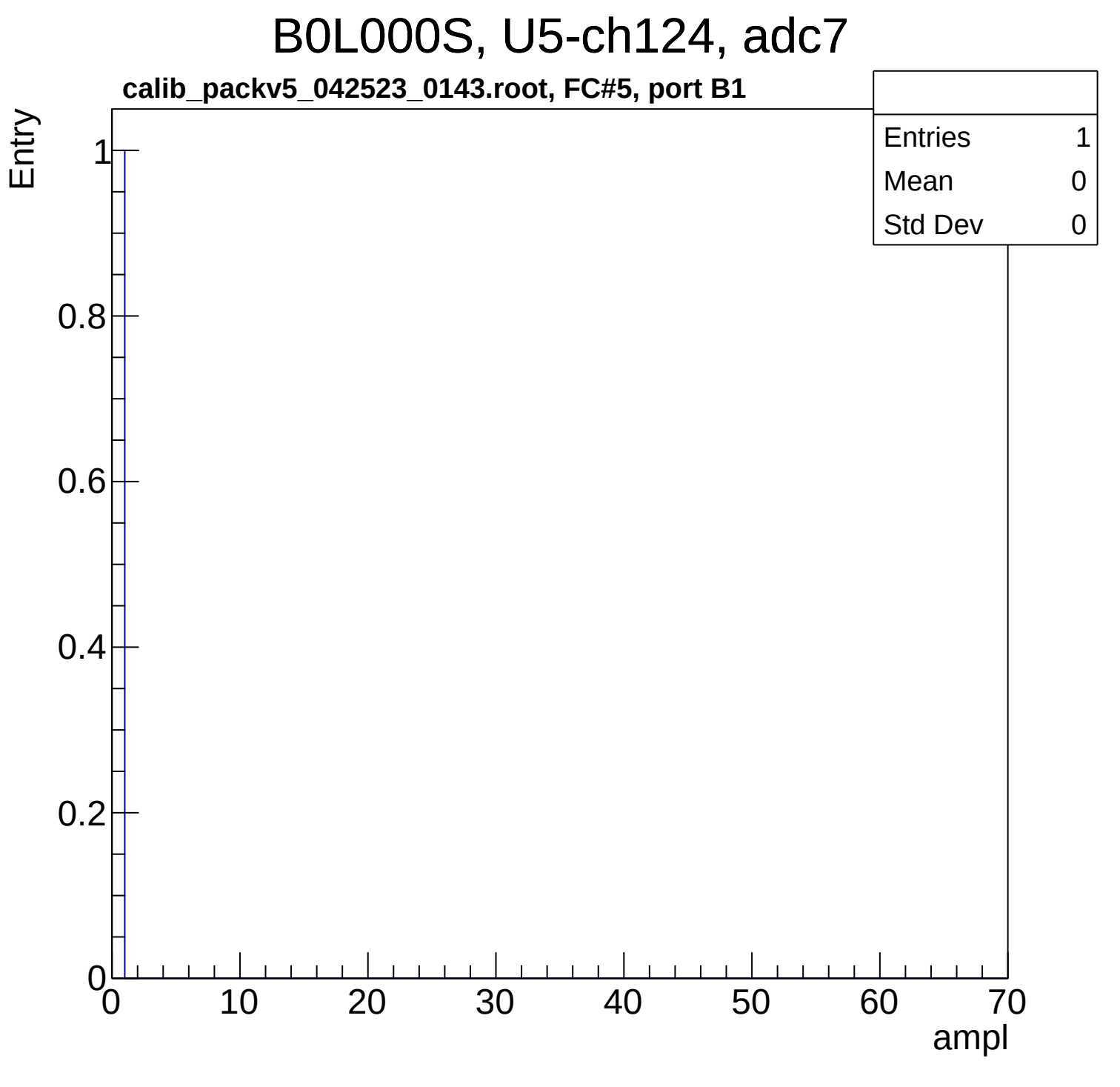

B0L000S, U5-ch124, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 43.69
Std Dev 4.569
Gaus mean : 43.5589
Gaus Width: 4.4863
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

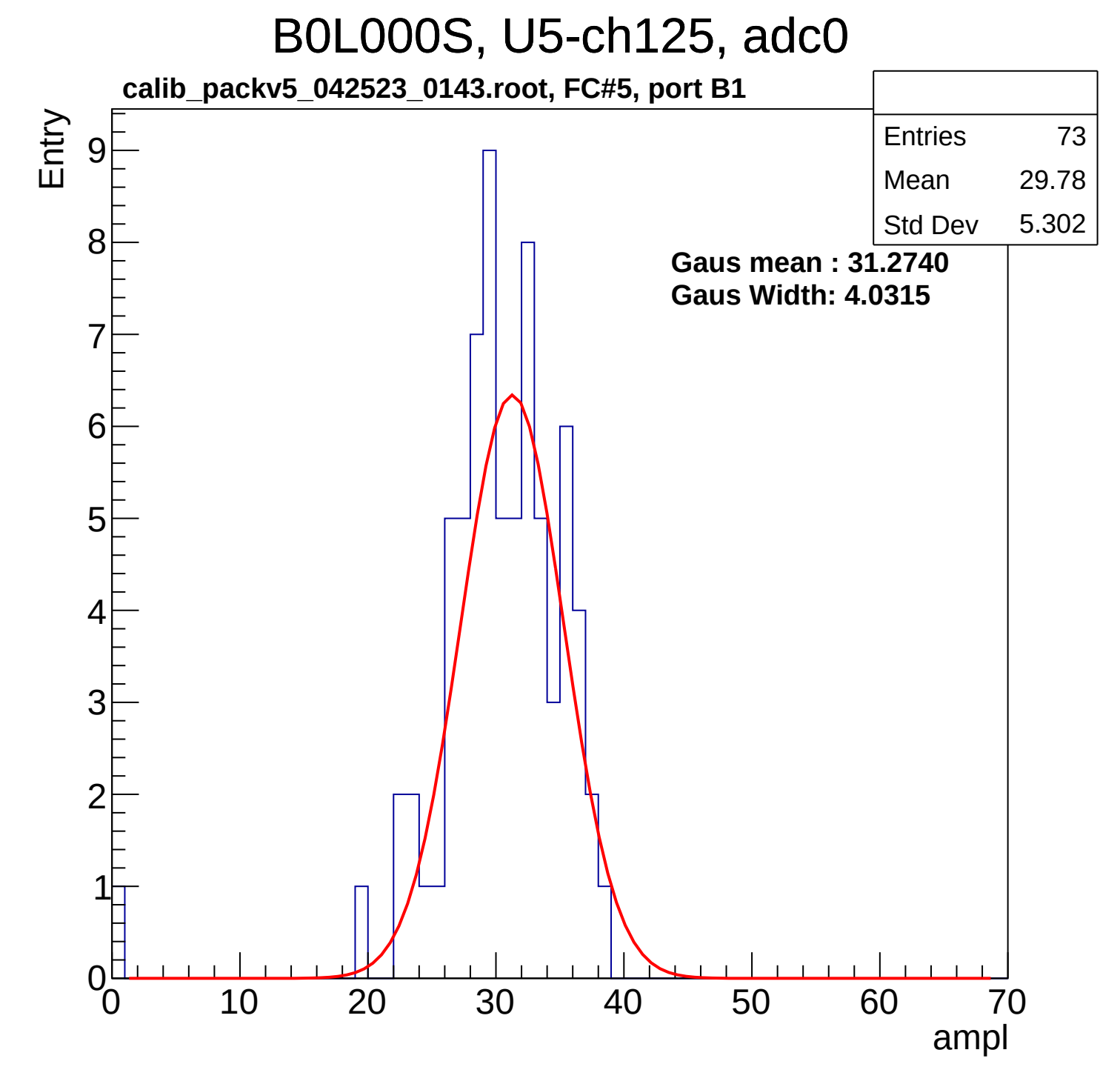

B0L000S, U5-ch124, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 61
Mean 50.52
Std Dev 3.574
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

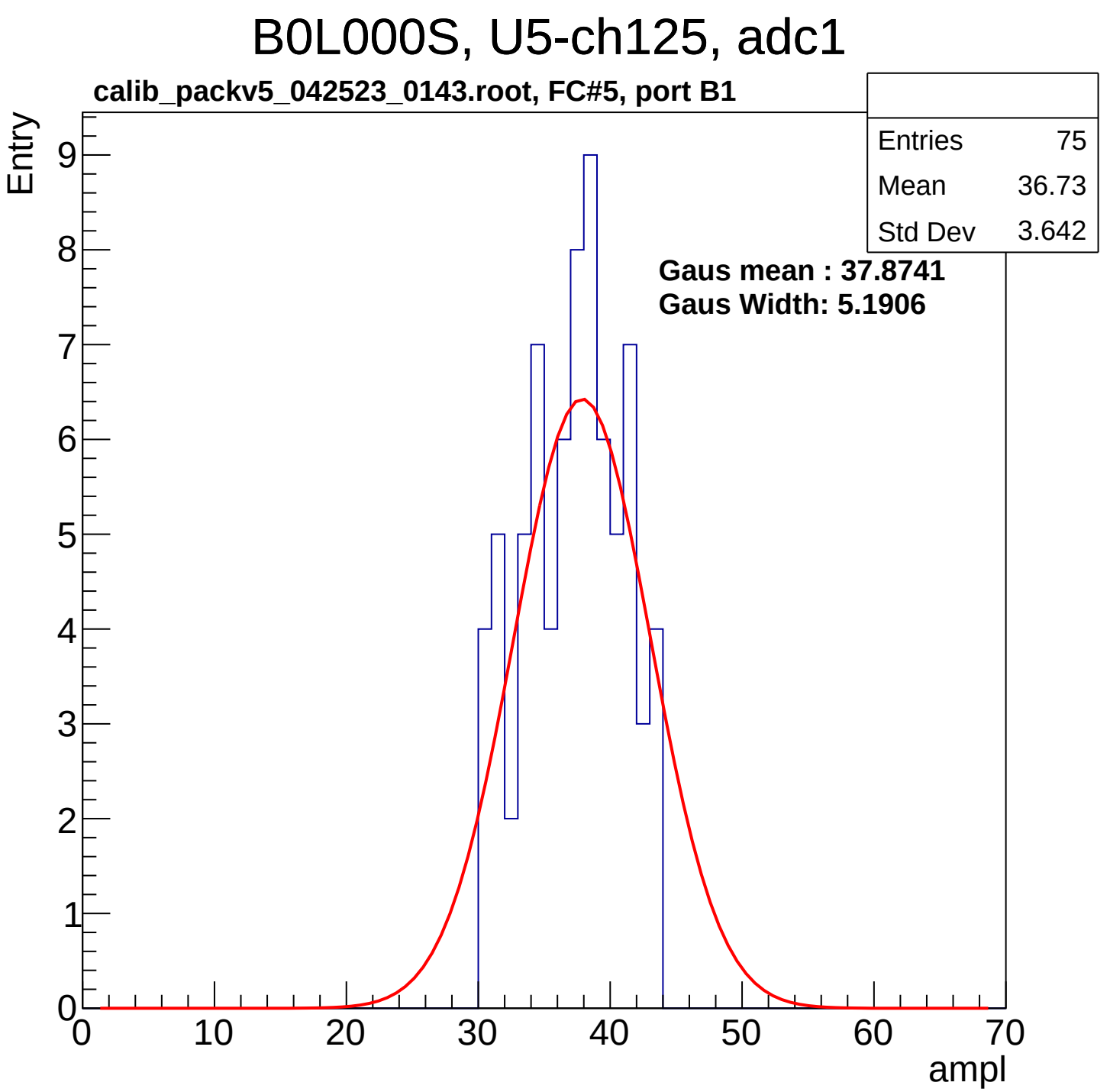

B0L000S, U5-ch124, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 50
Mean 55.94
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

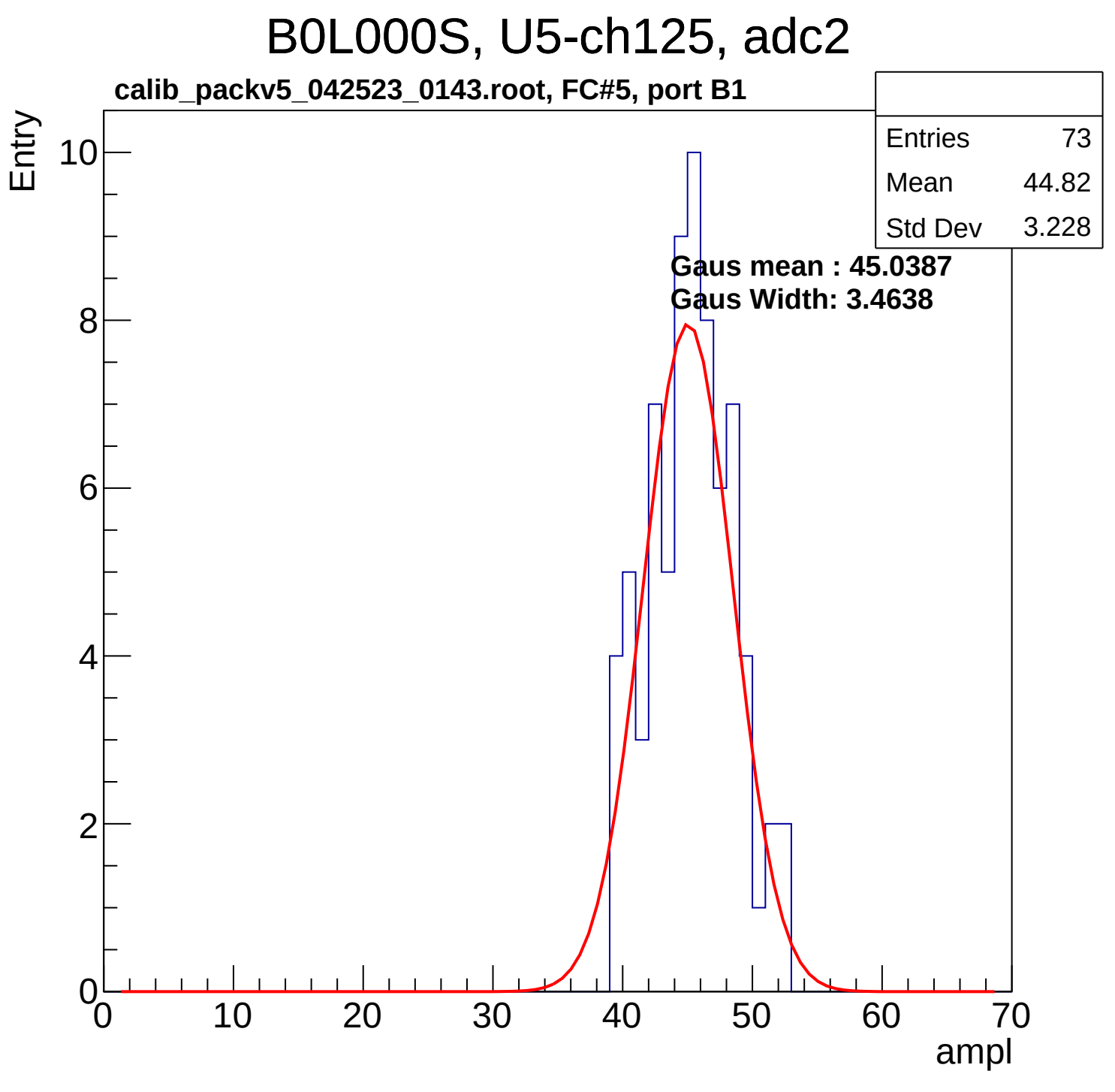

B0L000S, U5-ch124, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 46
Mean 59.85
Std Dev 2.562
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

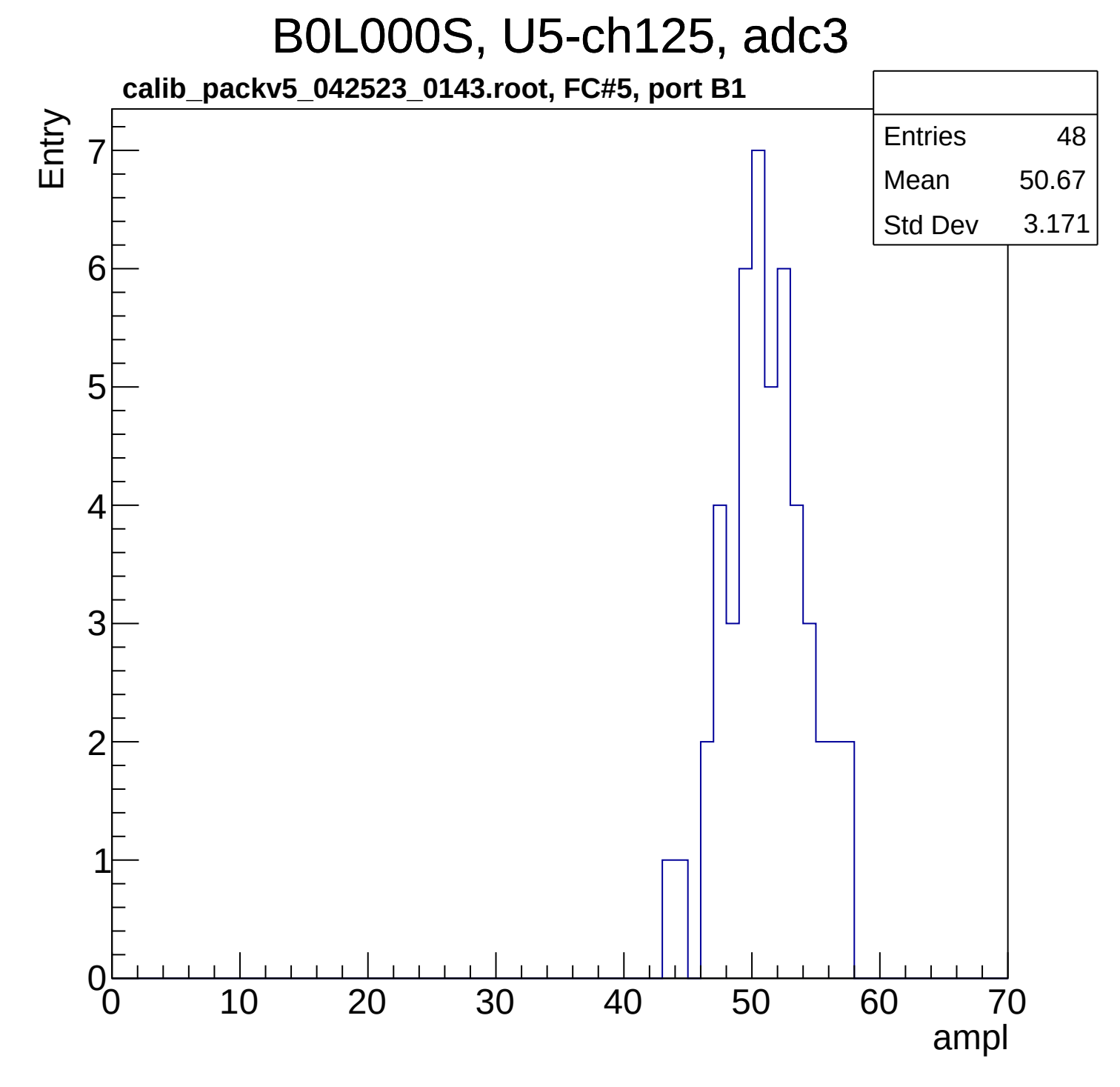

B0L000S, U5-ch124, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 4
Mean 45.5
Std Dev 26.29
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

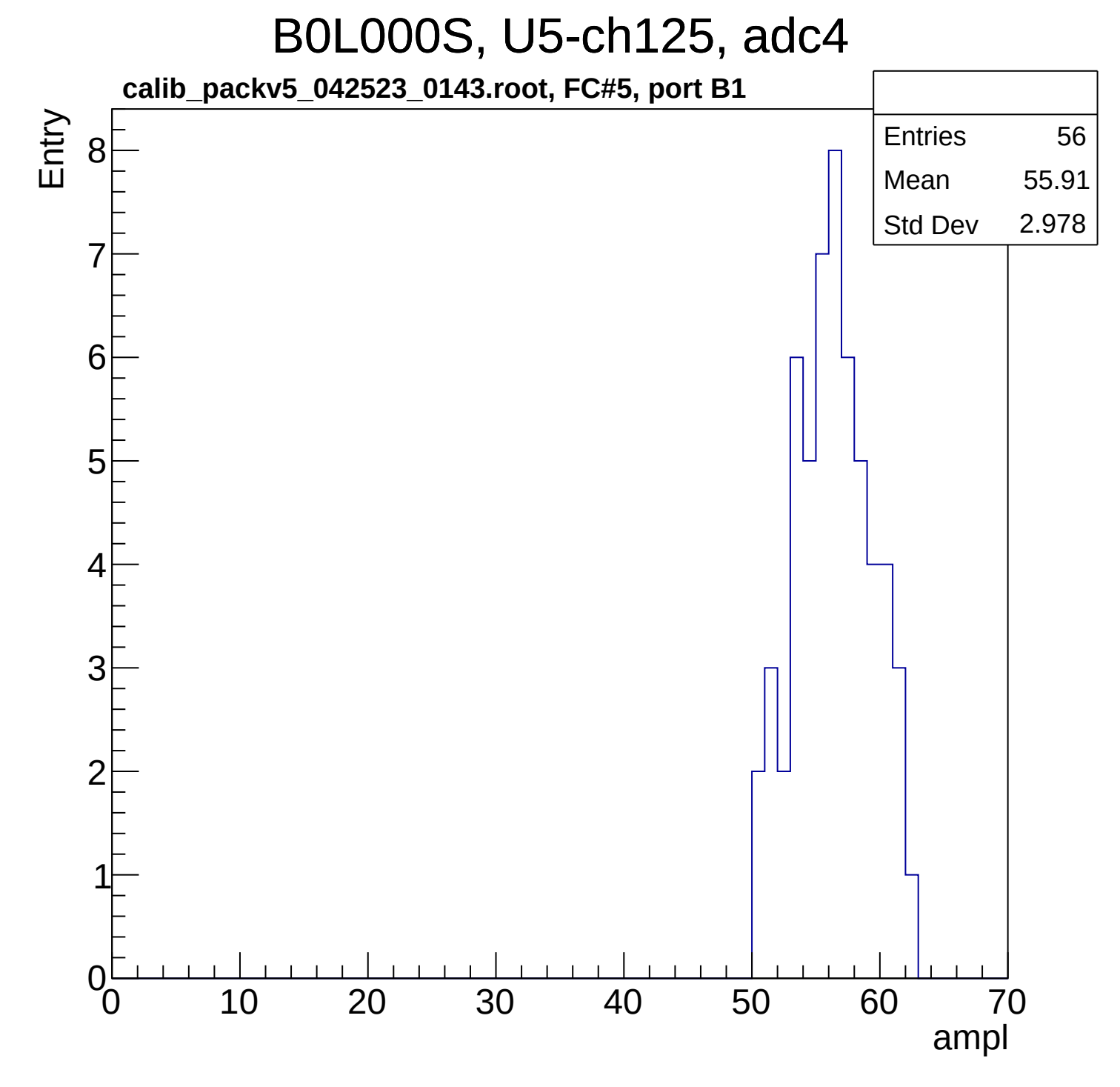

B0L000S, U5-ch124, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

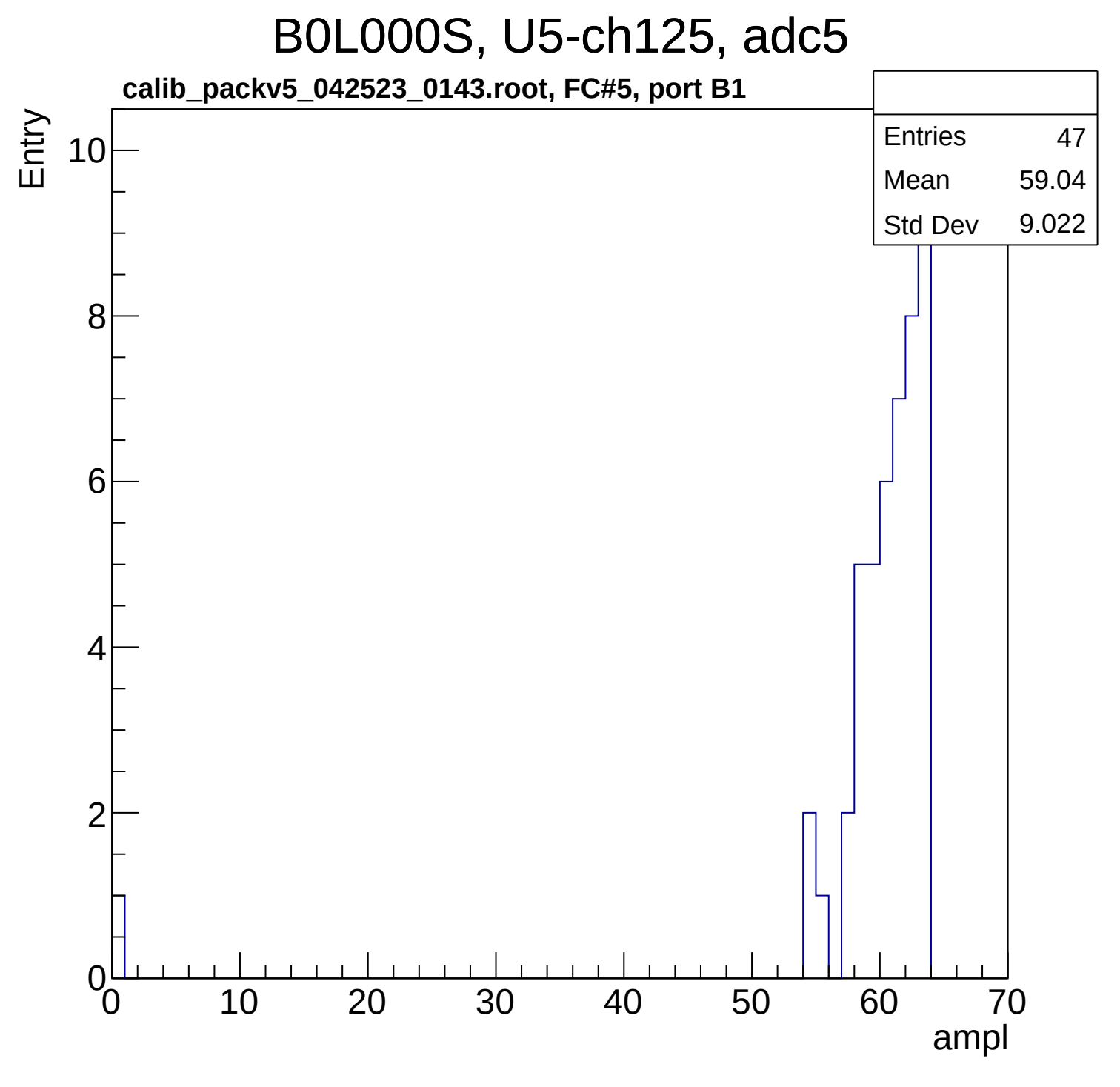

B0L000S, U5-ch125, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 29.78
Std Dev 5.302
Gaus mean : 31.2740
Gaus Width: 4.0315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

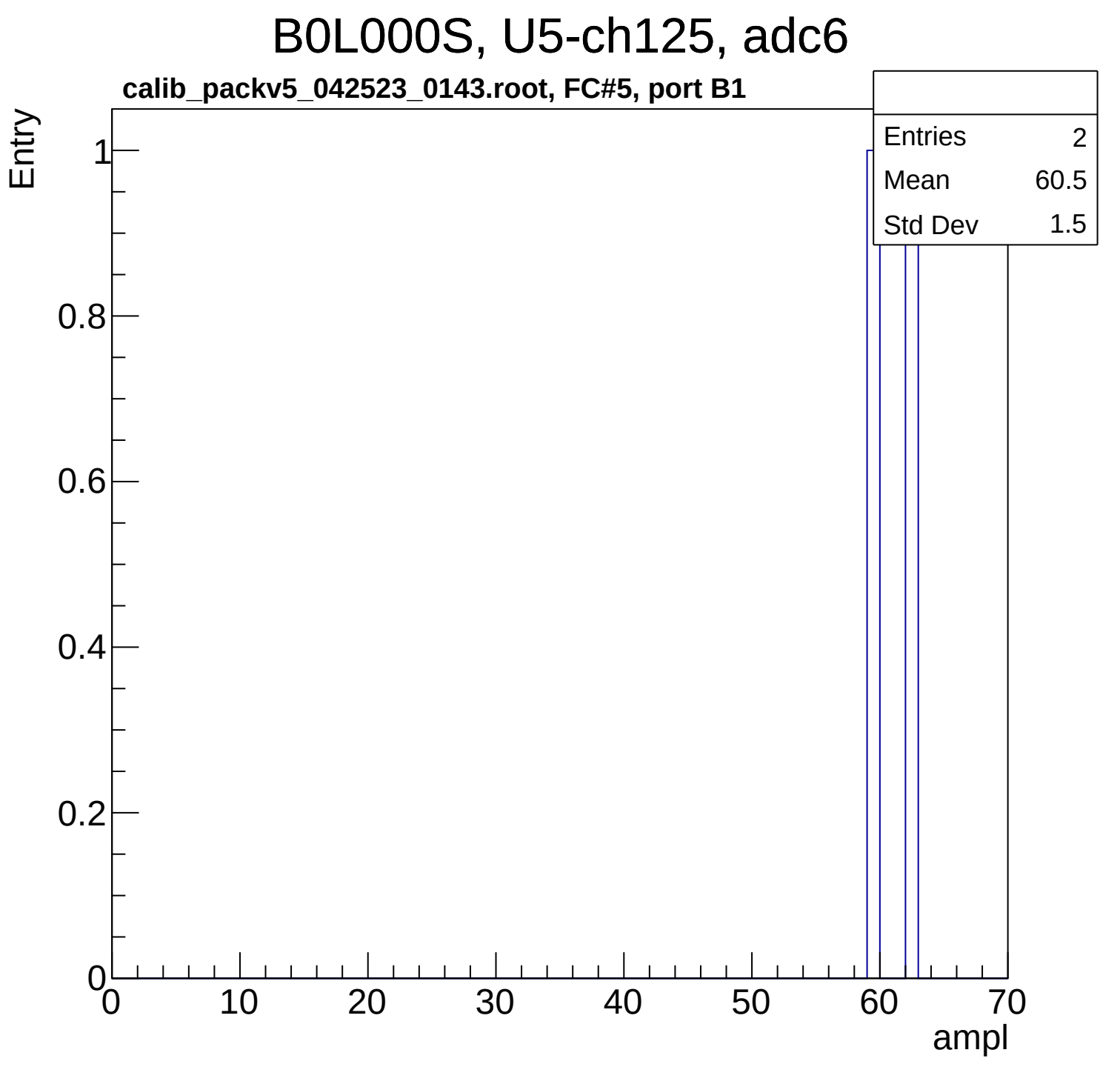

B0L000S, U5-ch125, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 36.73
Std Dev 3.642
Gaus mean : 37.8741
Gaus Width: 5.1906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

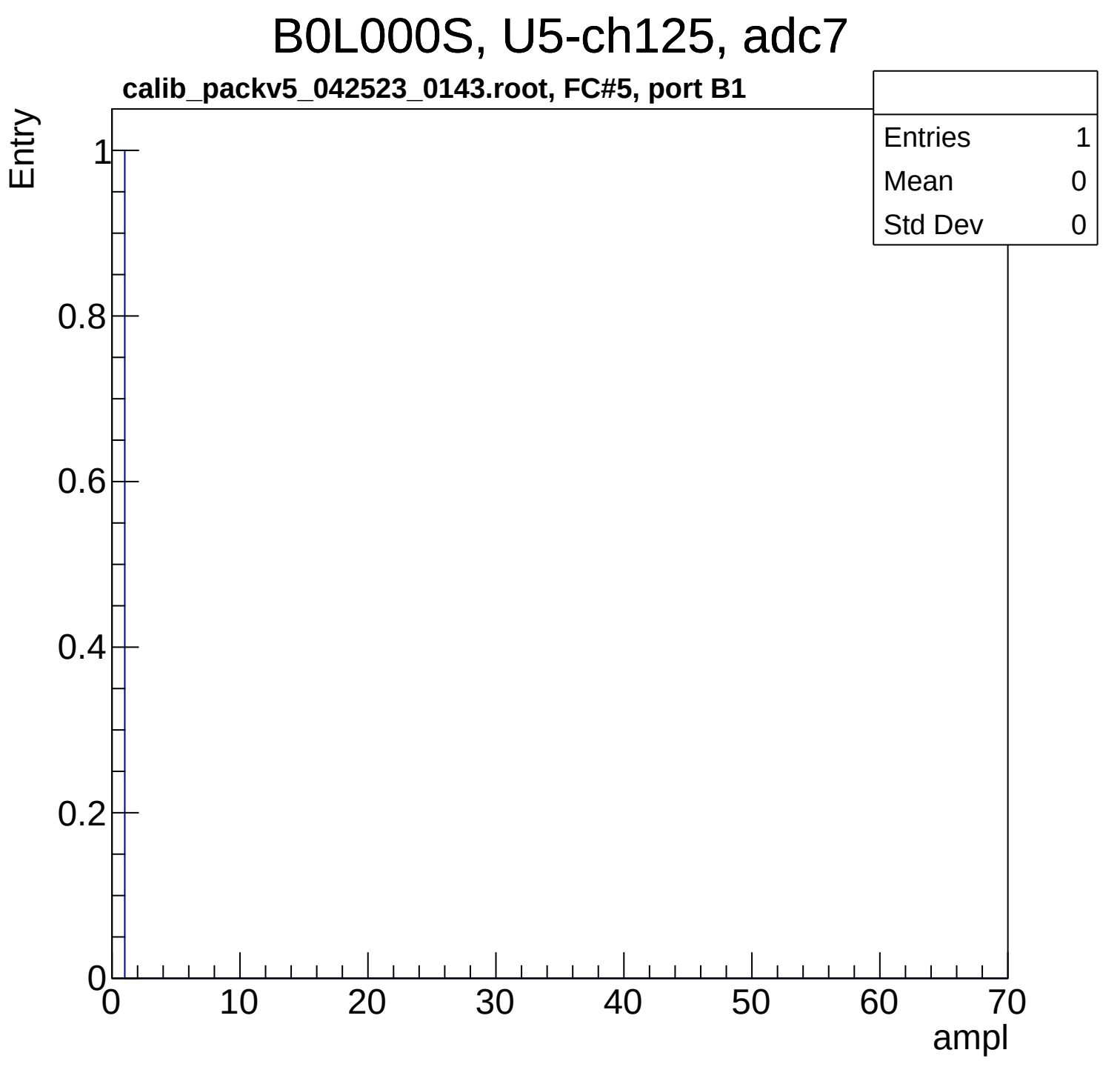

B0L000S, U5-ch125, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 73
Mean 44.82
Std Dev 3.228
Gaus mean : 45.0387
Gaus Width: 3.4638
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

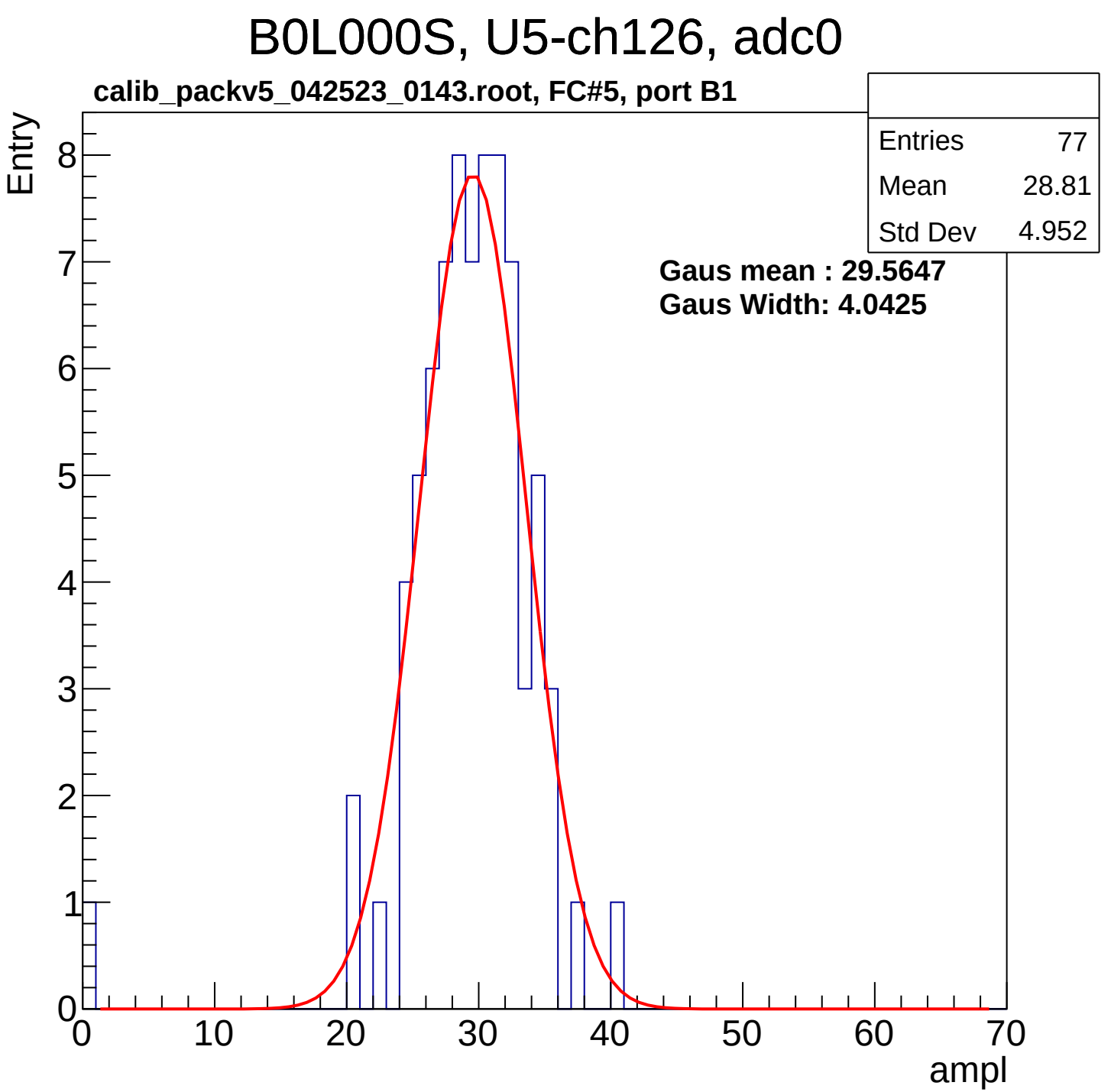

B0L000S, U5-ch125, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 48
Mean 50.67
Std Dev 3.171
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

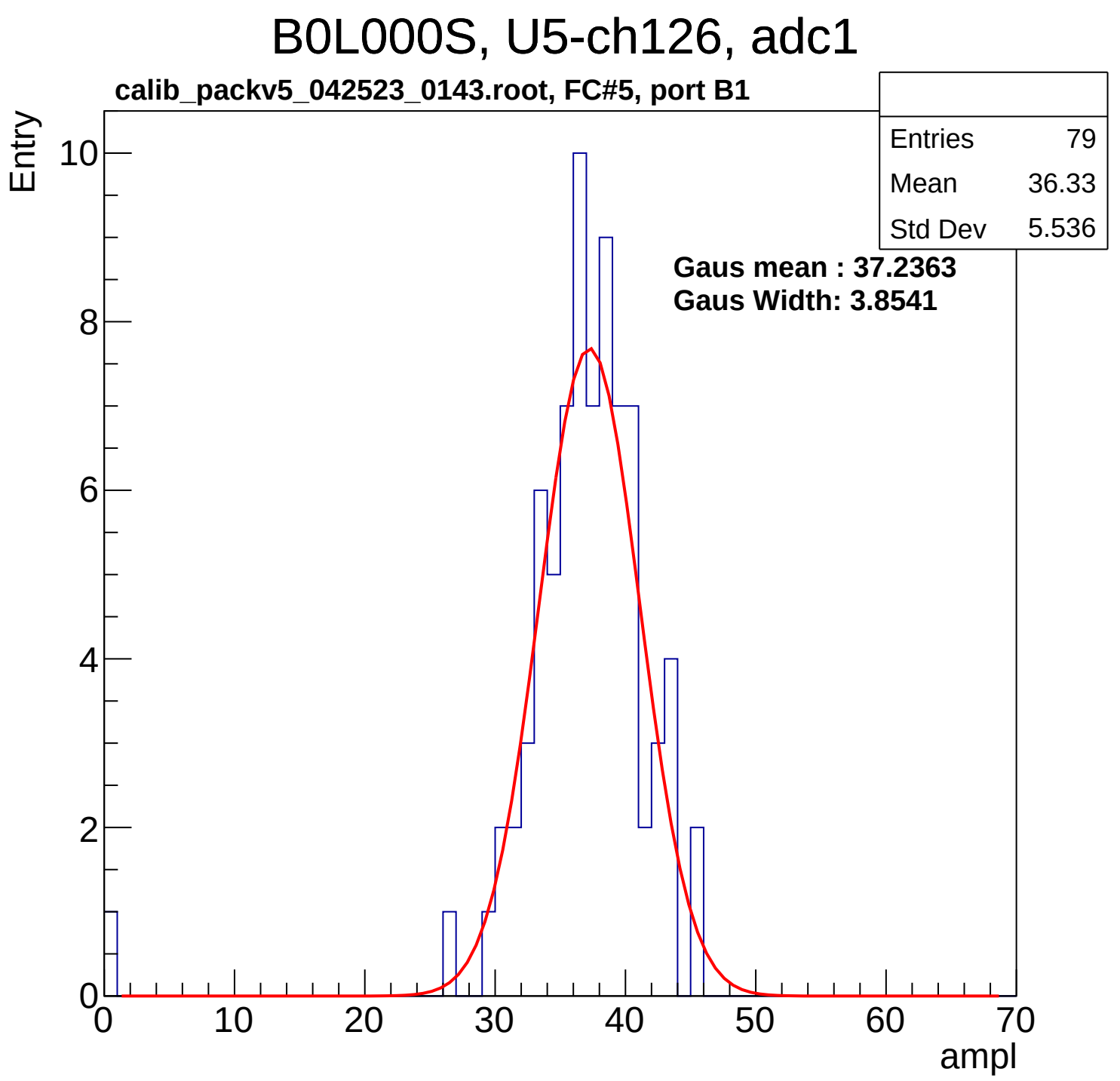

B0L000S, U5-ch125, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 56
Mean 55.91
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

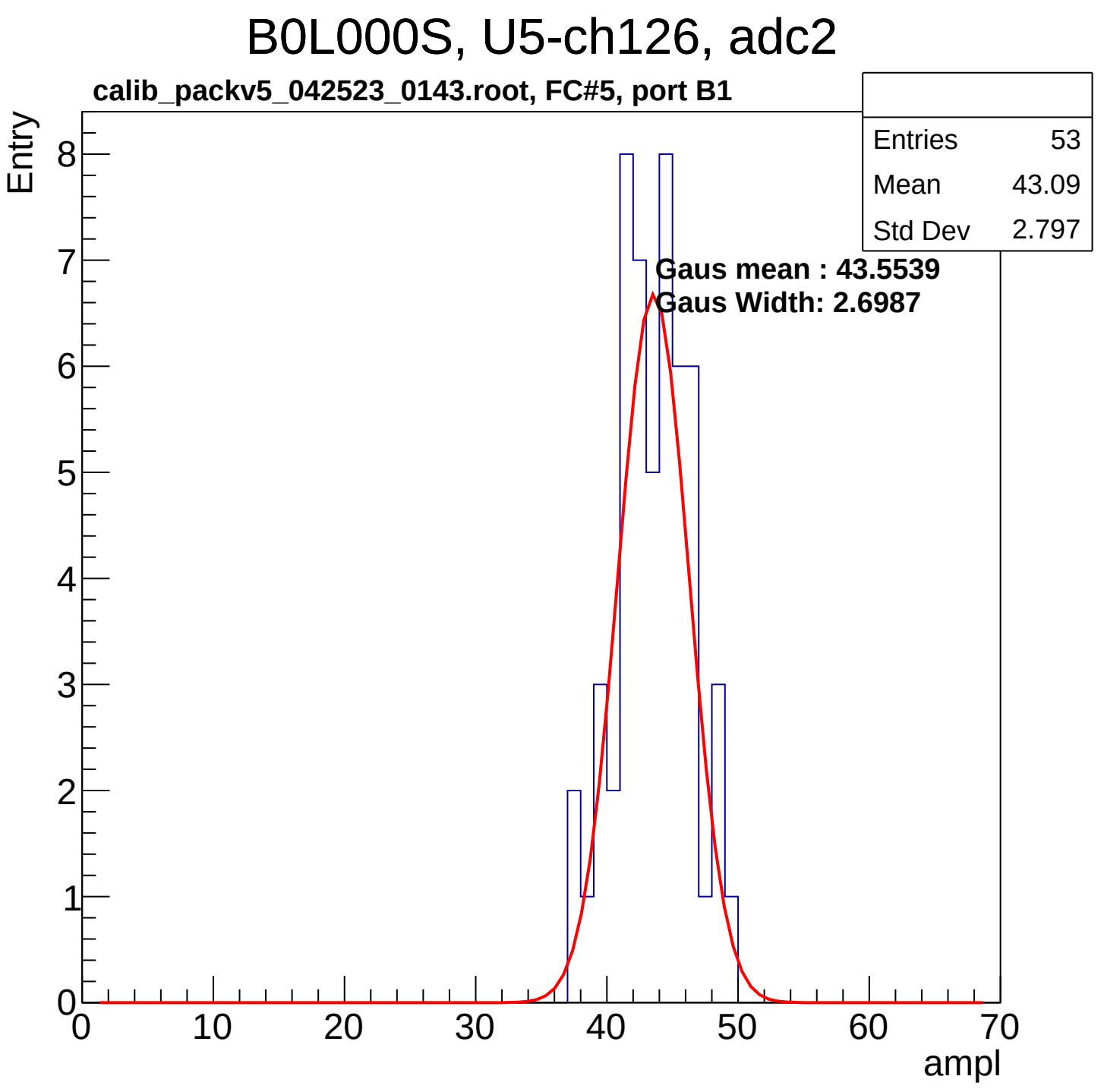

B0L000S, U5-ch125, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 47
Mean 59.04
Std Dev 9.022
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch125, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L000S, U5-ch125, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch126, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 77
Mean 28.81
Std Dev 4.952
Gaus mean : 29.5647
Gaus Width: 4.0425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch126, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 79
Mean 36.33
Std Dev 5.536
Gaus mean : 37.2363
Gaus Width: 3.8541
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L000S, U5-ch126, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 53
Mean 43.09
Std Dev 2.797
Gaus mean : 43.5539
Gaus Width: 2.6987
Entry
ampl
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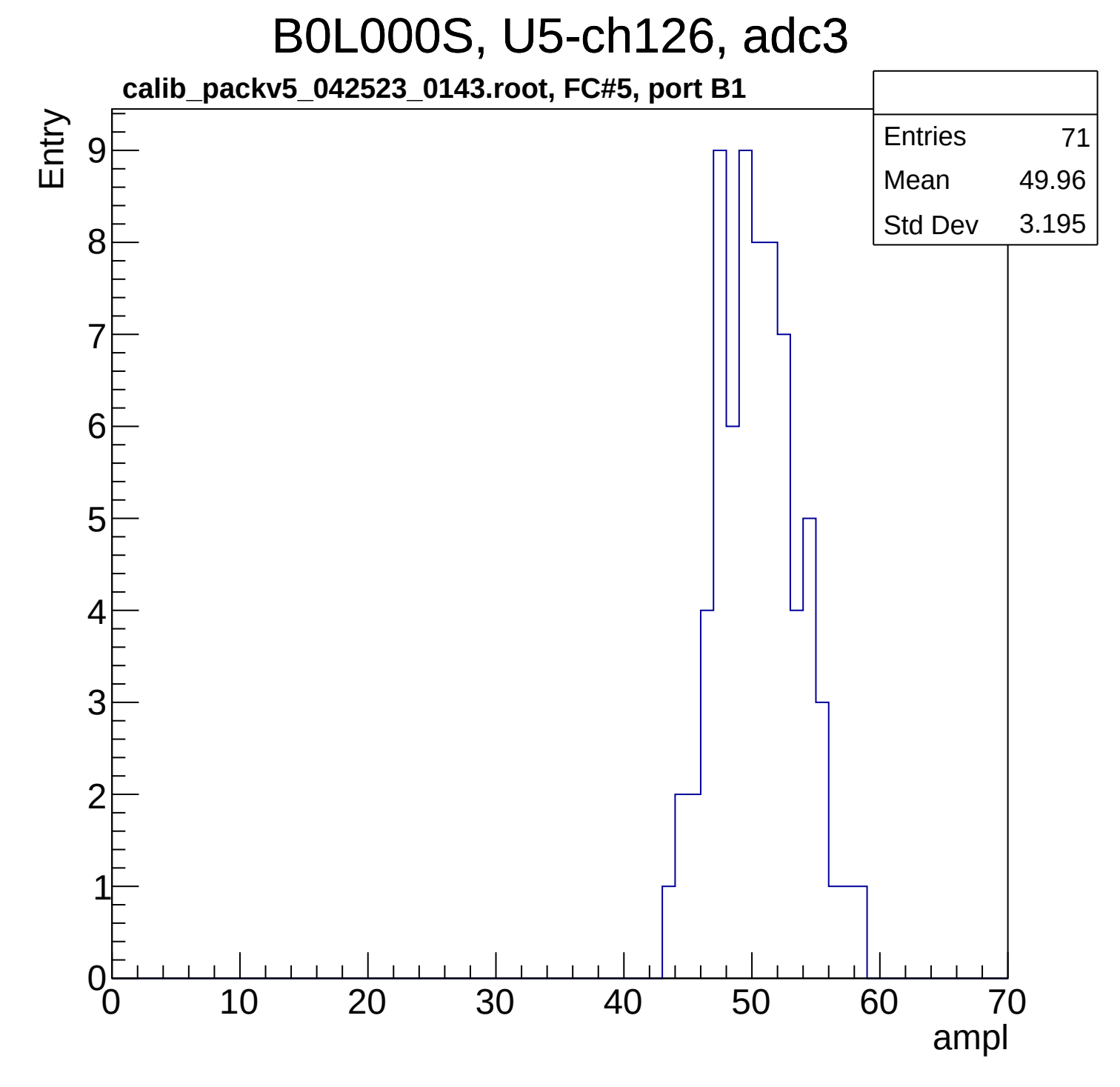
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch126, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 71
Mean 49.96
Std Dev 3.195
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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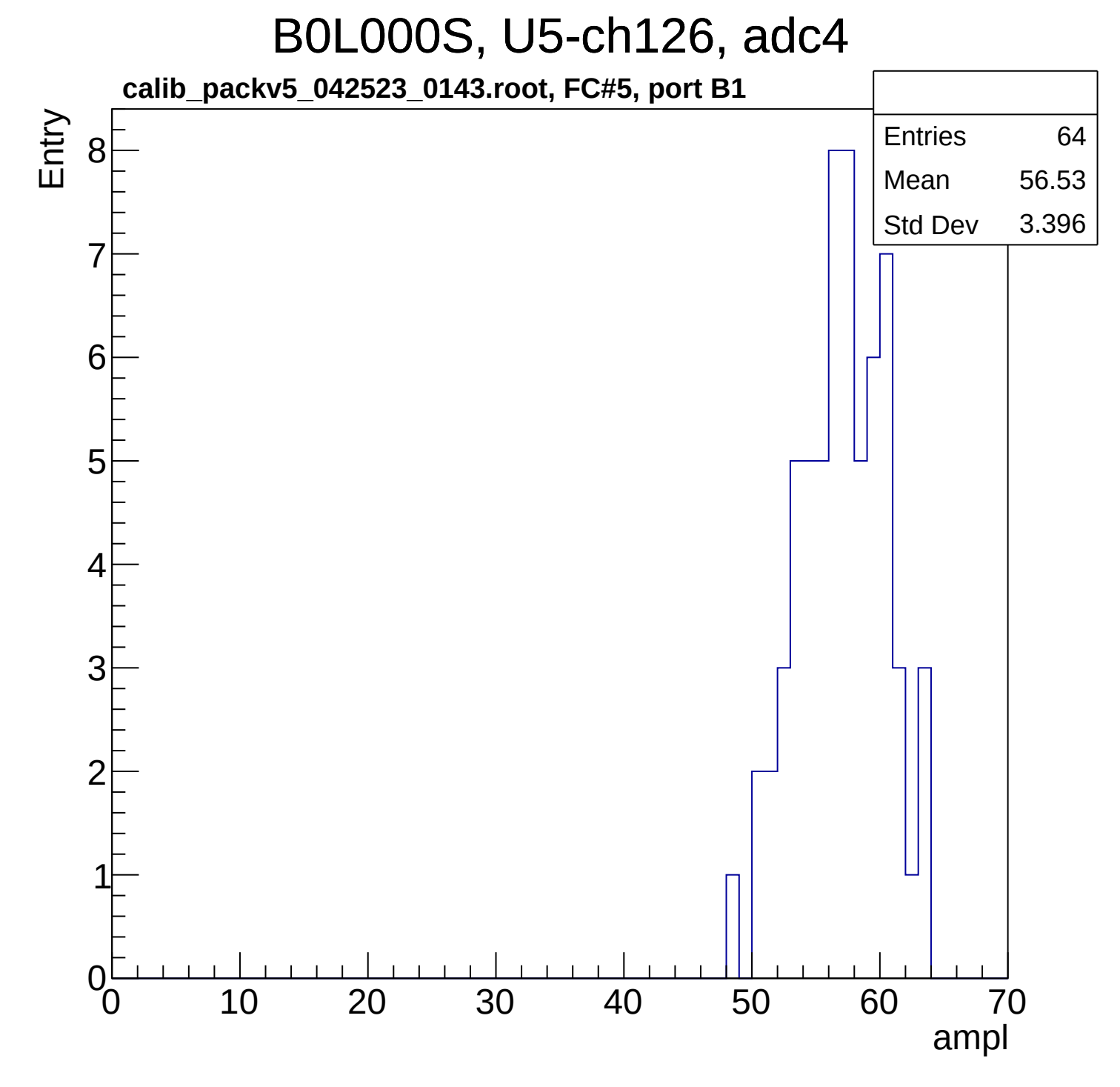
B0L000S, U5-ch126, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 64
Mean 56.53
Std Dev 3.396
Entry
ampl
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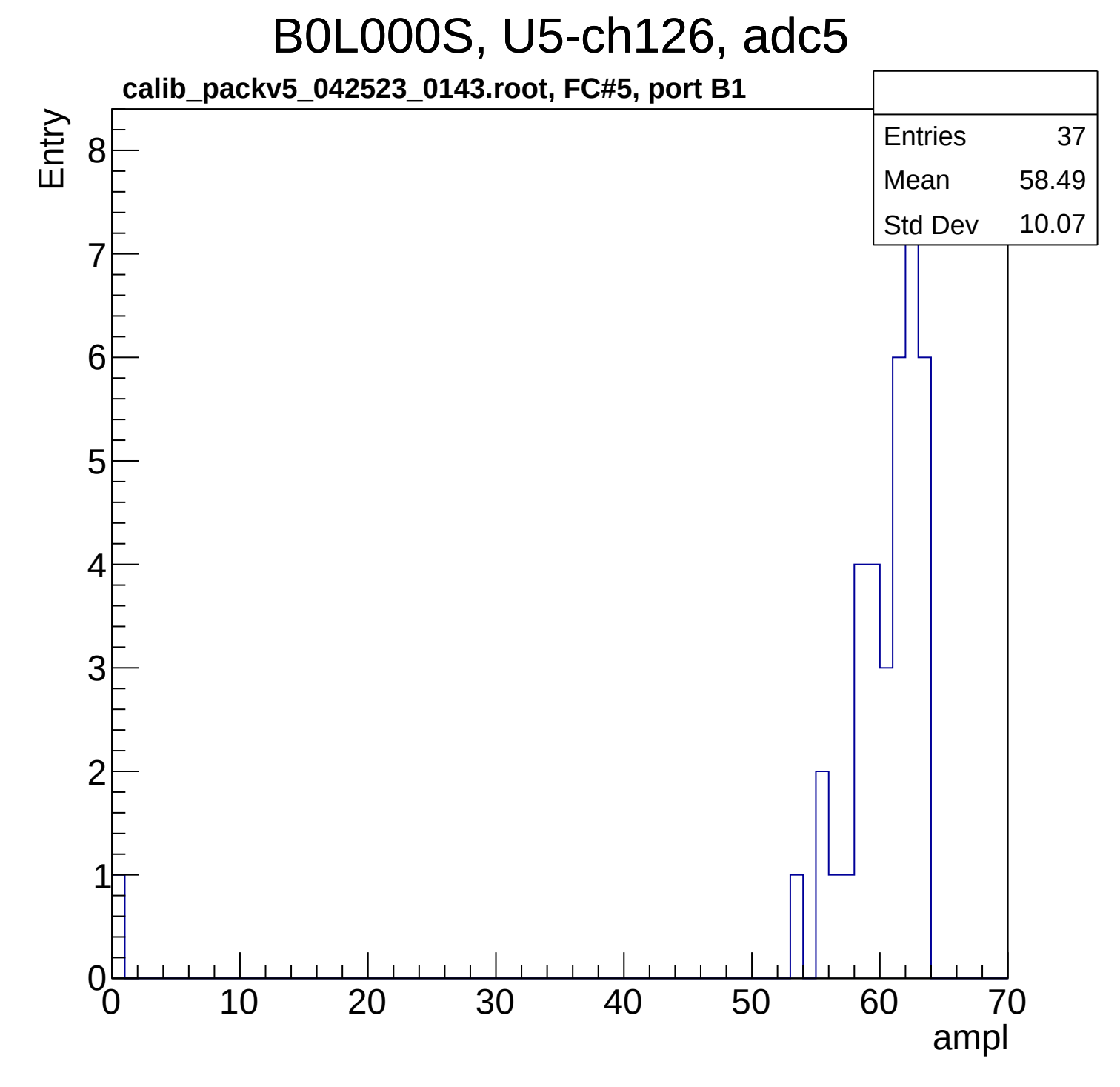
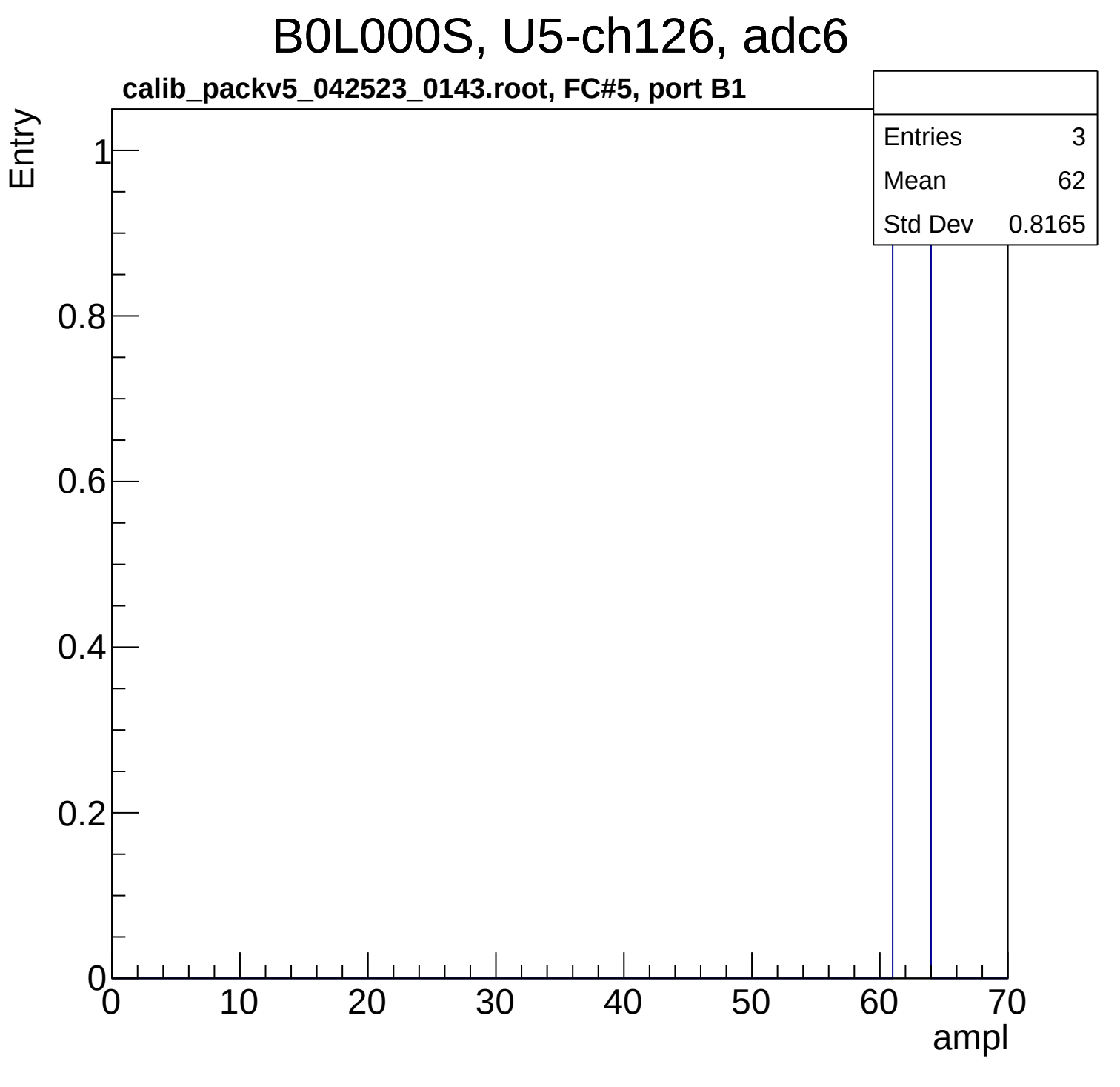
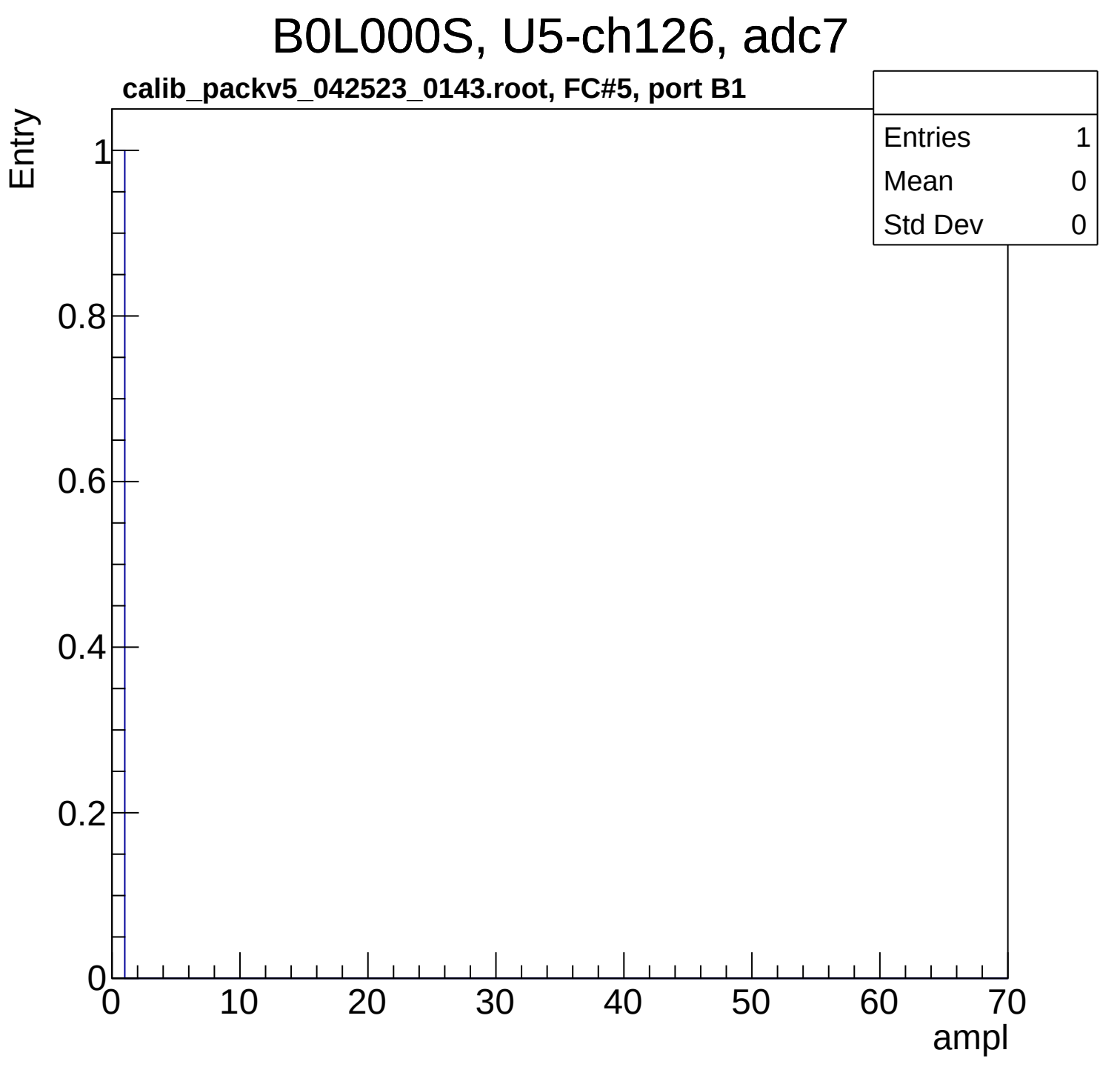
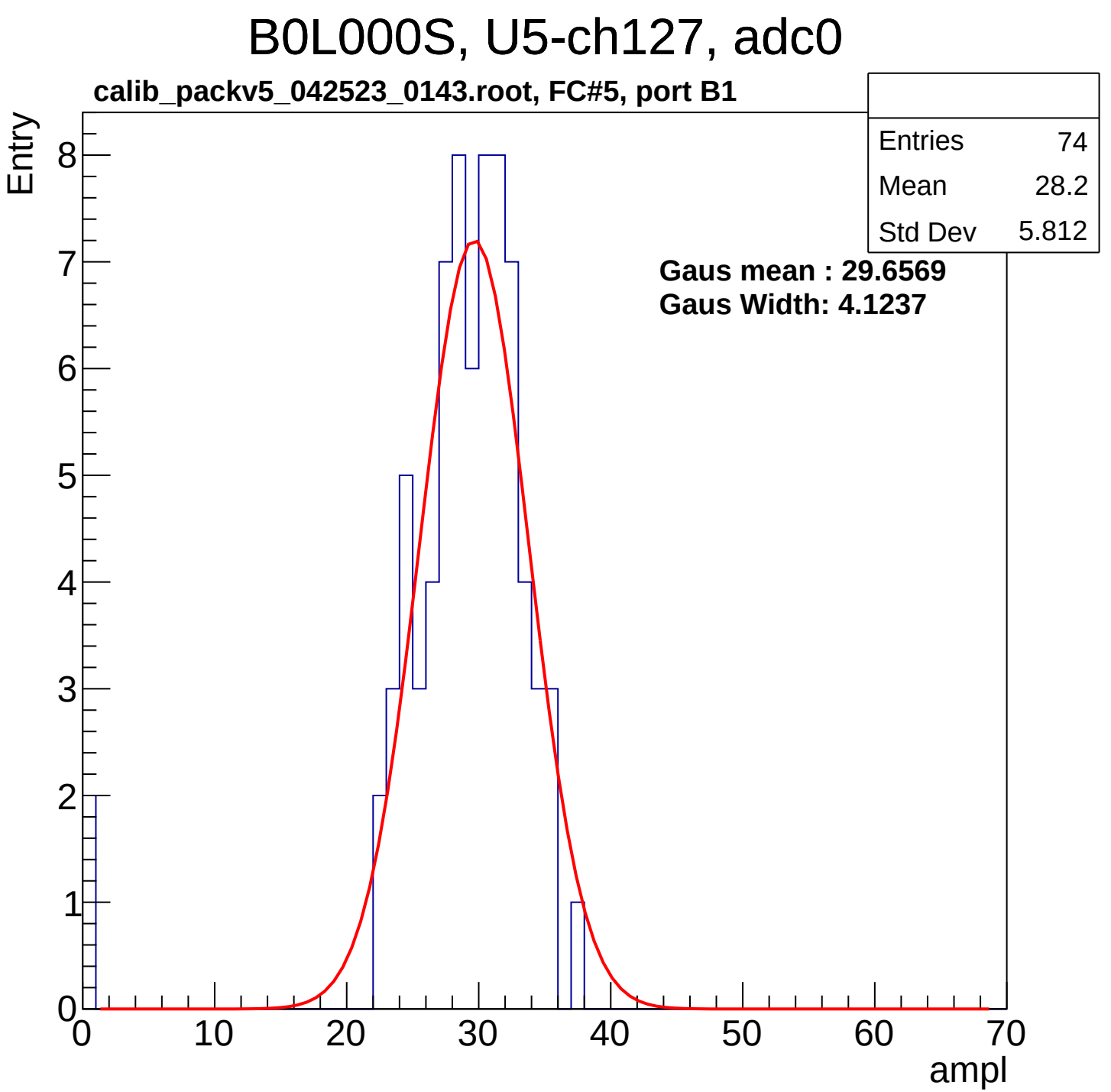
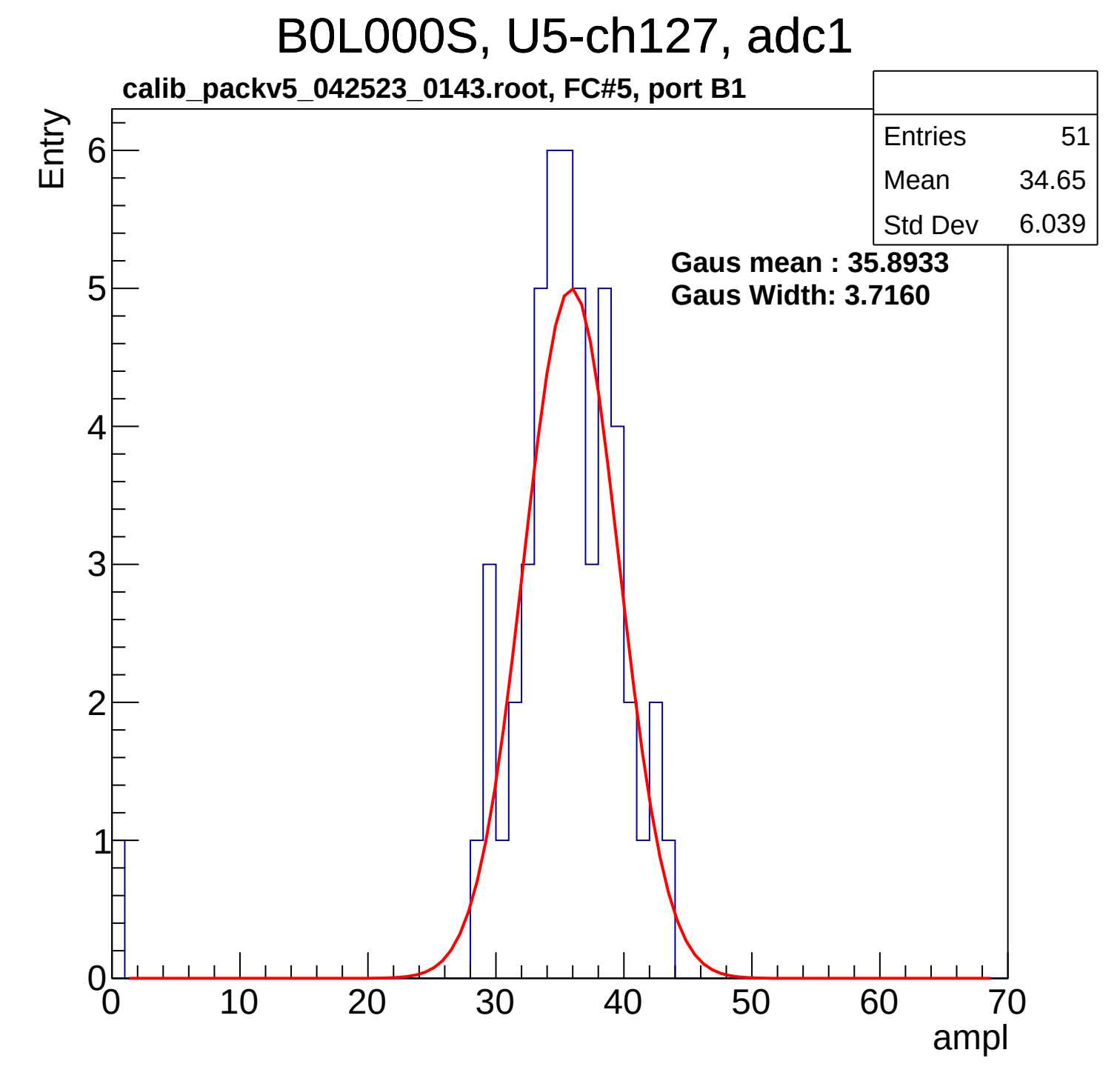
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

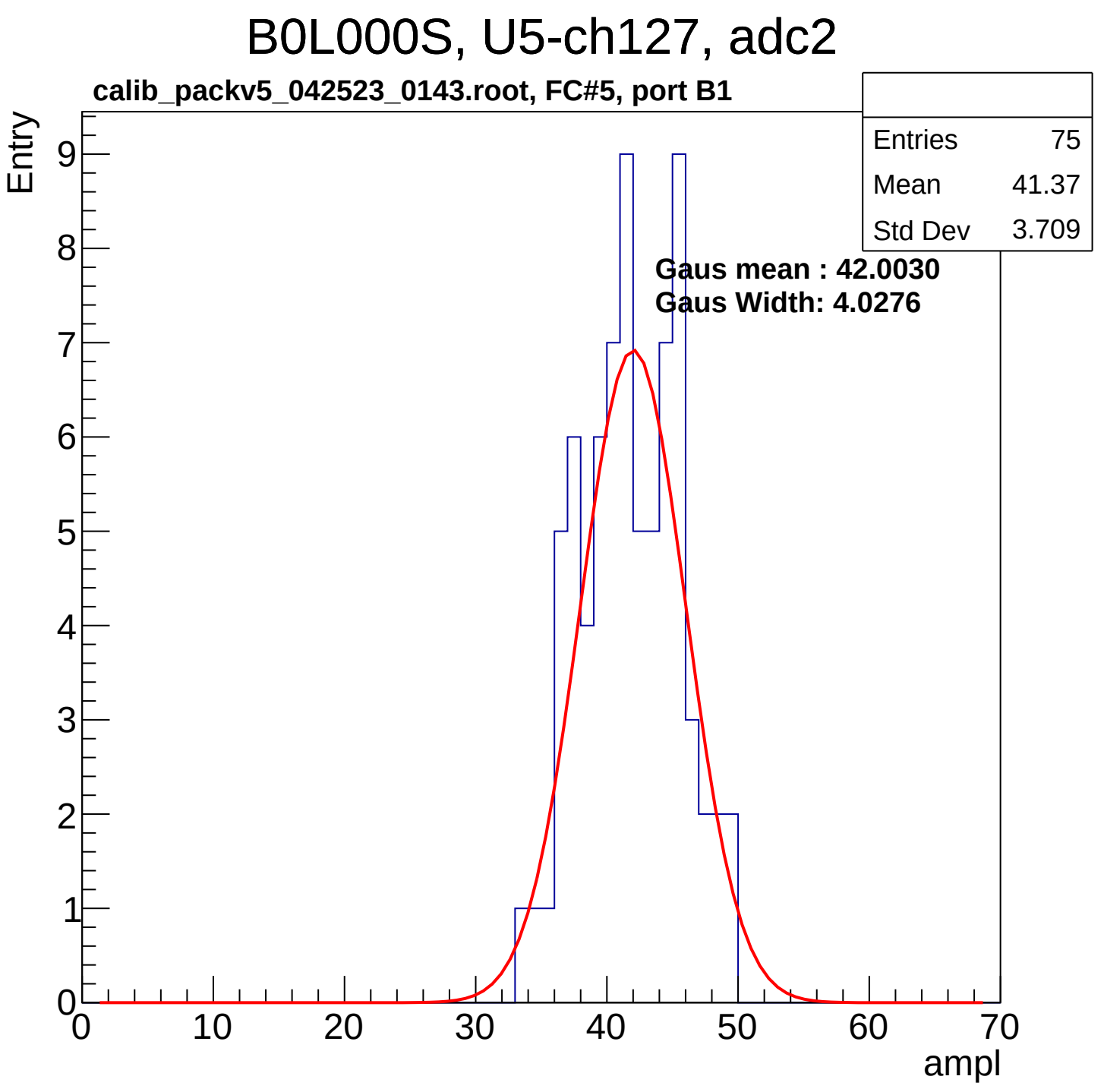

B0L000S, U5-ch126, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 37
Mean 58.49
Std Dev 10.07
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

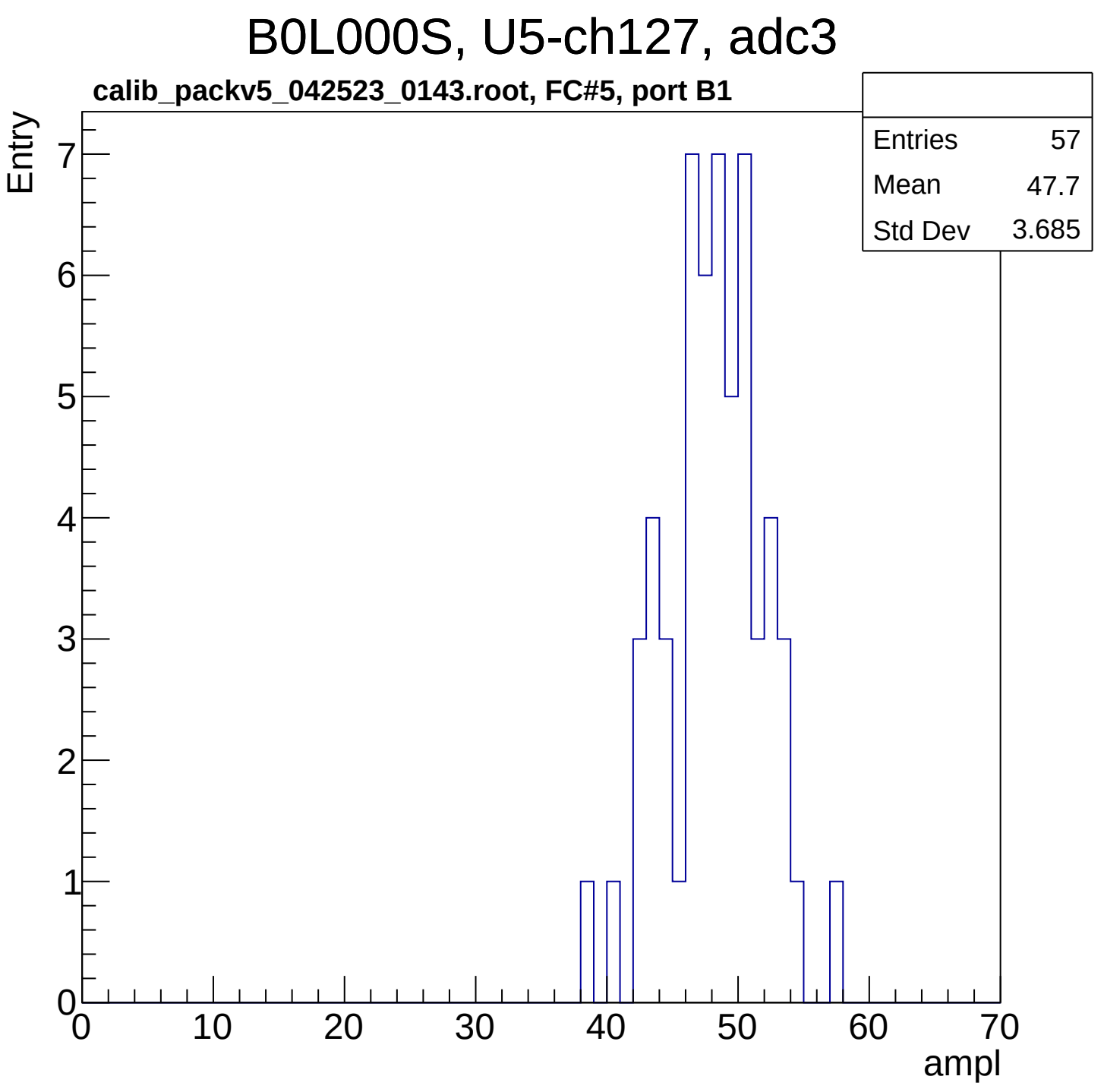

B0L000S, U5-ch126, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

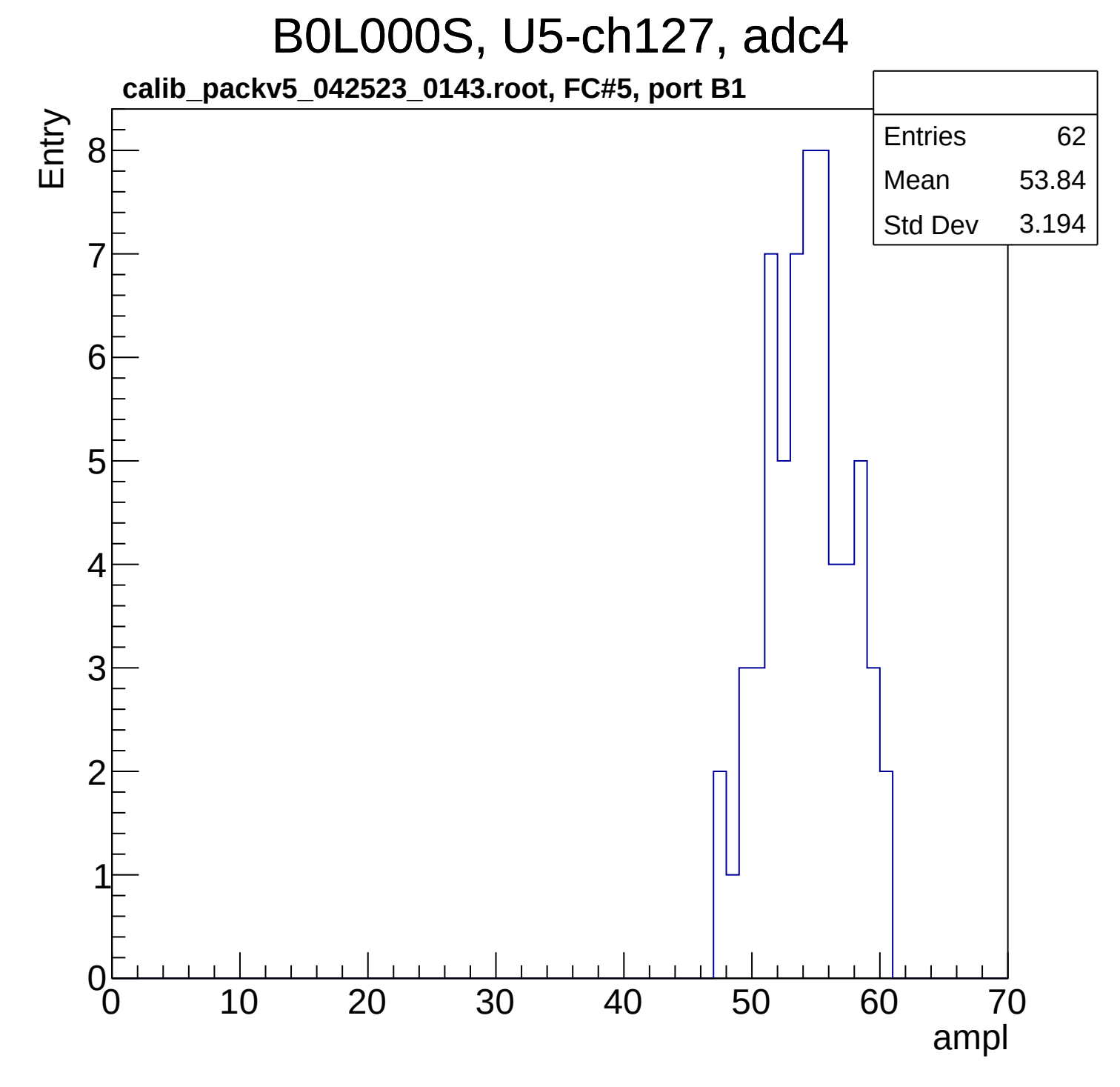

B0L000S, U5-ch126, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

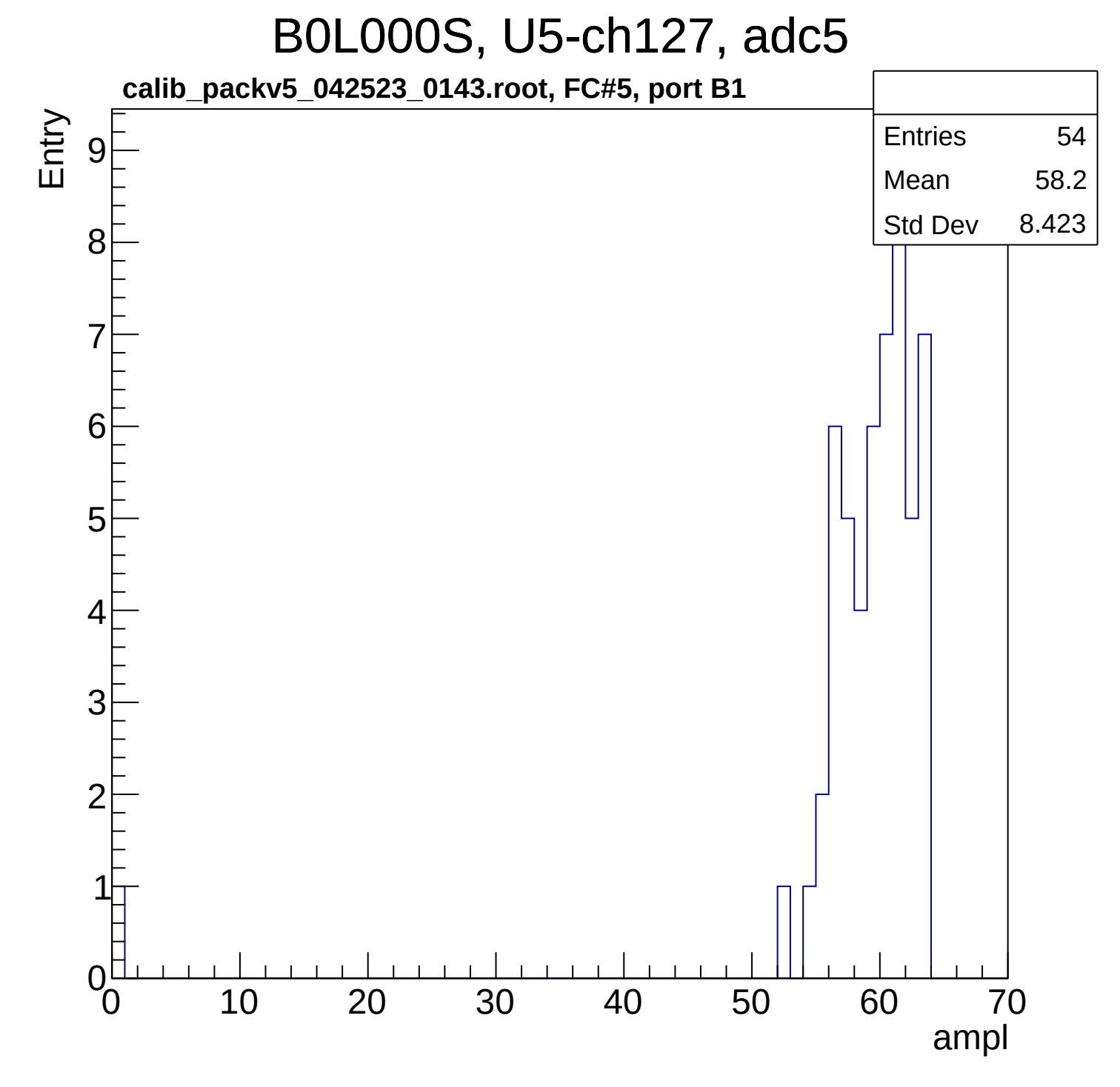

B0L000S, U5-ch127, adc0
calib_packv5_042523_0143.root, FC#5, port B1
Entries 74
Mean 28.2
Std Dev 5.812
Gaus mean : 29.6569
Gaus Width: 4.1237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

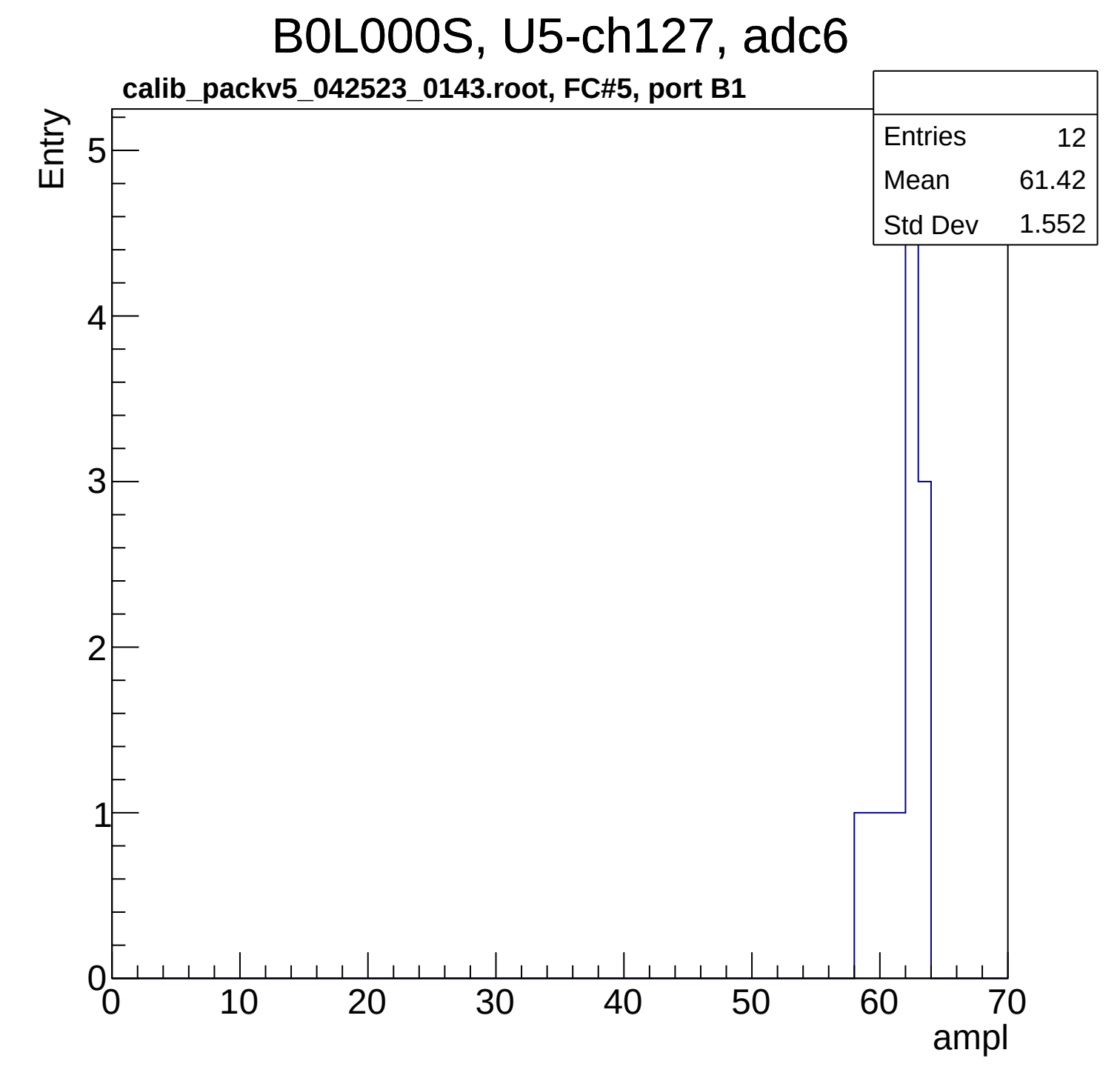

B0L000S, U5-ch127, adc1
calib_packv5_042523_0143.root, FC#5, port B1
Entries 51
Mean 34.65
Std Dev 6.039
Gaus mean : 35.8933
Gaus Width: 3.7160
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

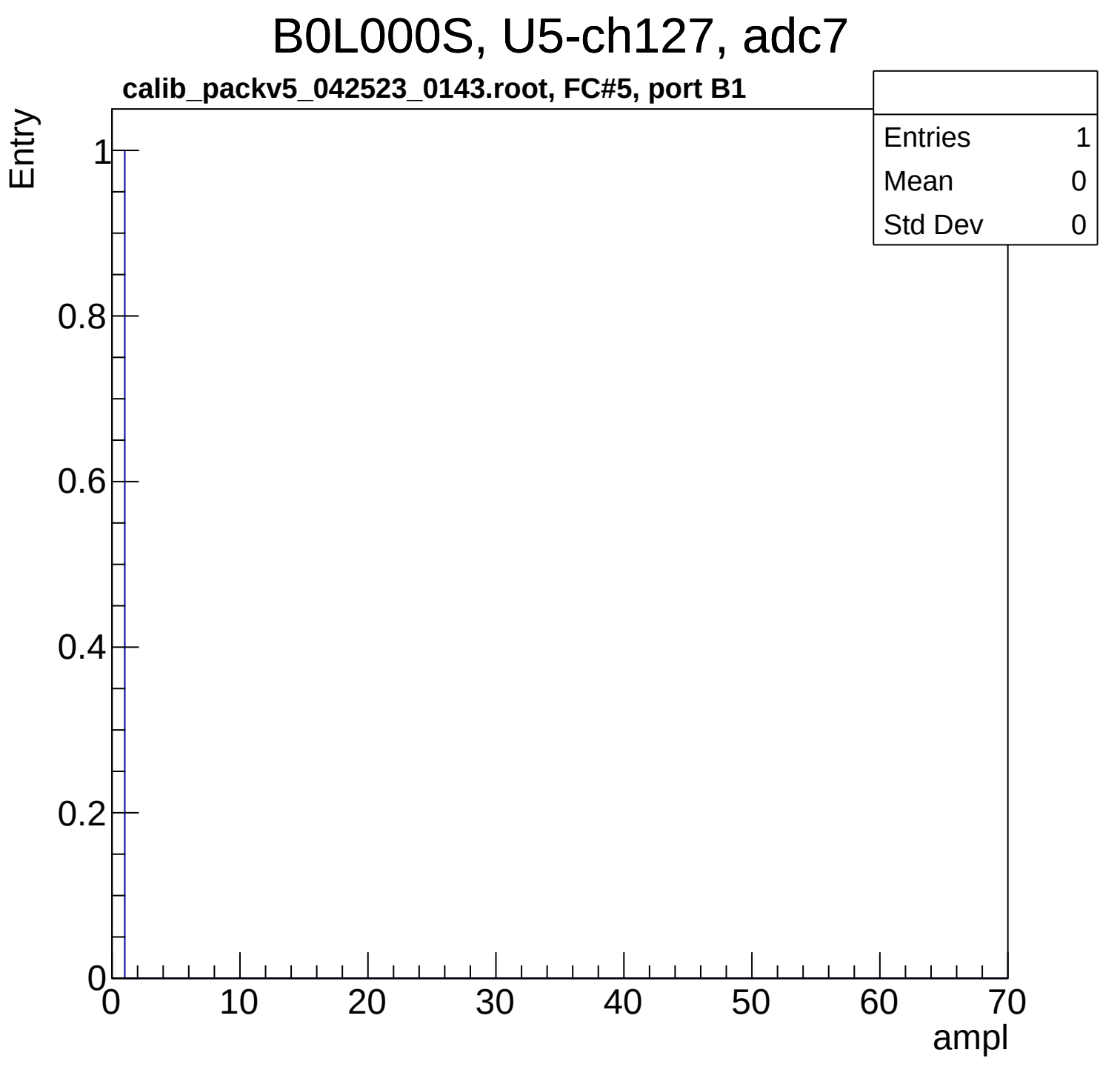

B0L000S, U5-ch127, adc2
calib_packv5_042523_0143.root, FC#5, port B1
Entries 75
Mean 41.37
Std Dev 3.709
Gaus mean : 42.0030
Gaus Width: 4.0276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

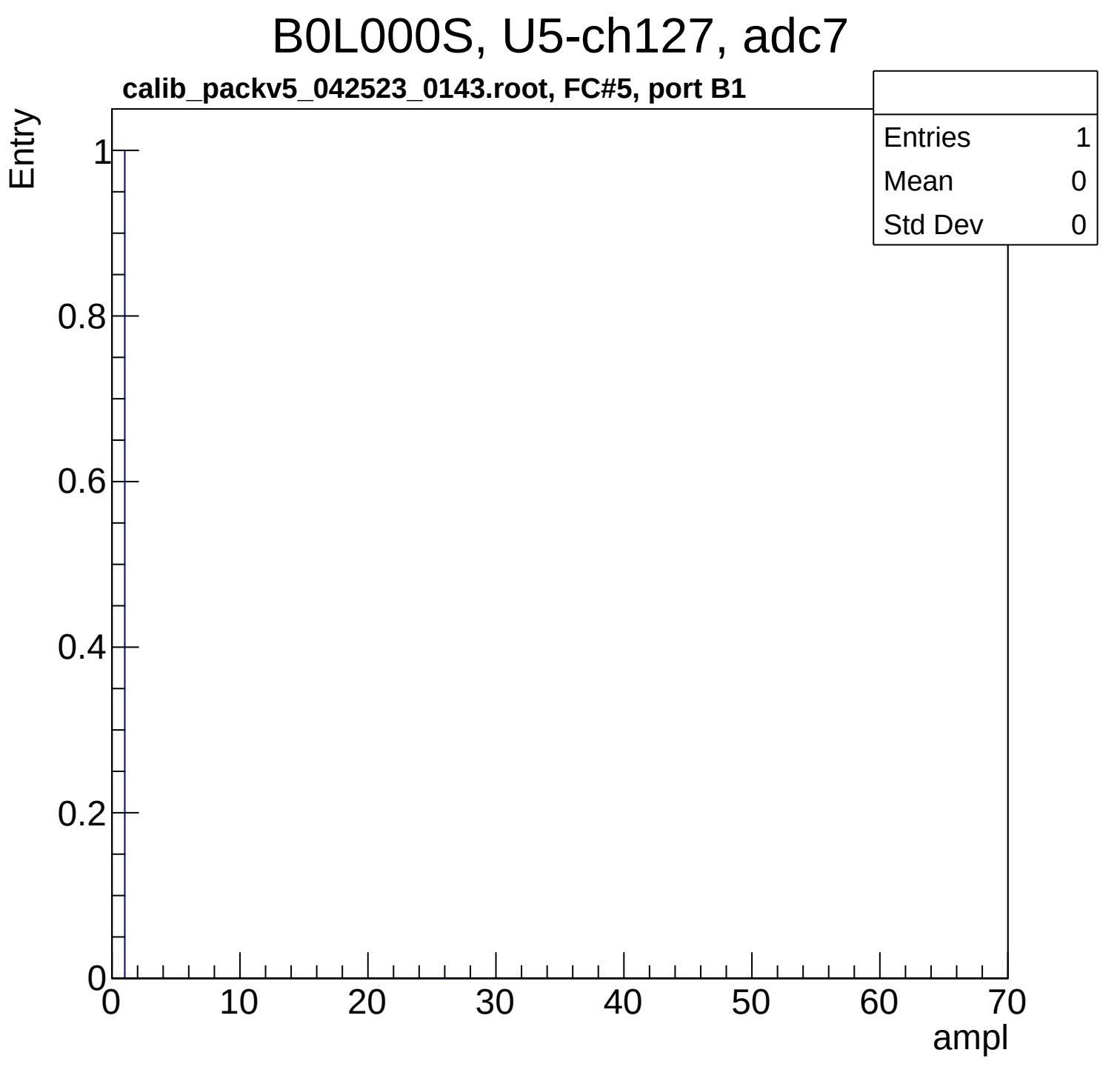

B0L000S, U5-ch127, adc3
calib_packv5_042523_0143.root, FC#5, port B1
Entries 57
Mean 47.7
Std Dev 3.685
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch127, adc4
calib_packv5_042523_0143.root, FC#5, port B1
Entries 62
Mean 53.84
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L000S, U5-ch127, adc5
calib_packv5_042523_0143.root, FC#5, port B1
Entries 54
Mean 58.2
Std Dev 8.423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L000S, U5-ch127, adc6
calib_packv5_042523_0143.root, FC#5, port B1
Entries 12
Mean 61.42
Std Dev 1.552
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L000S, U5-ch127, adc7
calib_packv5_042523_0143.root, FC#5, port B1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl