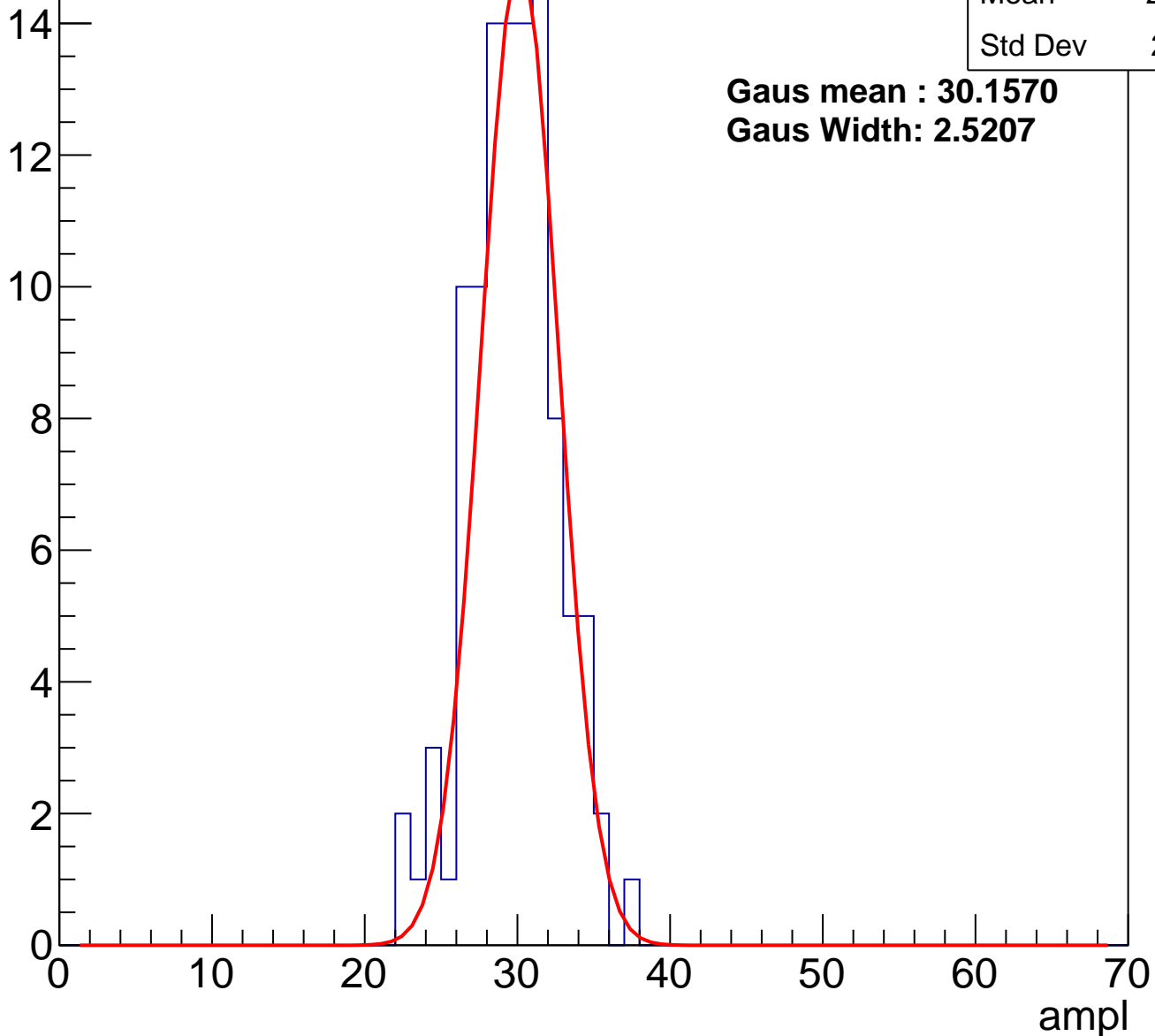


B1L001S, U14-ch0, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	105
Mean	29.29
Std Dev	2.841

B1L001S, U14-ch0, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.29
Std Dev	3.438

Gaus mean : 35.8610

Gaus Width: 3.2309

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

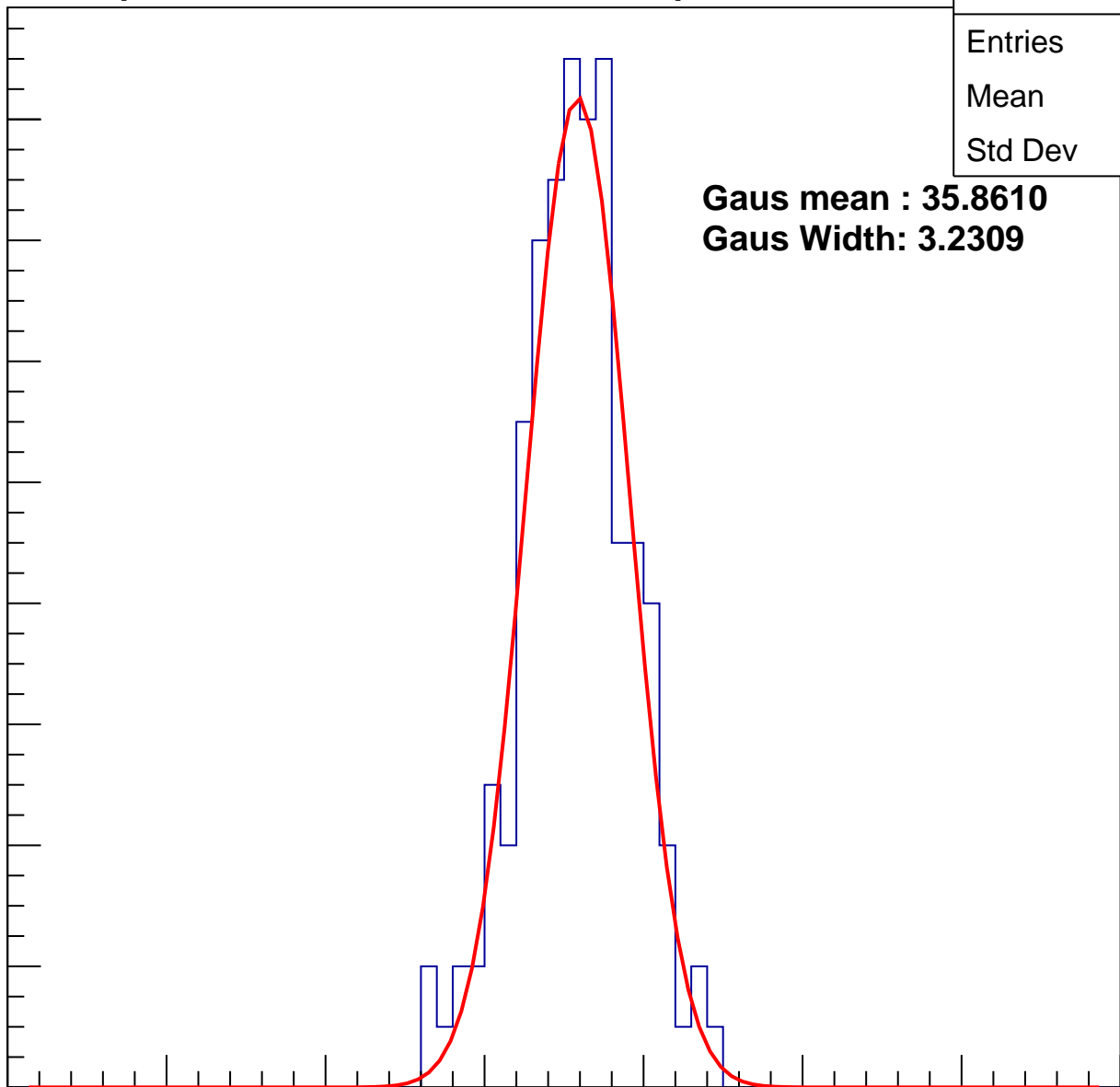
40

50

60

70

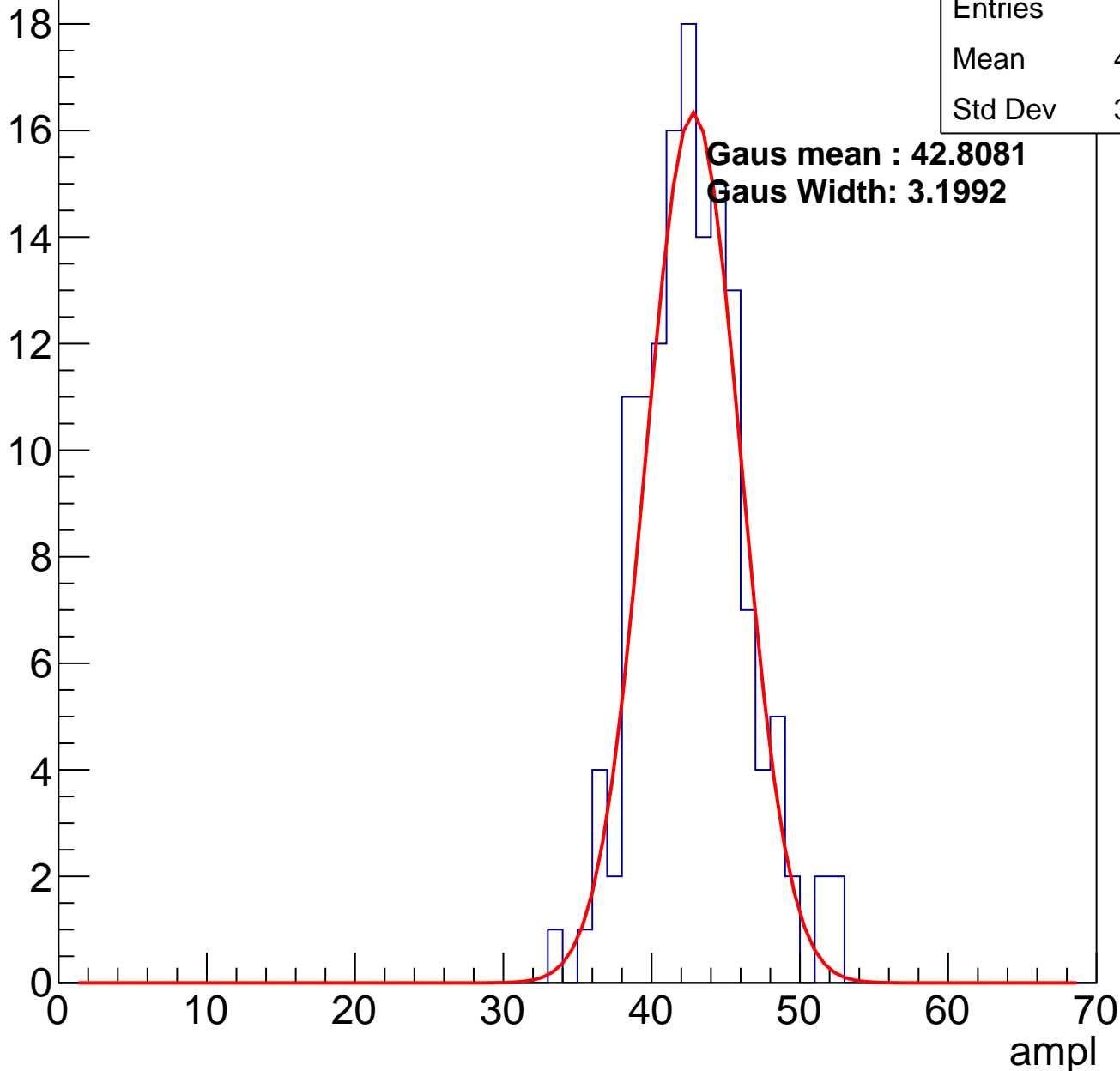
ampl



B1L001S, U14-ch0, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

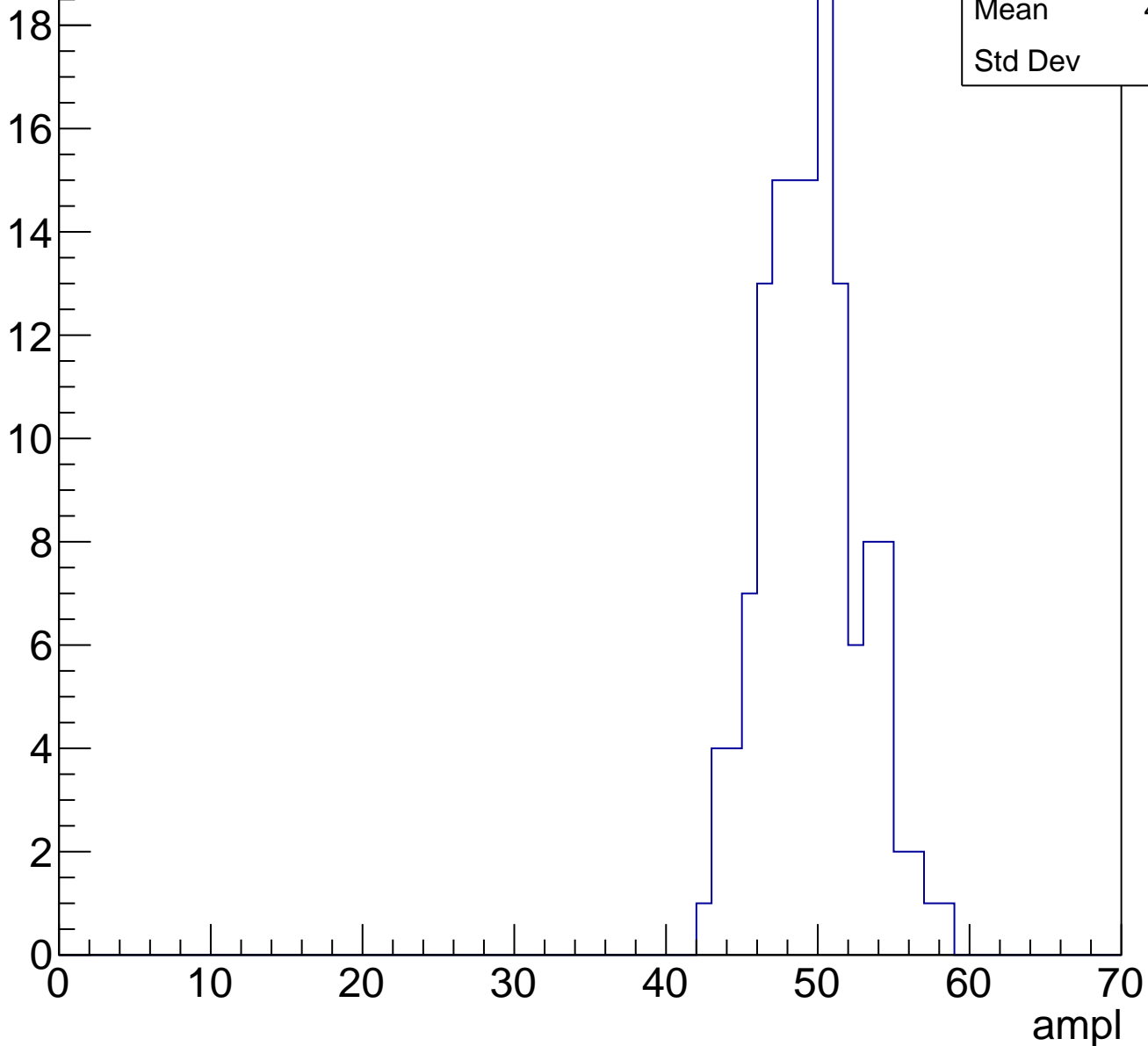


B1L001S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	49.11
Std Dev	3.19

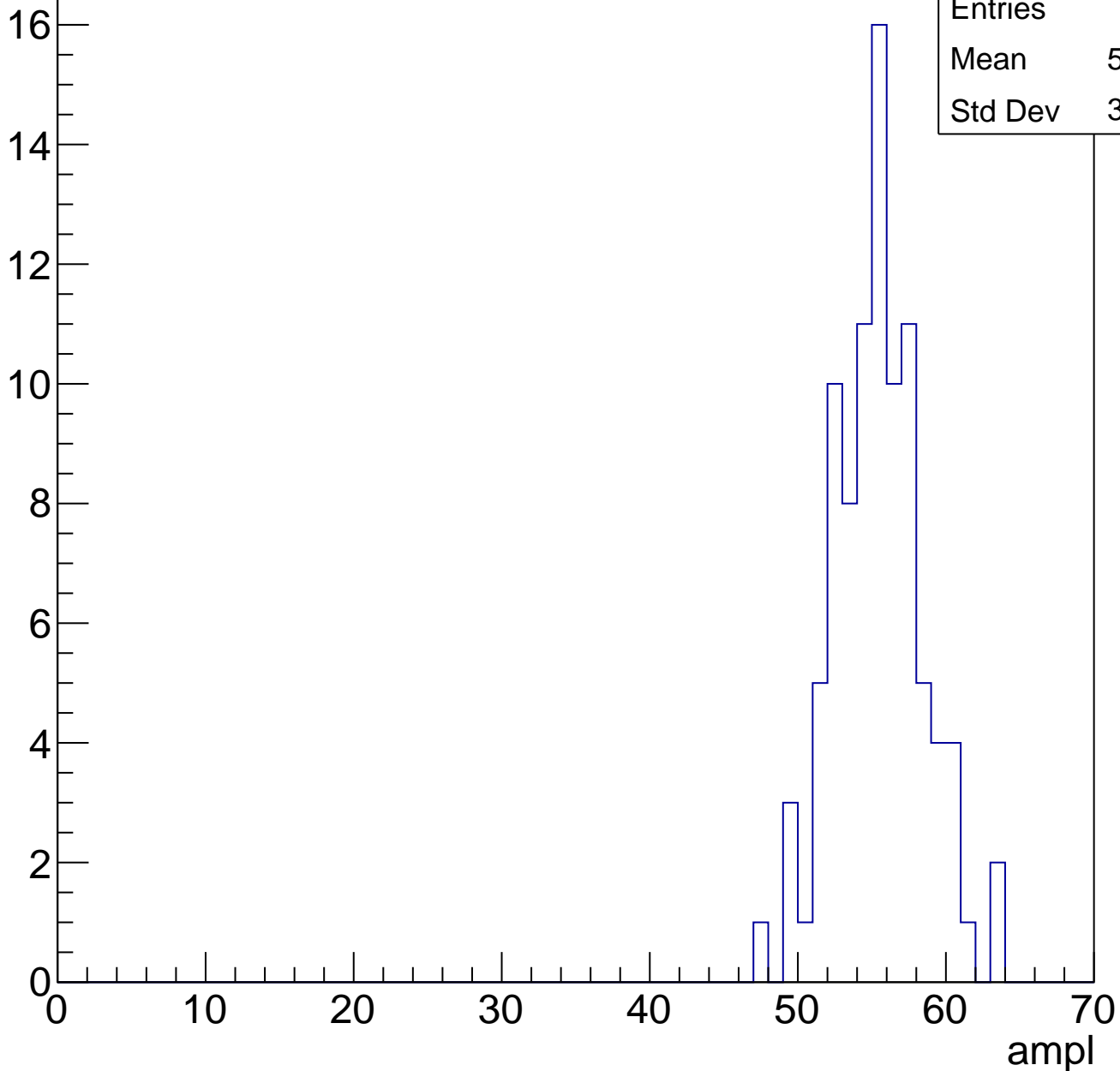
Entry



B1L001S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch0, adc5

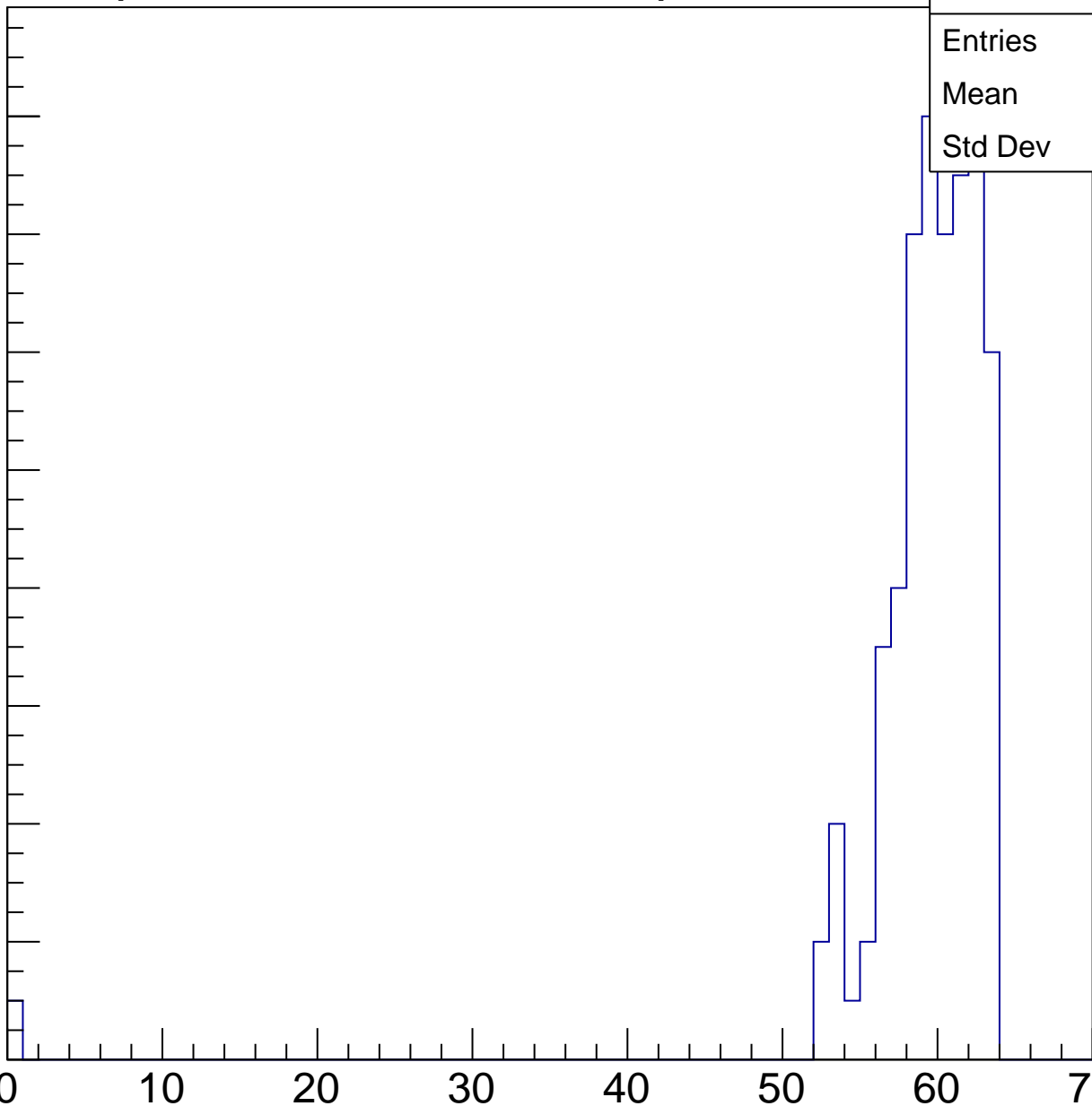
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	113
Mean	58.84
Std Dev	6.172

ampl

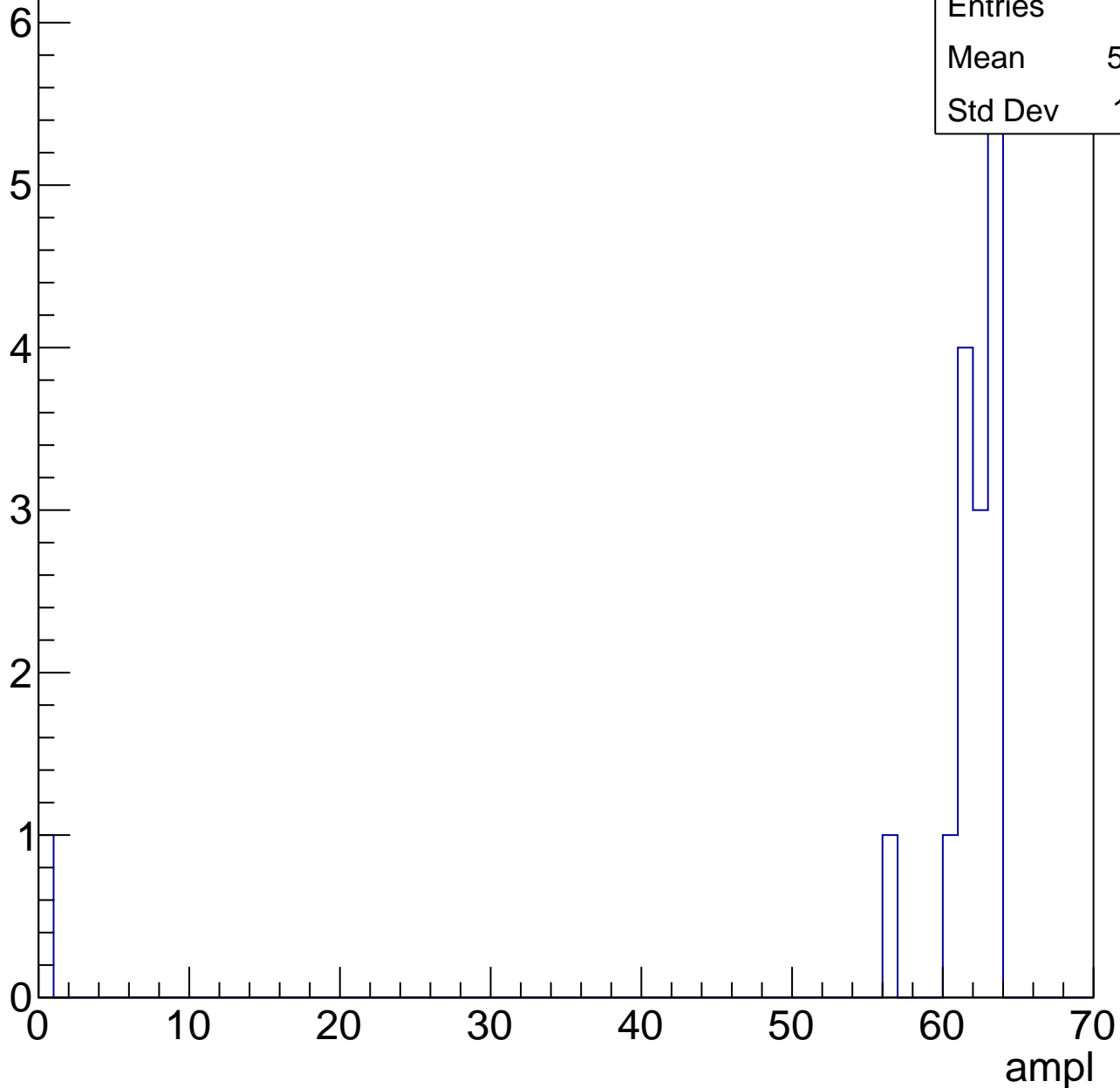


B1L001S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

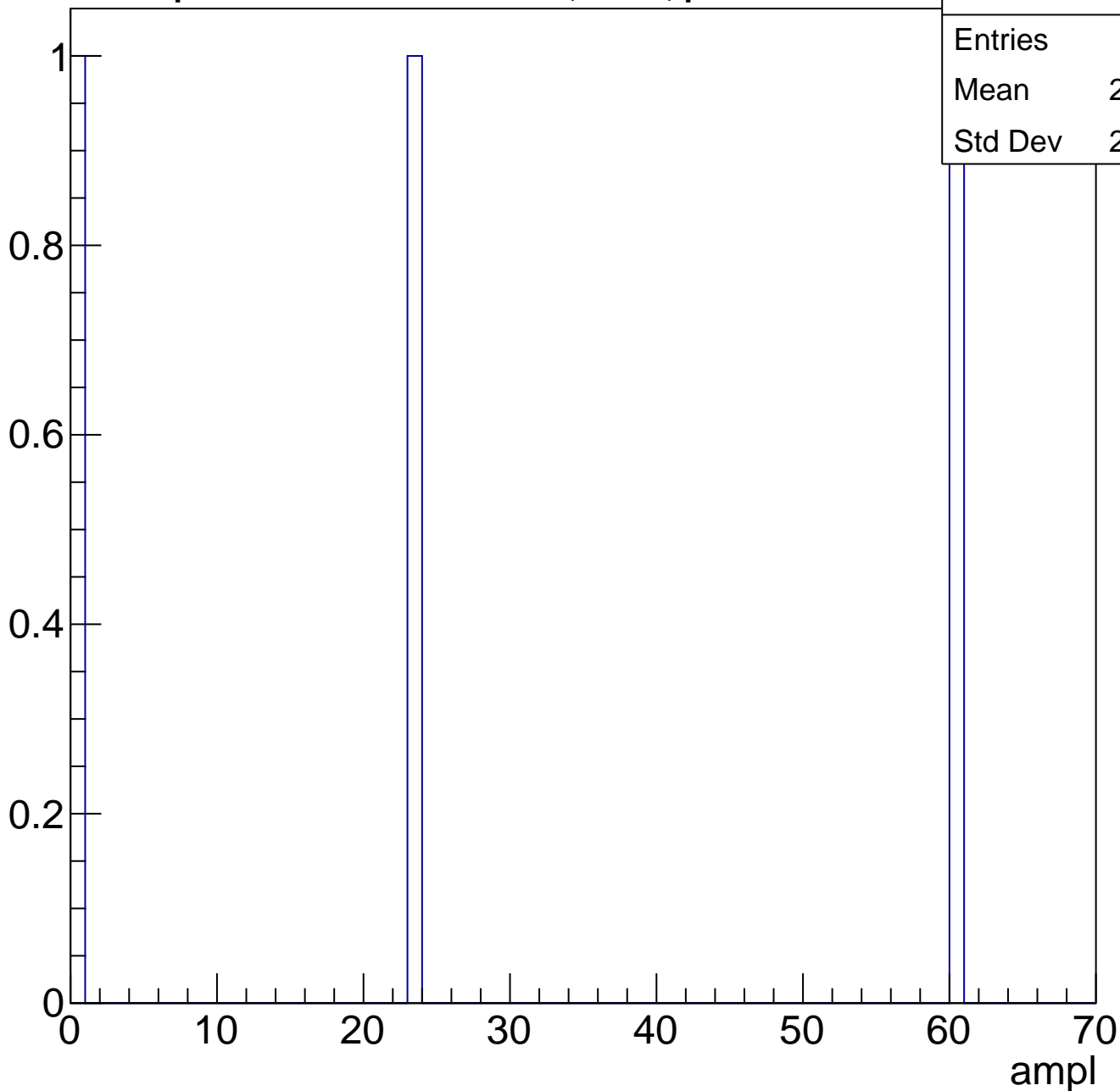
Entries	16
Mean	57.75
Std Dev	15.01



B1L001S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	27.67
Std Dev	24.72

B1L001S, U14-ch1, adc0

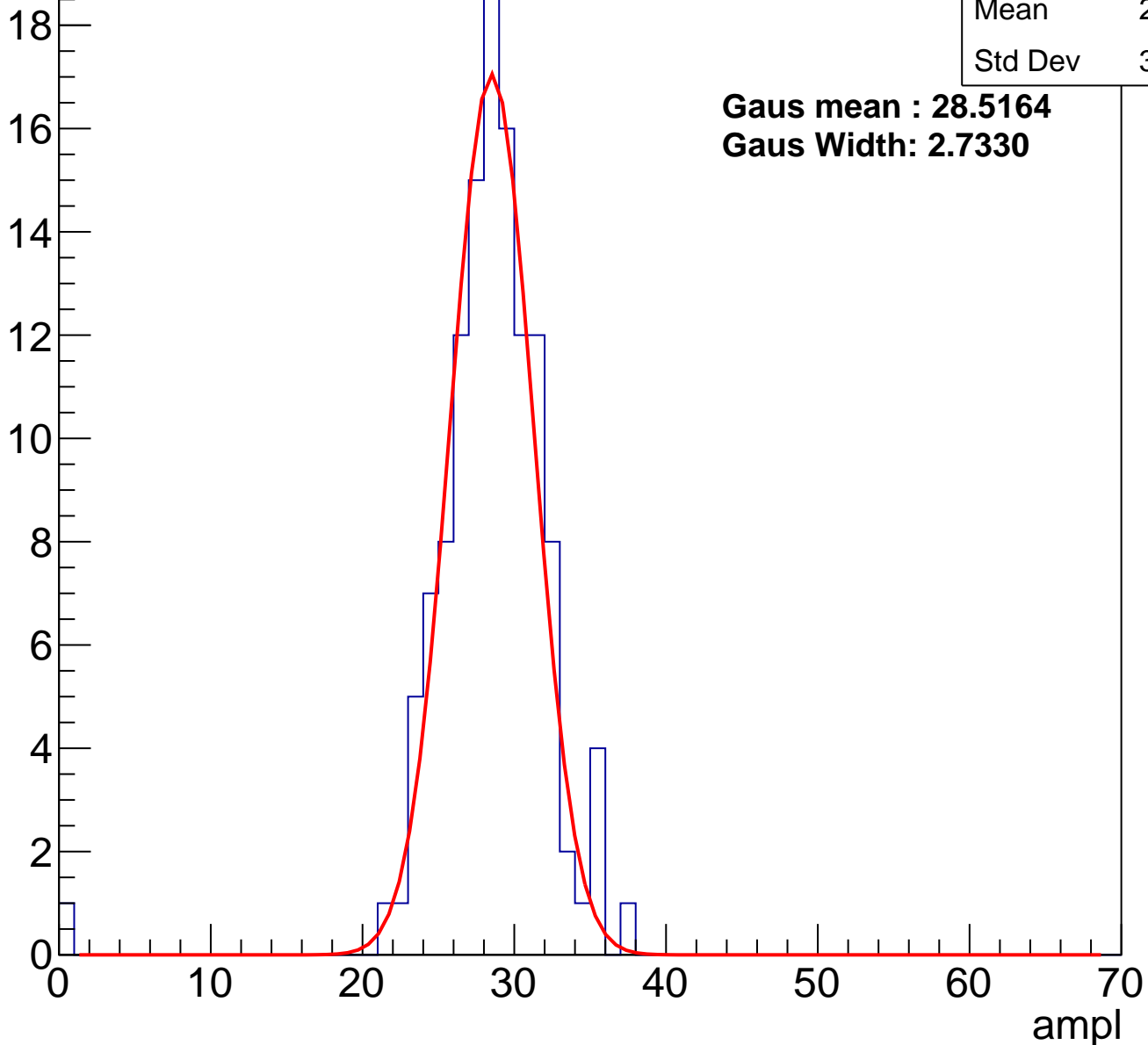
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	28.03
Std Dev	3.903

Gaus mean : 28.5164

Gaus Width: 2.7330

Entry



B1L001S, U14-ch1, adc1

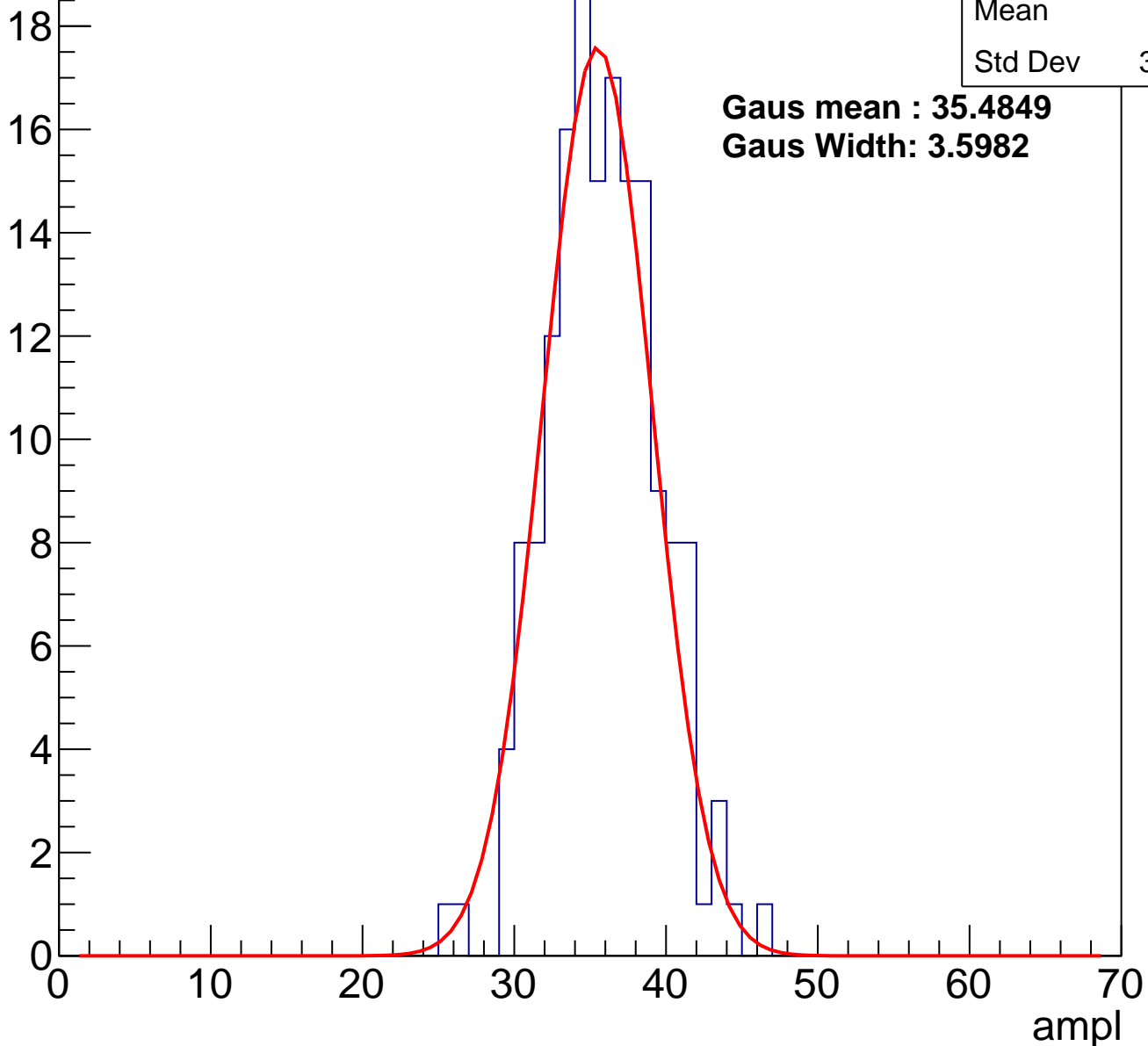
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	35.4
Std Dev	3.605

Gaus mean : 35.4849

Gaus Width: 3.5982

Entry



B1L001S, U14-ch1, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

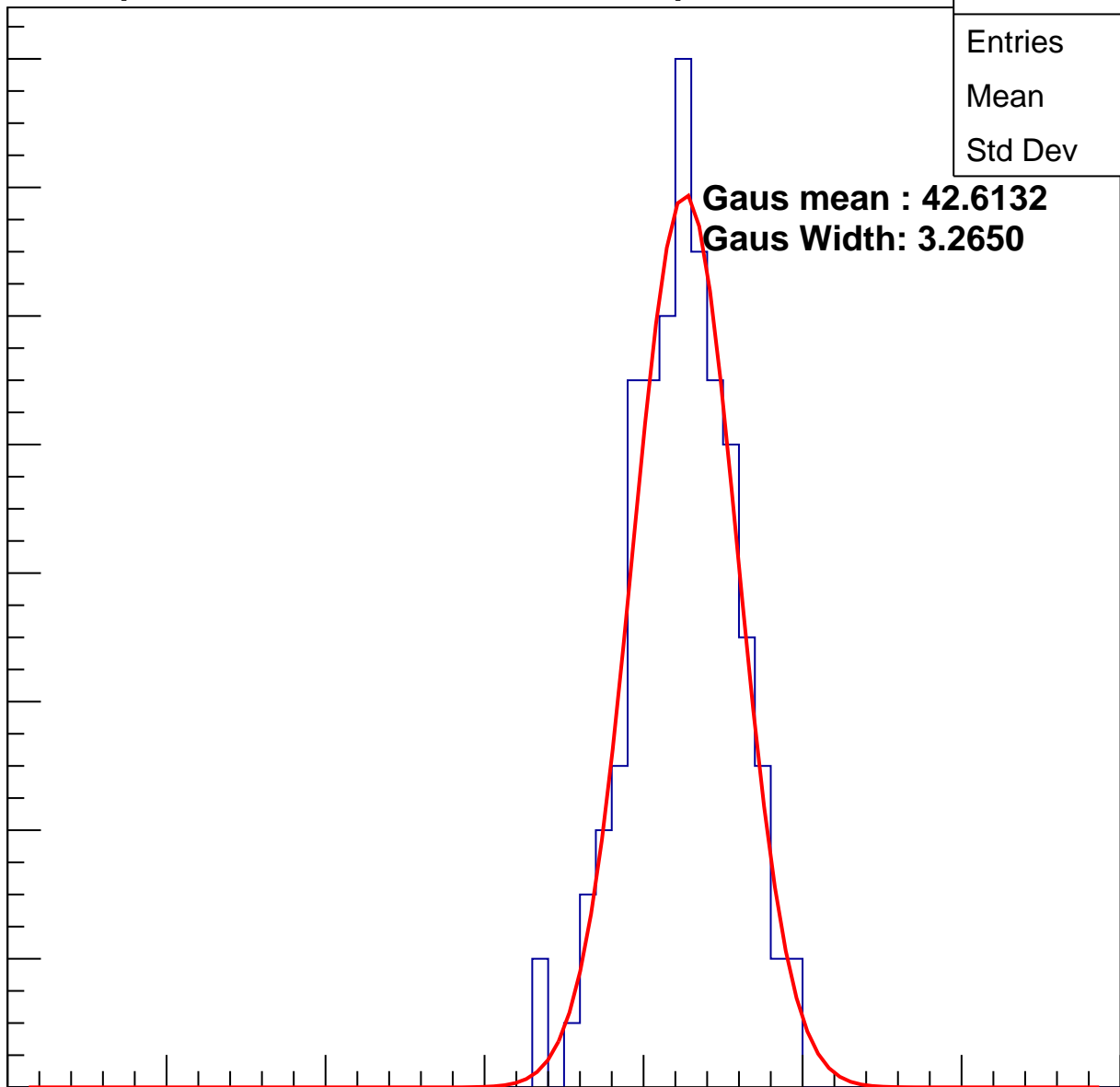
Entries	115
Mean	41.95
Std Dev	3.241

Gaus mean : 42.6132

Gaus Width: 3.2650

0 10 20 30 40 50 60 70

ampl

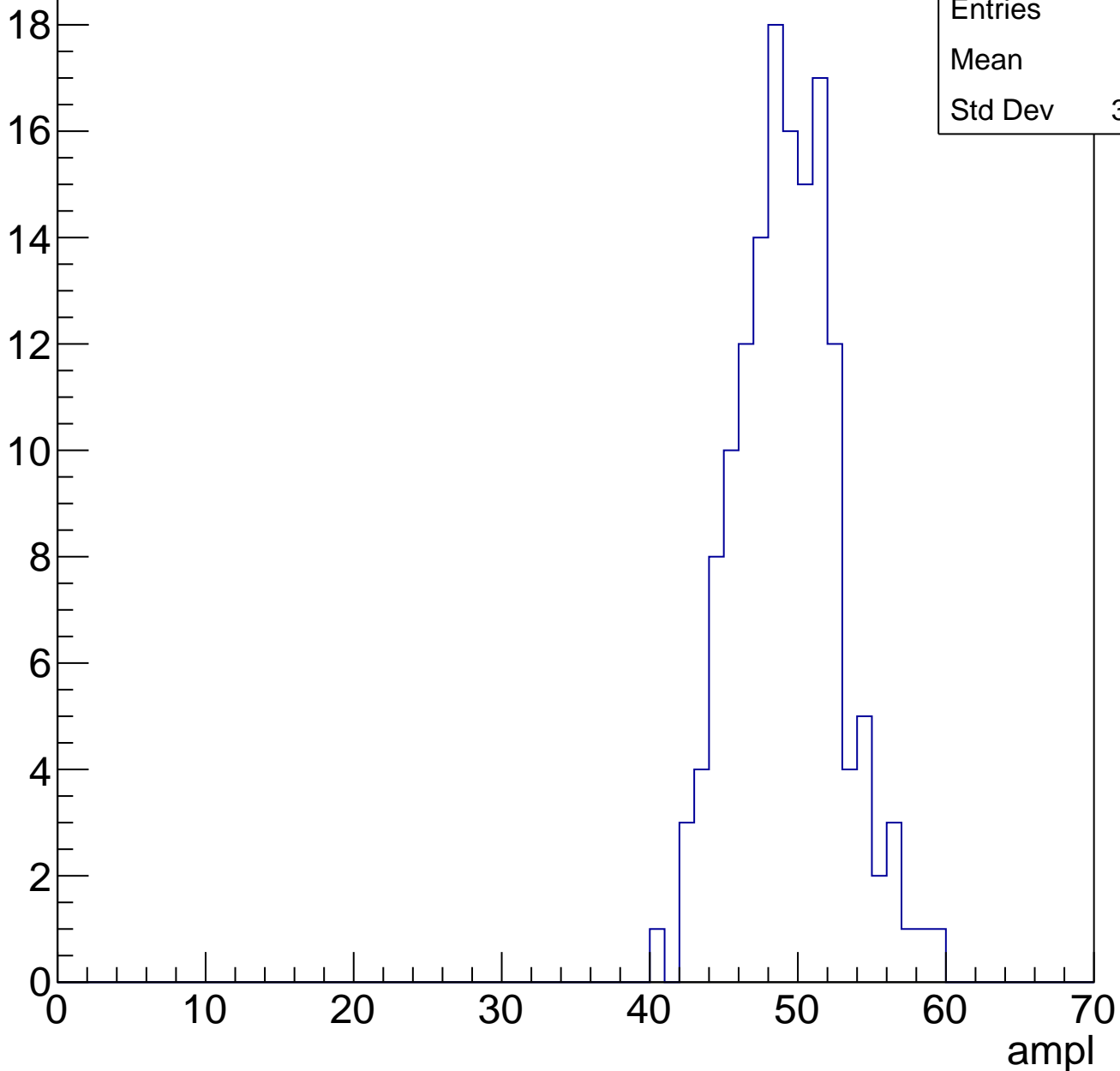


B1L001S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	48.8
Std Dev	3.452

Entry



B1L001S, U14-ch1, adc4

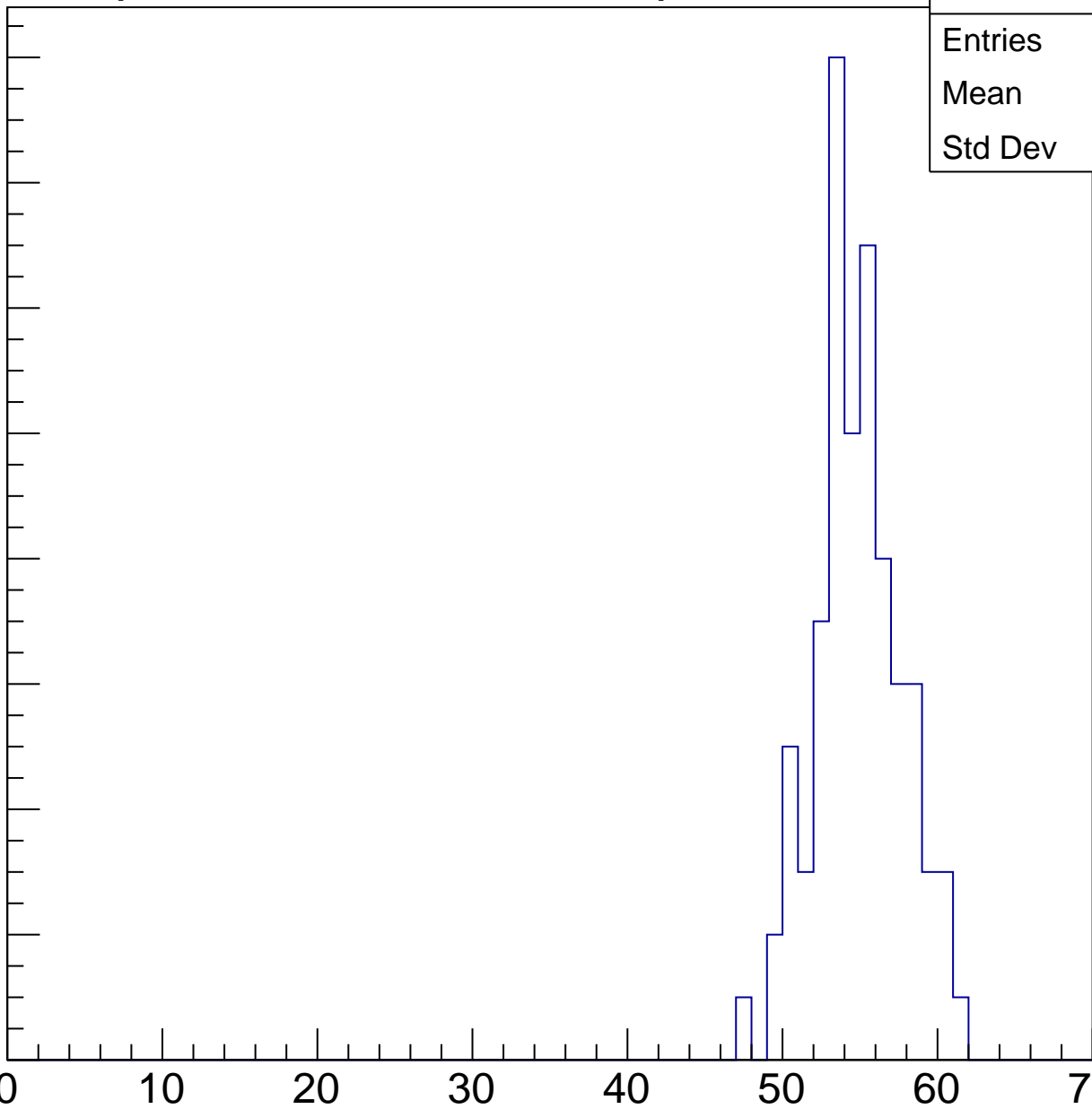
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	84
Mean	54.42
Std Dev	2.808

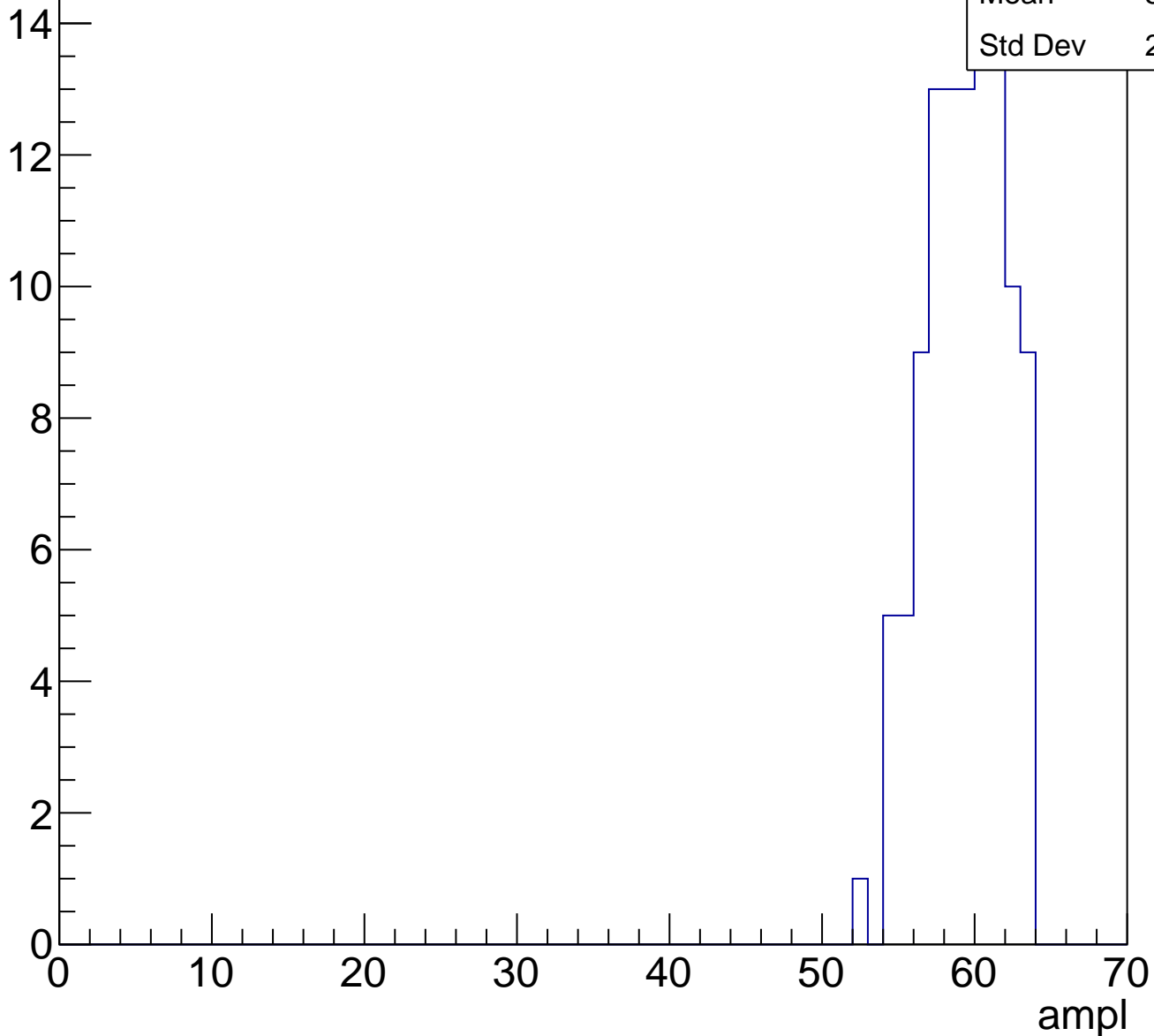
ampl



B1L001S, U14-ch1, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

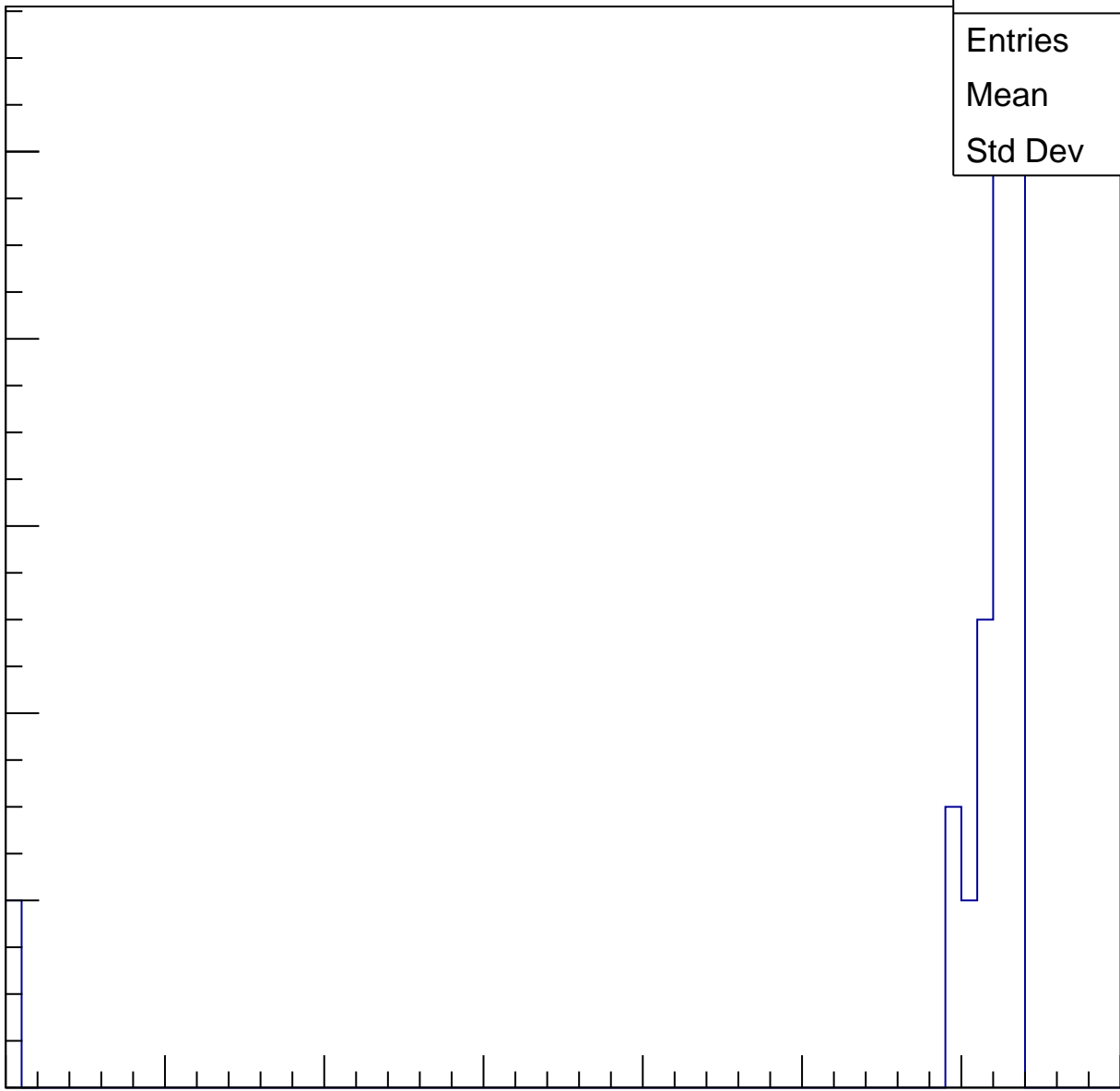
2

0

Entries	33
Mean	58.03
Std Dev	14.79

ampl

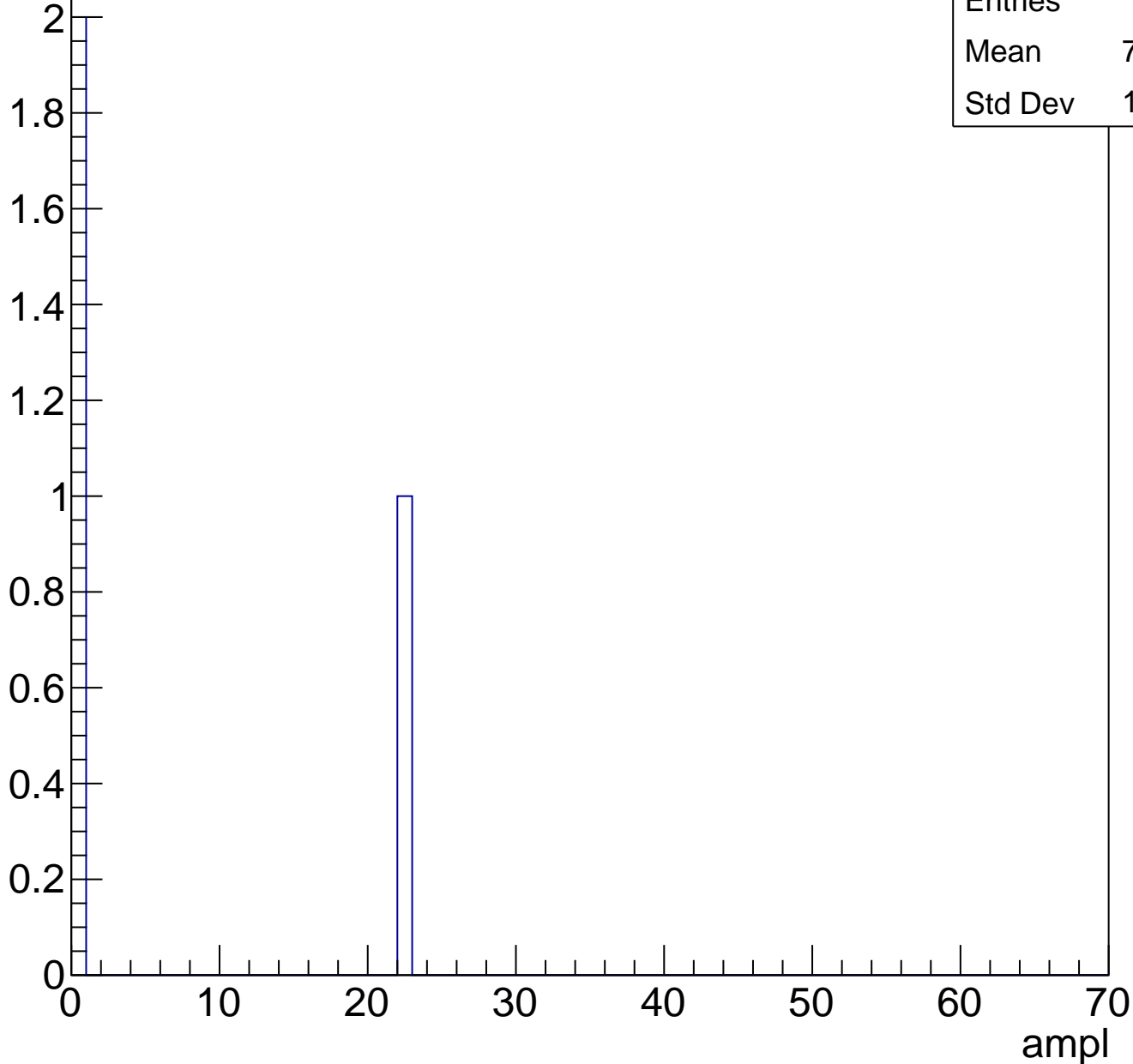
0 10 20 30 40 50 60 70



B1L001S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



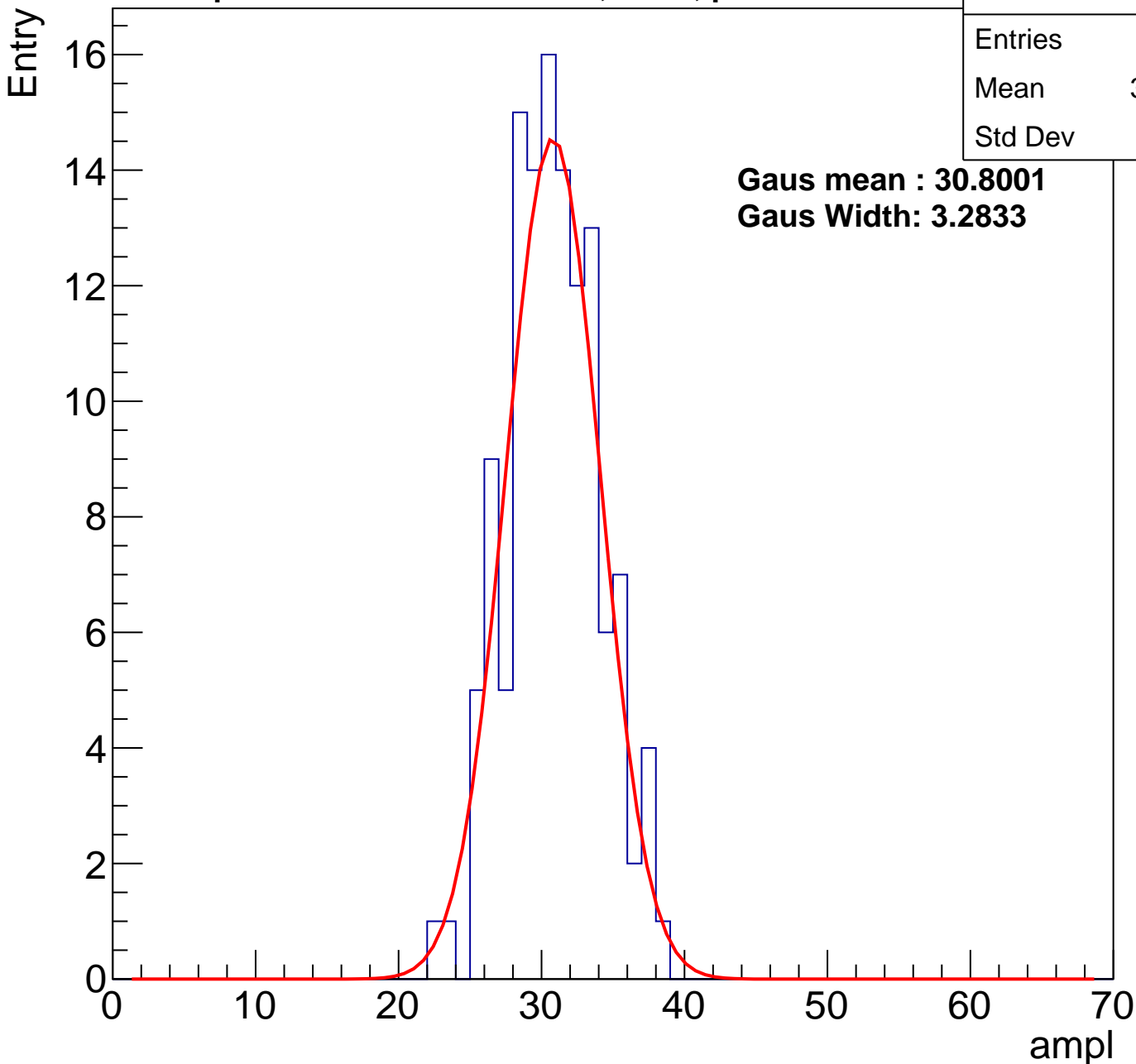
B1L001S, U14-ch2, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.39
Std Dev	3.18

Gaus mean : 30.8001

Gaus Width: 3.2833



B1L001S, U14-ch2, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	37.02
Std Dev	3.263

Gaus mean : 37.5981

Gaus Width: 3.4205

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

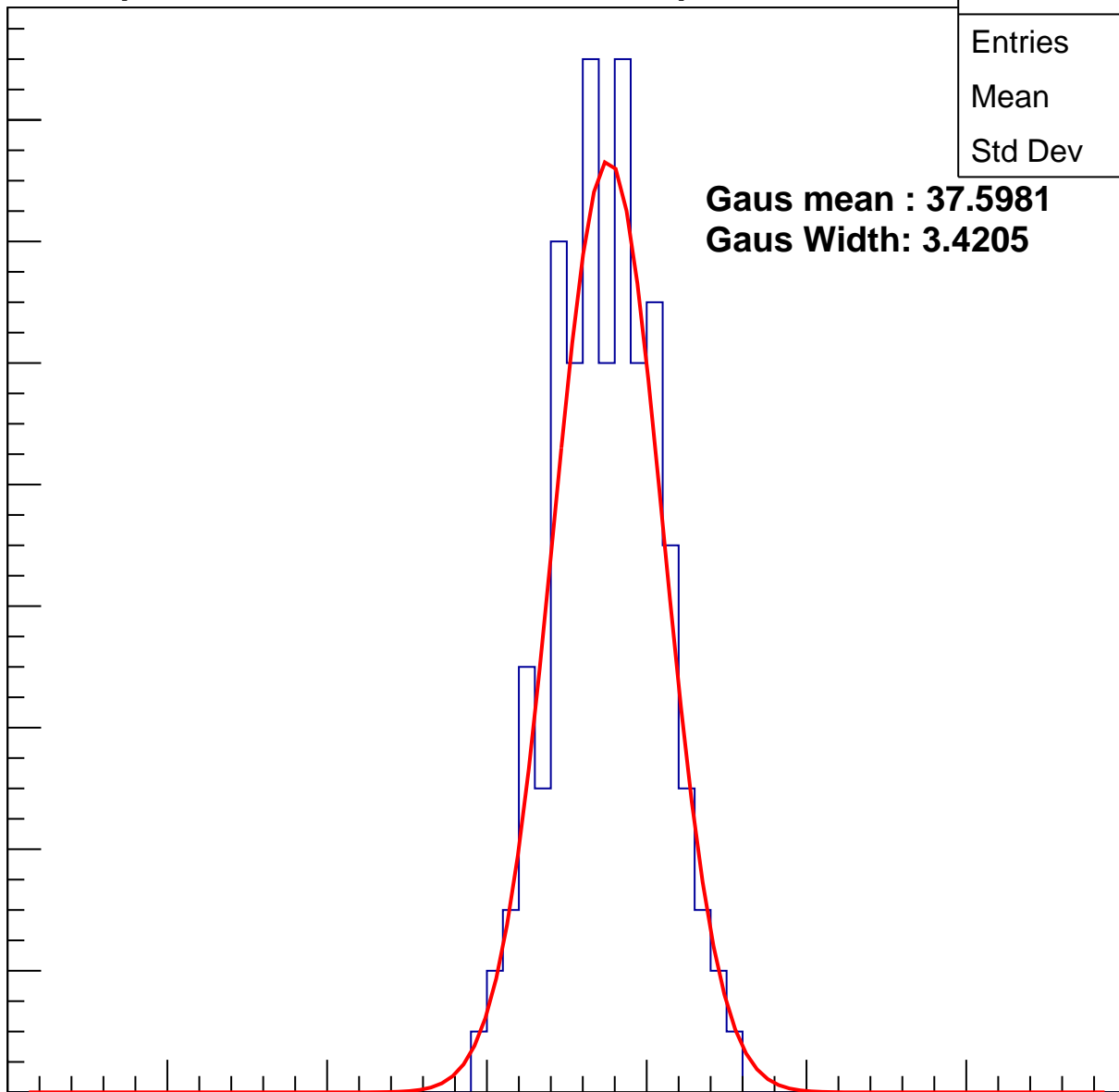
40

50

60

70

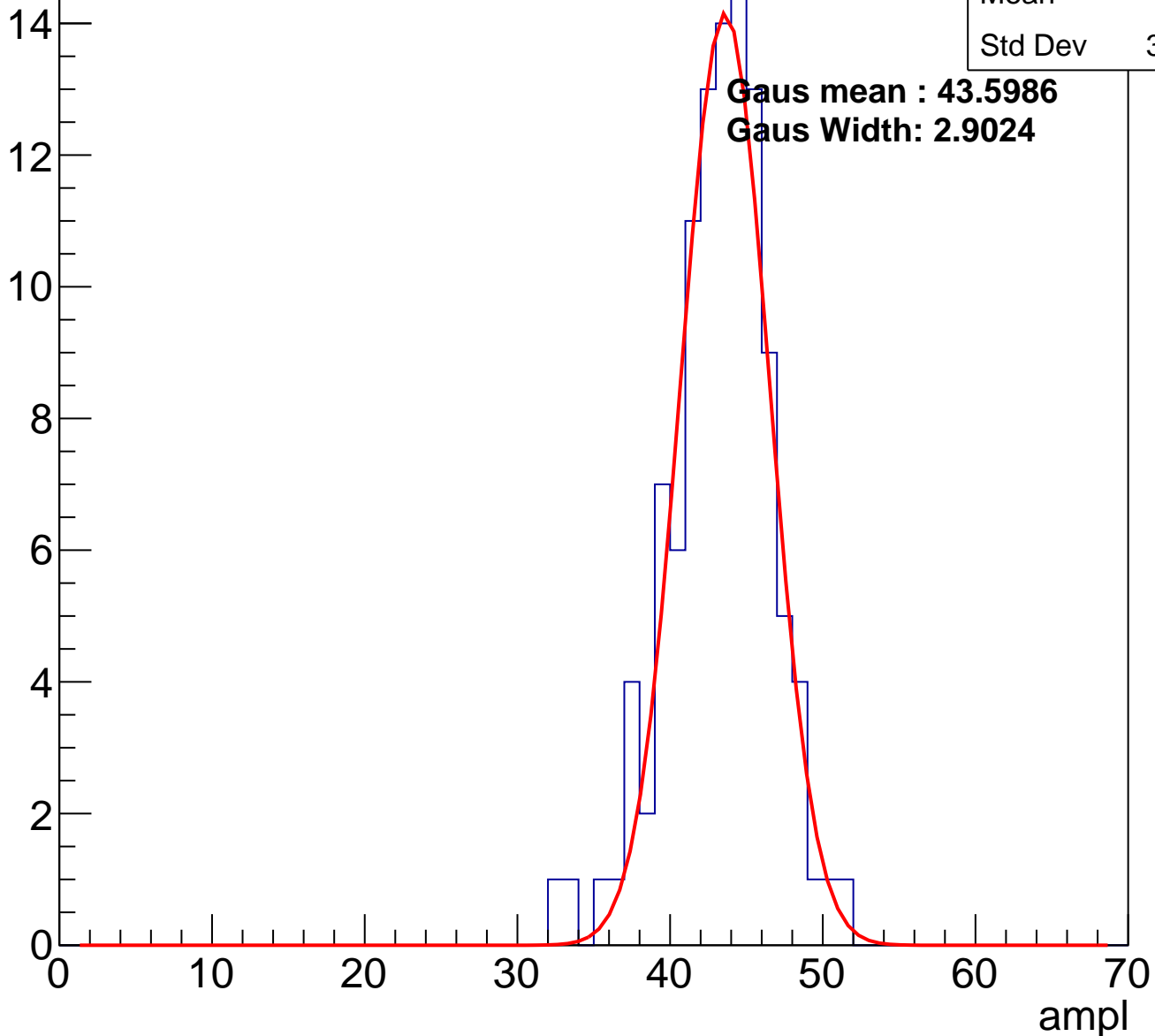
ampl



B1L001S, U14-ch2, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#2, port C2

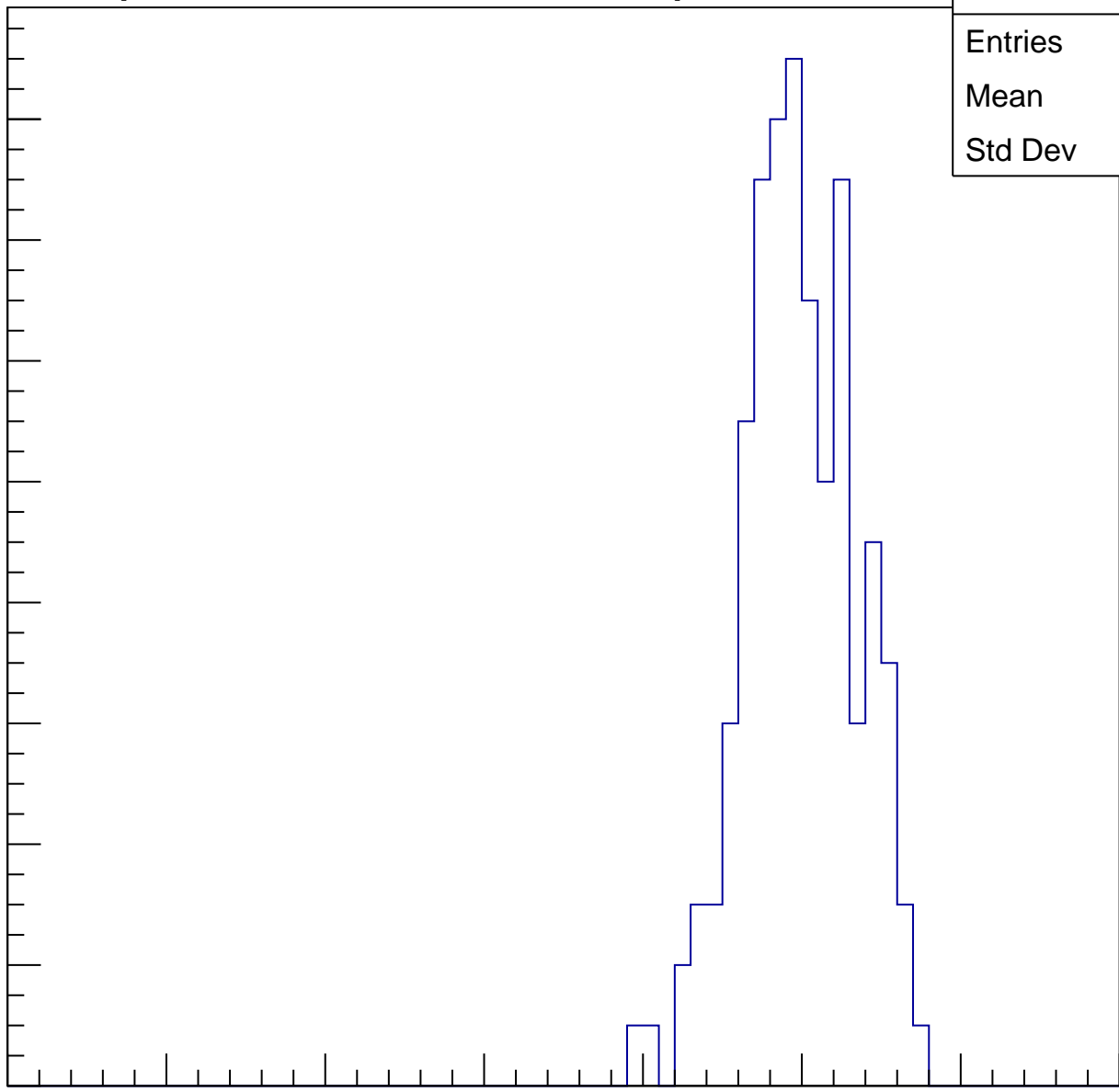
Entries	139
Mean	49.35
Std Dev	3.485

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	55.4
Std Dev	3.264

Entry

14
12
10
8
6
4
2
0

0

10

20

30

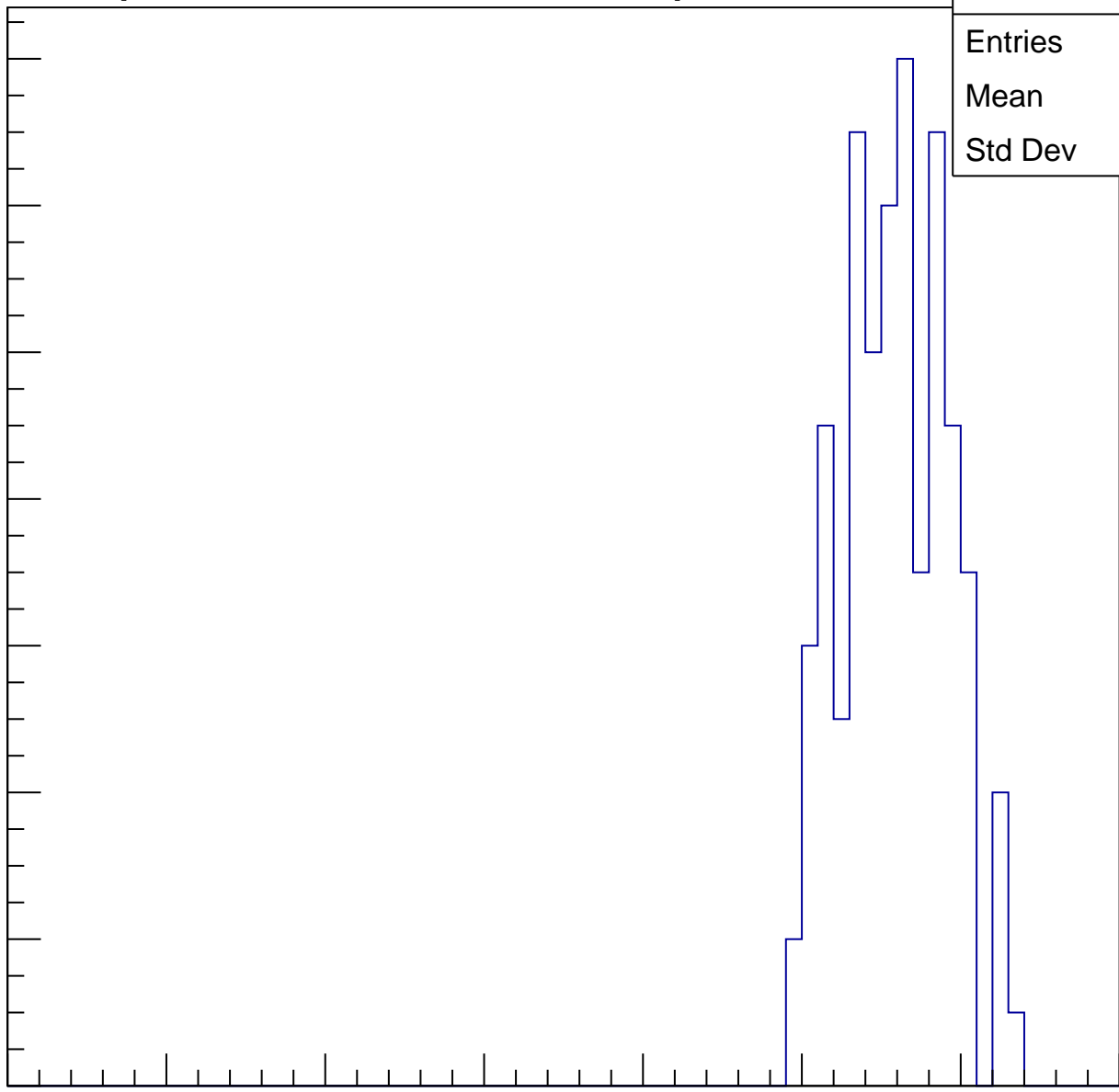
40

50

60

70

ampl



B1L001S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

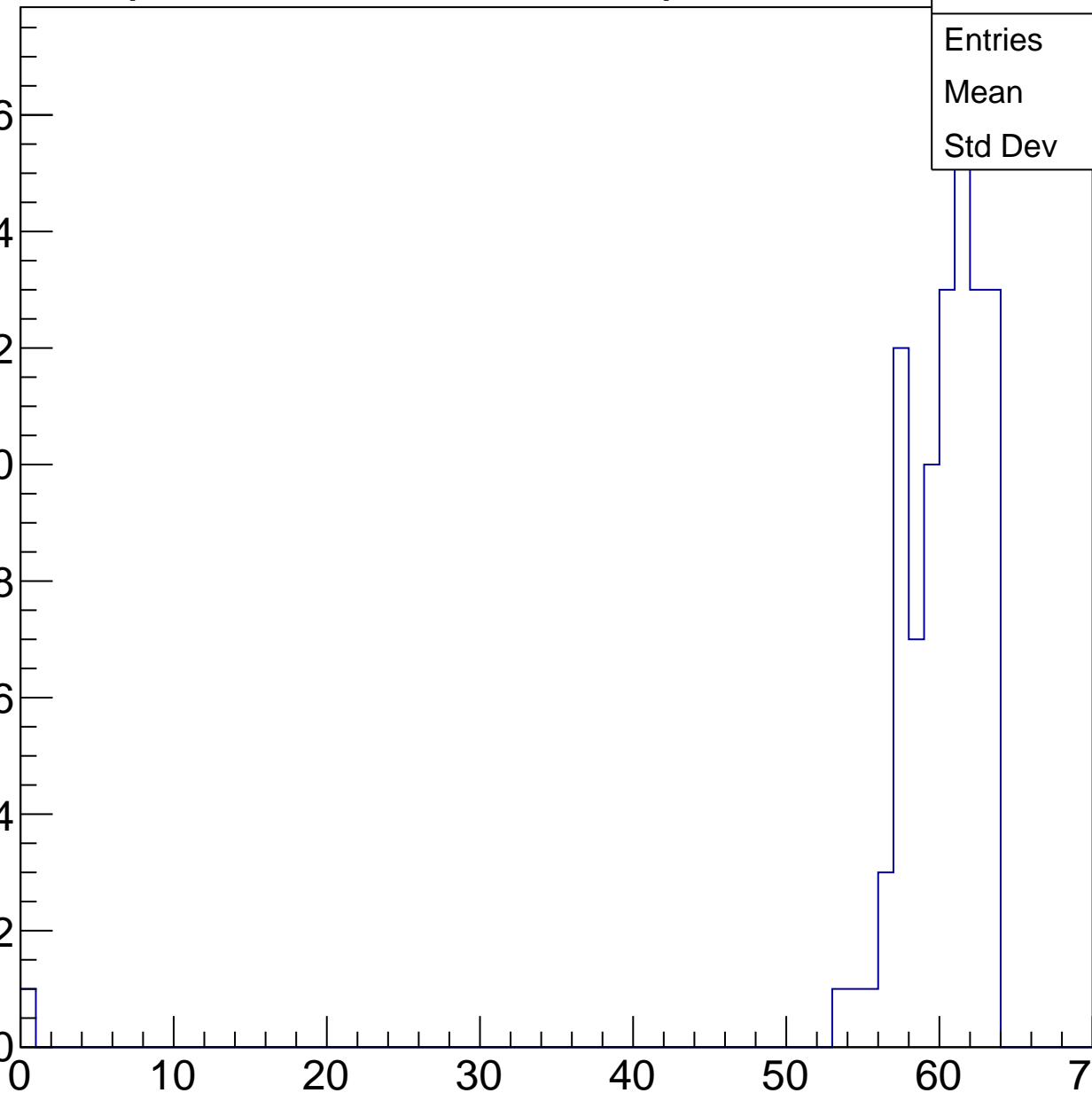
4

2

0

Entries	92
Mean	59.26
Std Dev	6.626

ampl

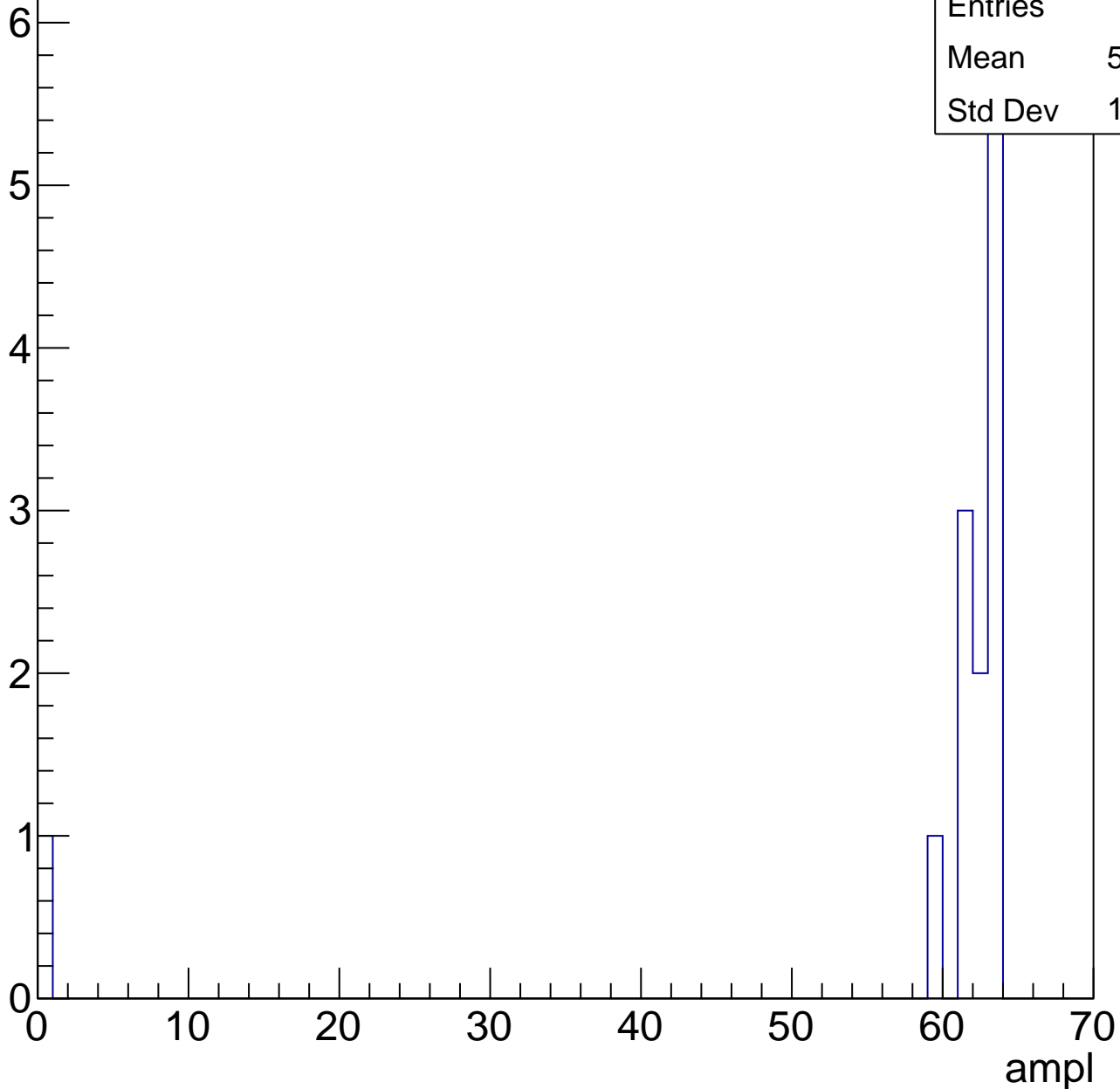


B1L001S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

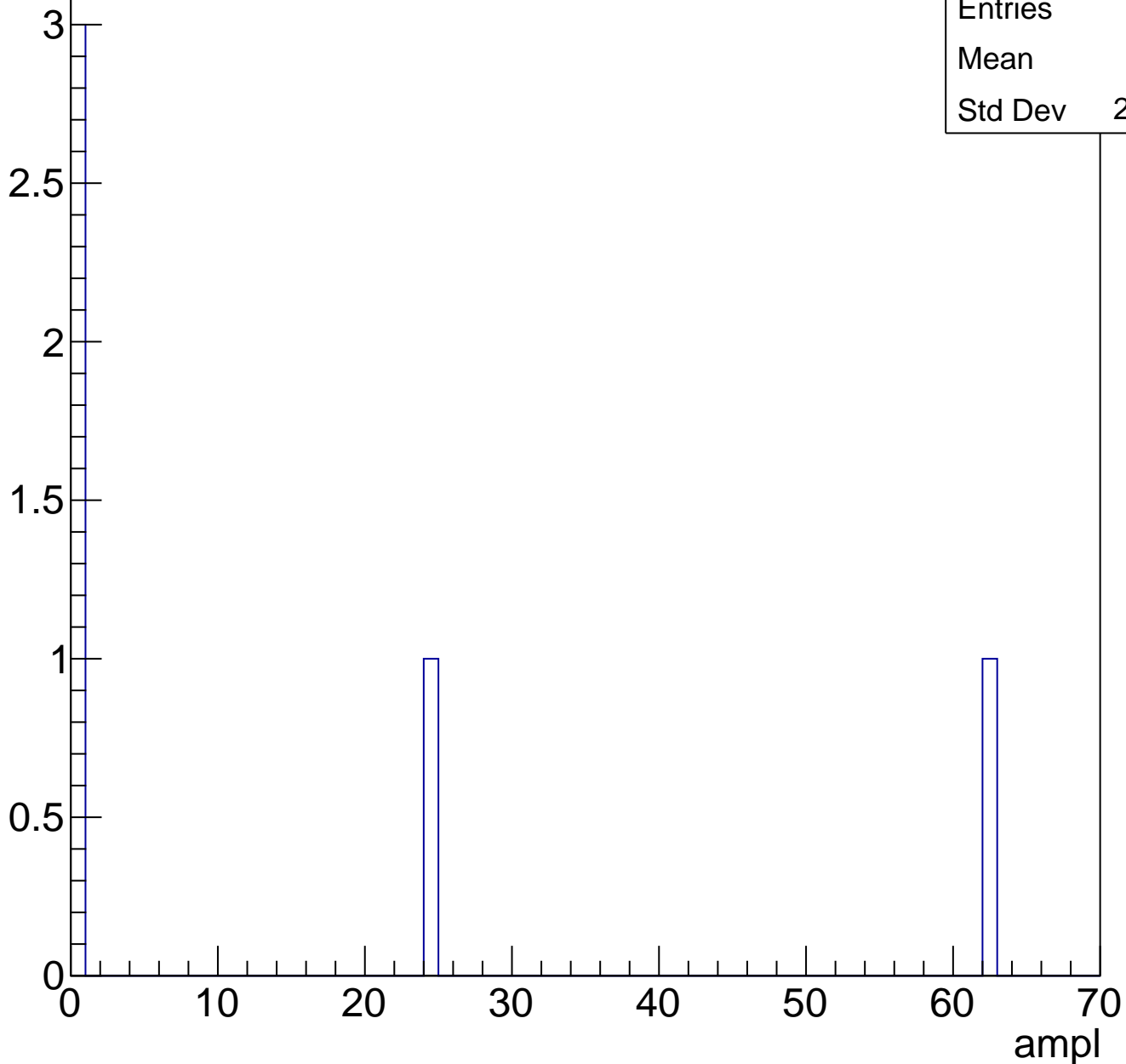
Entries	13
Mean	57.23
Std Dev	16.56



B1L001S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	17.2
Std Dev	24.25

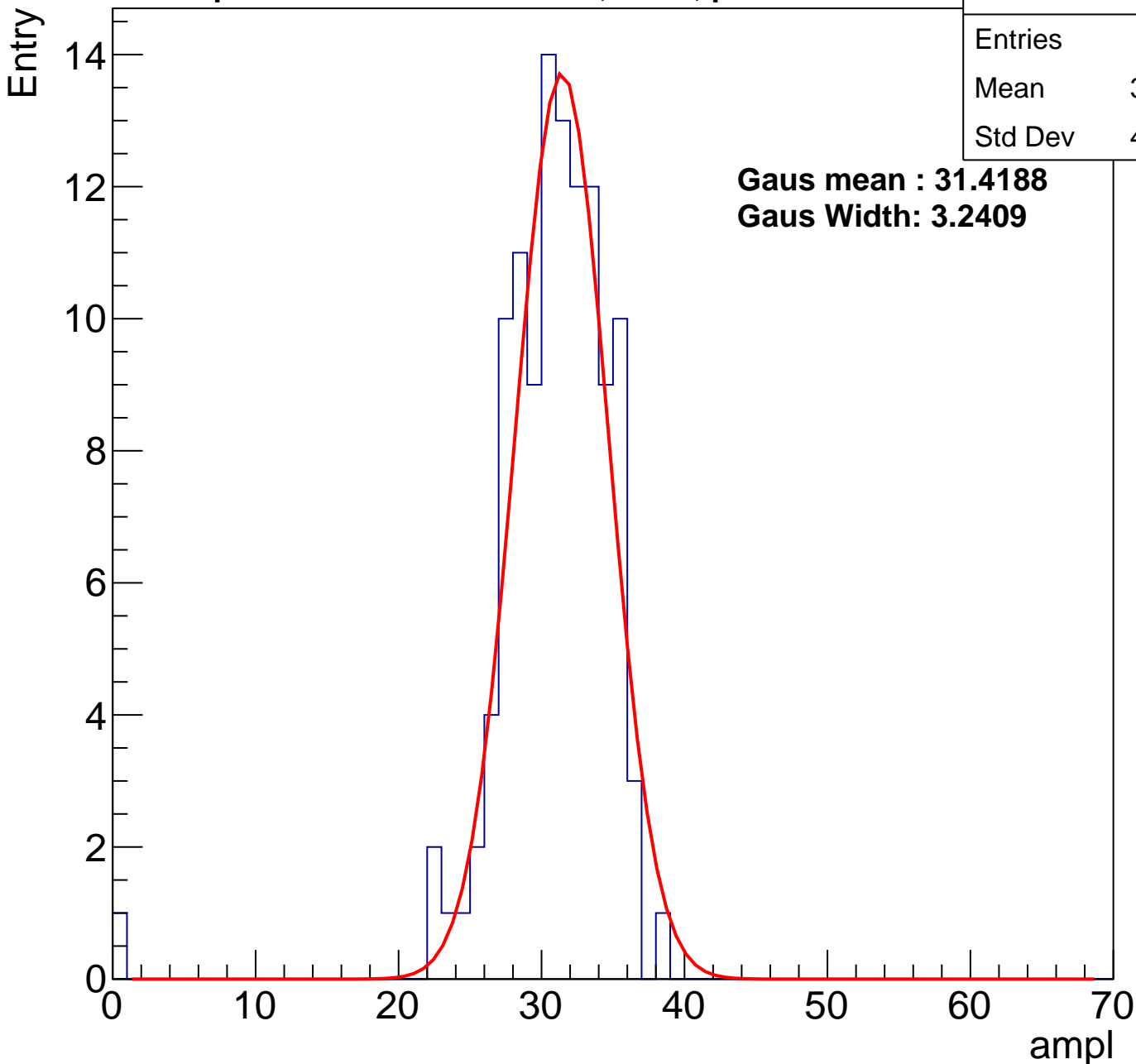
B1L001S, U14-ch3, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	30.34
Std Dev	4.265

Gaus mean : 31.4188

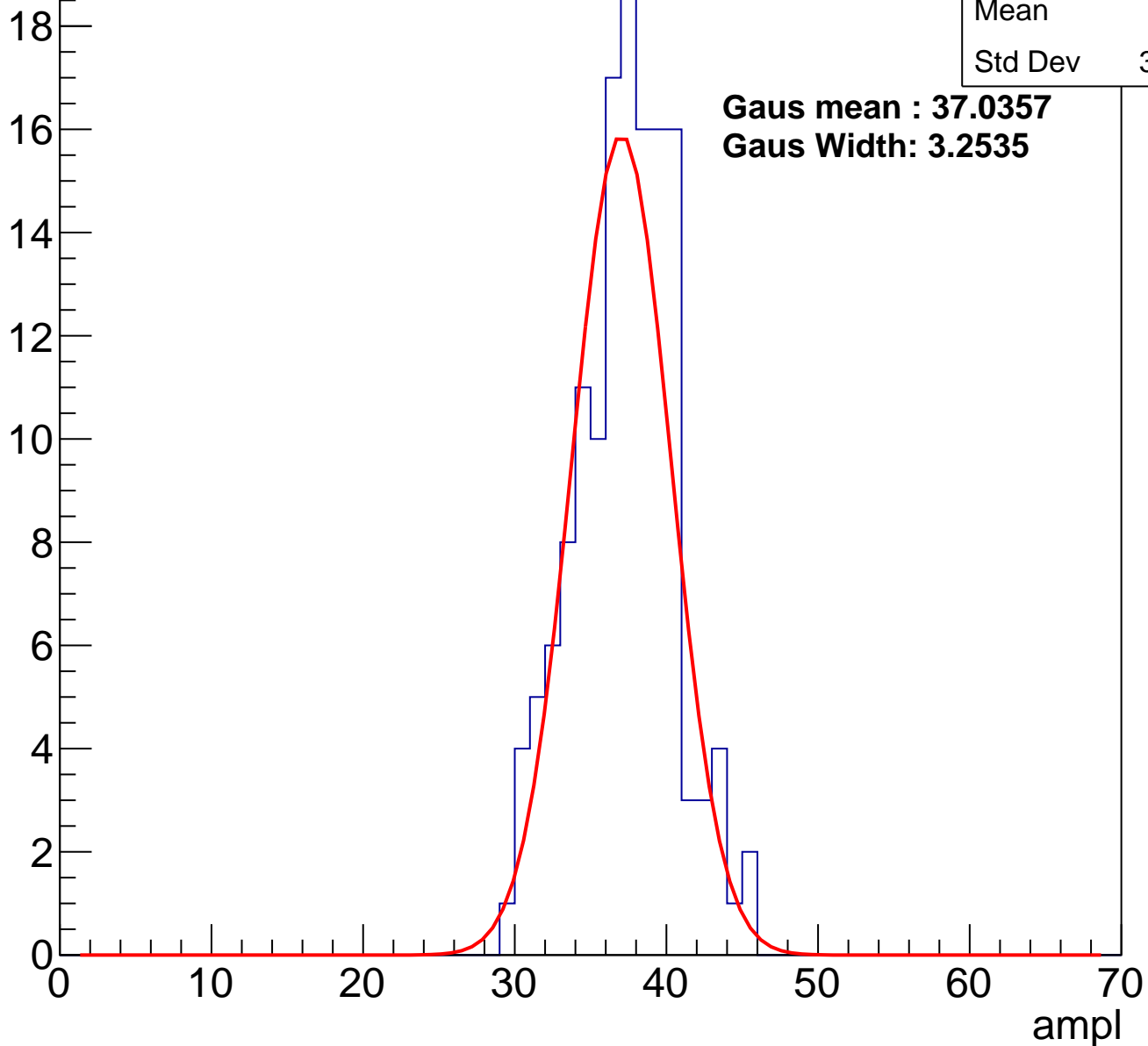
Gaus Width: 3.2409



B1L001S, U14-ch3, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch3, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

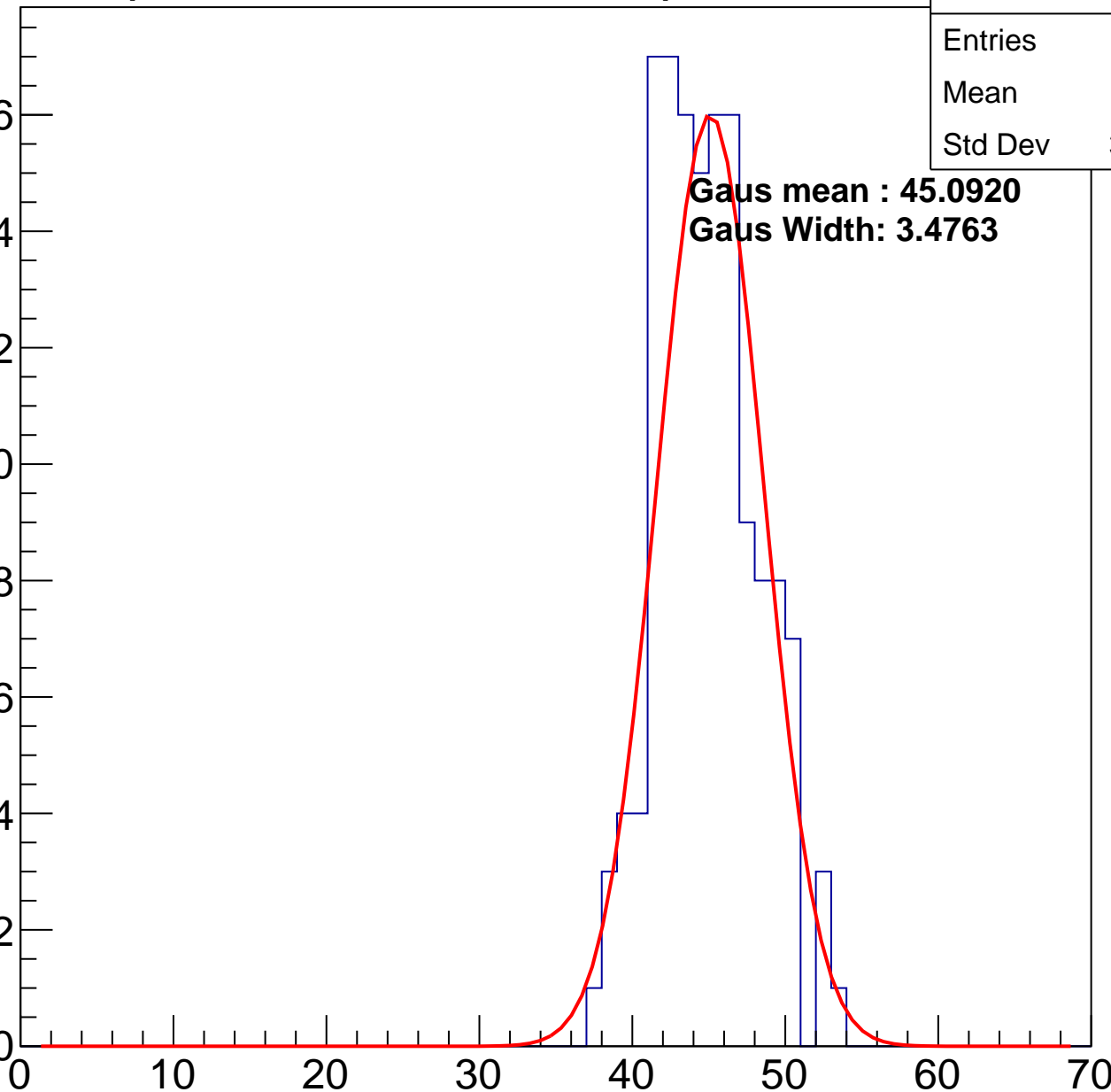
16
14
12
10
8
6
4
2
0

Entries	145
Mean	44.41
Std Dev	3.267

Gaus mean : 45.0920

Gaus Width: 3.4763

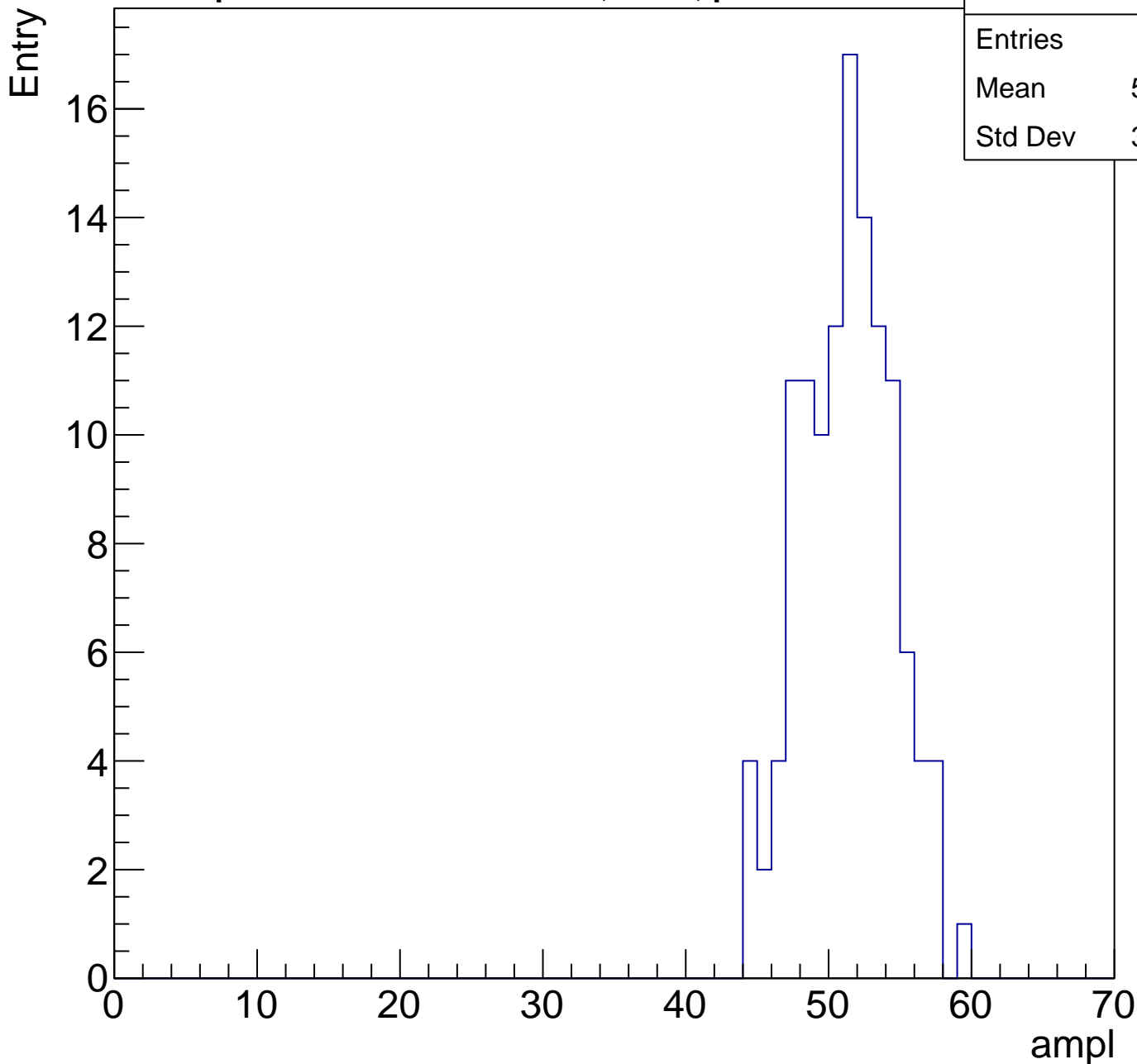
ampl



B1L001S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	50.82
Std Dev	3.196

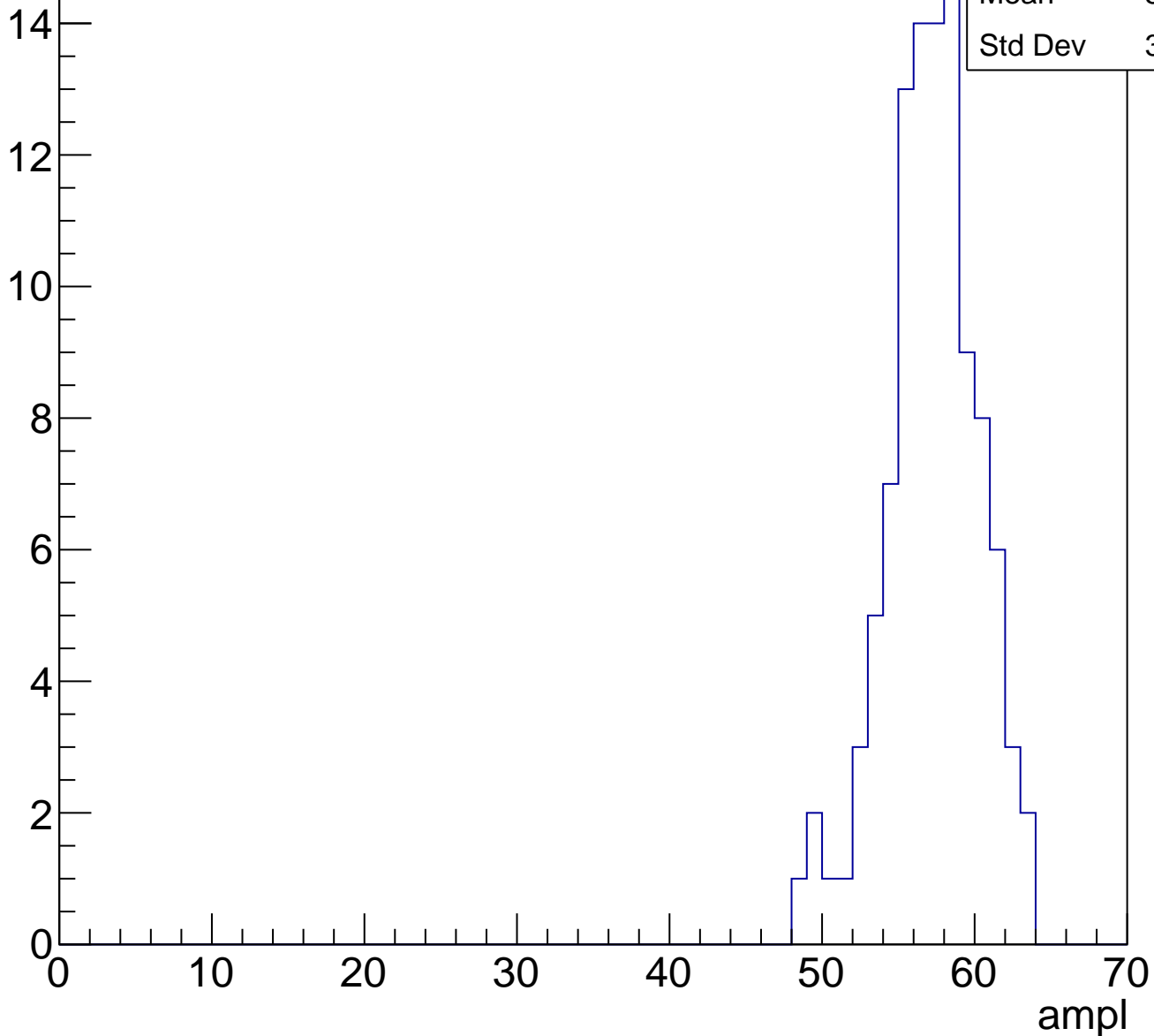


B1L001S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	56.75
Std Dev	3.002

Entry



B1L001S, U14-ch3, adc5

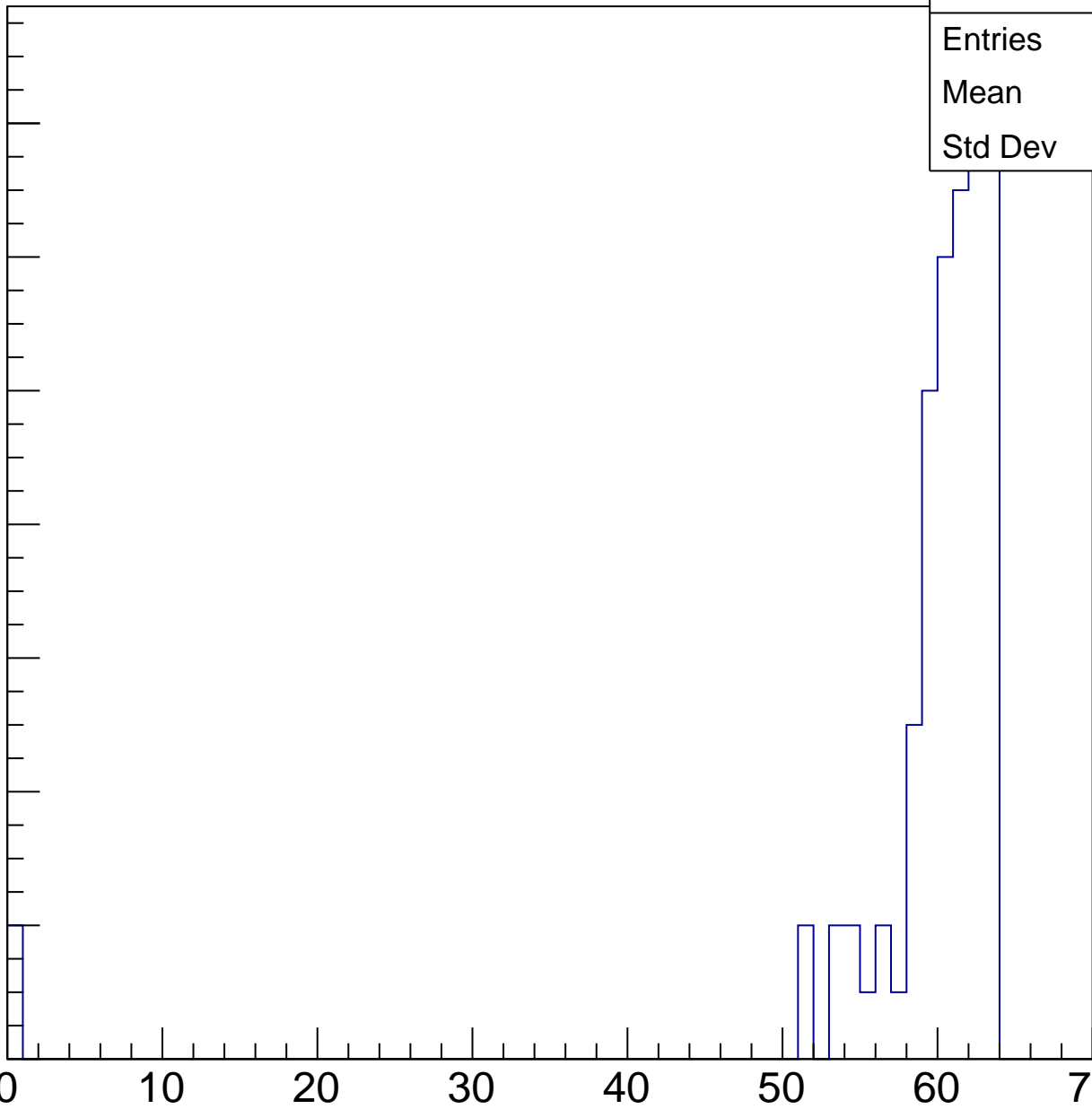
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	58.63
Std Dev	9.68

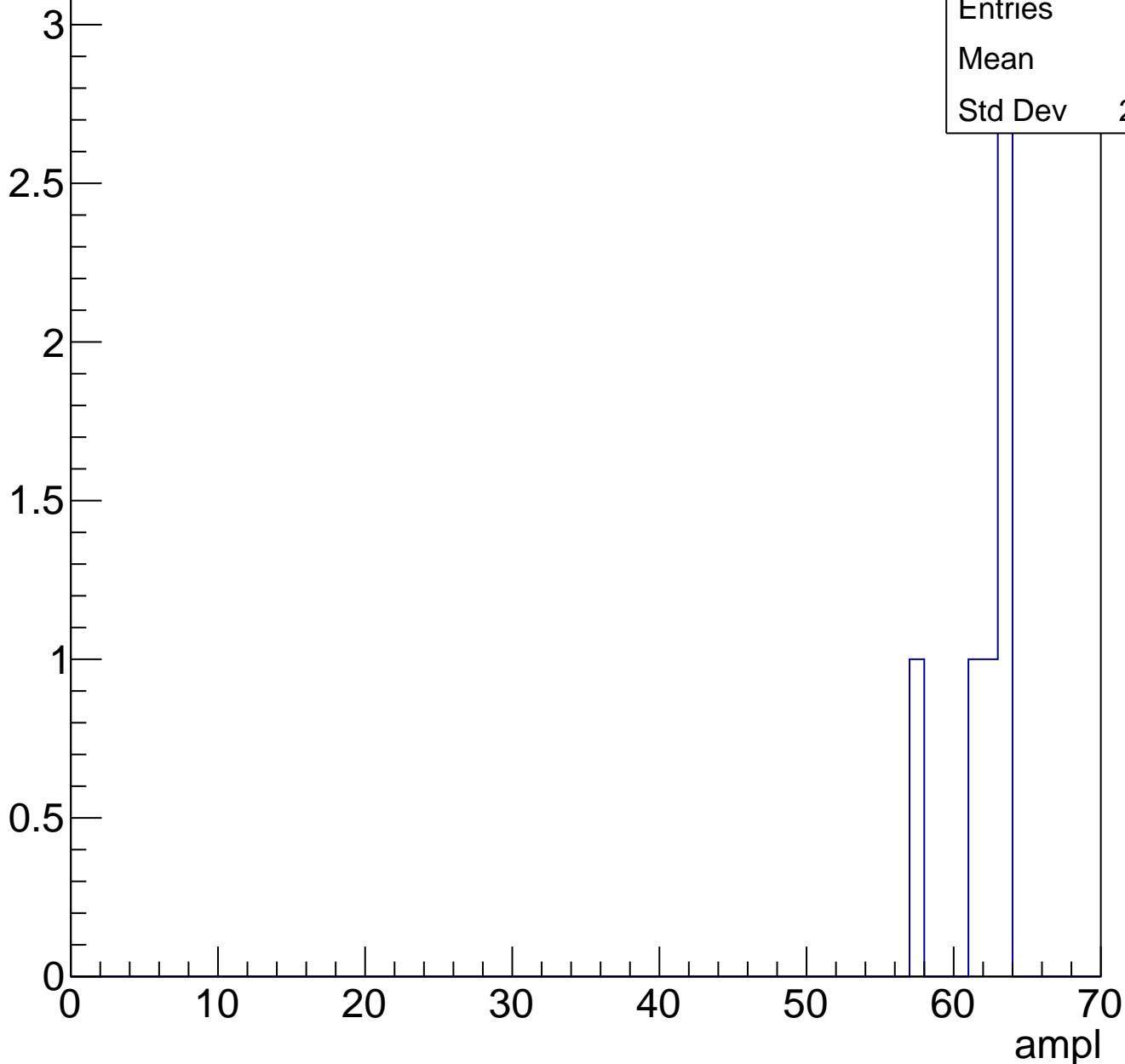
ampl



B1L001S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#2, port C2

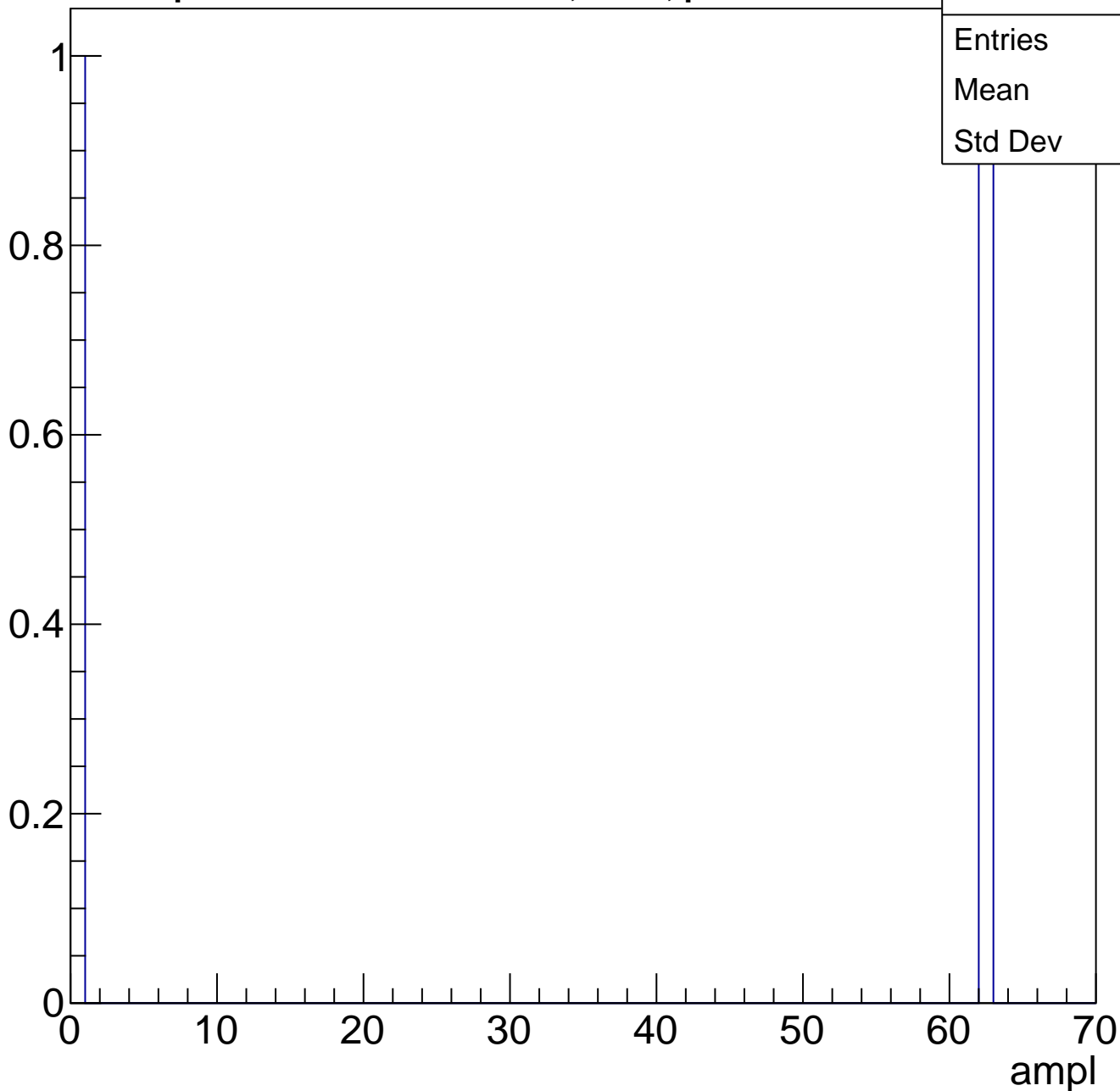
Entry



B1L001S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch4, adc0

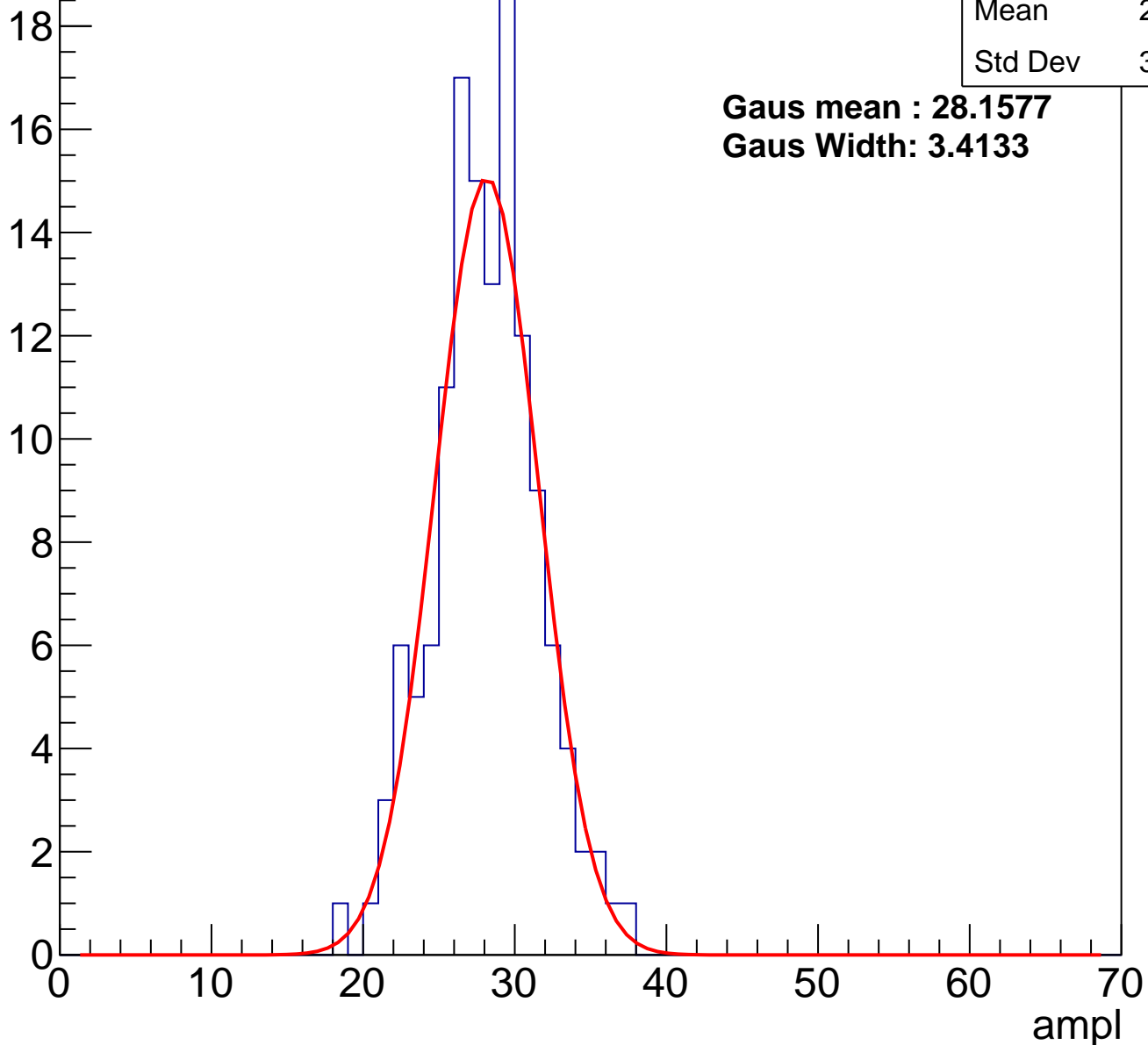
calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	27.63
Std Dev	3.424

Gaus mean : 28.1577

Gaus Width: 3.4133

Entry



B1L001S, U14-ch4, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	33.88
Std Dev	3.174

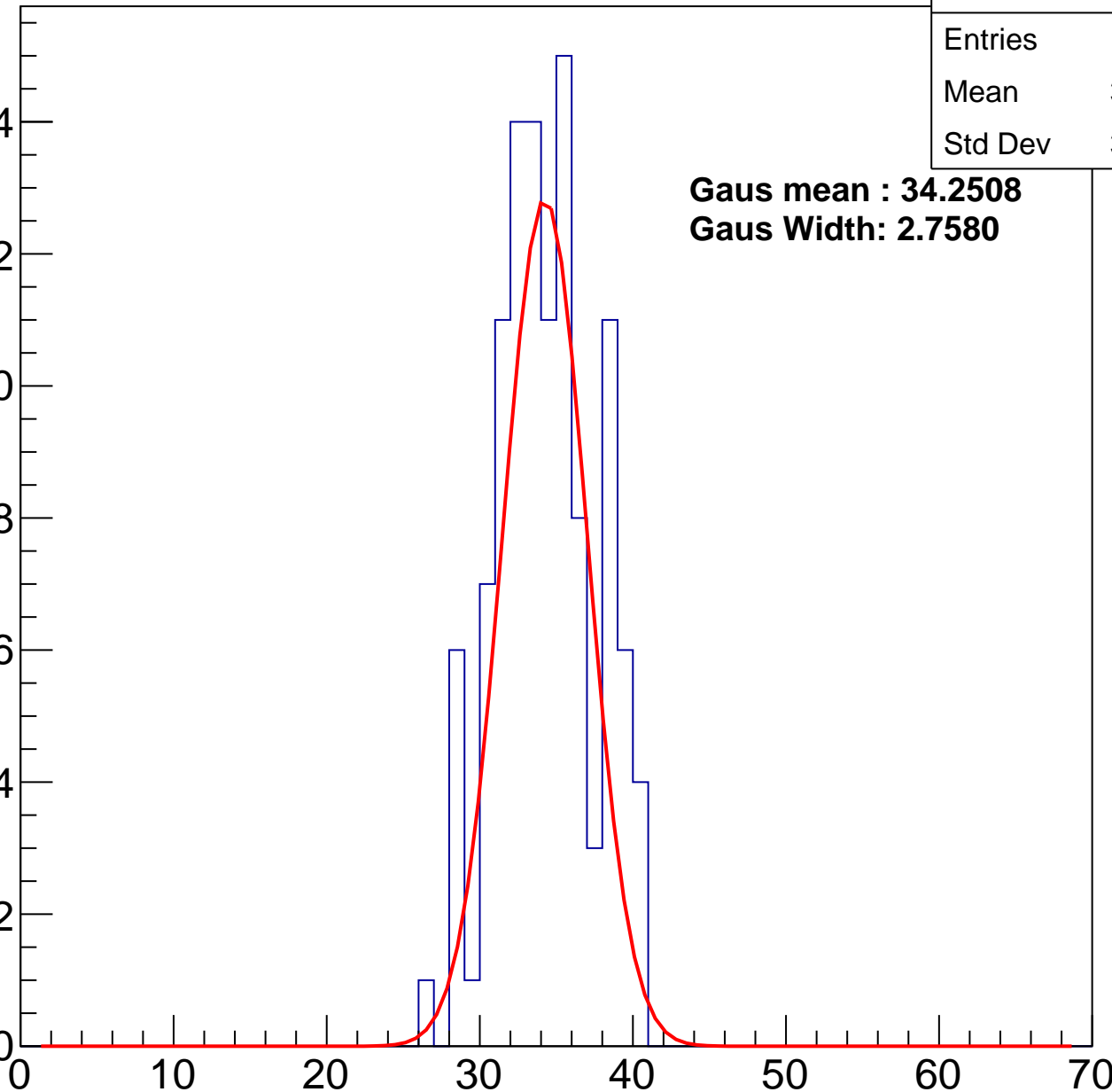
Gaus mean : 34.2508

Gaus Width: 2.7580

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch4, adc2

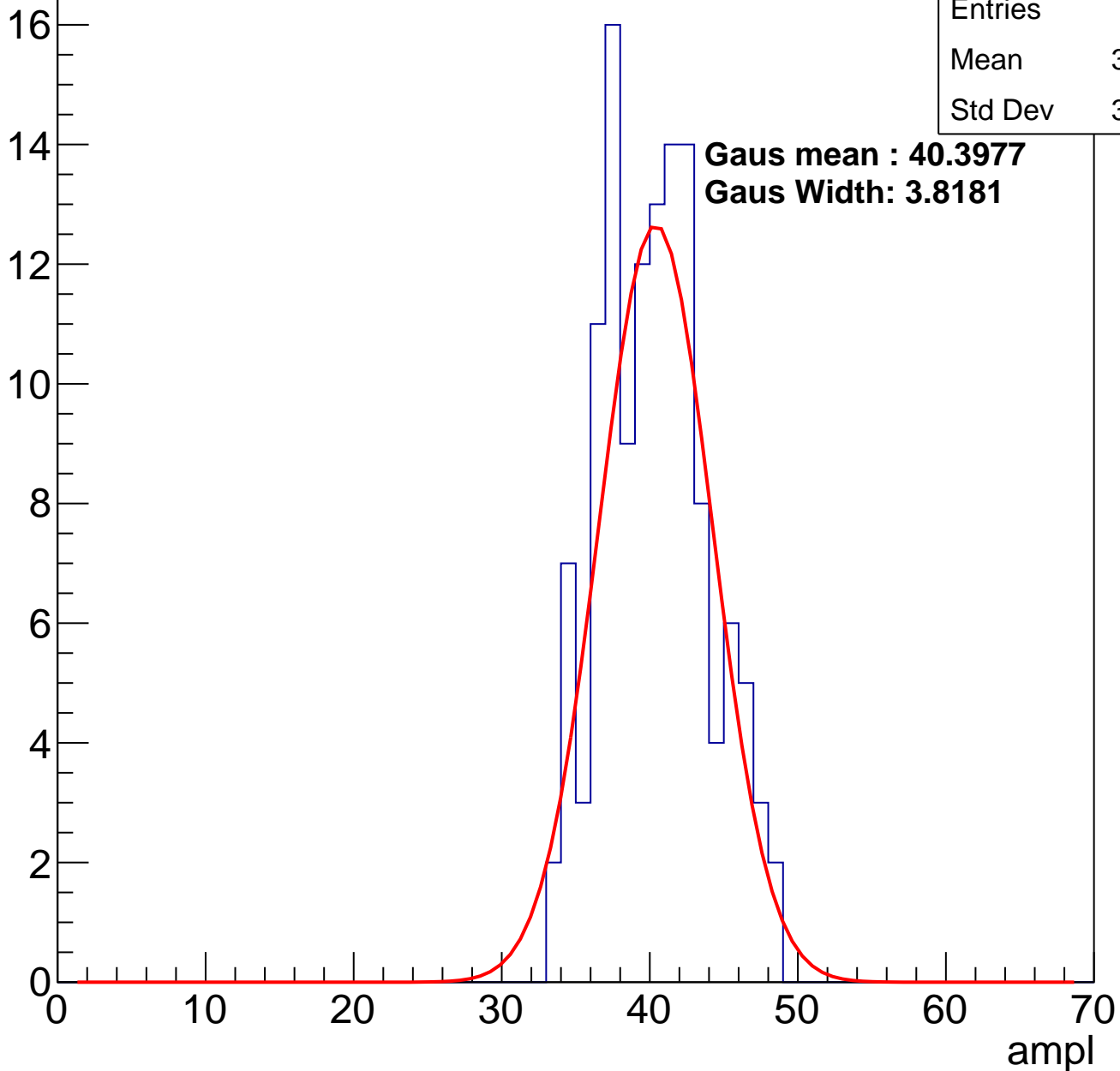
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	39.89
Std Dev	3.525

Gaus mean : 40.3977

Gaus Width: 3.8181

Entry



B1L001S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#2, port C2

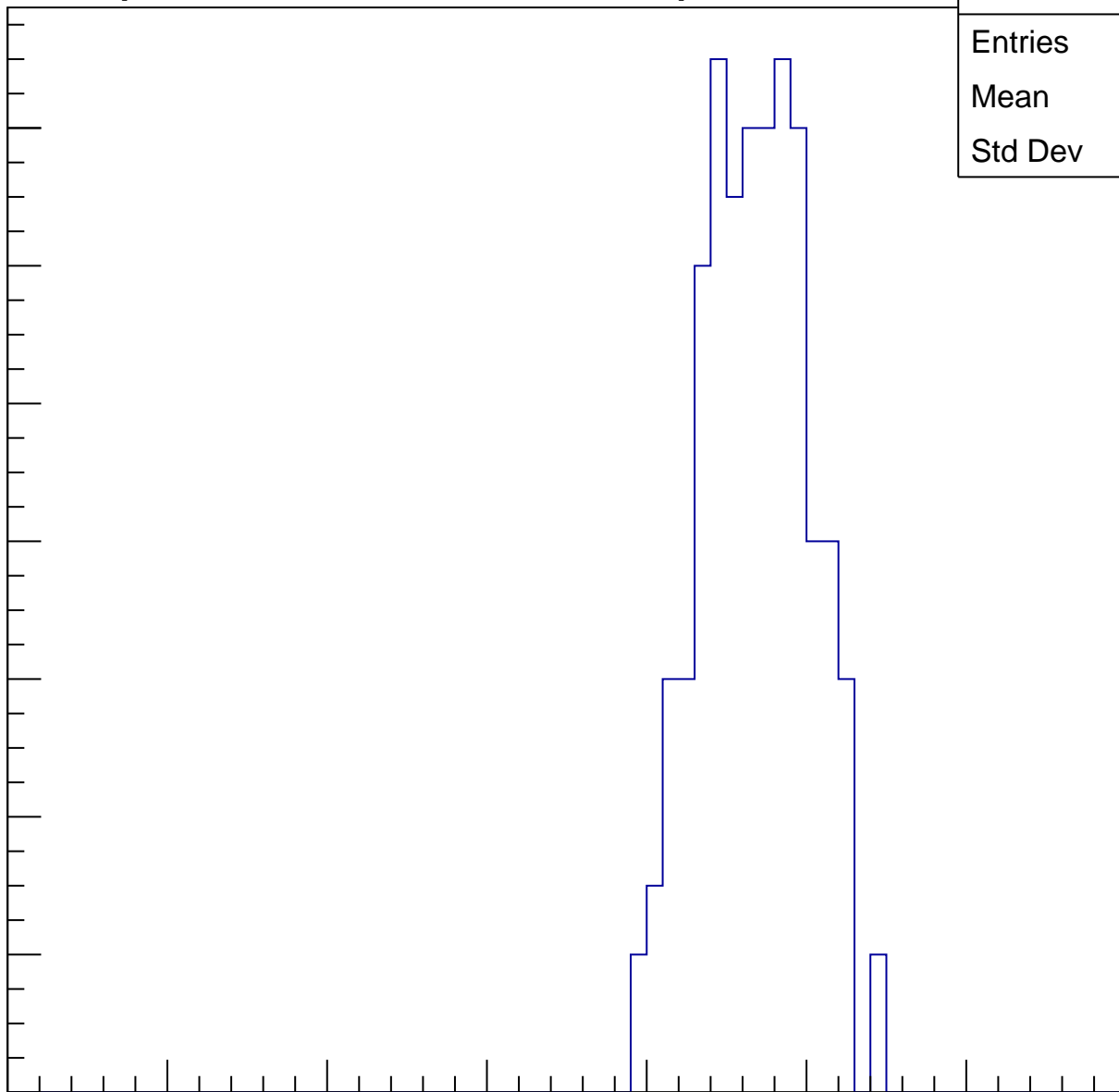
Entries	138
Mean	46.33
Std Dev	3.286

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch4, adc4

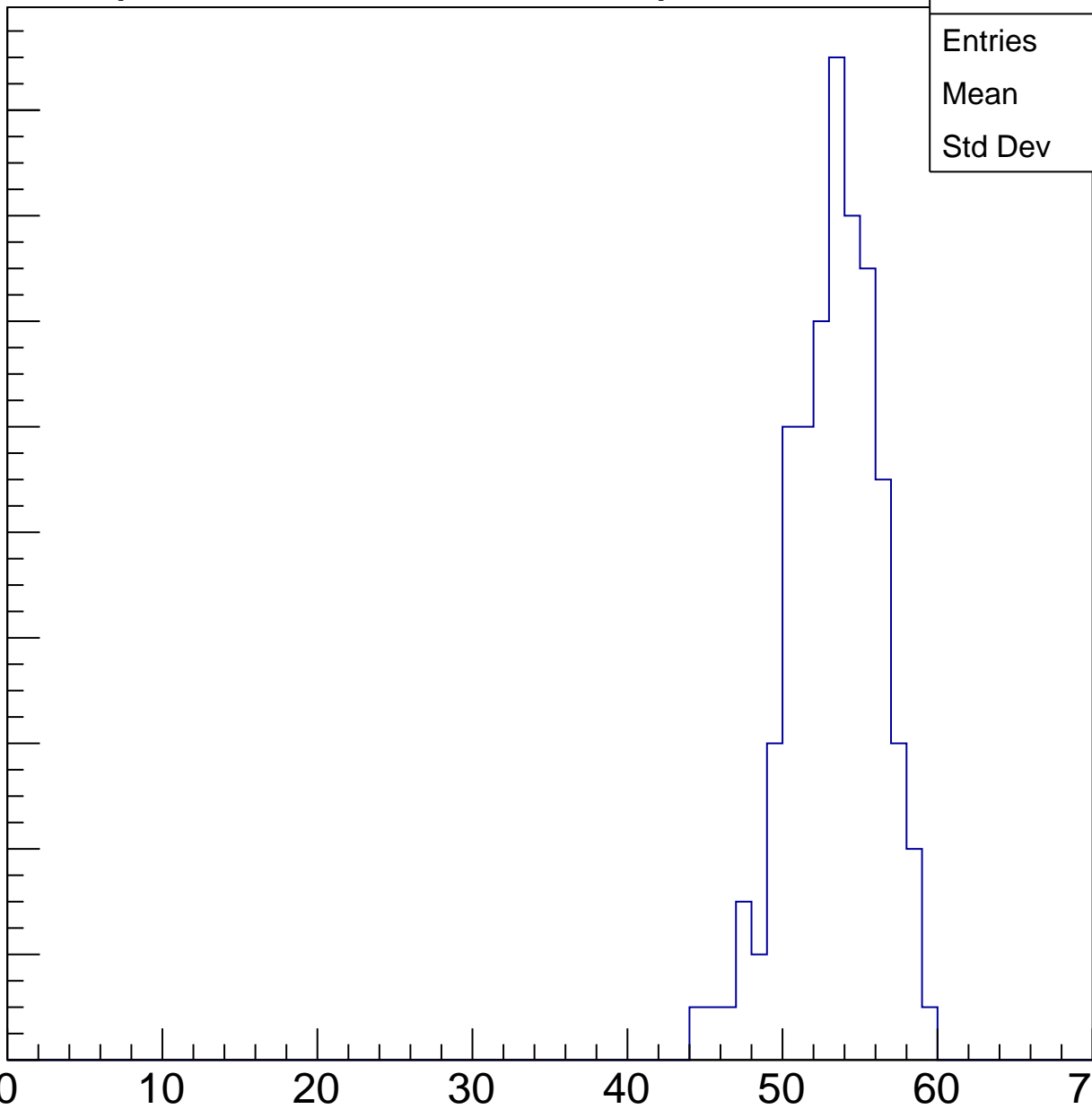
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	124
Mean	52.83
Std Dev	2.87

ampl



B1L001S, U14-ch4, adc5

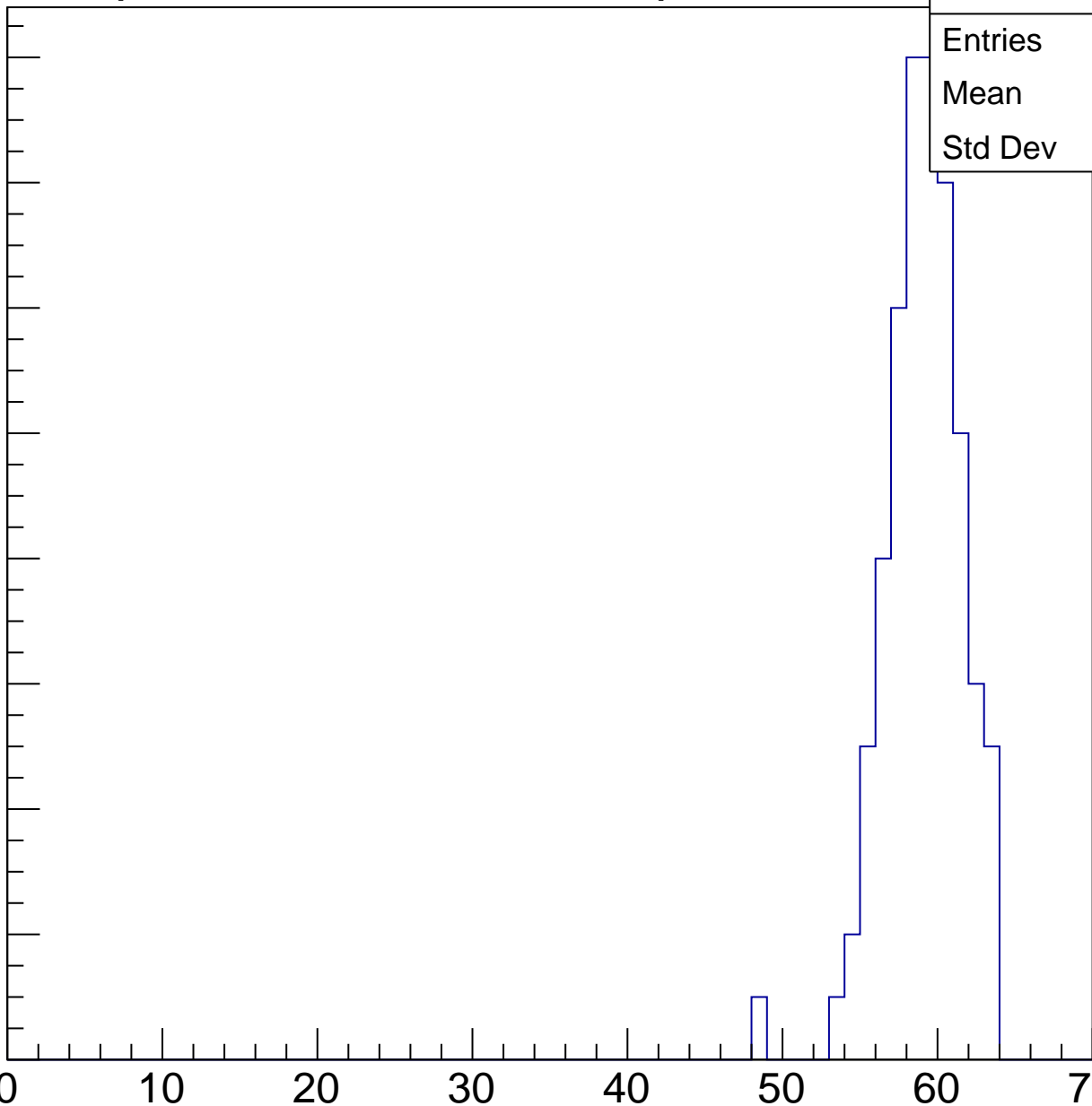
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	58.59
Std Dev	2.49

ampl



B1L001S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

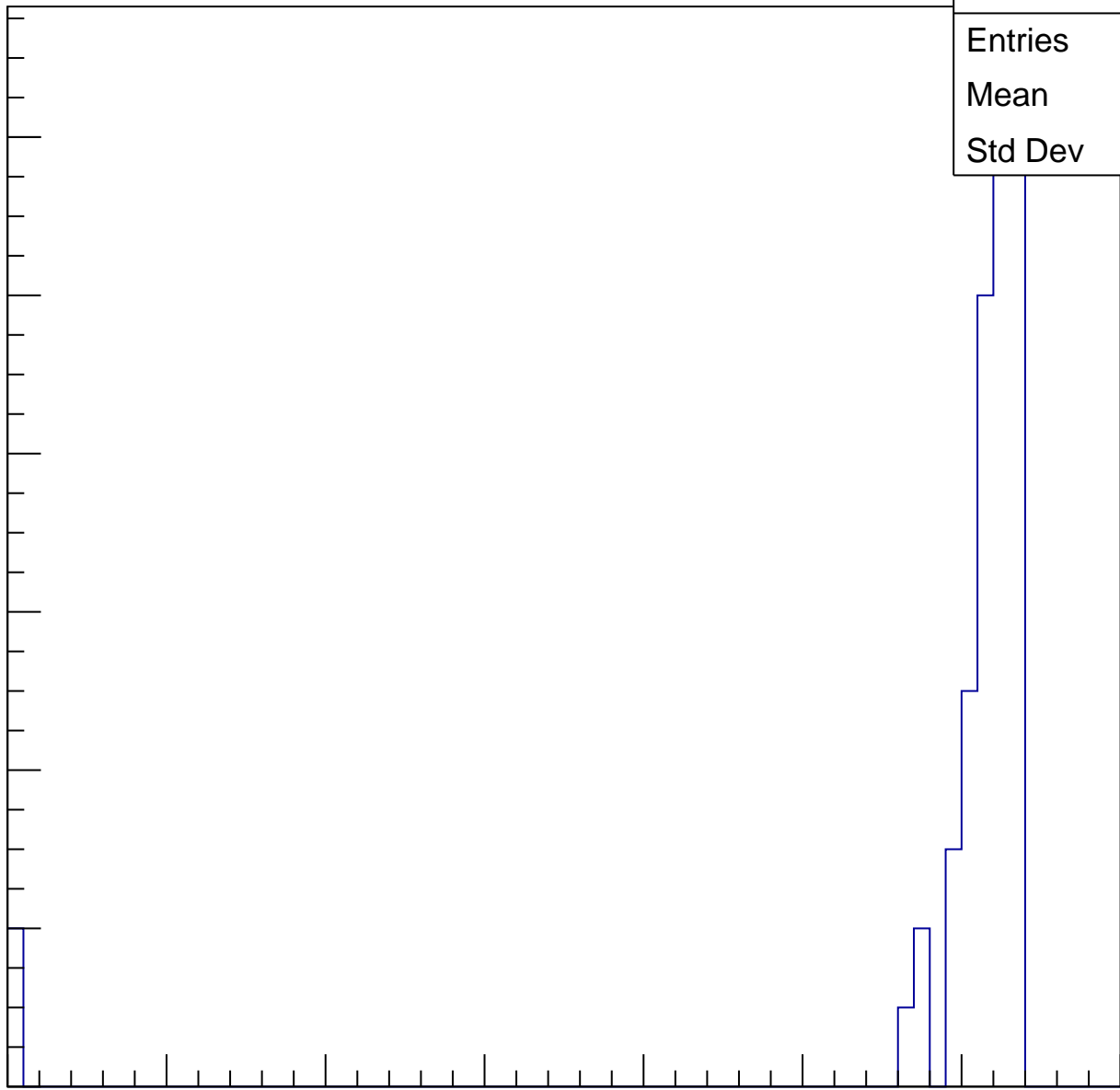
2

0

Entries	49
Mean	58.82
Std Dev	12.25

ampl

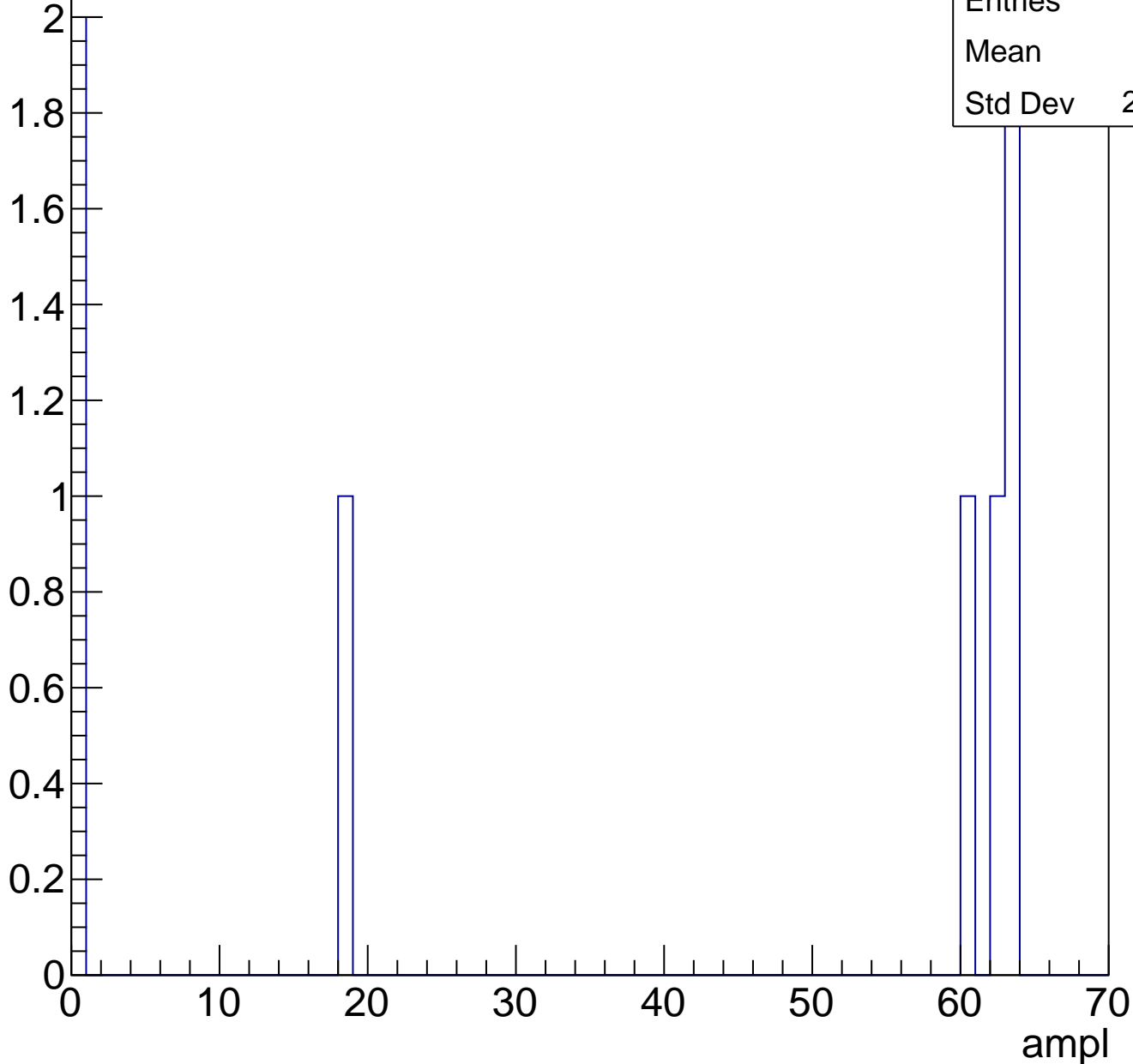
0 10 20 30 40 50 60 70



B1L001S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch5, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	29.74
Std Dev	3.17

Gaus mean : 30.2554

Gaus Width: 3.2777

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

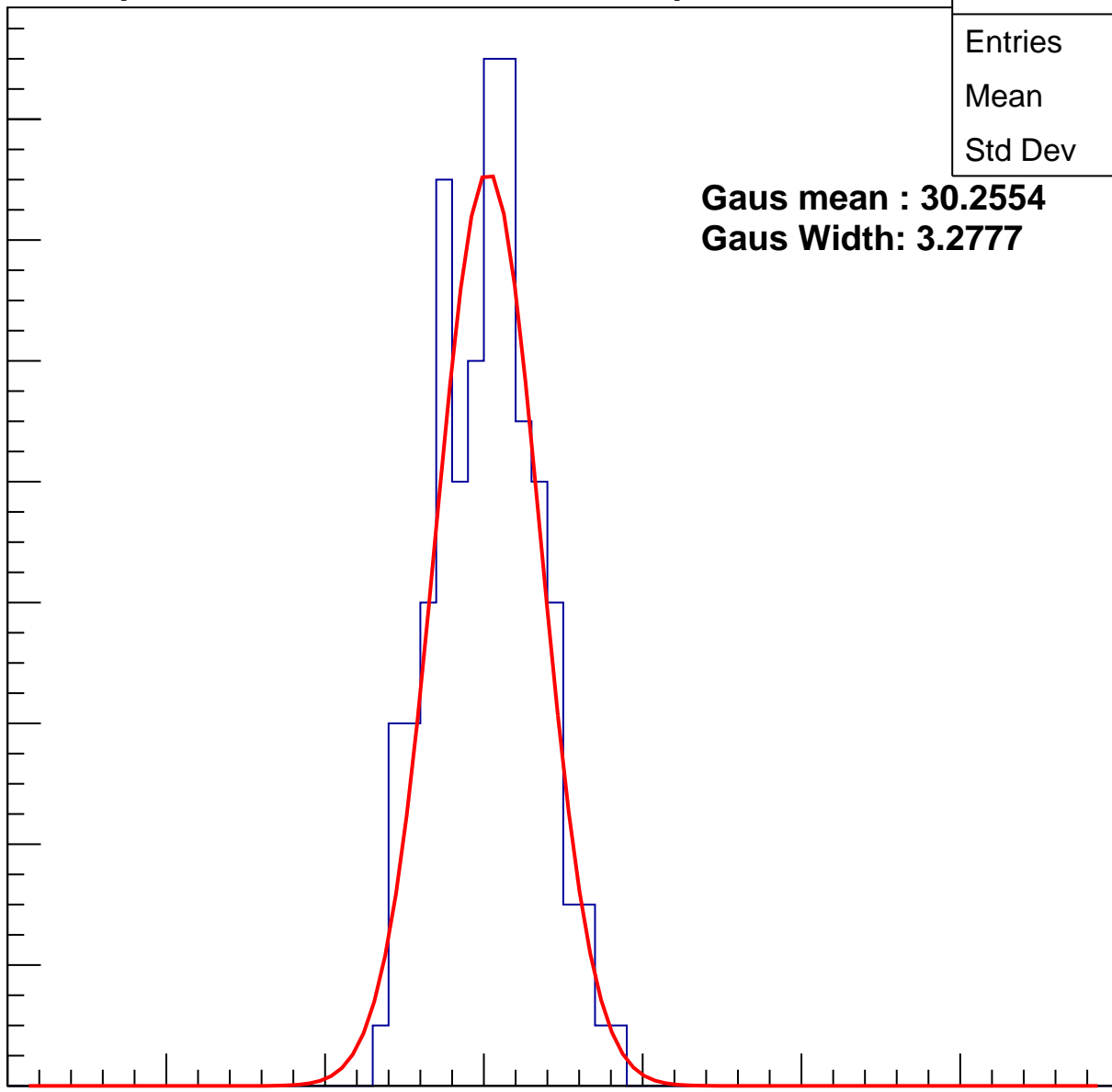
40

50

60

70

ampl



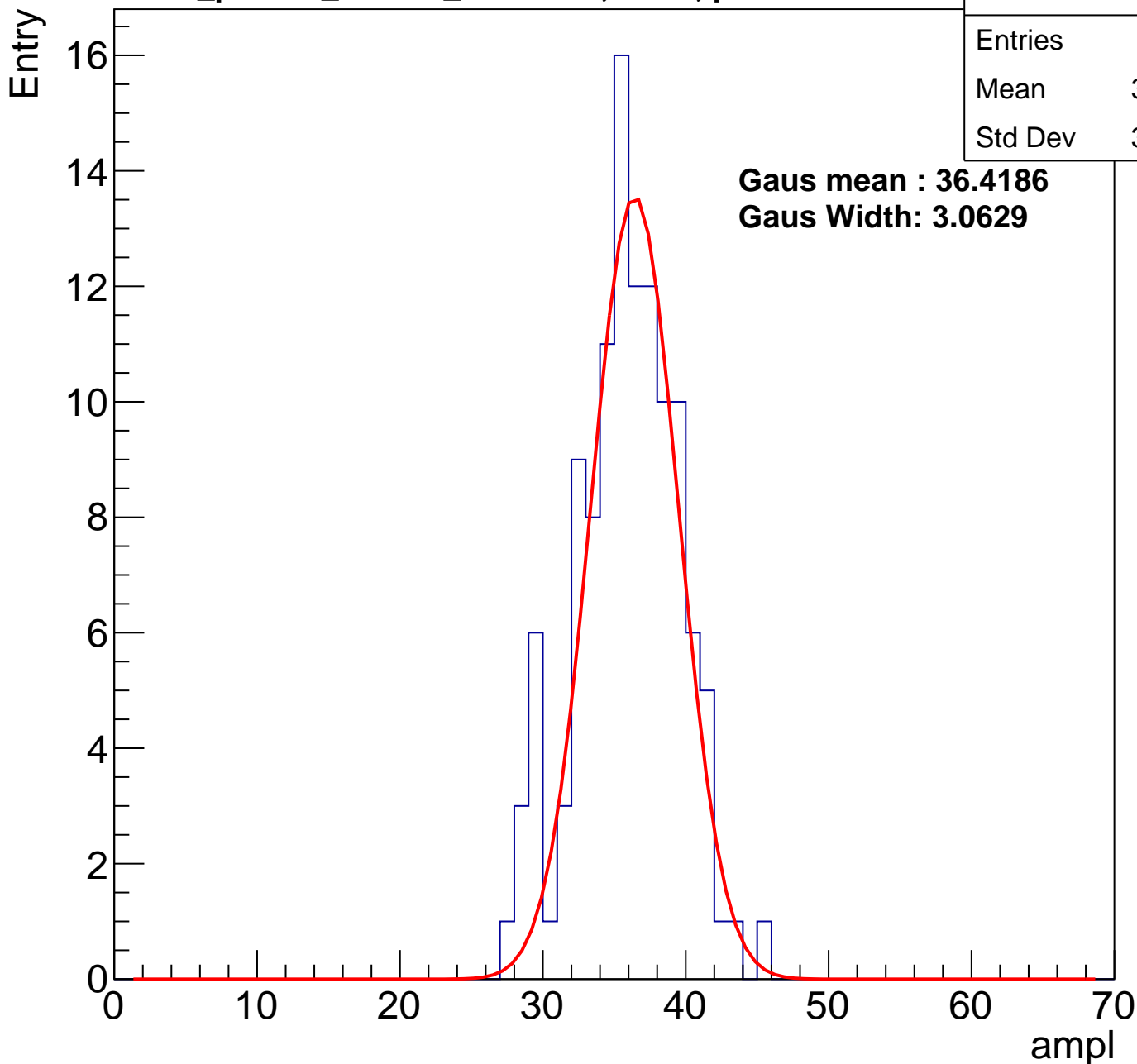
B1L001S, U14-ch5, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	35.47
Std Dev	3.537

Gaus mean : 36.4186

Gaus Width: 3.0629



B1L001S, U14-ch5, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	42.1
Std Dev	3.51

Gaus mean : 42.7709

Gaus Width: 3.5088

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

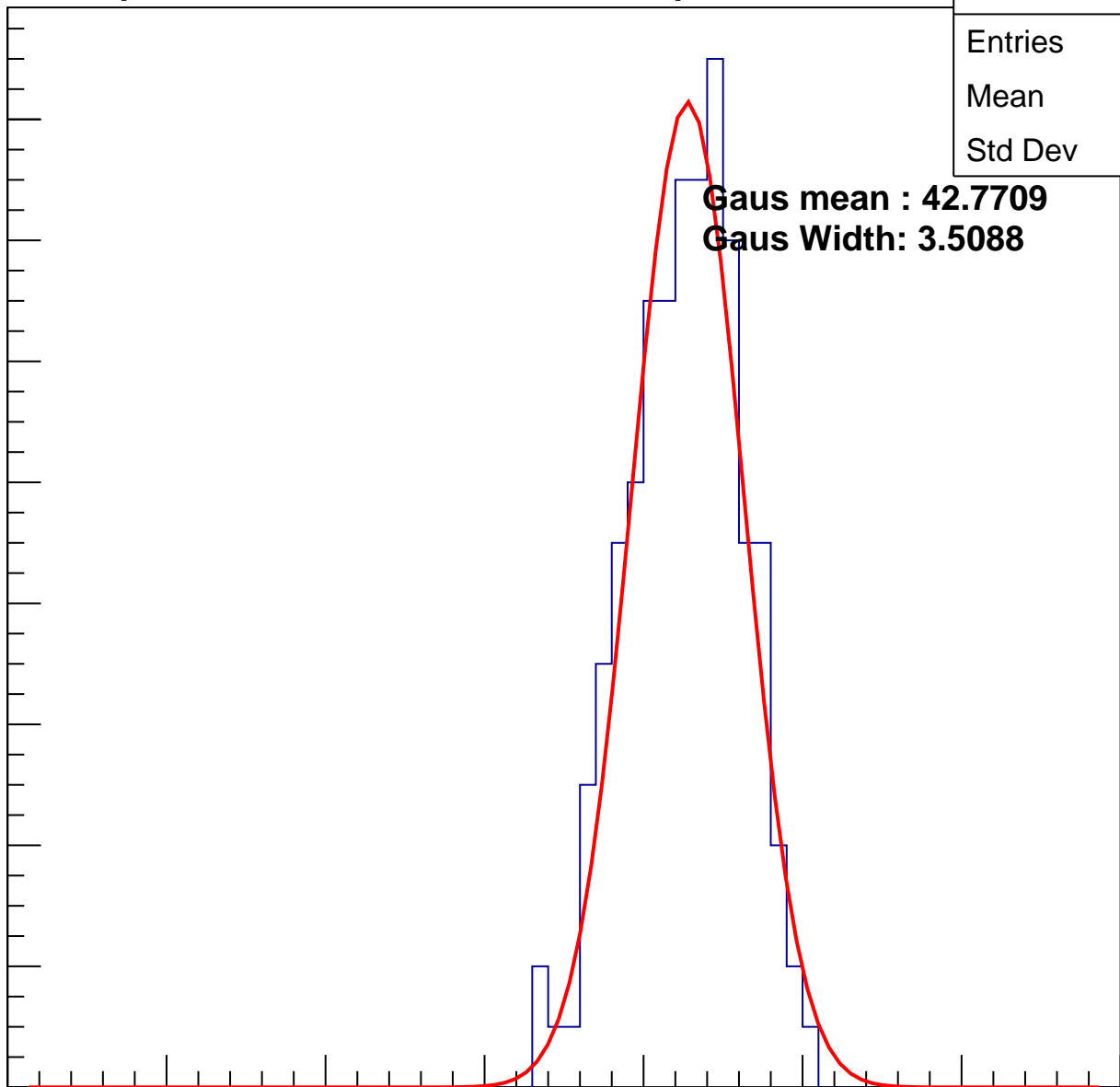
40

50

60

70

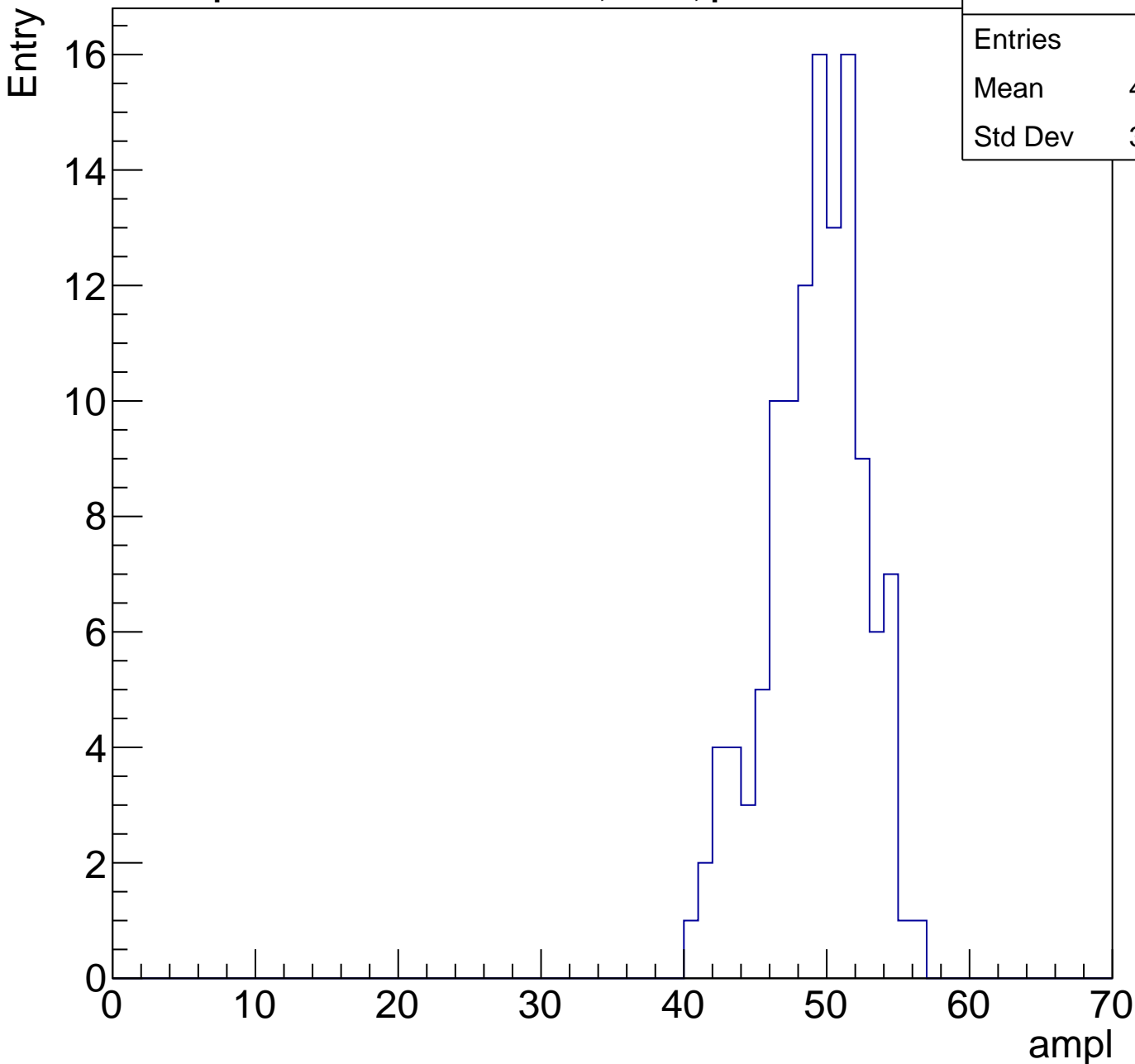
ampl



B1L001S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#2, port C2

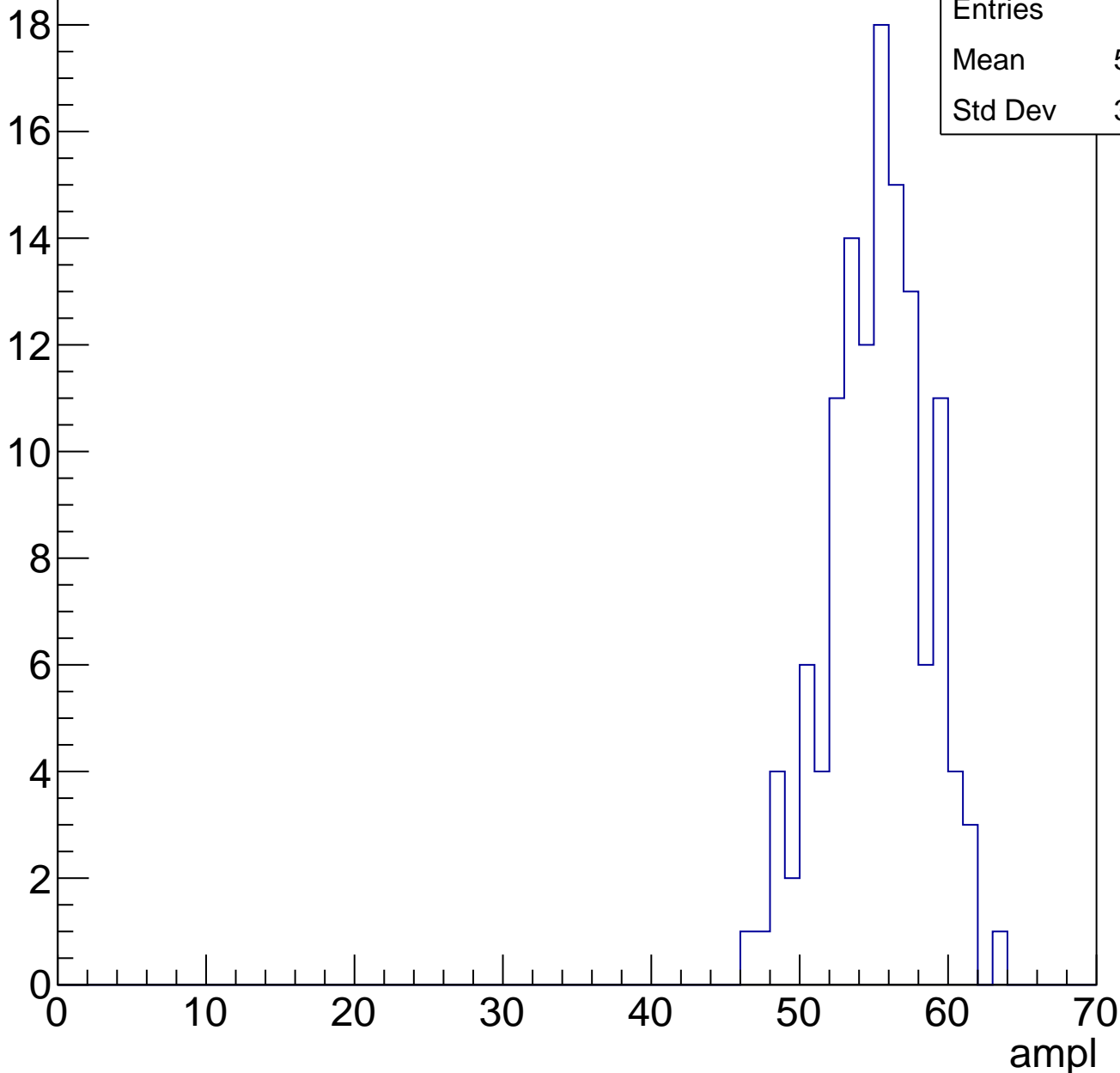
Entries	120
Mean	48.75
Std Dev	3.357



B1L001S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch5, adc5

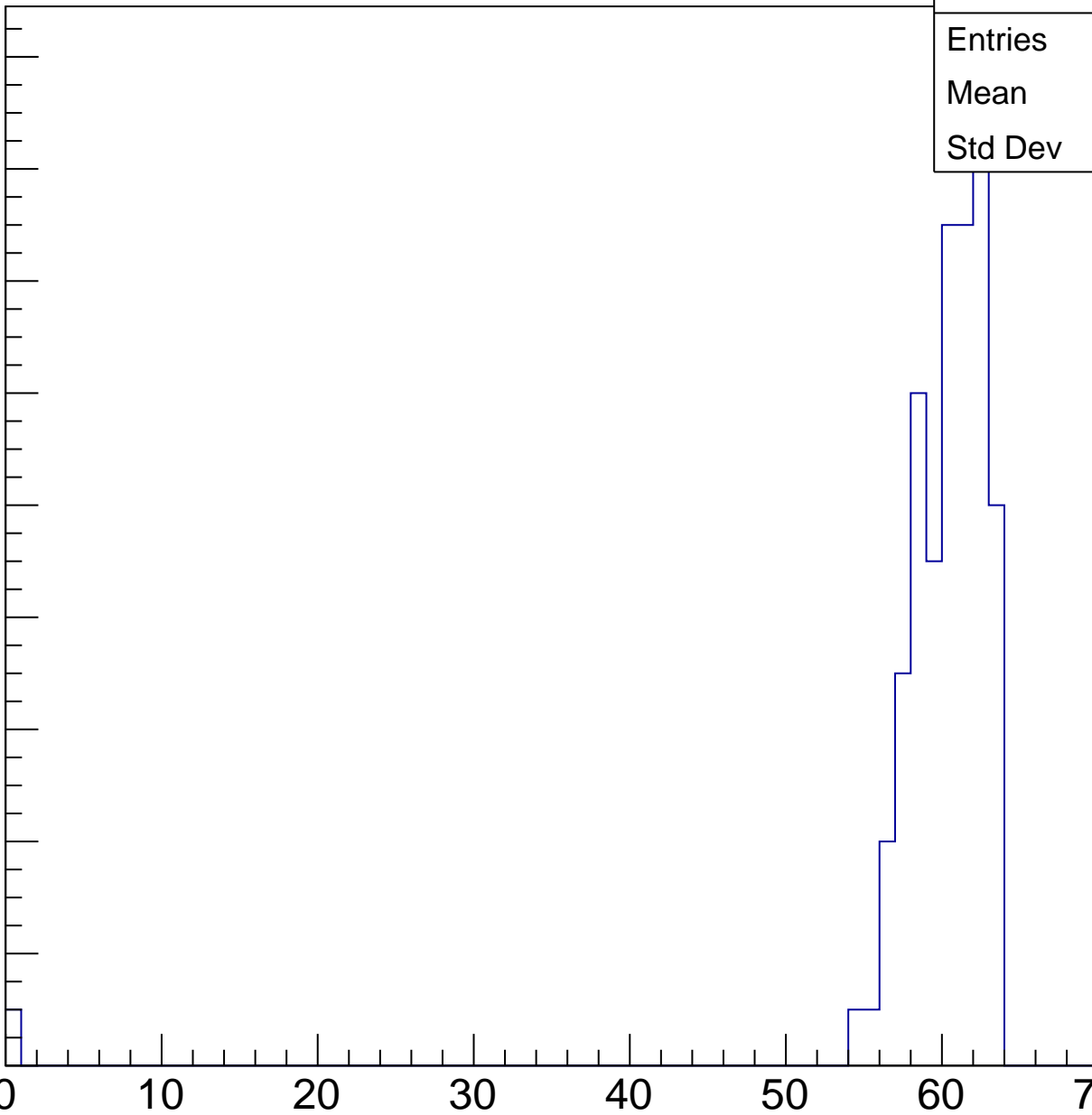
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	93
Mean	59.35
Std Dev	6.545

ampl

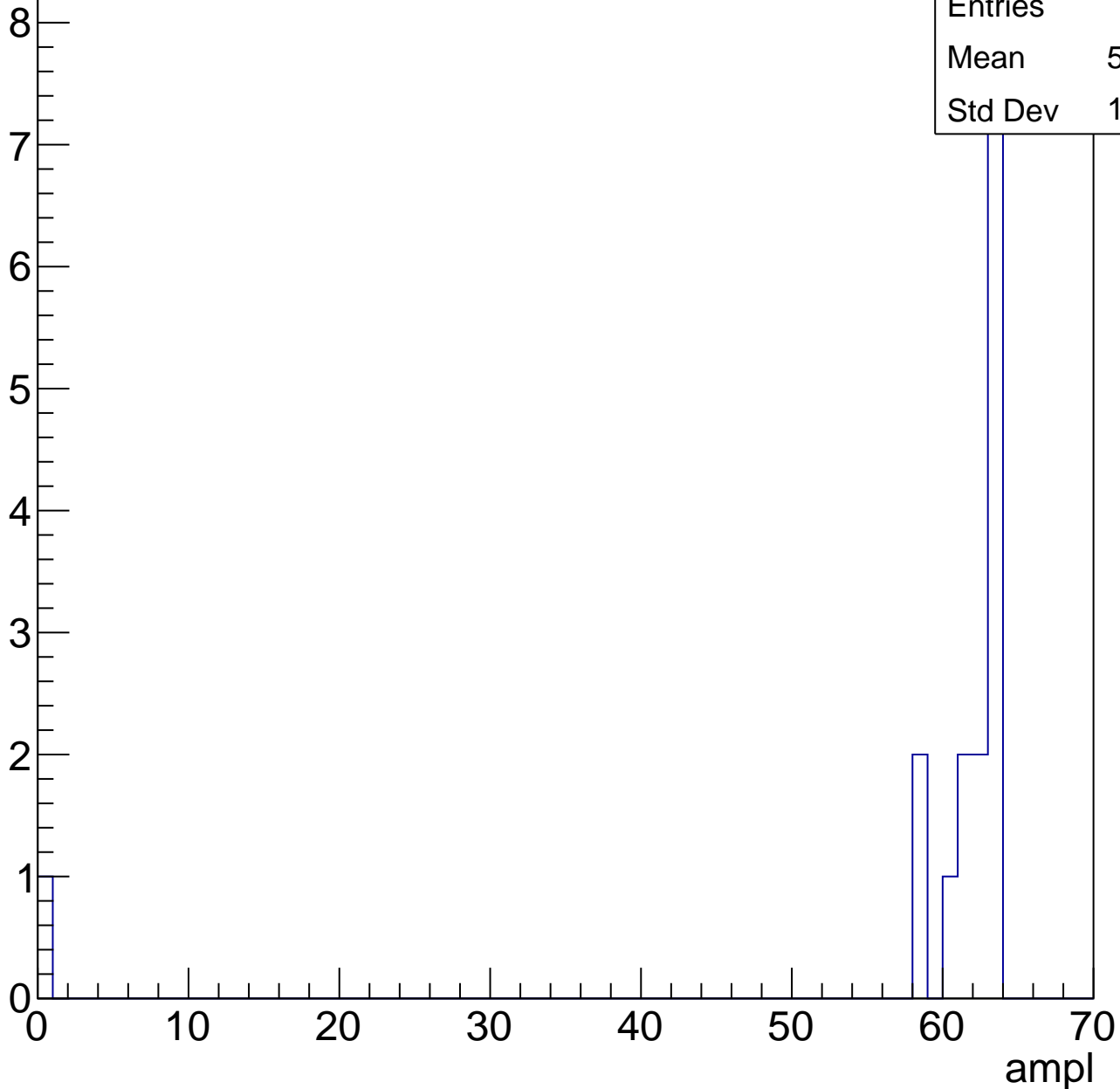


B1L001S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

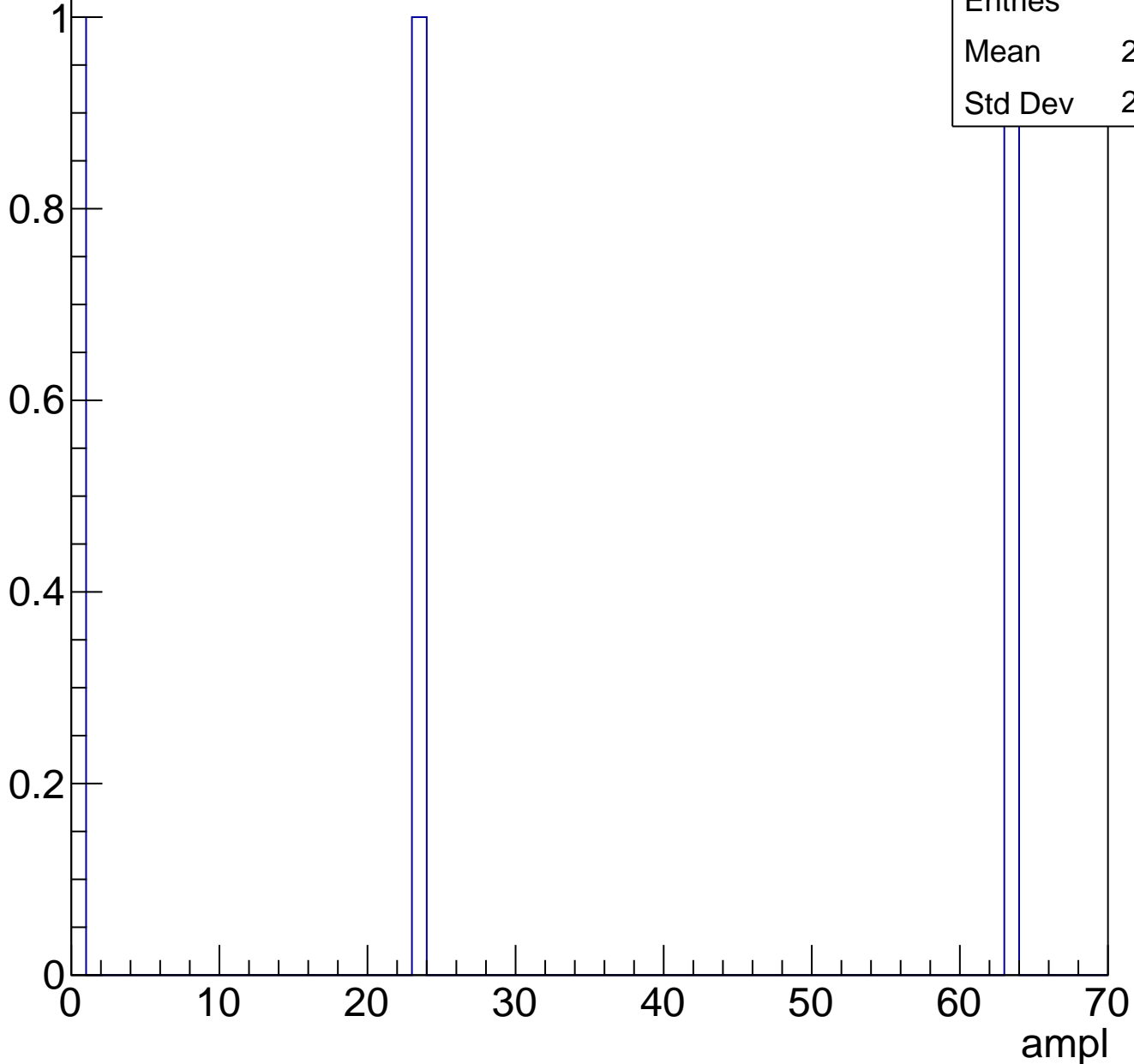
Entries	16
Mean	57.88
Std Dev	15.04



B1L001S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28.67
Std Dev	26.03

B1L001S, U14-ch6, adc0

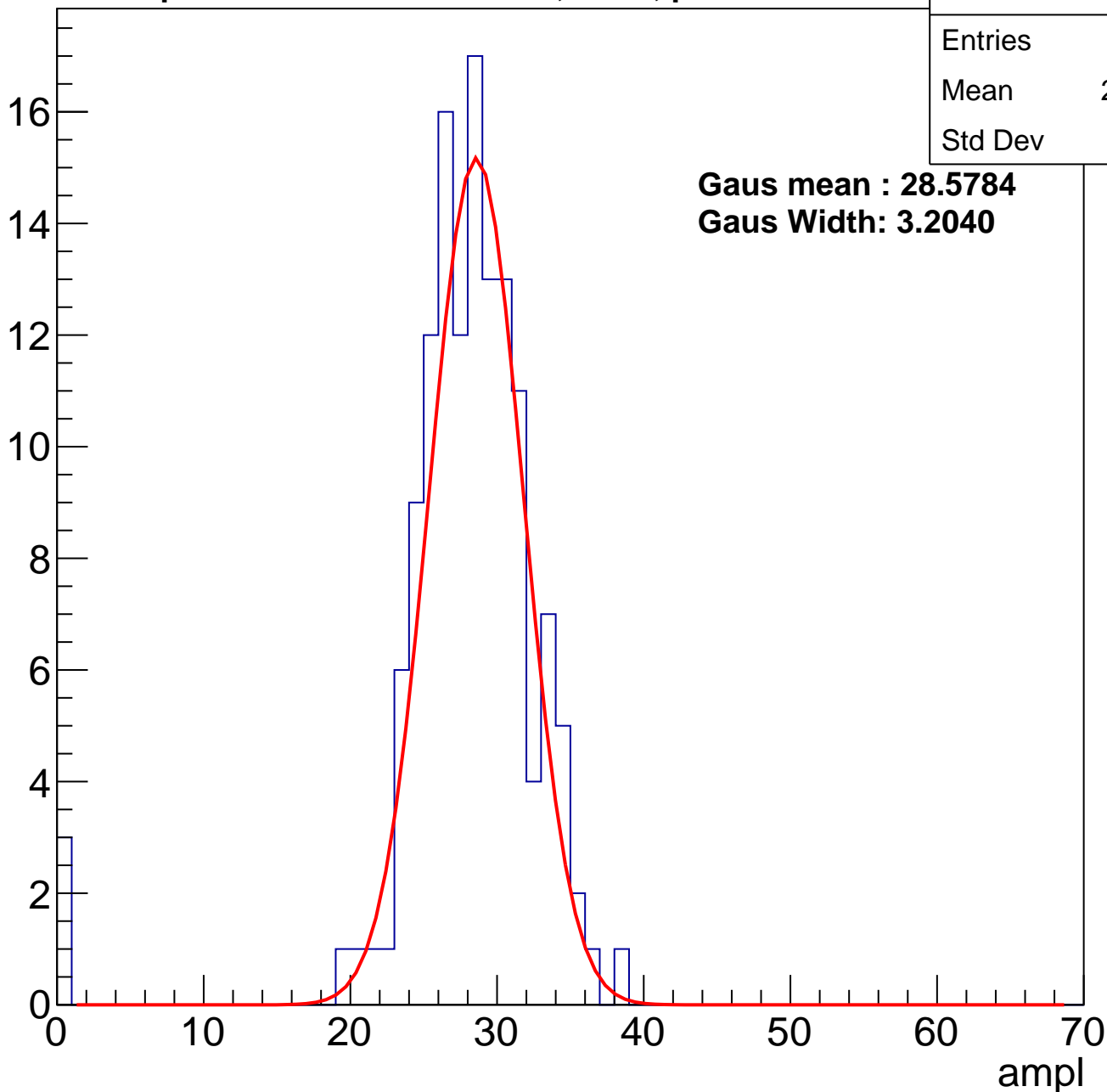
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	27.45
Std Dev	5.33

Gaus mean : 28.5784

Gaus Width: 3.2040

Entry



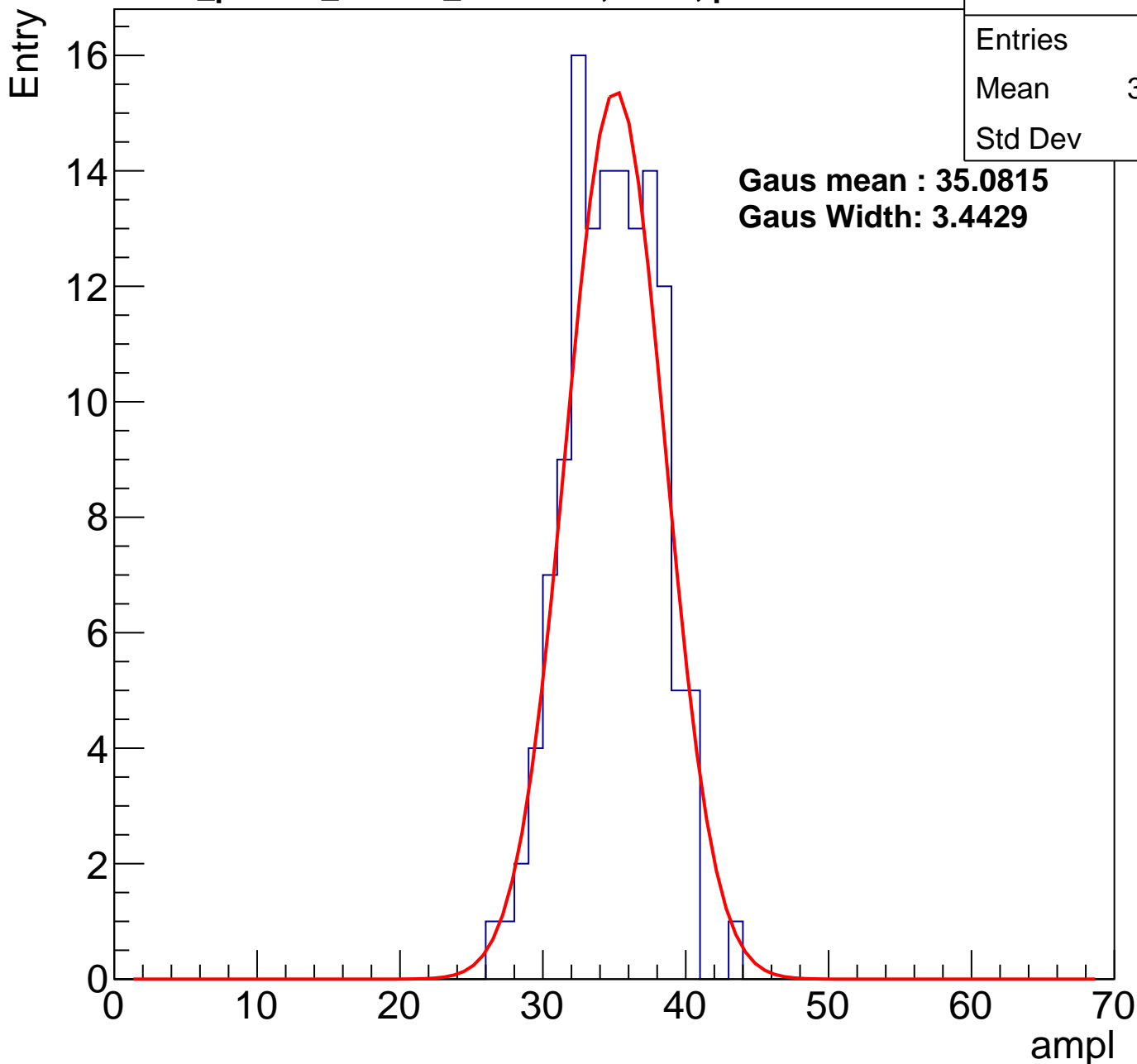
B1L001S, U14-ch6, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	34.36
Std Dev	3.16

Gaus mean : 35.0815

Gaus Width: 3.4429



B1L001S, U14-ch6, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

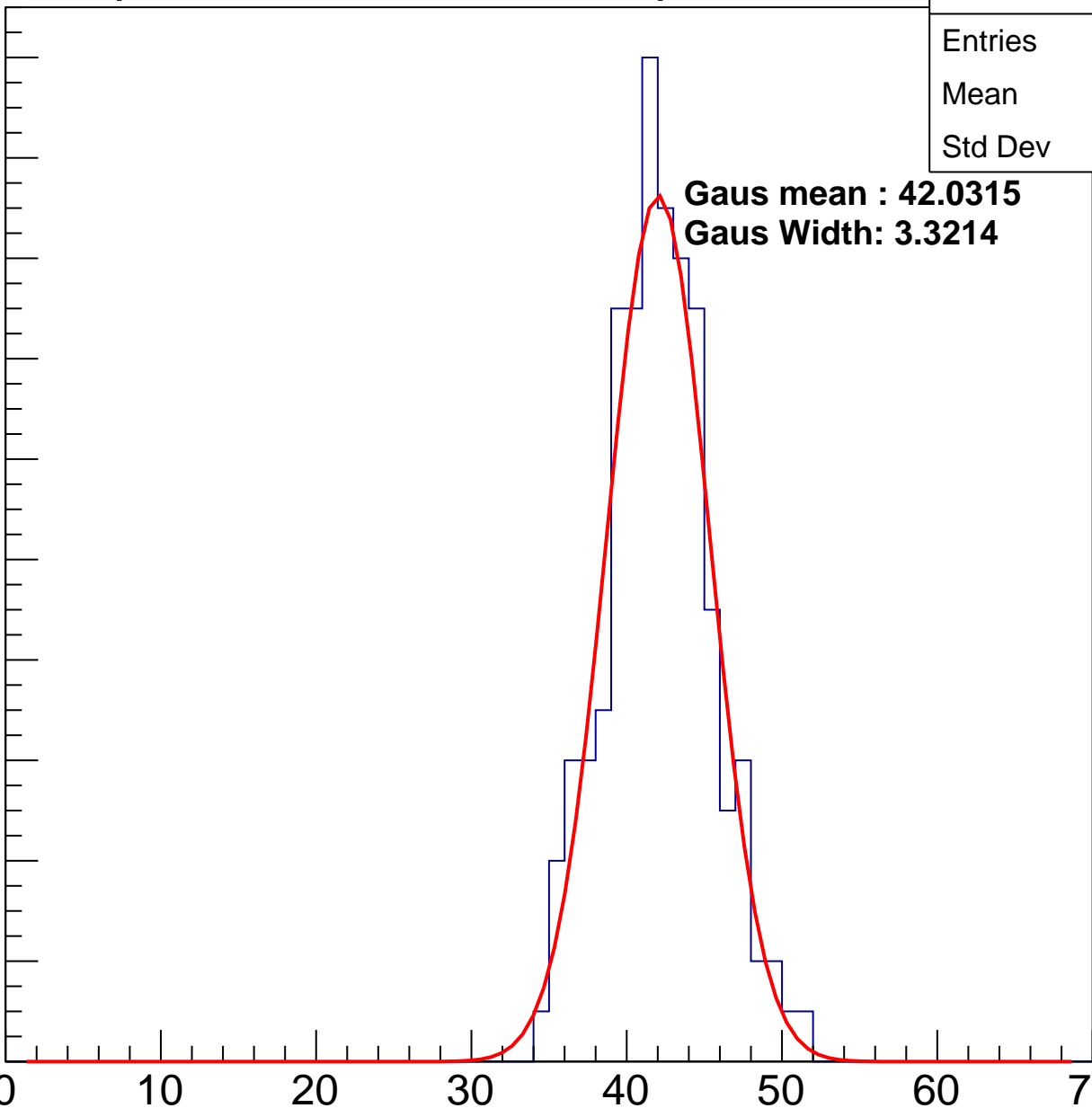
20
18
16
14
12
10
8
6
4
2
0

Entries	148
Mean	41.6
Std Dev	3.32

Gaus mean : 42.0315

Gaus Width: 3.3214

0 10 20 30 40 50 60 70
ampl



B1L001S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	48.29
Std Dev	2.979

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

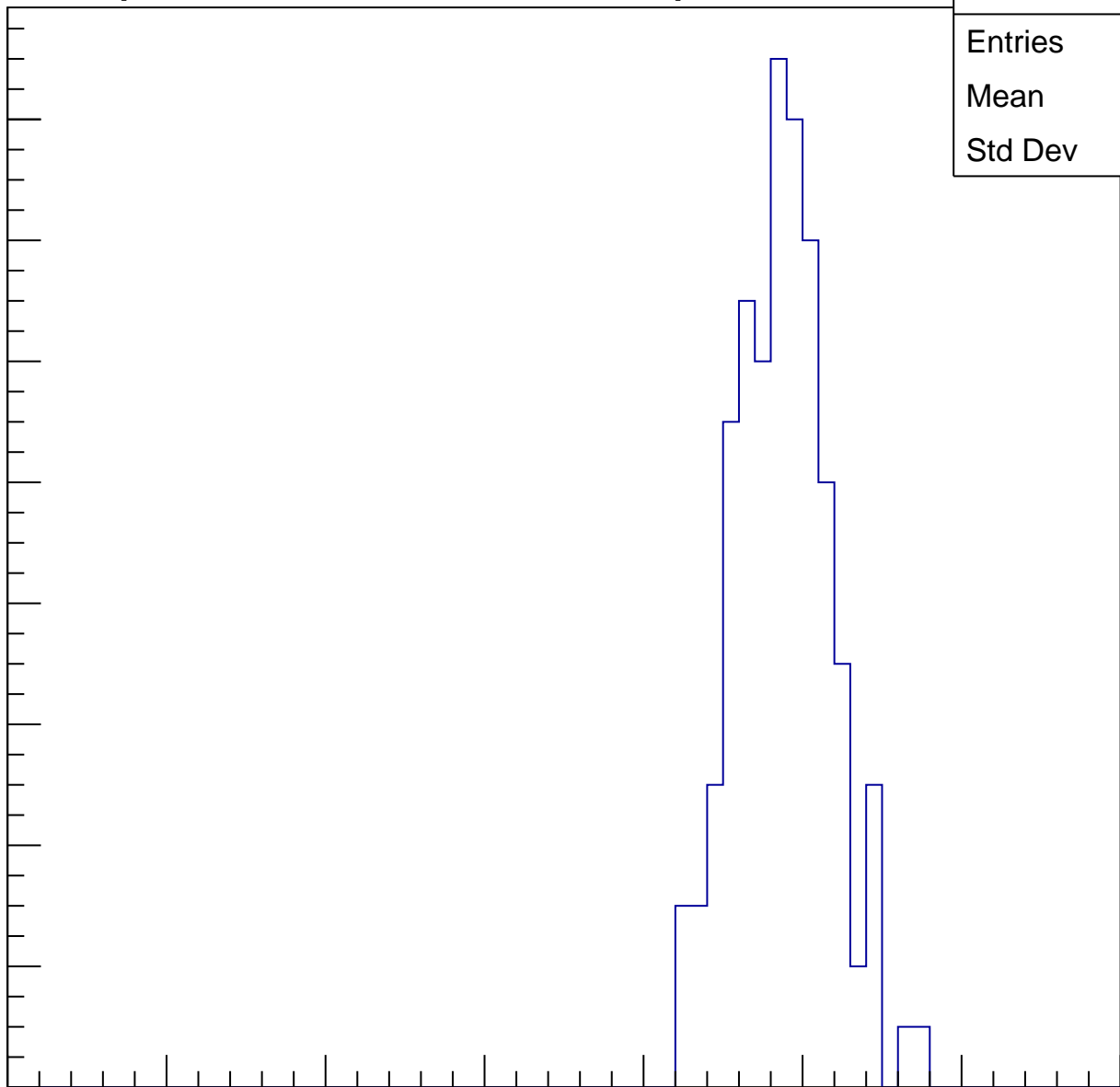
40

50

60

70

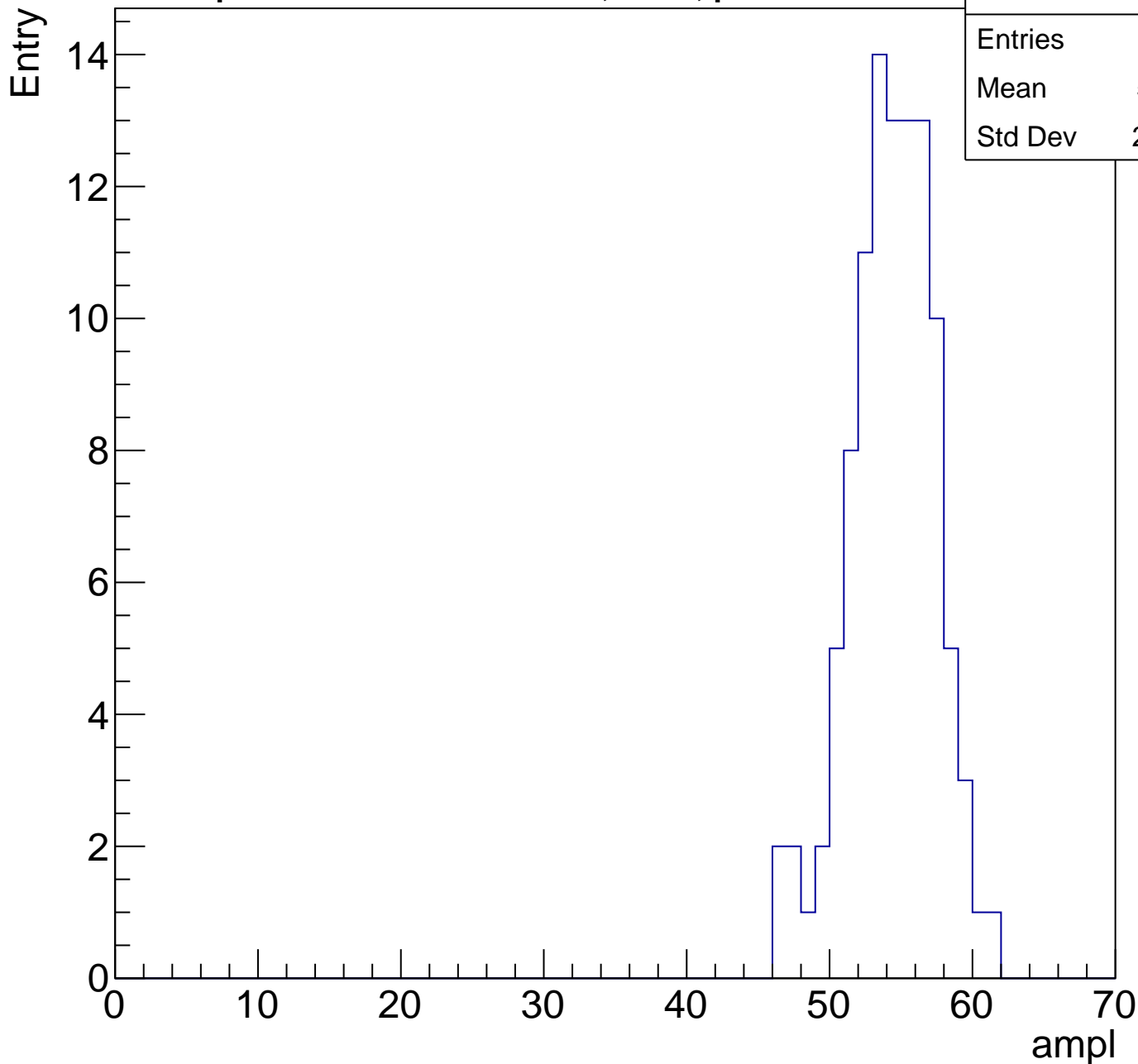
ampl



B1L001S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	53.91
Std Dev	2.968



B1L001S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#2, port C2

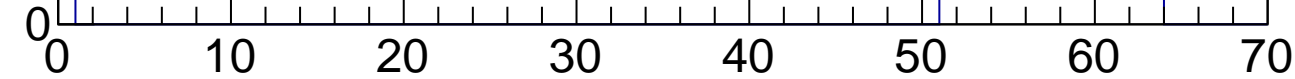
Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.32
Std Dev	6.239

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	32
Mean	59.59
Std Dev	10.82

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	9.5
Std Dev	9.5

B1L001S, U14-ch7, adc0

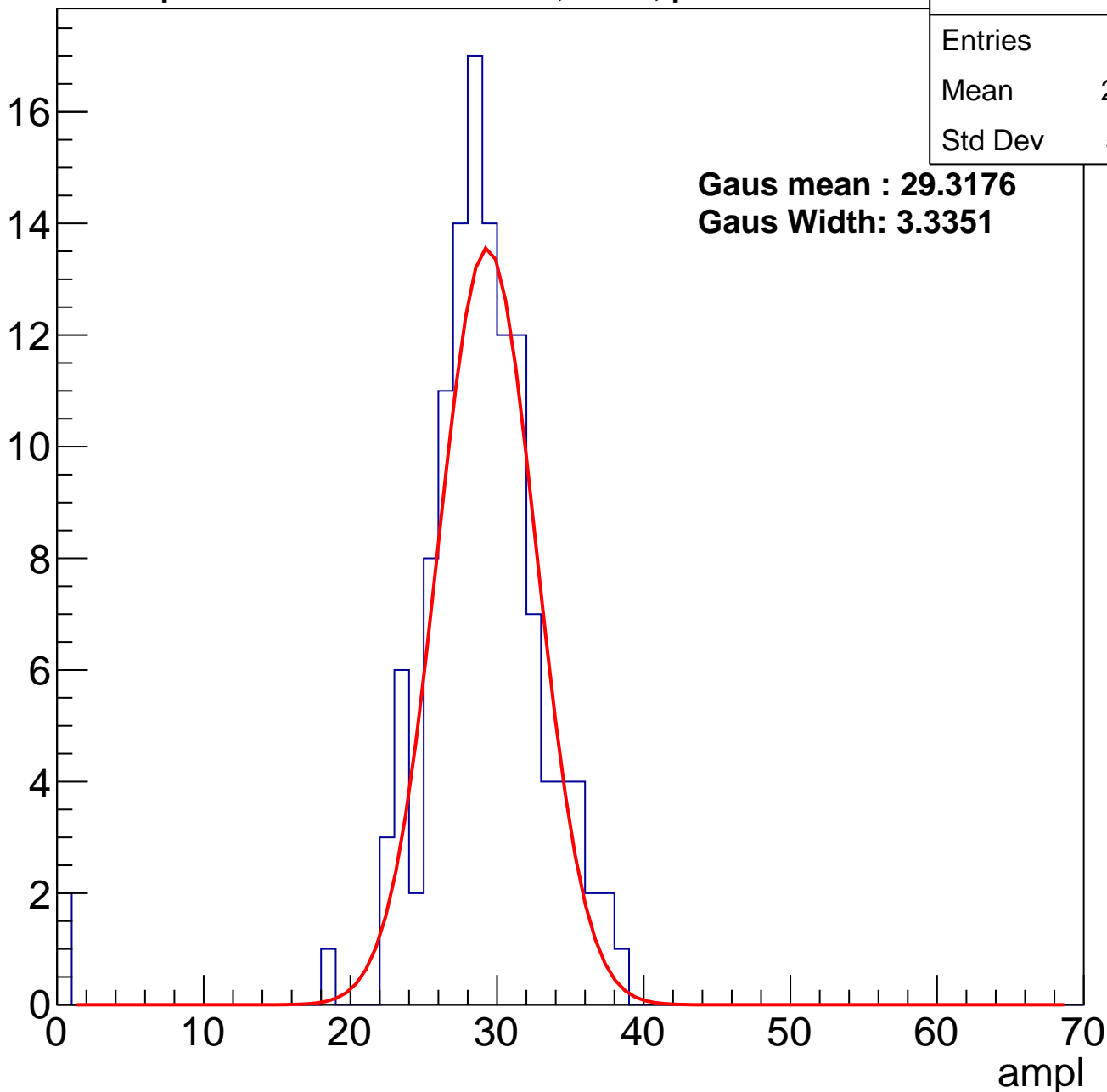
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	28.29
Std Dev	5.031

Gaus mean : 29.3176

Gaus Width: 3.3351

Entry



B1L001S, U14-ch7, adc1

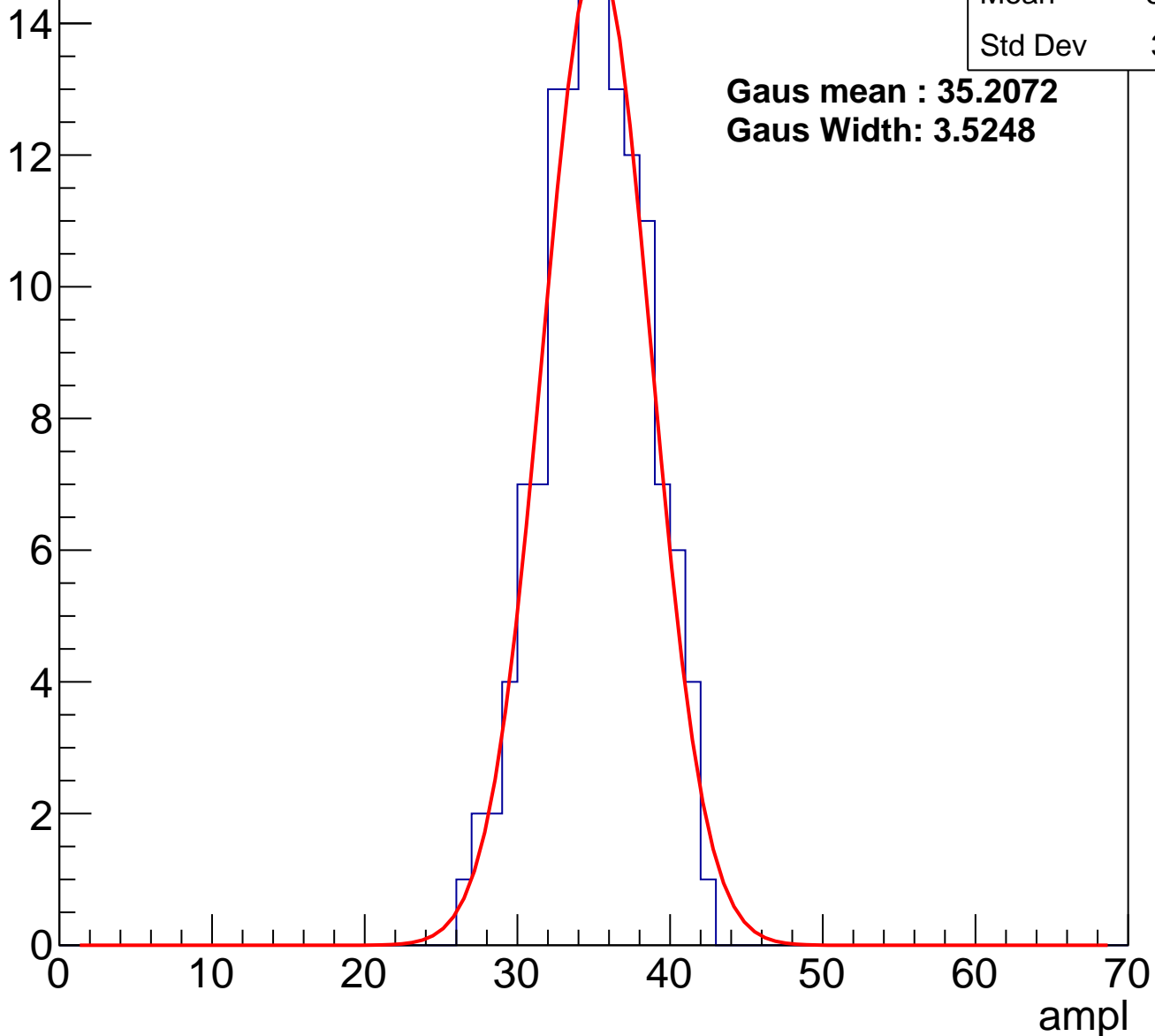
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	34.65
Std Dev	3.381

Gaus mean : 35.2072

Gaus Width: 3.5248

Entry



B1L001S, U14-ch7, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	128
Mean	41.45
Std Dev	3.506

Gaus mean : 42.0668

Gaus Width: 3.2103

0

2

4

6

8

10

12

14

B1L001S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#2, port C2

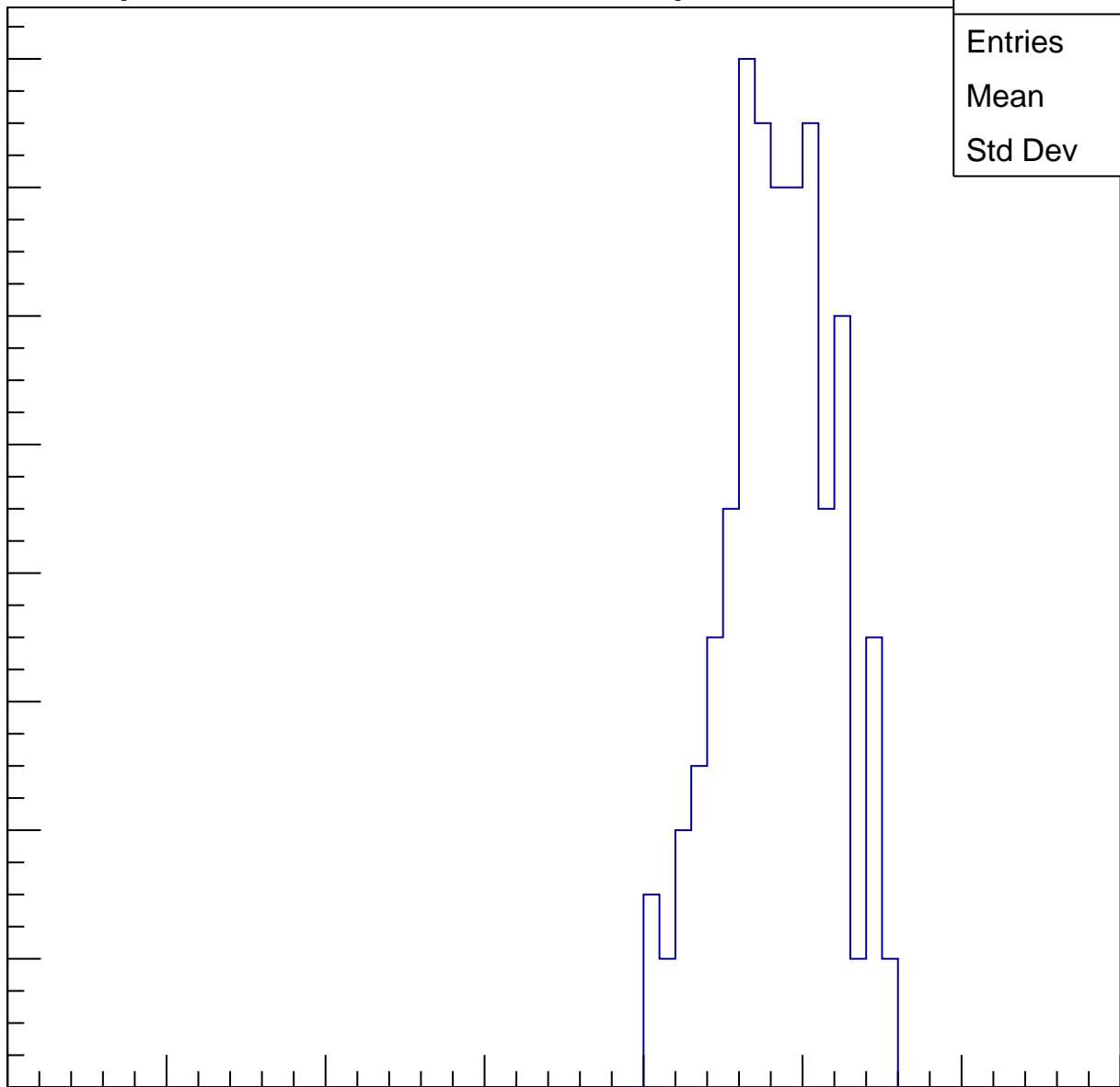
Entries	136
Mean	47.97
Std Dev	3.406

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

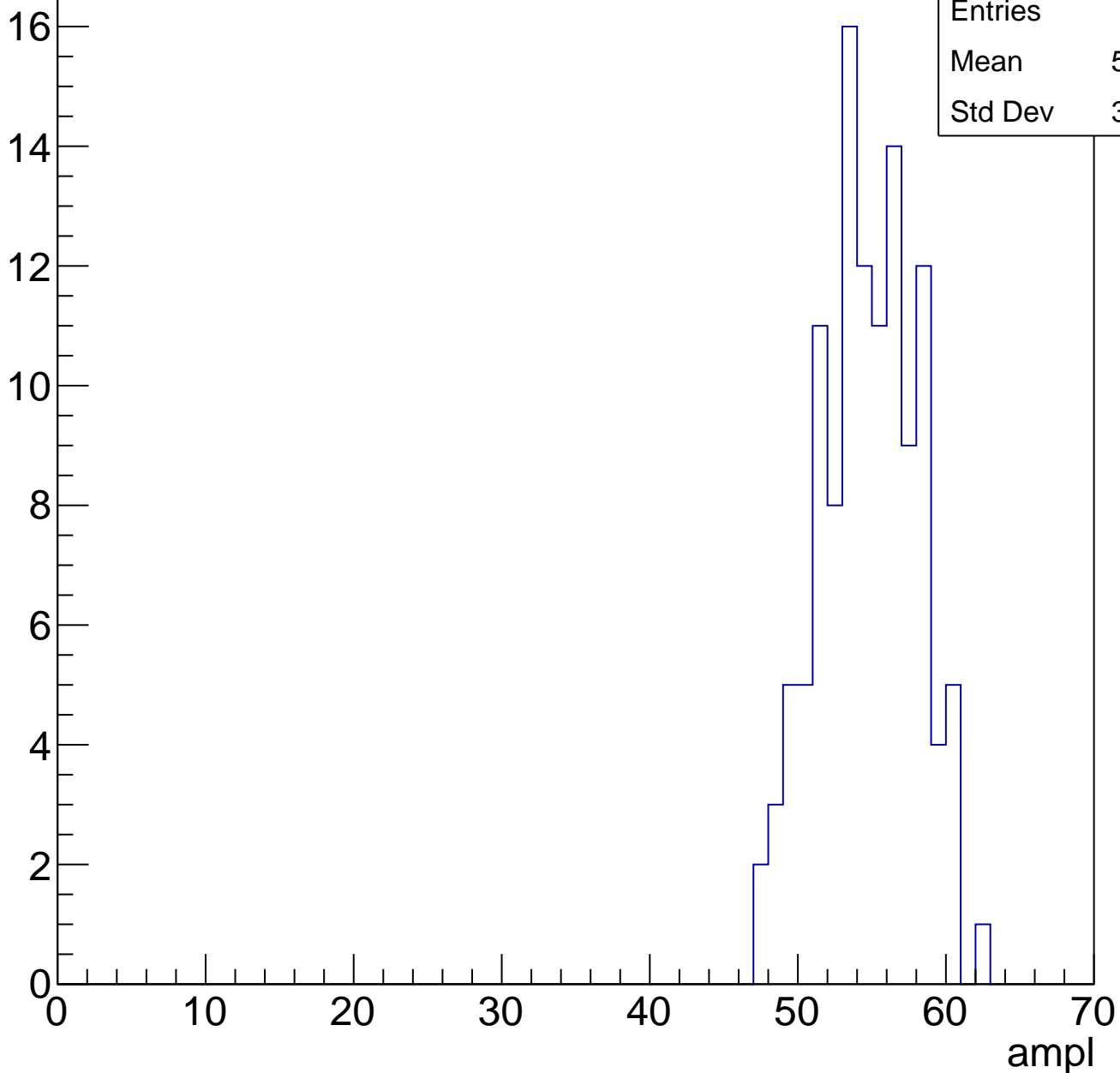
ampl



B1L001S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	118
Mean	54.25
Std Dev	3.234

B1L001S, U14-ch7, adc5

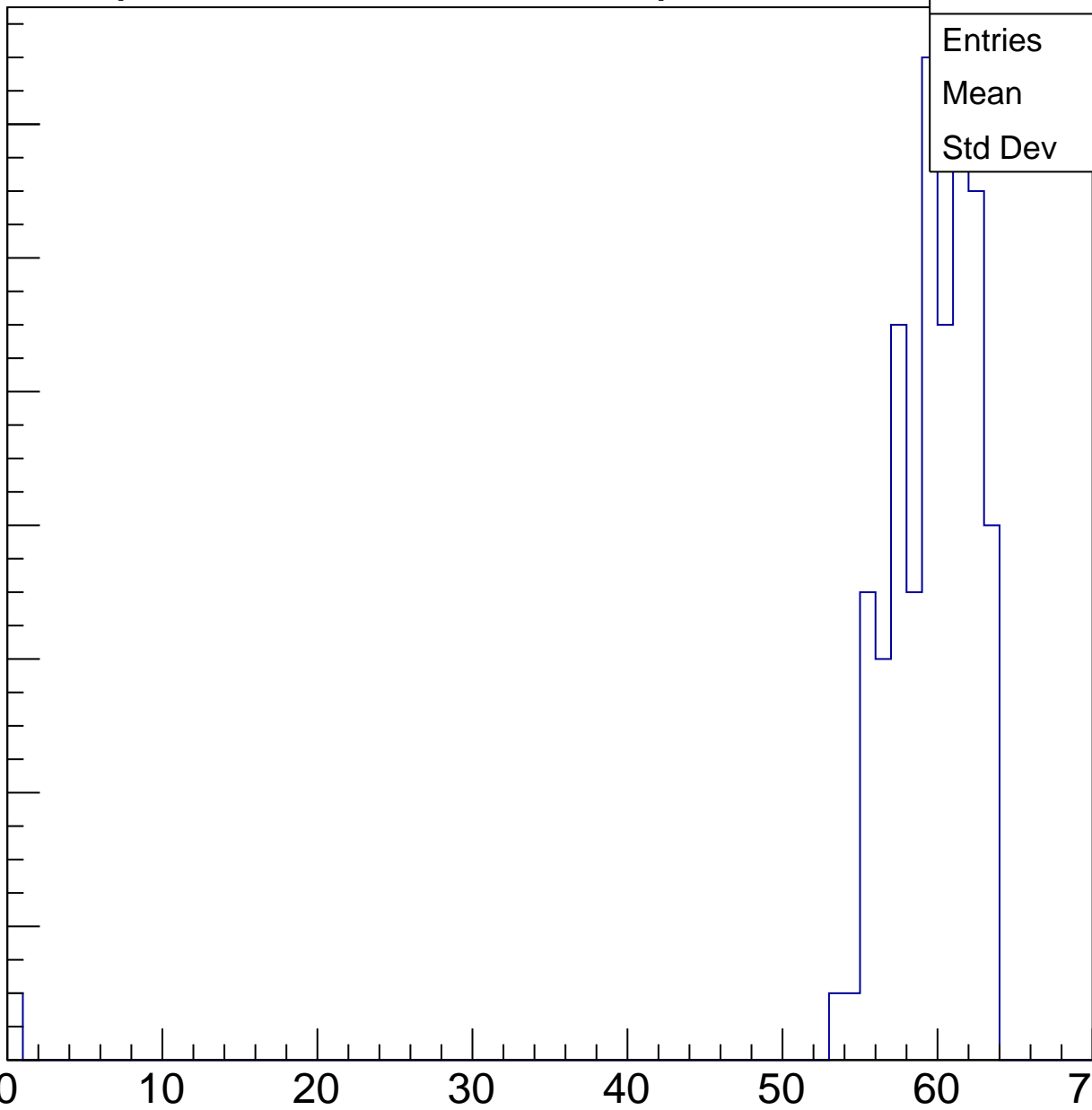
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	95
Mean	58.63
Std Dev	6.536

ampl



B1L001S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	30
Mean	59.63
Std Dev	11.16

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



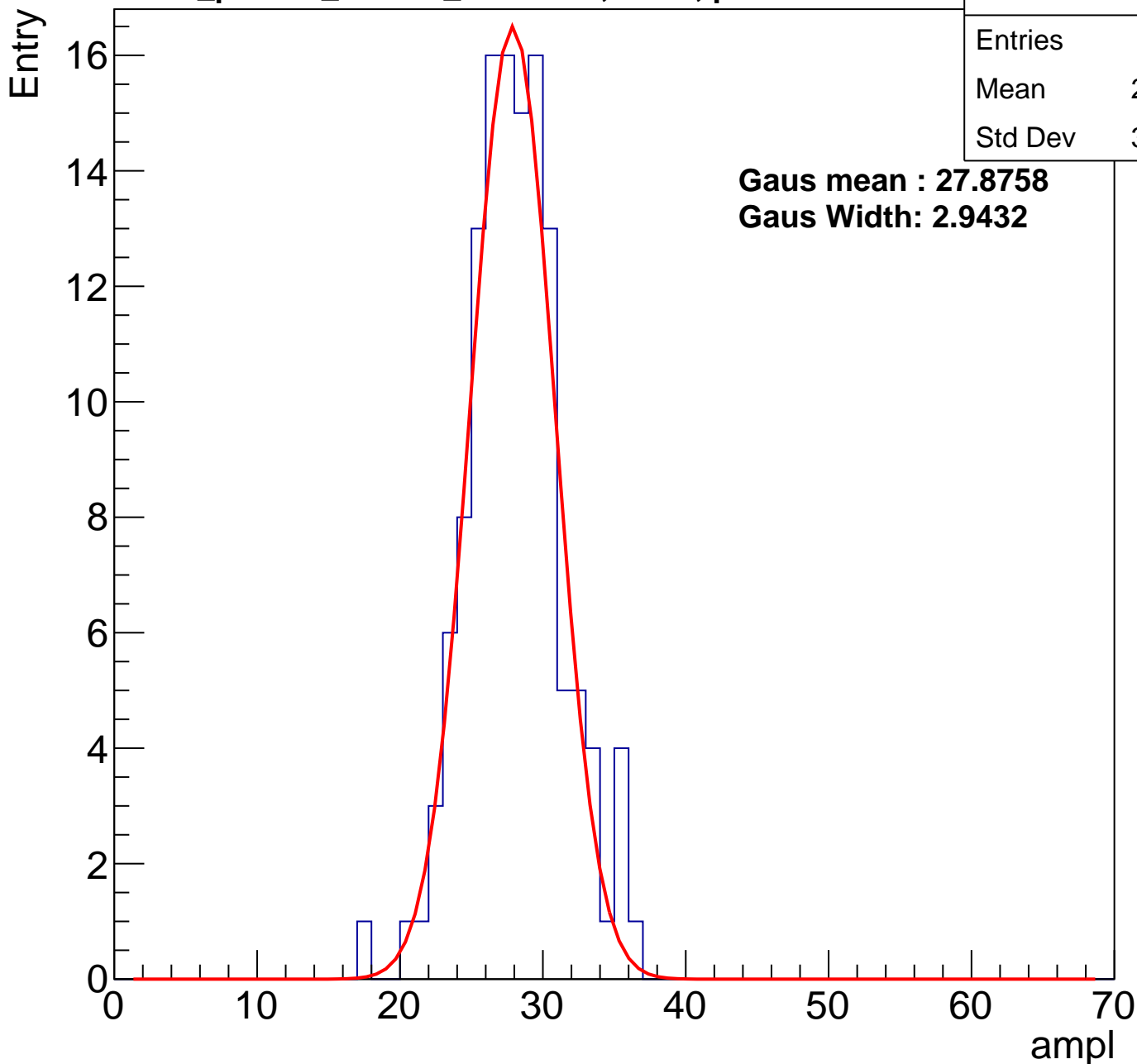
B1L001S, U14-ch8, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	27.58
Std Dev	3.275

Gaus mean : 27.8758

Gaus Width: 2.9432



B1L001S, U14-ch8, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	34.7
Std Dev	3.568

Gaus mean : 35.2529

Gaus Width: 3.6131

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

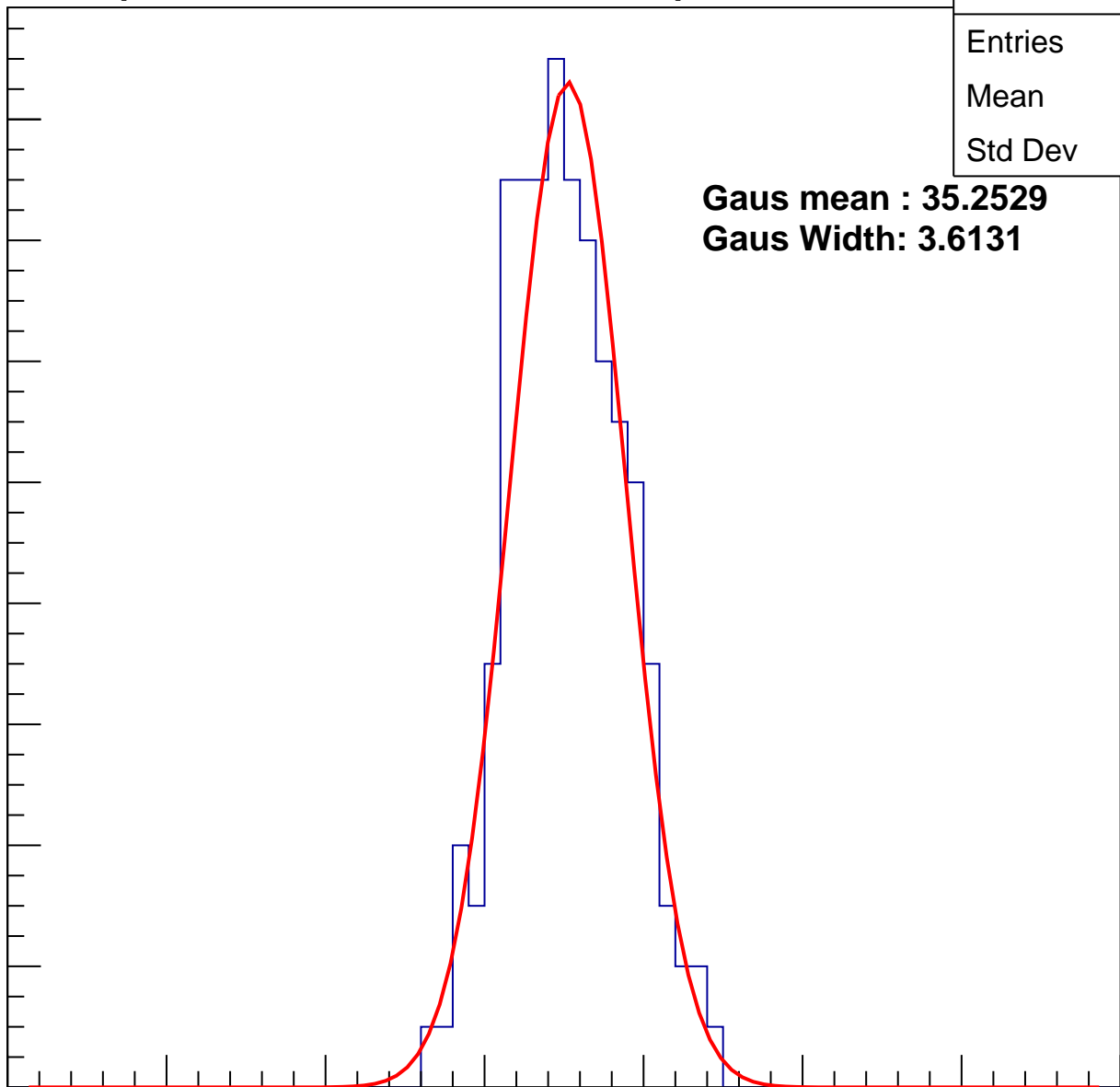
40

50

60

70

ampl



B1L001S, U14-ch8, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

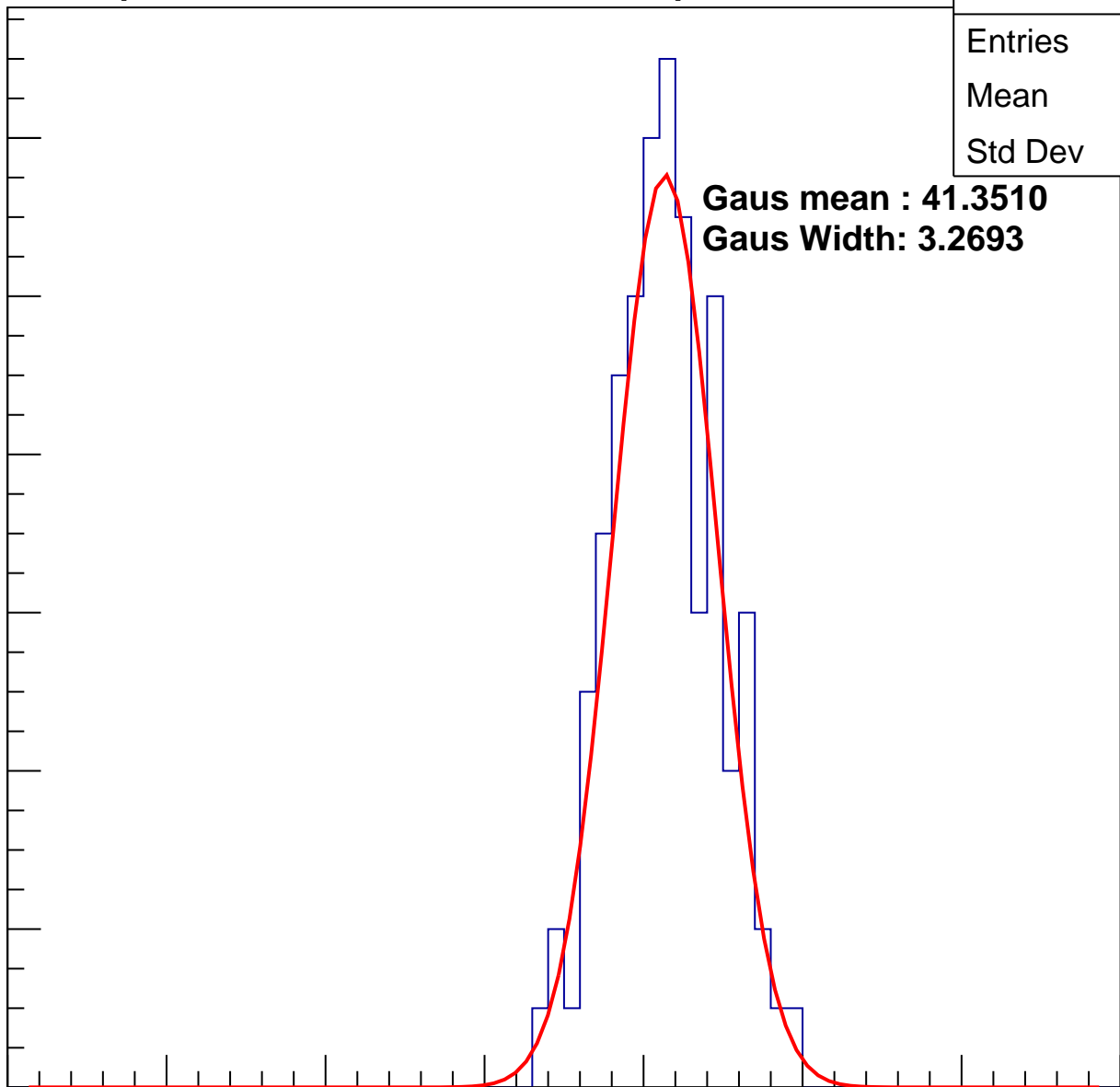
Entries	101
Mean	40.86
Std Dev	3.285

Gaus mean : 41.3510

Gaus Width: 3.2693

0 10 20 30 40 50 60 70

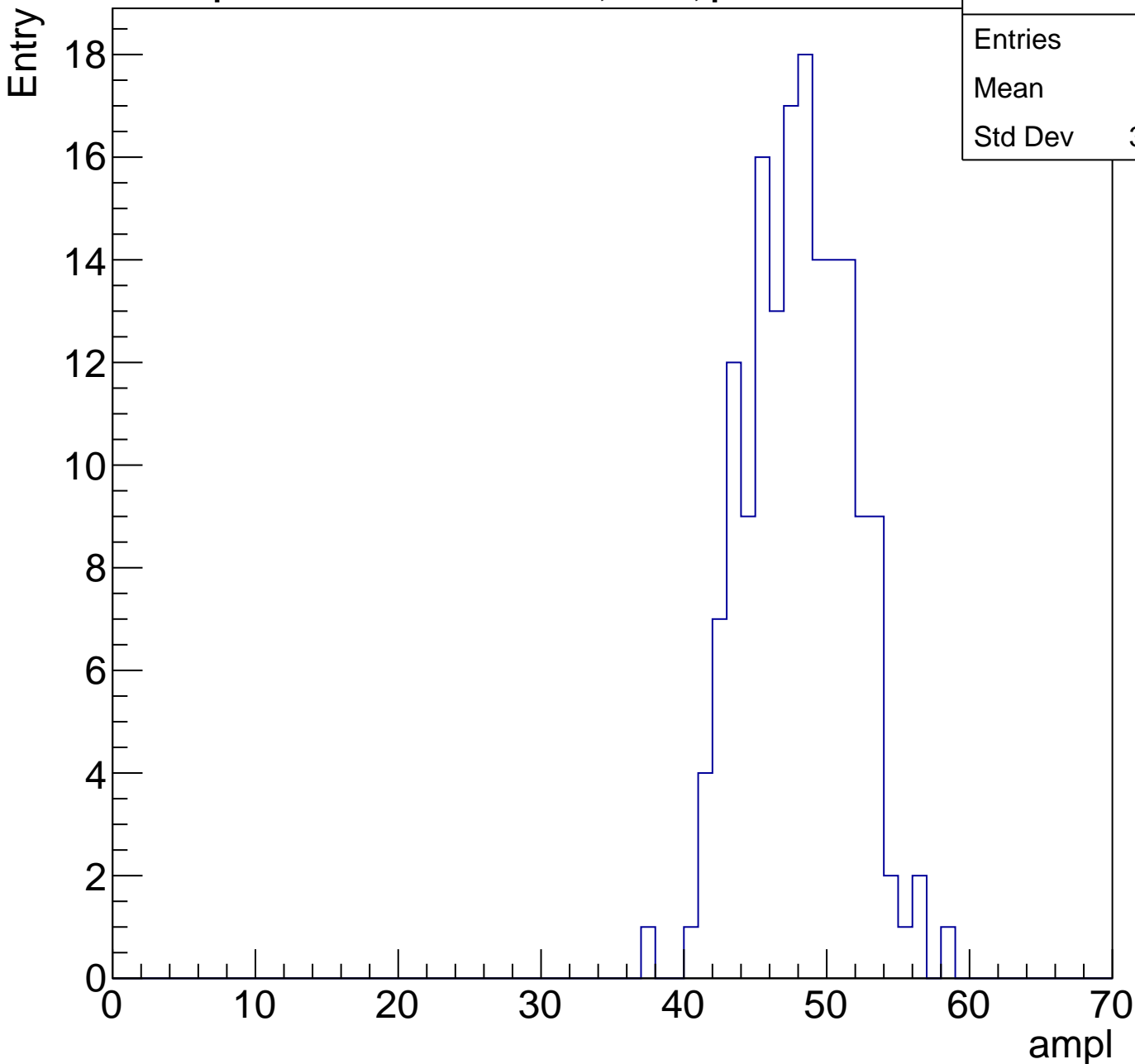
ampl



B1L001S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	47.6
Std Dev	3.644



B1L001S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#2, port C2

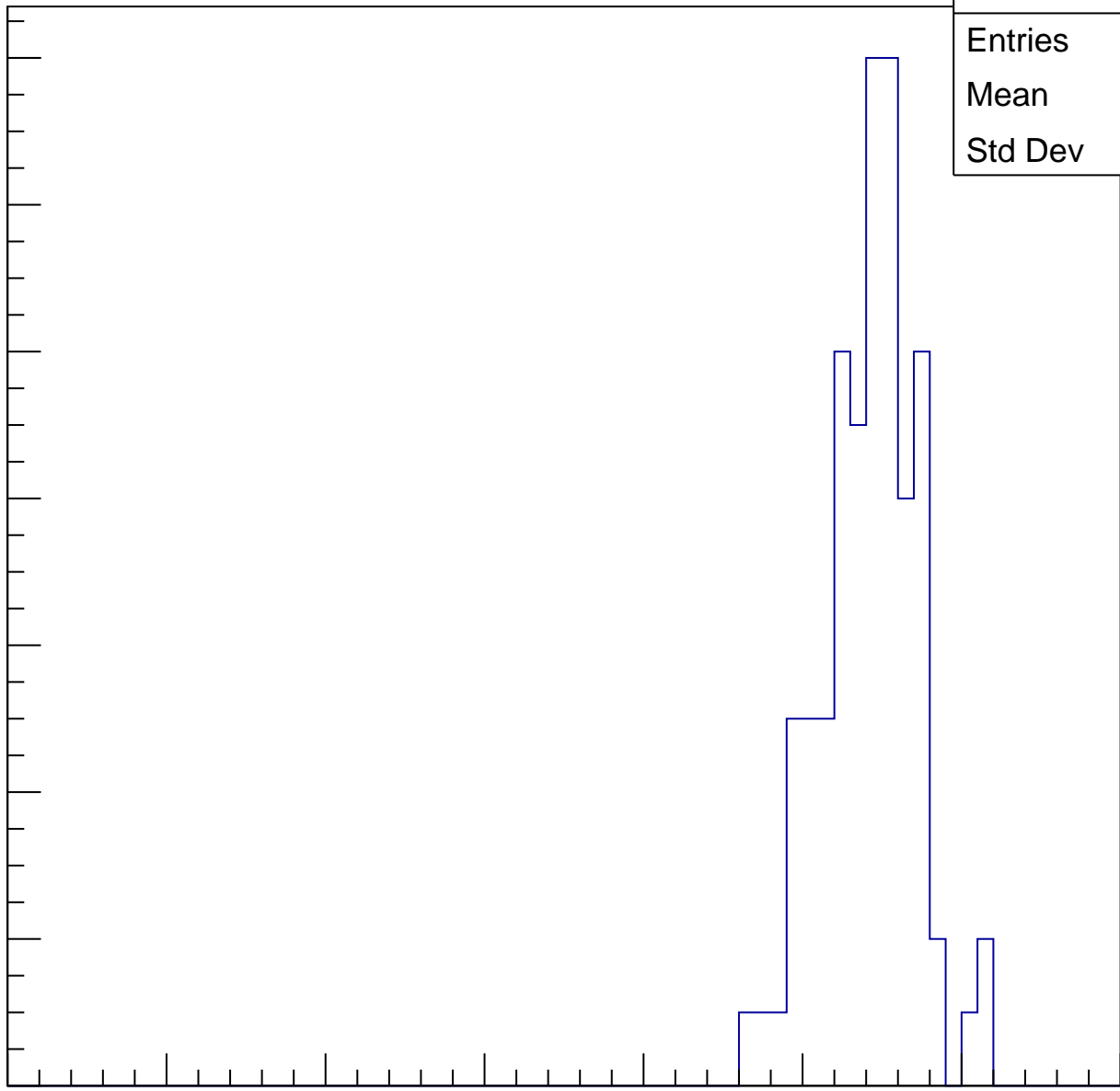
Entries	88
Mean	53.75
Std Dev	2.909

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

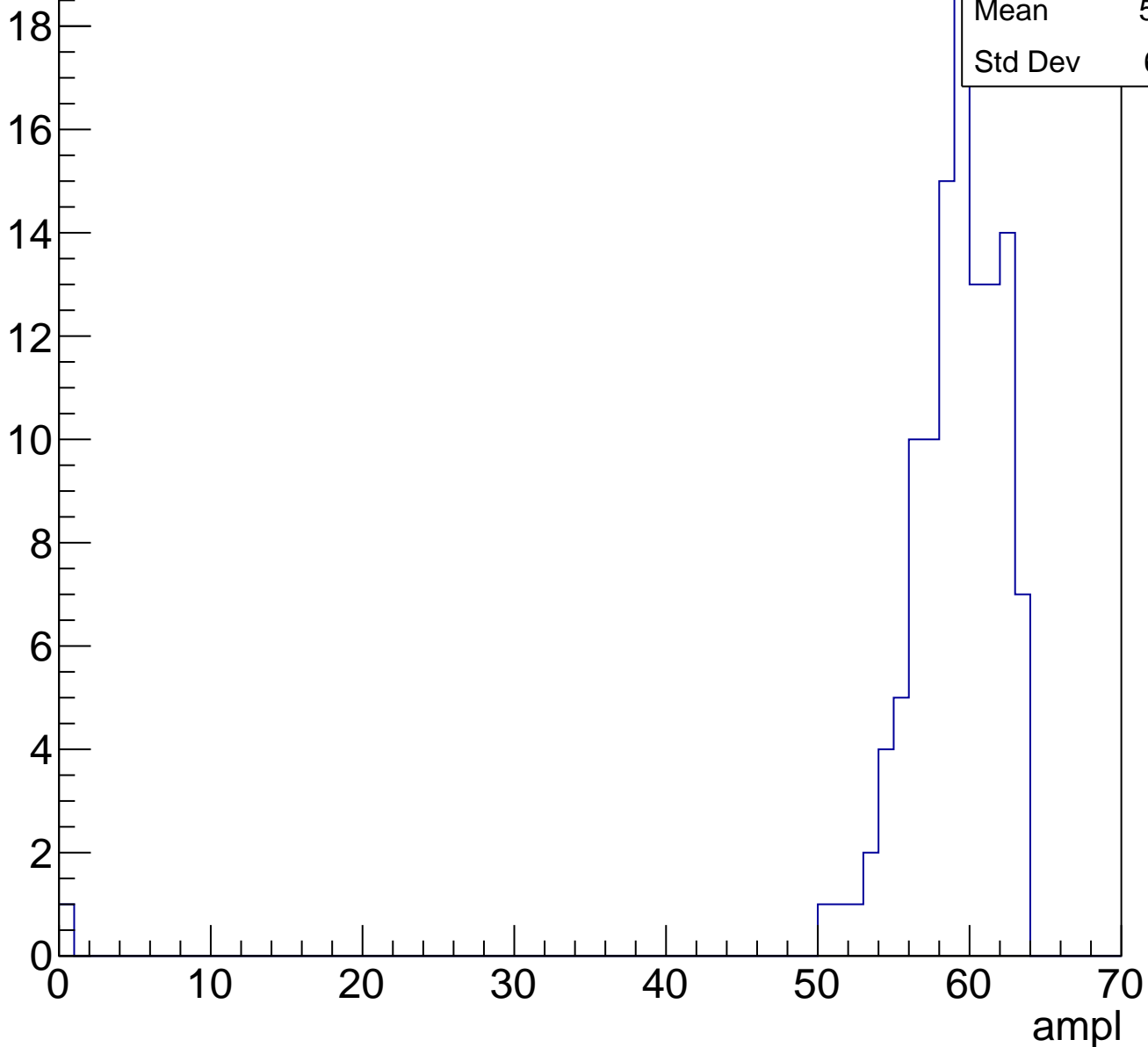
ampl



B1L001S, U14-ch8, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	33
Mean	59.67
Std Dev	10.66

ampl

0

10

20

30

40

50

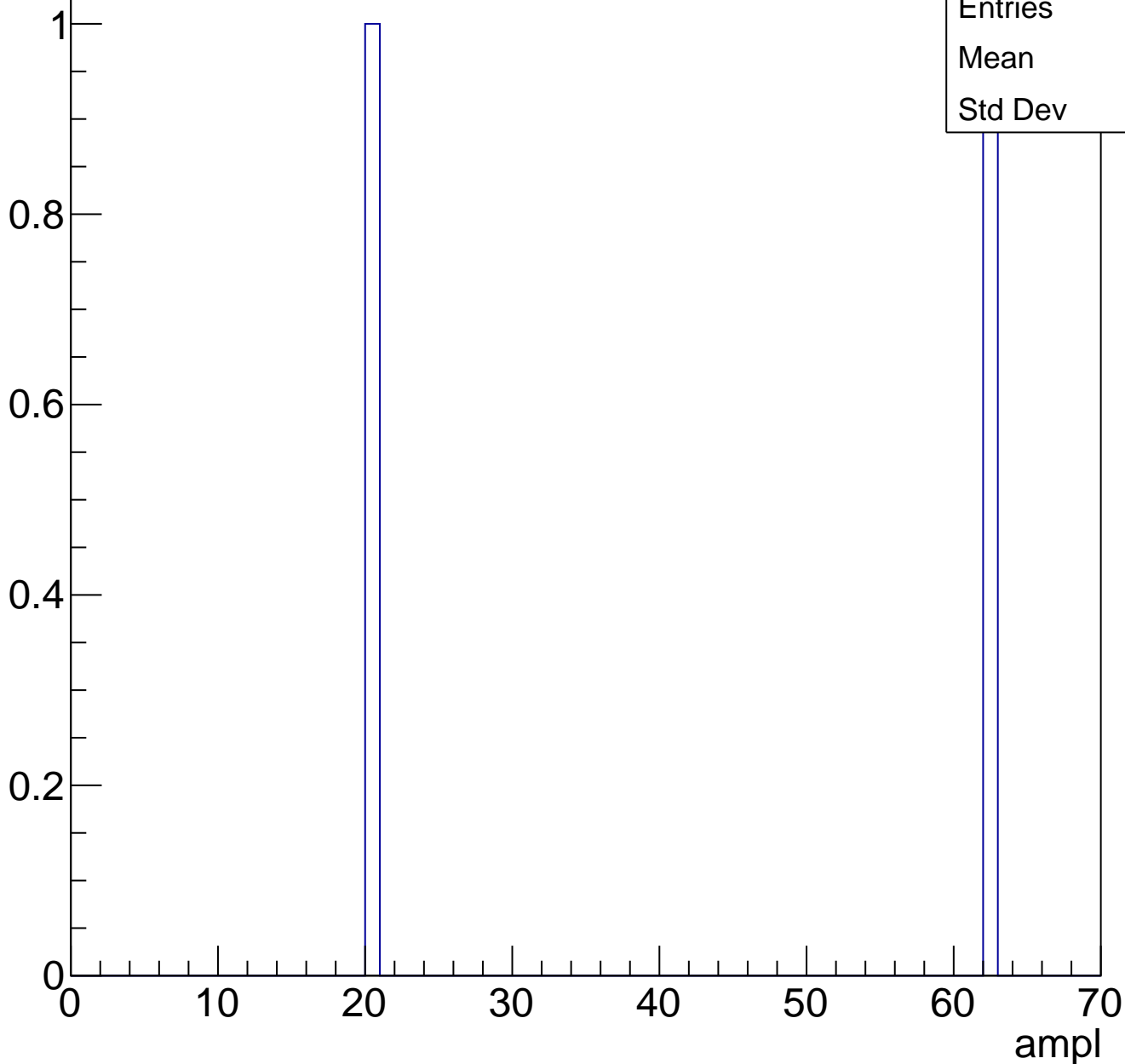
60

70

B1L001S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch9, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

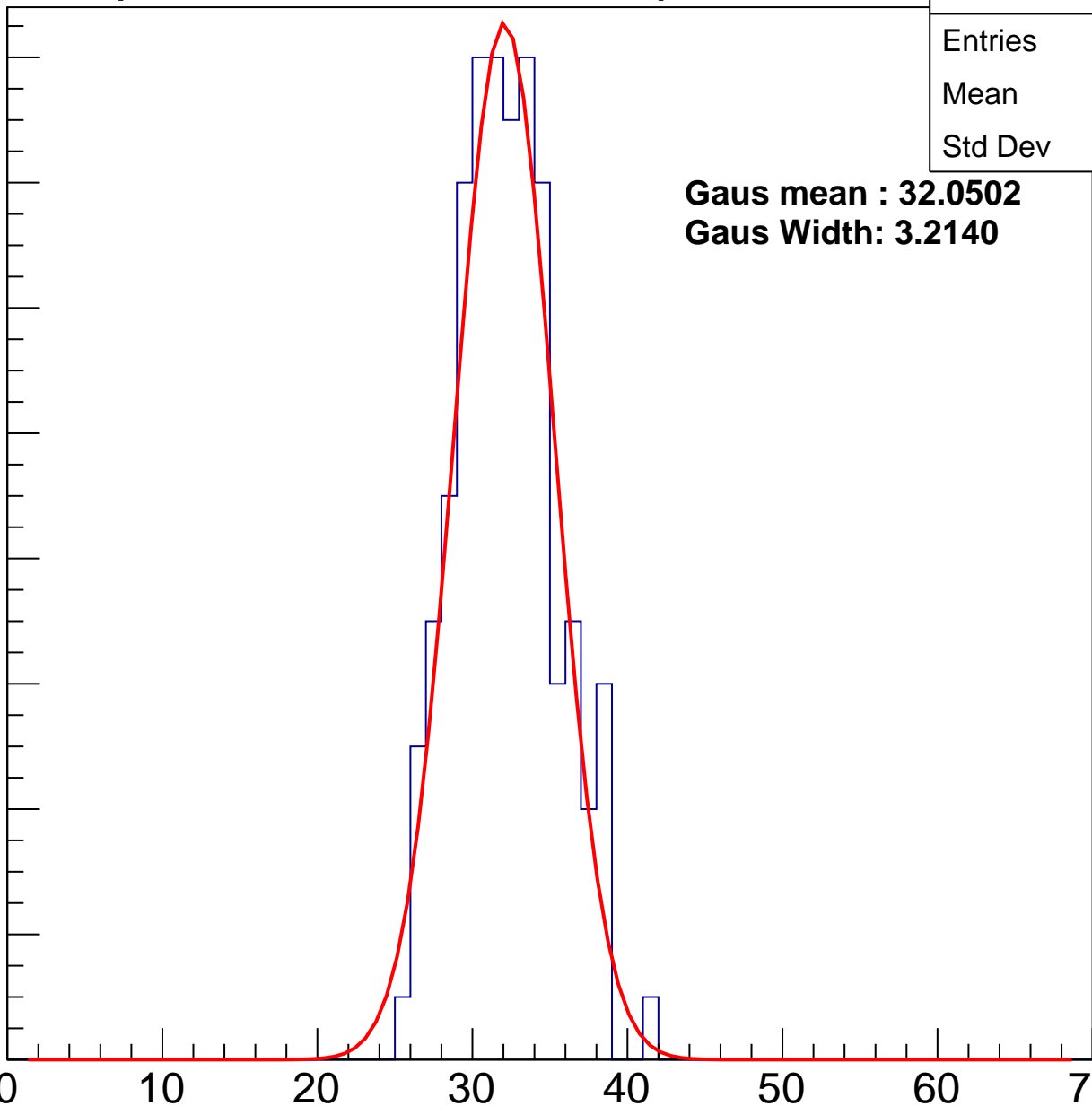
16
14
12
10
8
6
4
2
0

Entries	137
Mean	31.69
Std Dev	3.169

Gaus mean : 32.0502

Gaus Width: 3.2140

ampl



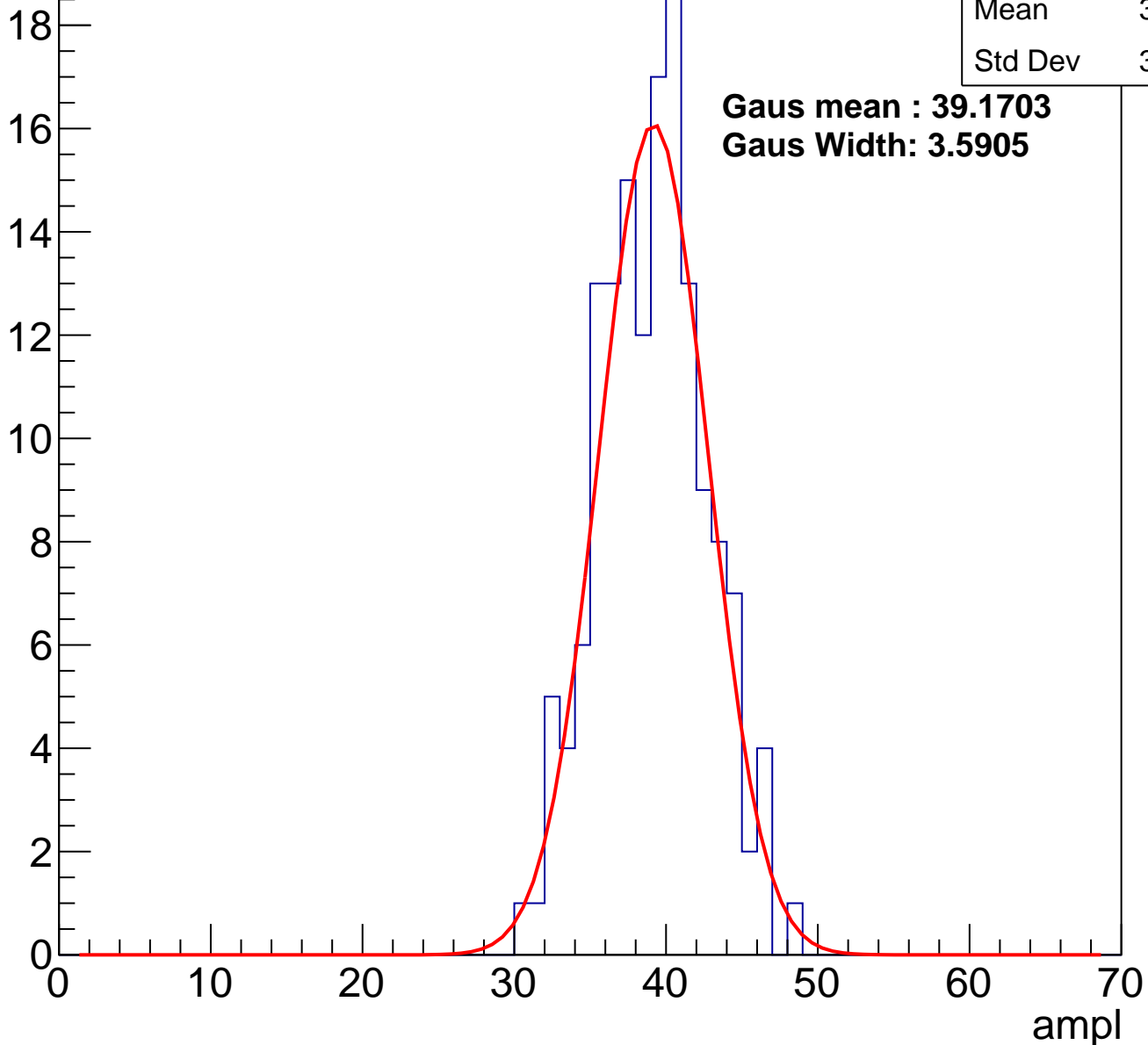
B1L001S, U14-ch9, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	38.66
Std Dev	3.529

Gaus mean : 39.1703
Gaus Width: 3.5905

Entry



B1L001S, U14-ch9, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	45.63
Std Dev	3.504

Gaus mean : 46.2968

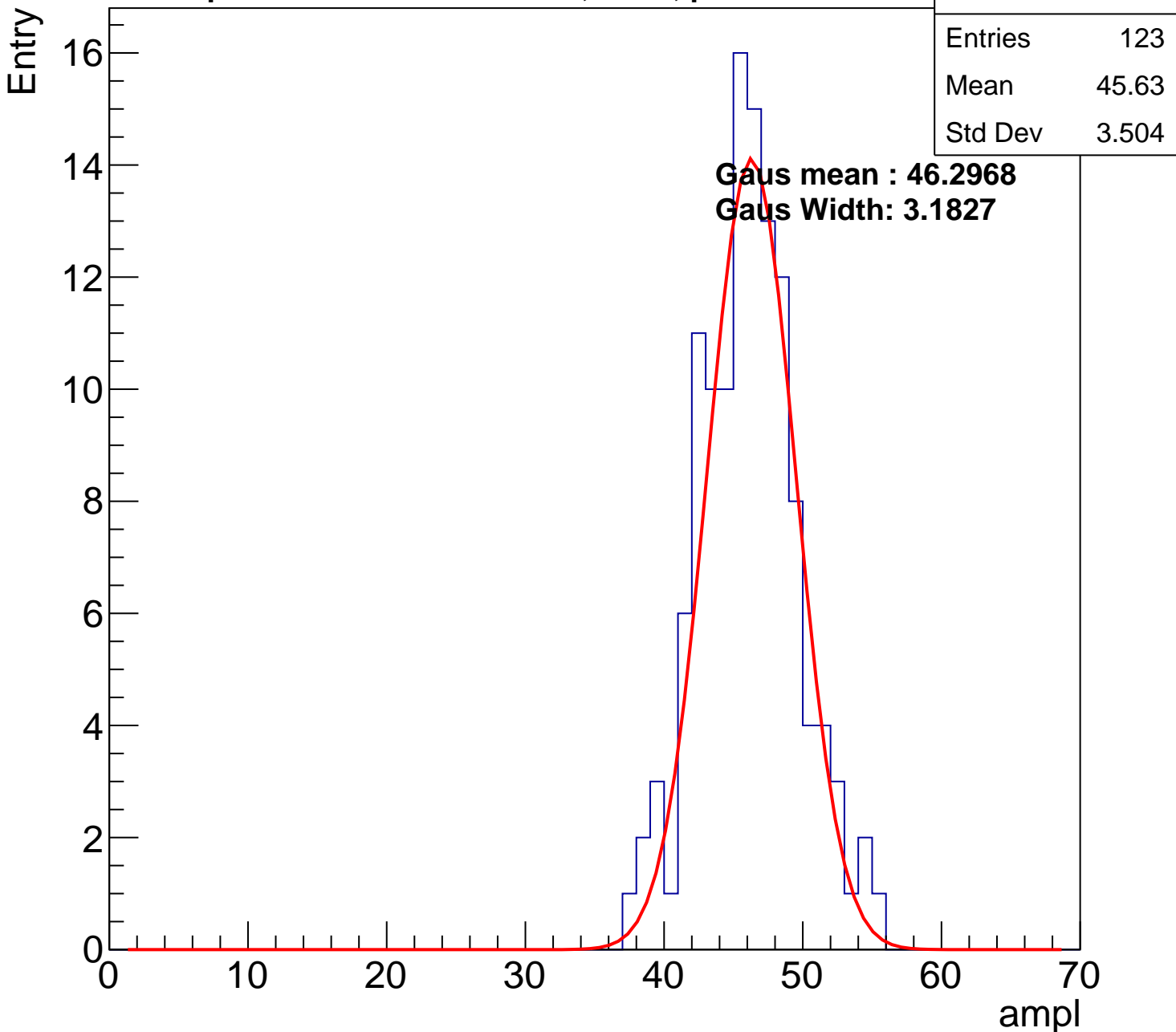
Gaus Width: 3.1827

Entry

16
14
12
10
8
6
4
2
0

ampl

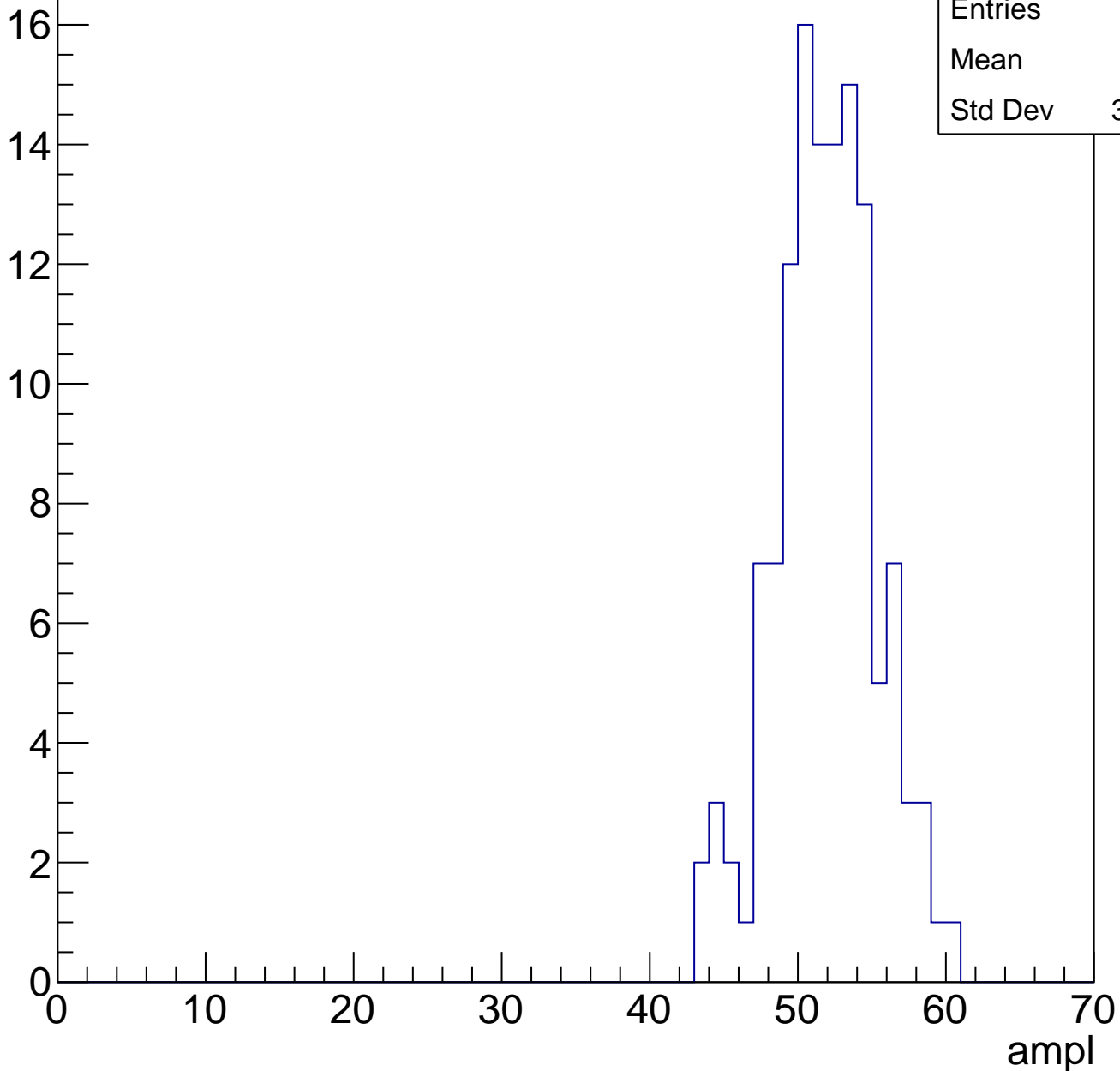
0 10 20 30 40 50 60 70



B1L001S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

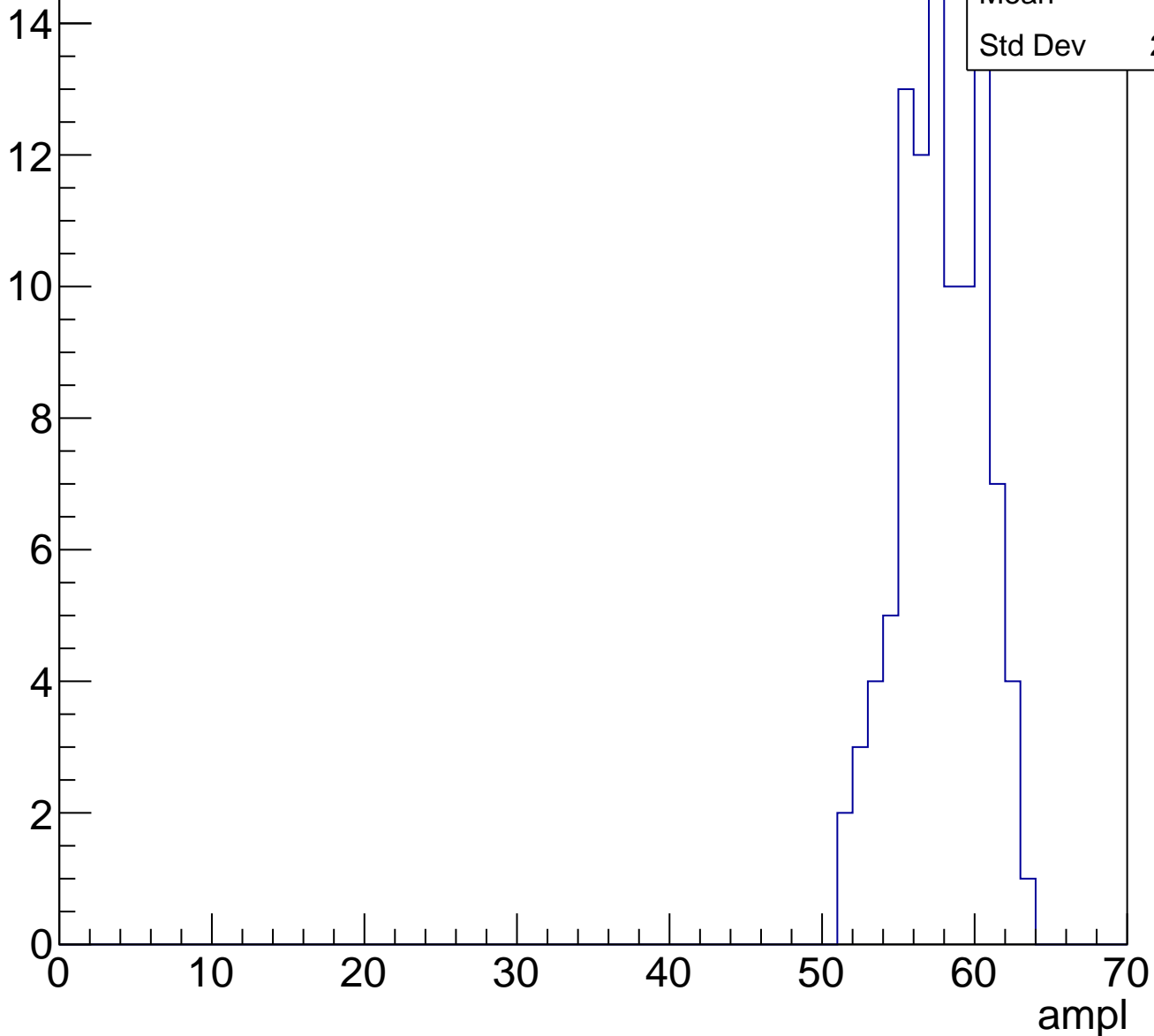


Entries	126
Mean	51.4
Std Dev	3.376

B1L001S, U14-ch9, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	100
Mean	57.3
Std Dev	2.711

B1L001S, U14-ch9, adc5

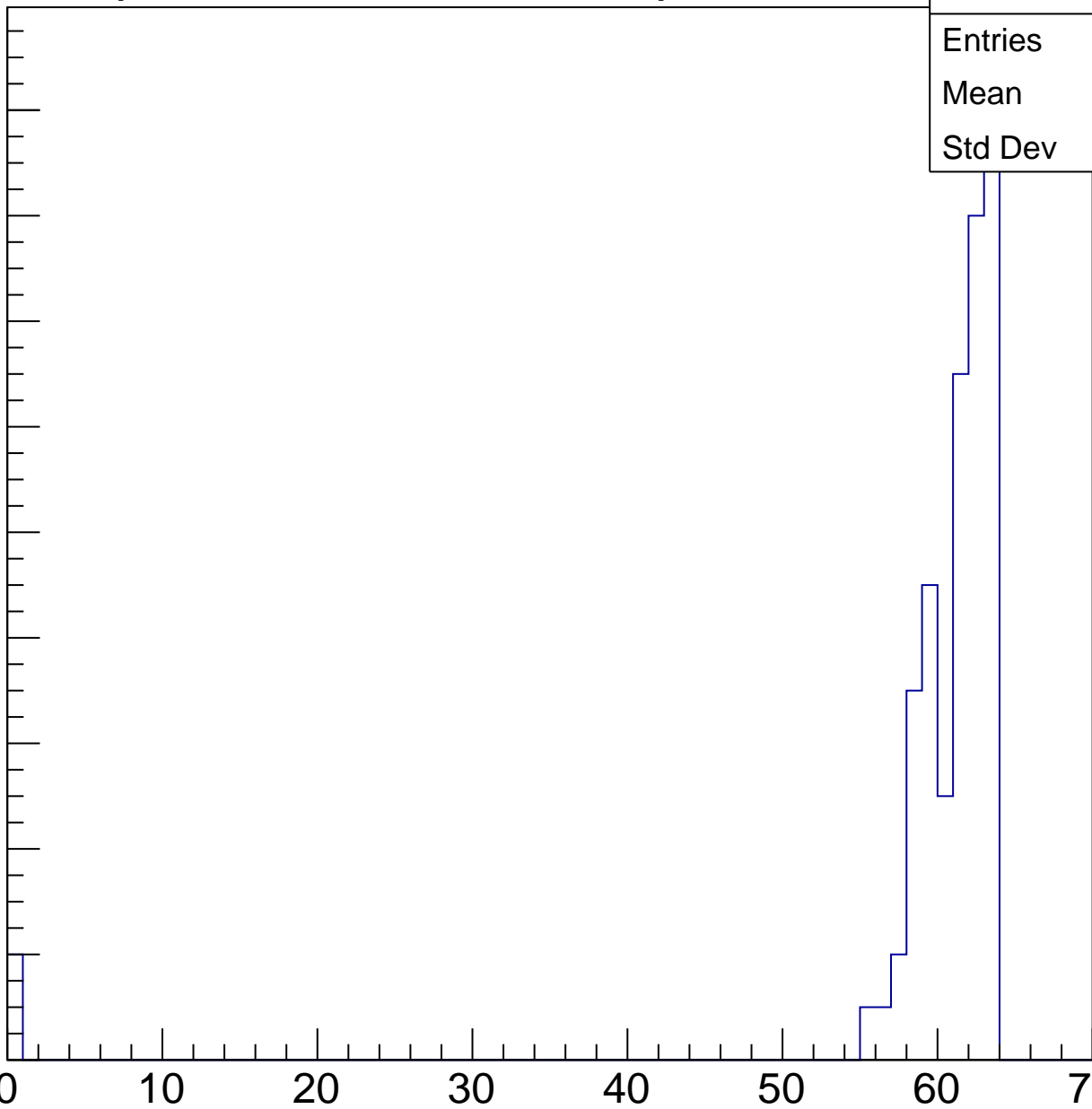
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	75
Mean	59.25
Std Dev	10

ampl



B1L001S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch9, adc7

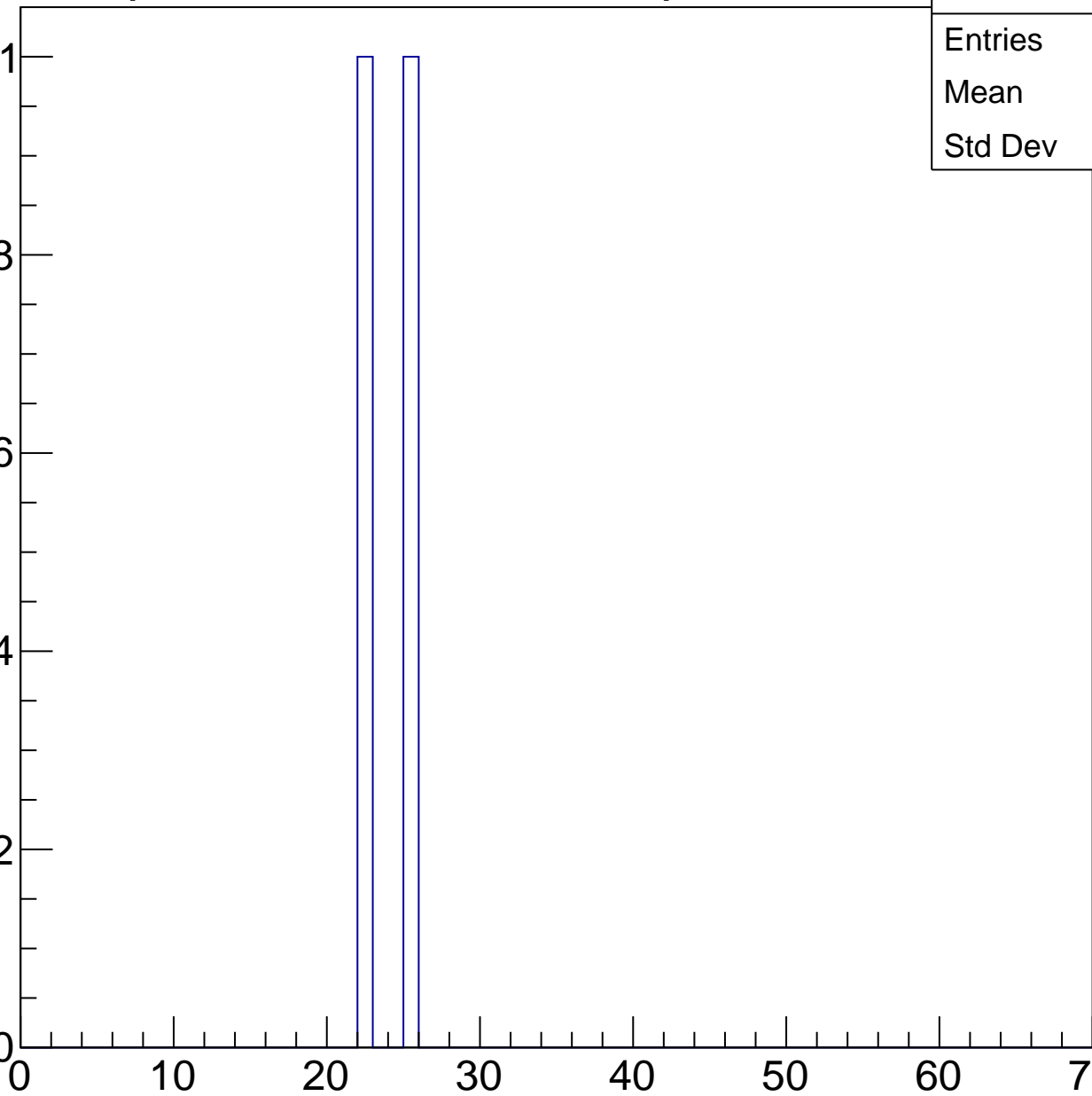
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	23.5
Std Dev	1.5

ampl



B1L001S, U14-ch10, adc0

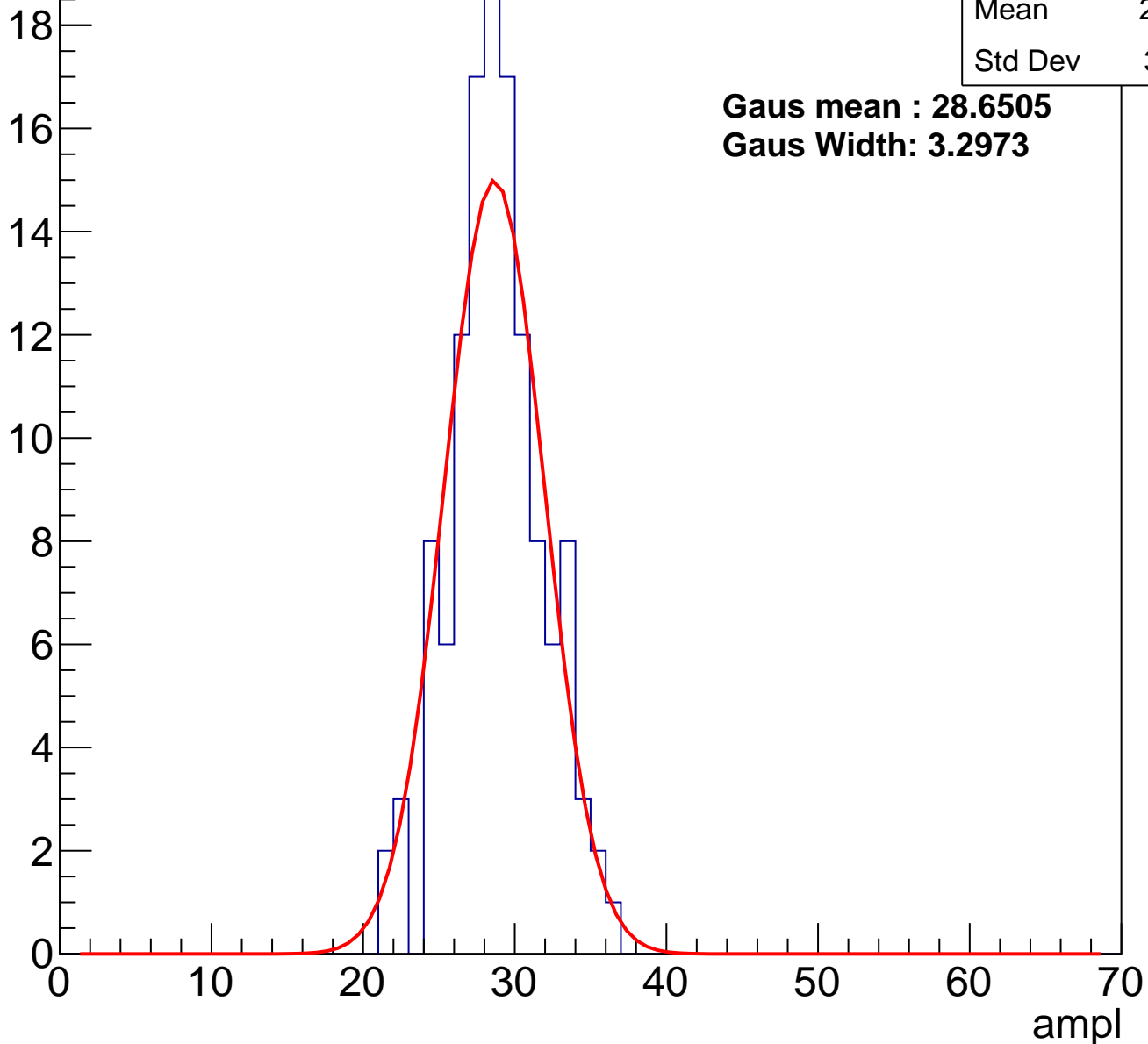
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	28.37
Std Dev	3.031

Gaus mean : 28.6505

Gaus Width: 3.2973

Entry



B1L001S, U14-ch10, adc1

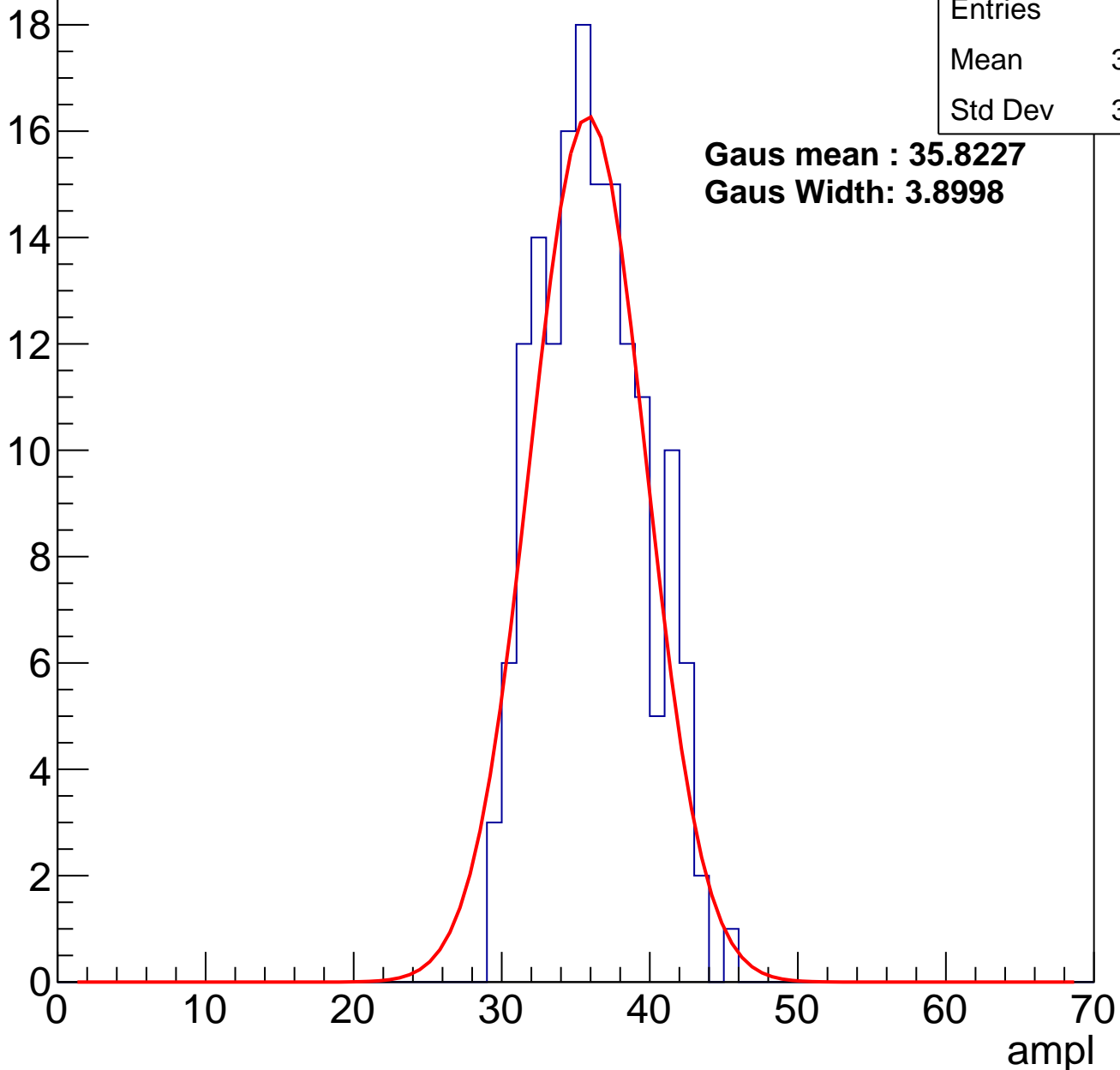
calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	35.63
Std Dev	3.507

Gaus mean : 35.8227

Gaus Width: 3.8998

Entry



B1L001S, U14-ch10, adc2

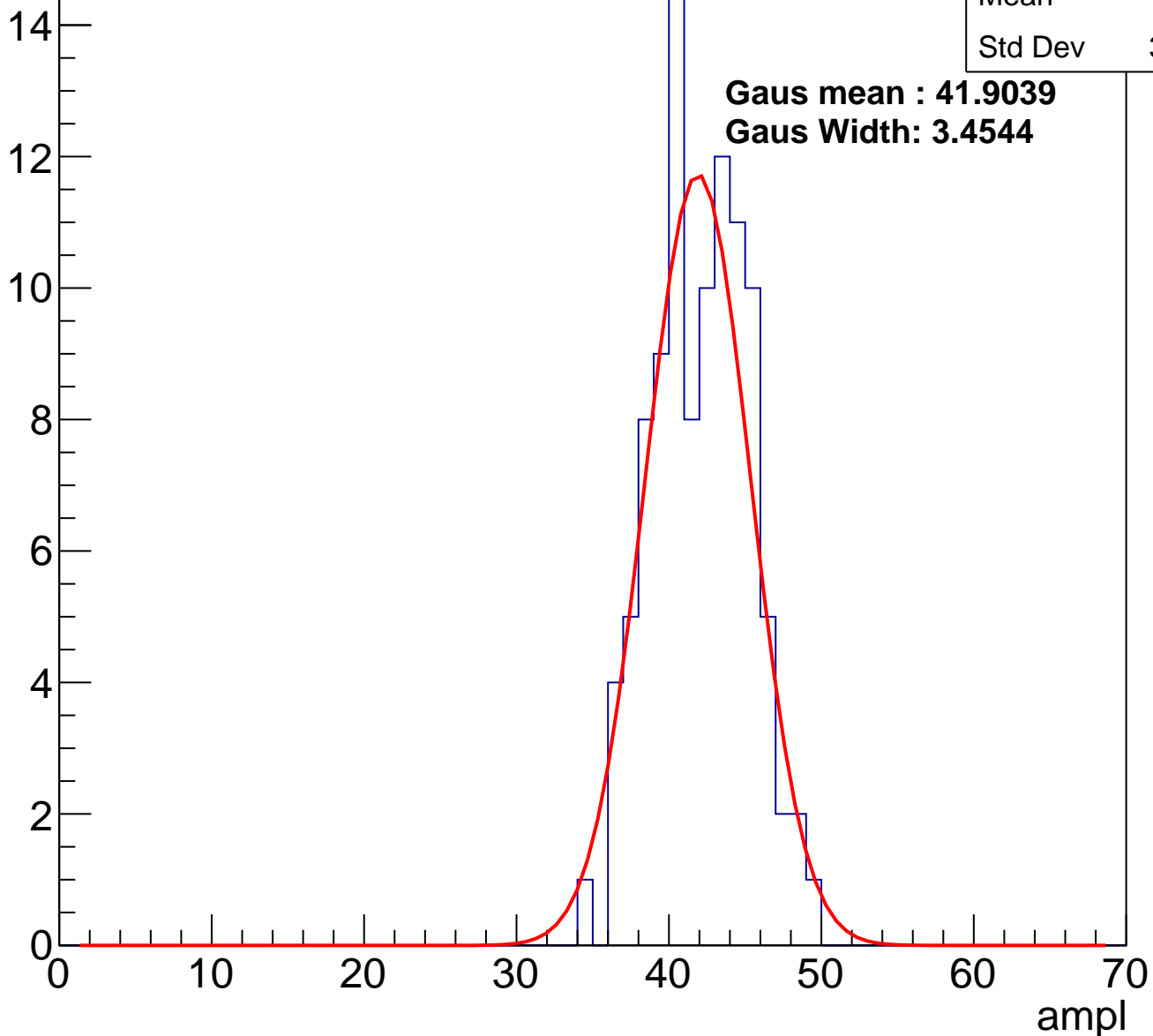
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	41.6
Std Dev	3.101

Gaus mean : 41.9039

Gaus Width: 3.4544

Entry



B1L001S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	47.82
Std Dev	3.13

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

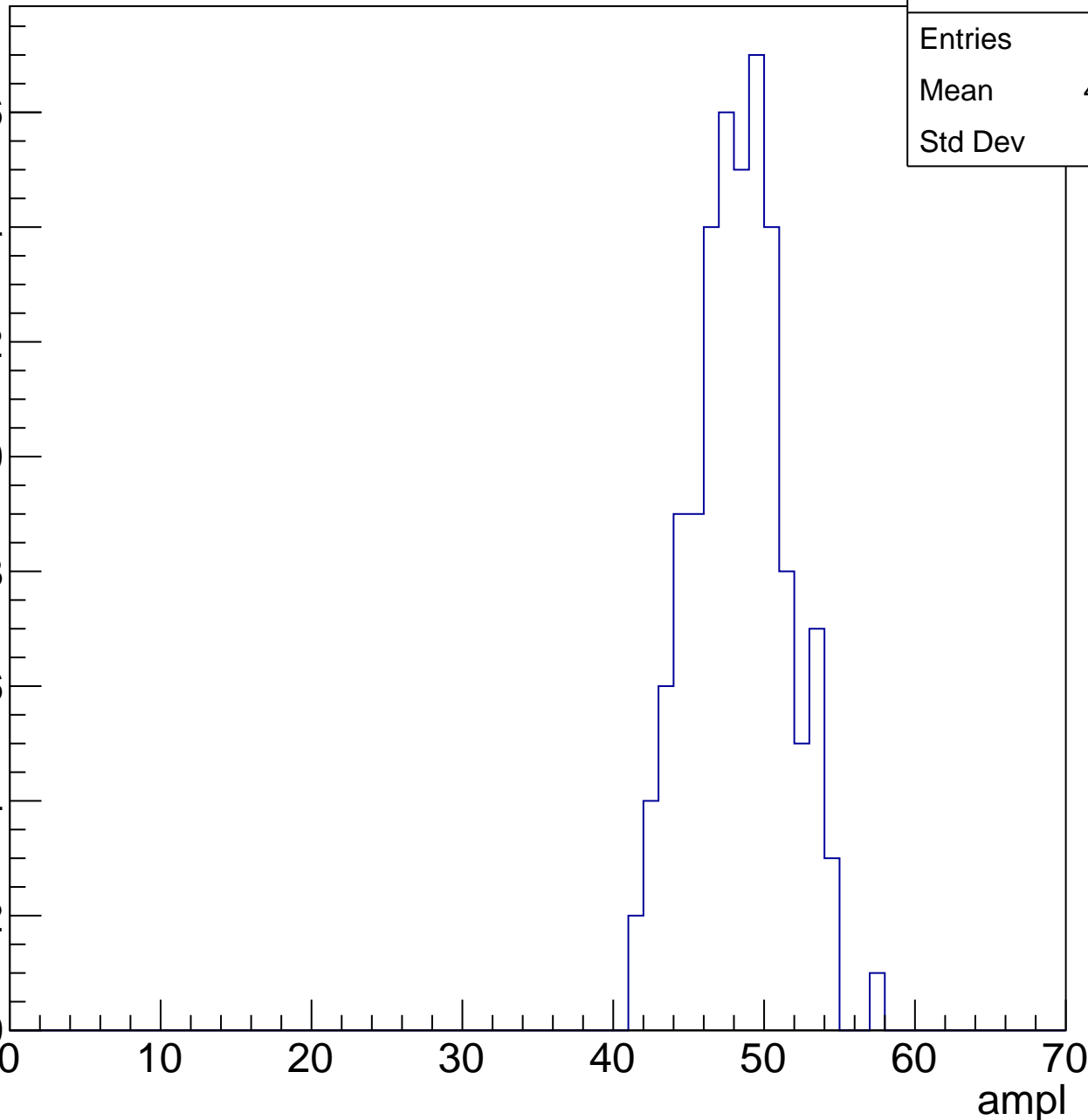
40

50

60

70

ampl



B1L001S, U14-ch10, adc4

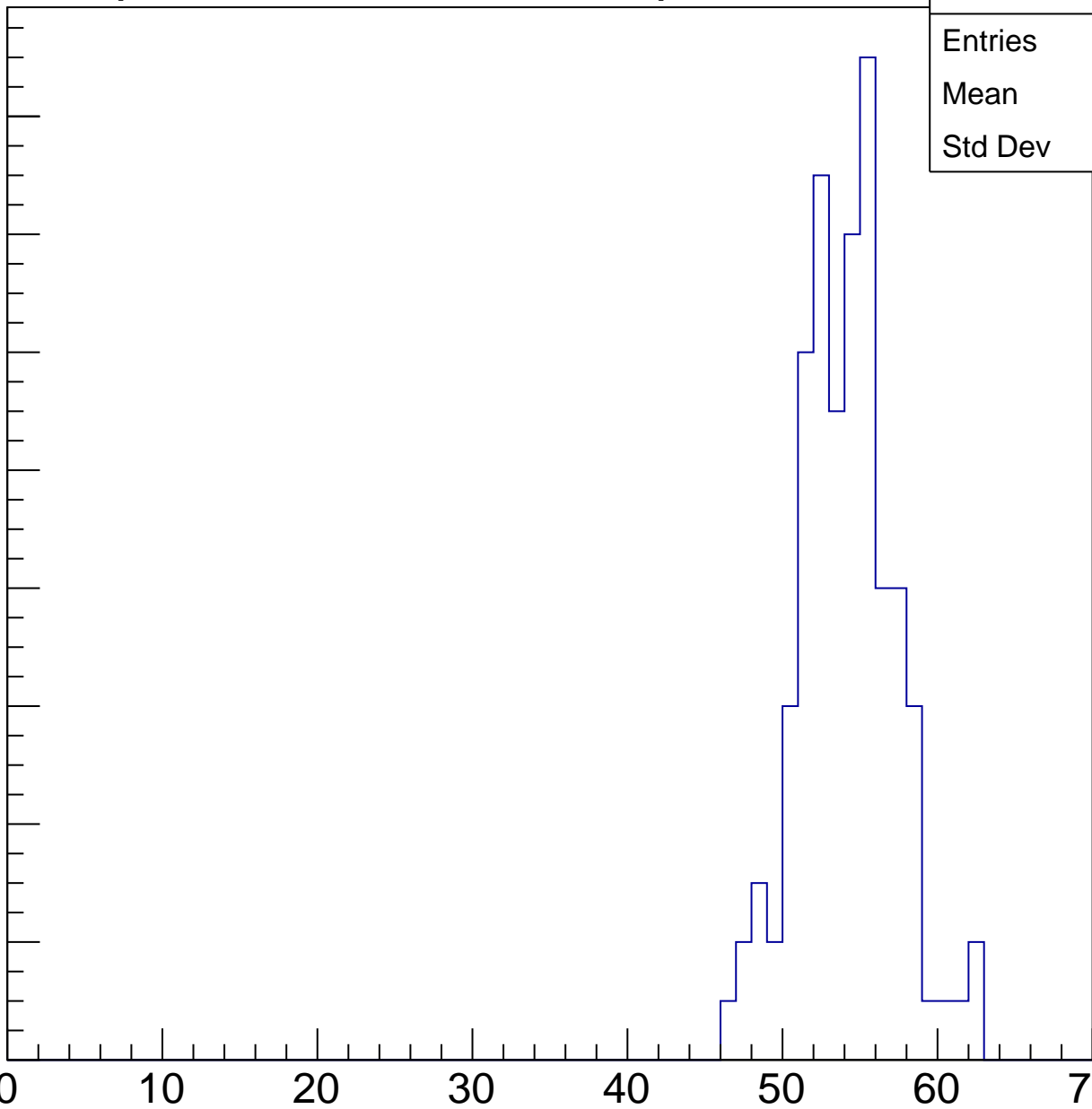
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	110
Mean	53.67
Std Dev	3.072

ampl



B1L001S, U14-ch10, adc5

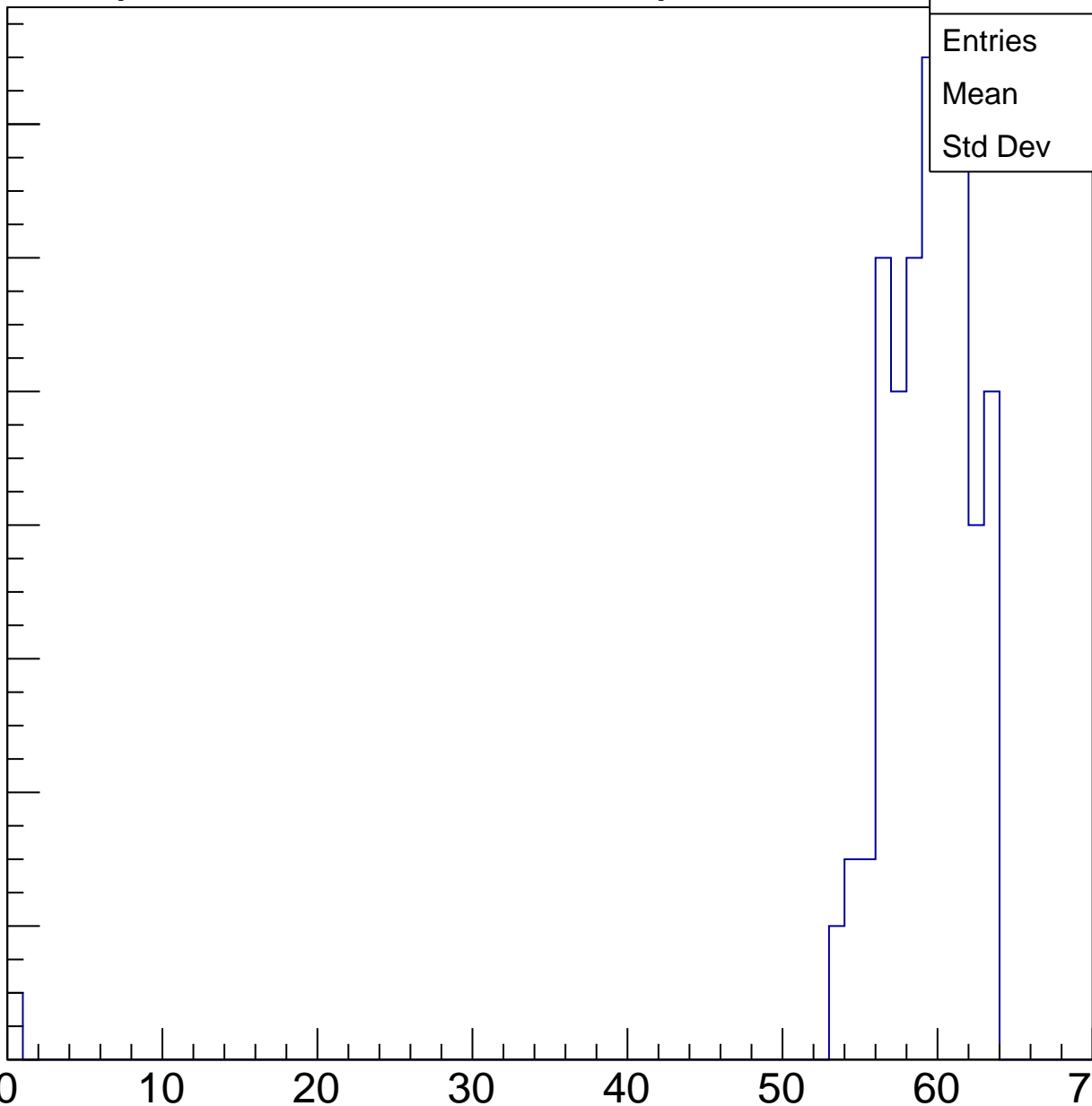
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	104
Mean	58.42
Std Dev	6.281

ampl

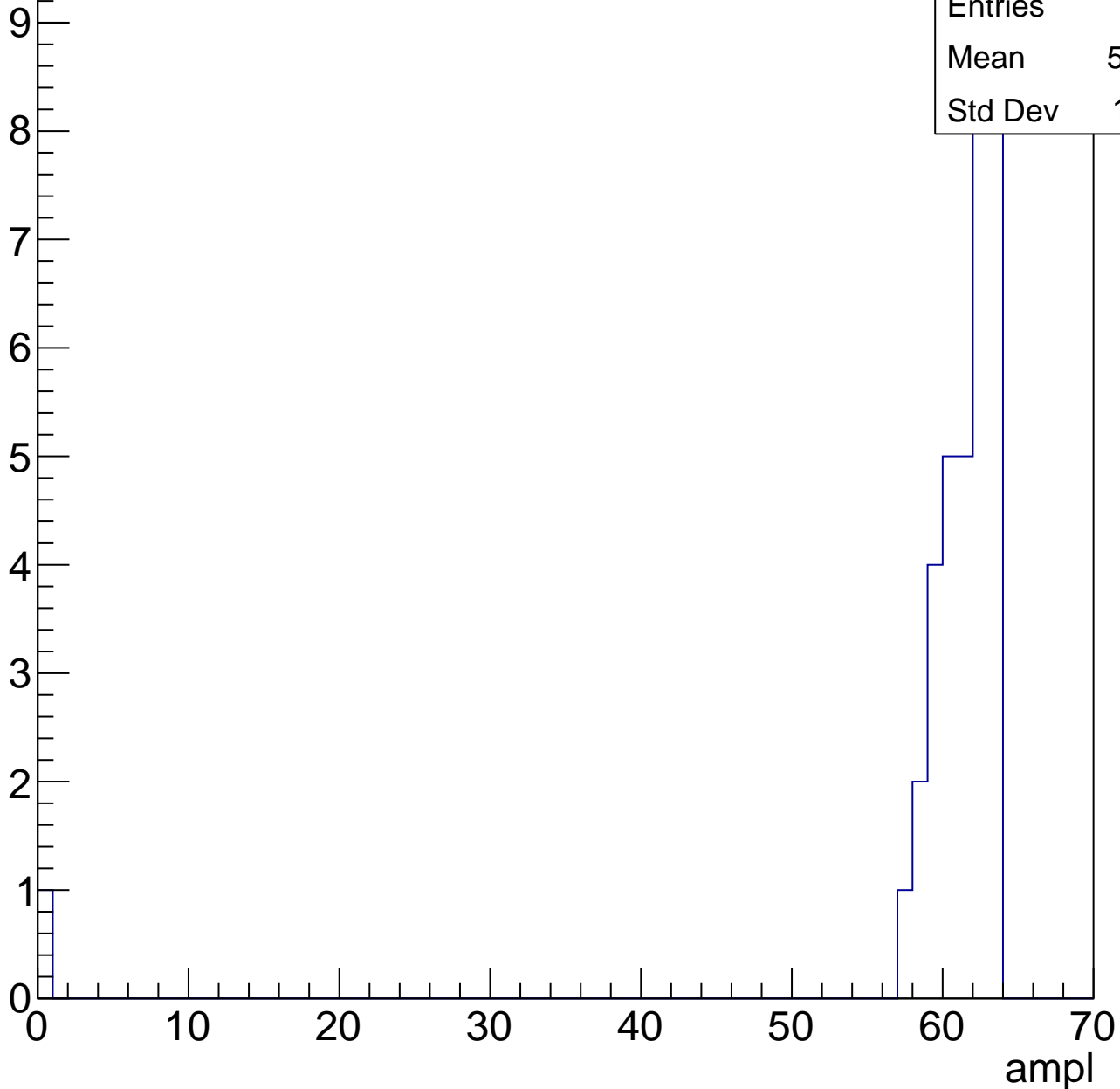


B1L001S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

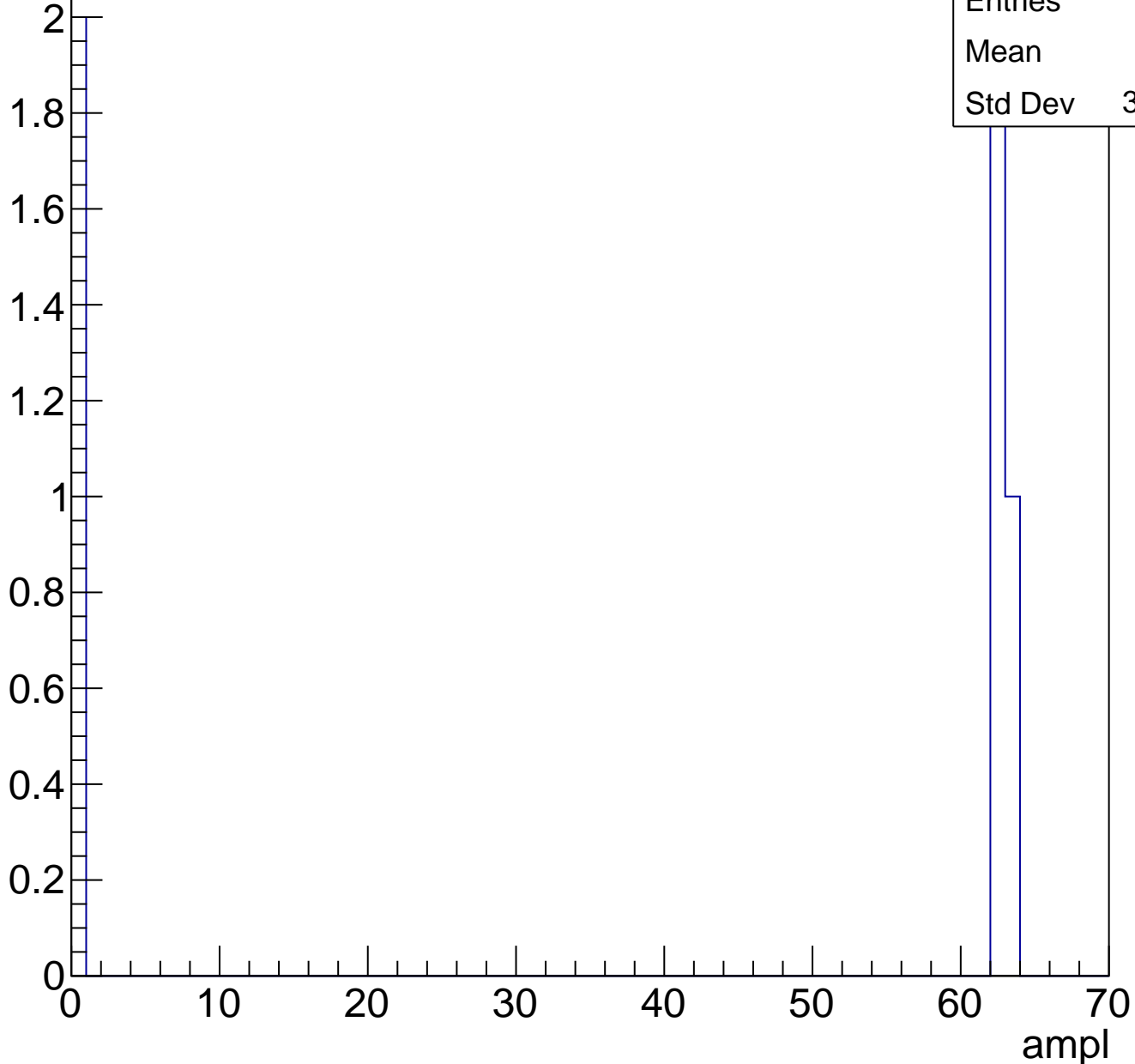
Entries	35
Mean	59.34
Std Dev	10.31



B1L001S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	37.4
Std Dev	30.54

B1L001S, U14-ch11, adc0

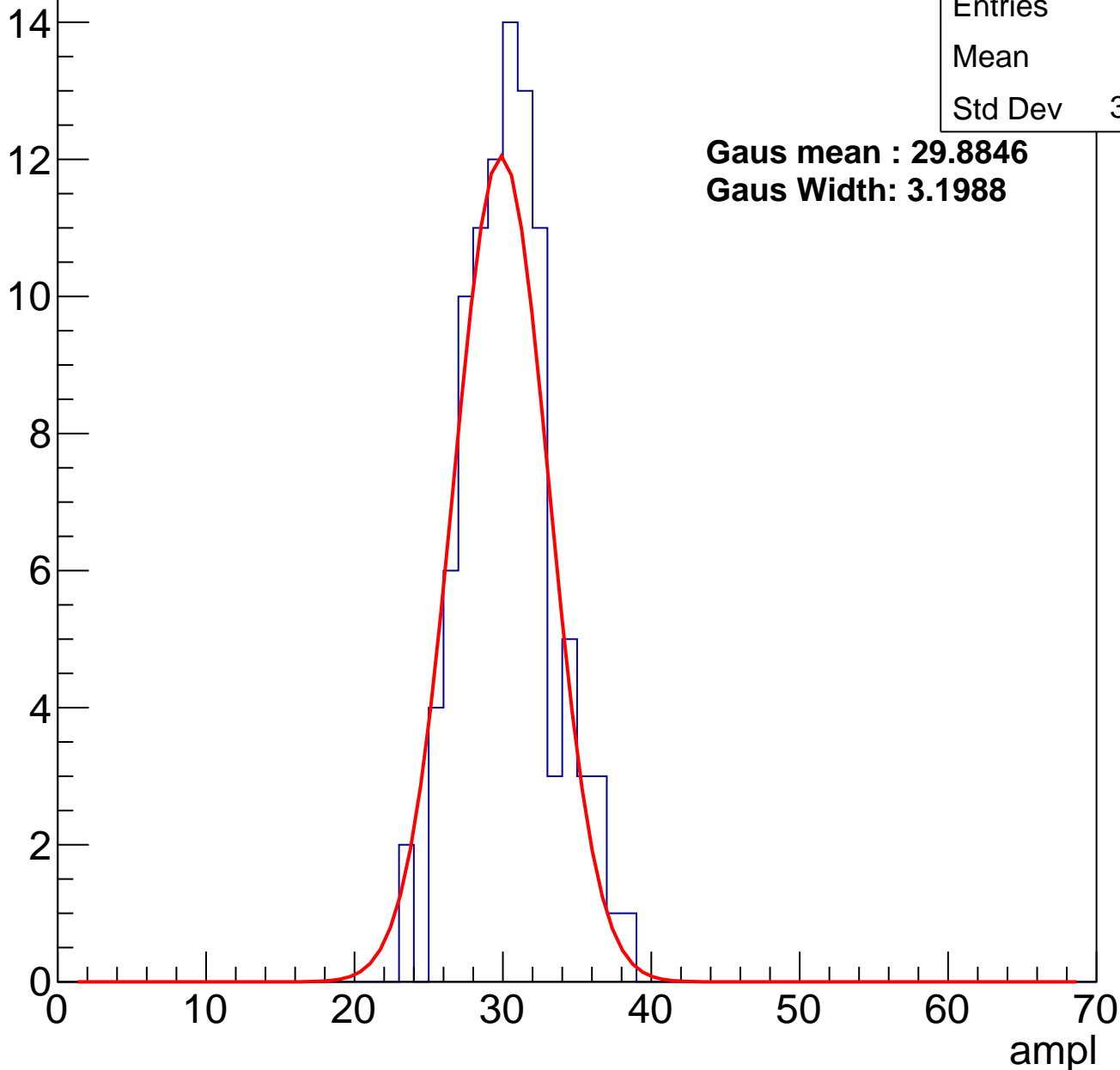
calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	29.9
Std Dev	3.013

Gaus mean : 29.8846

Gaus Width: 3.1988

Entry



B1L001S, U14-ch11, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	36.3
Std Dev	3.547

Gaus mean : 36.7024

Gaus Width: 3.5499

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

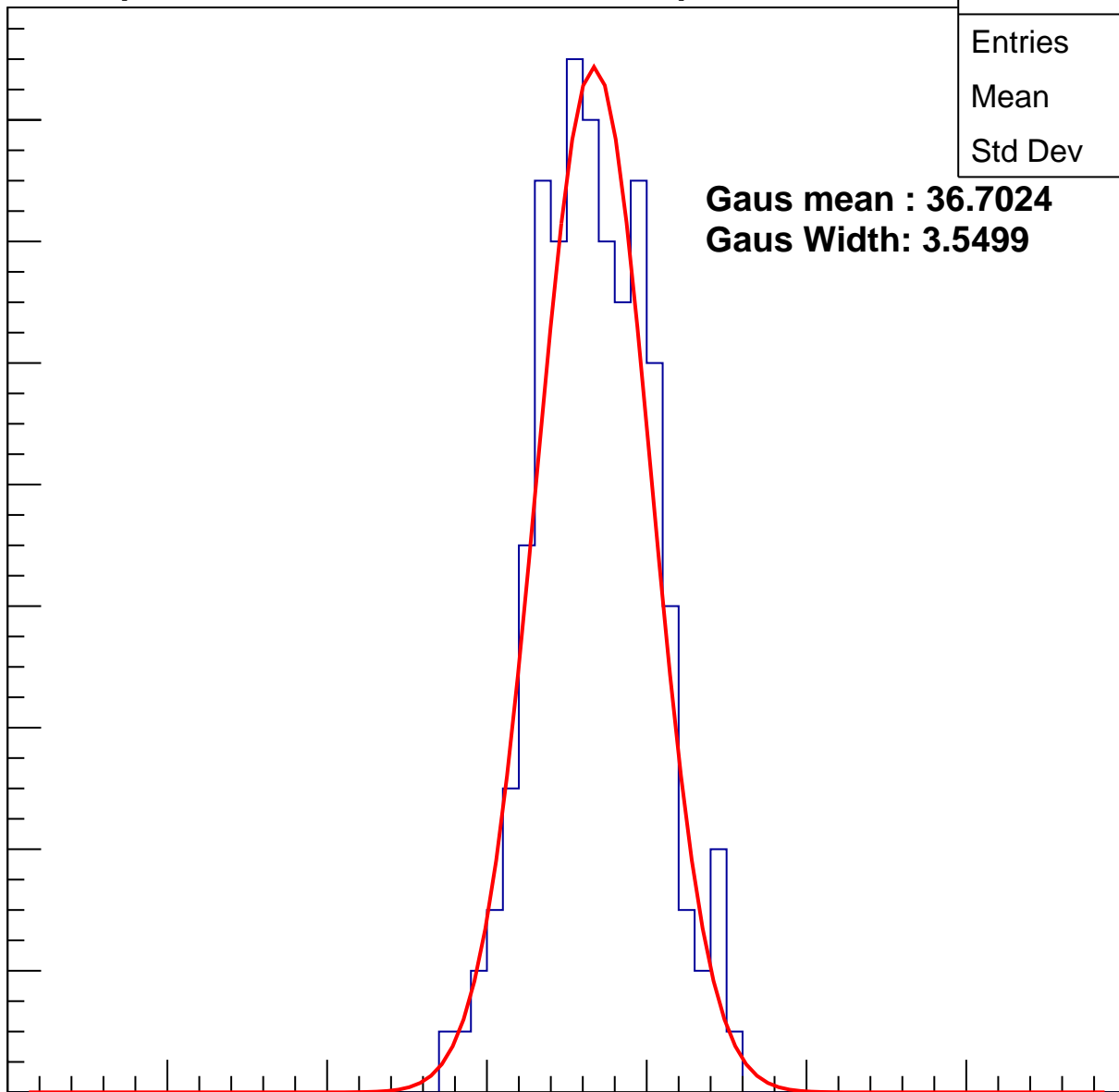
40

50

60

70

ampl



B1L001S, U14-ch11, adc2

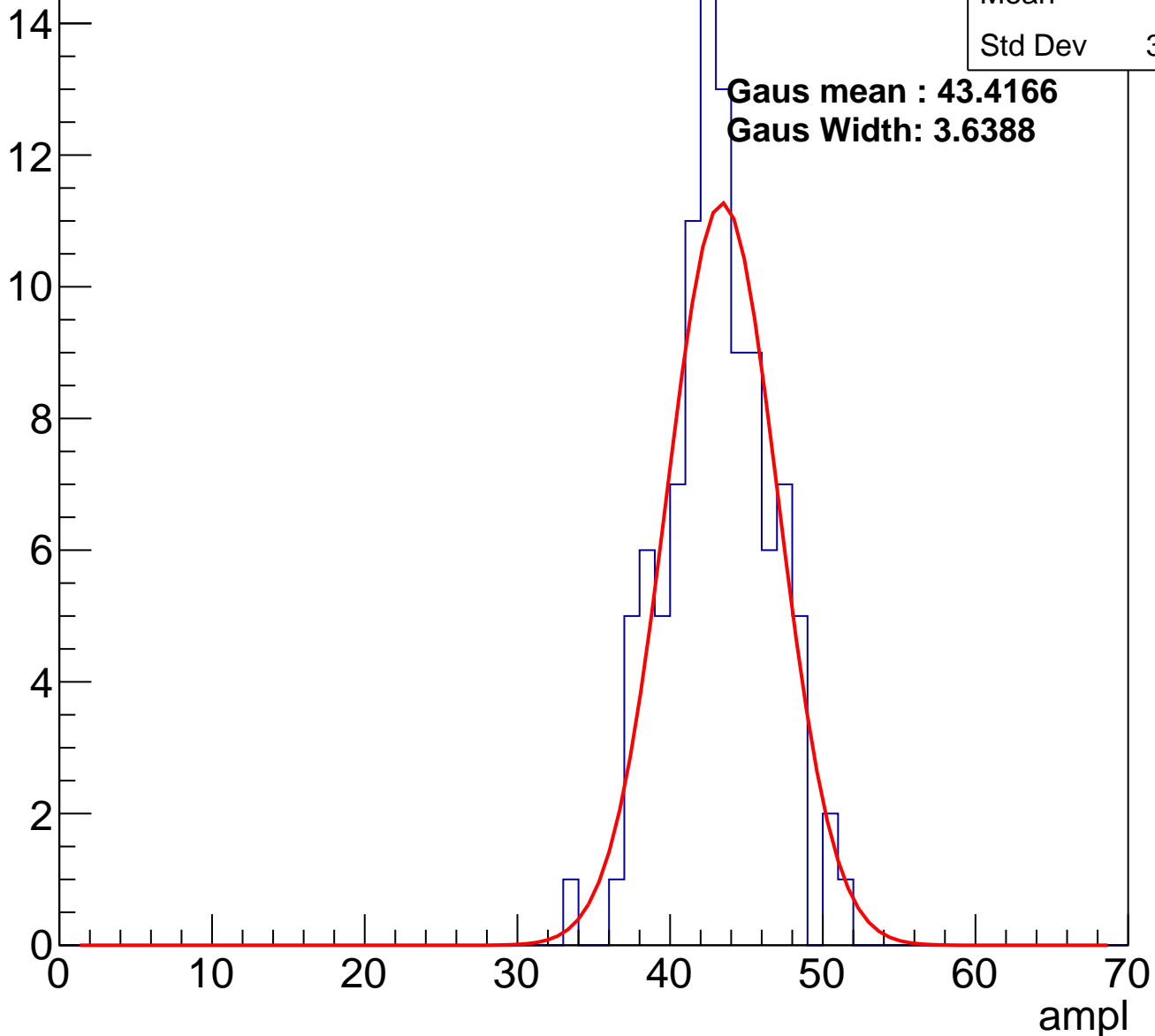
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	42.66
Std Dev	3.366

Gaus mean : 43.4166

Gaus Width: 3.6388

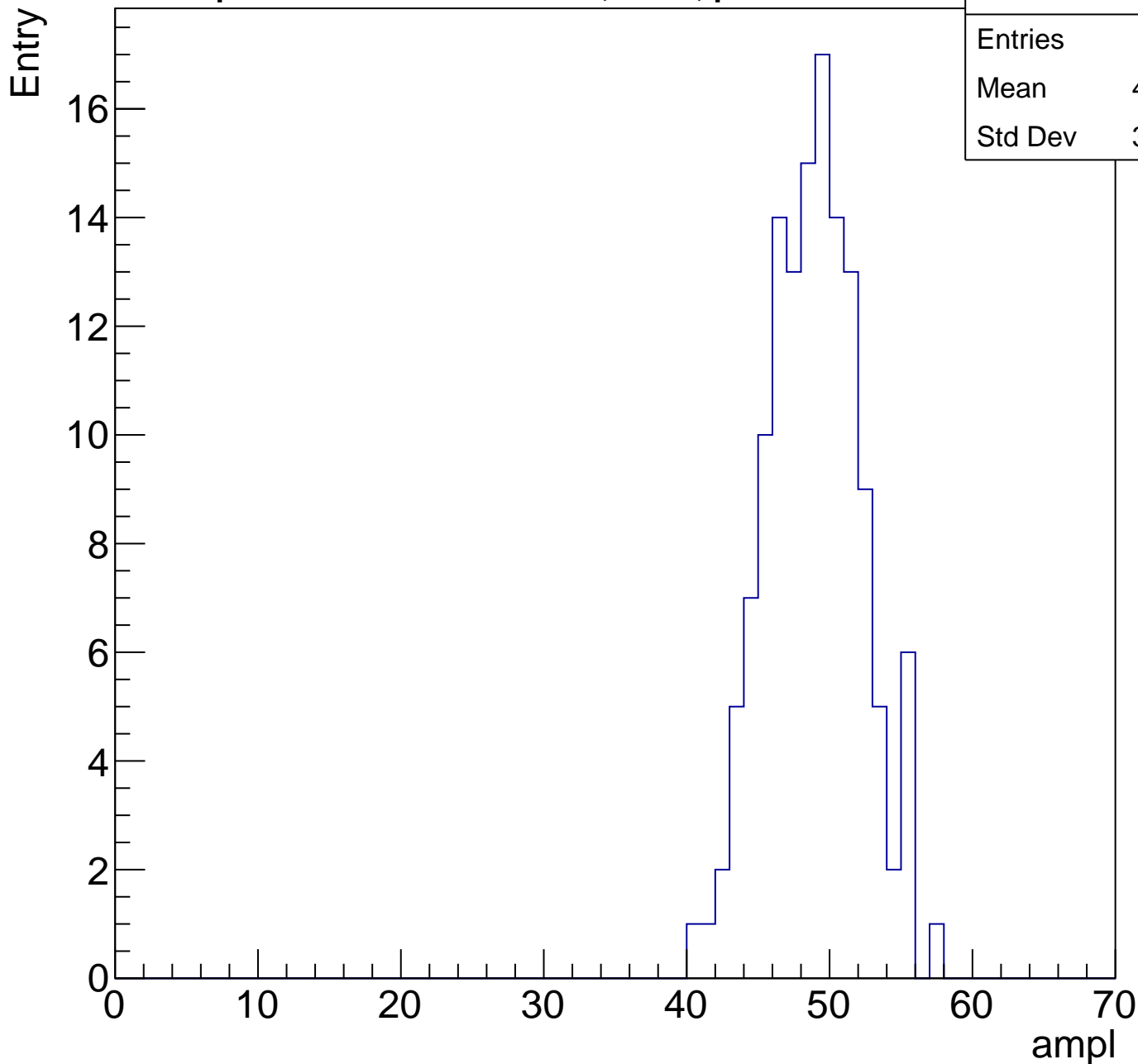
Entry



B1L001S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#2, port C2

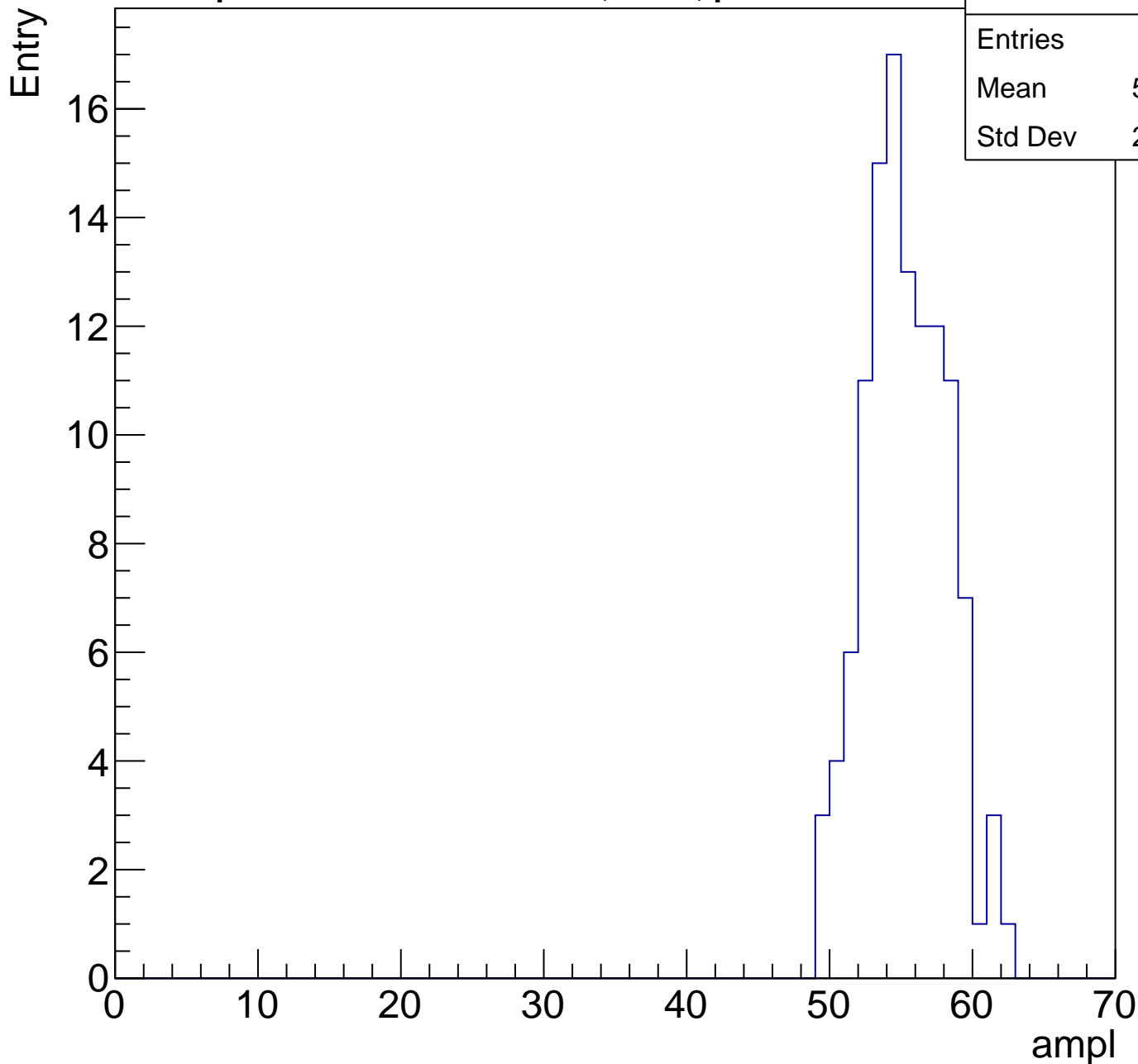
Entries	135
Mean	48.42
Std Dev	3.292



B1L001S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	54.87
Std Dev	2.833



B1L001S, U14-ch11, adc5

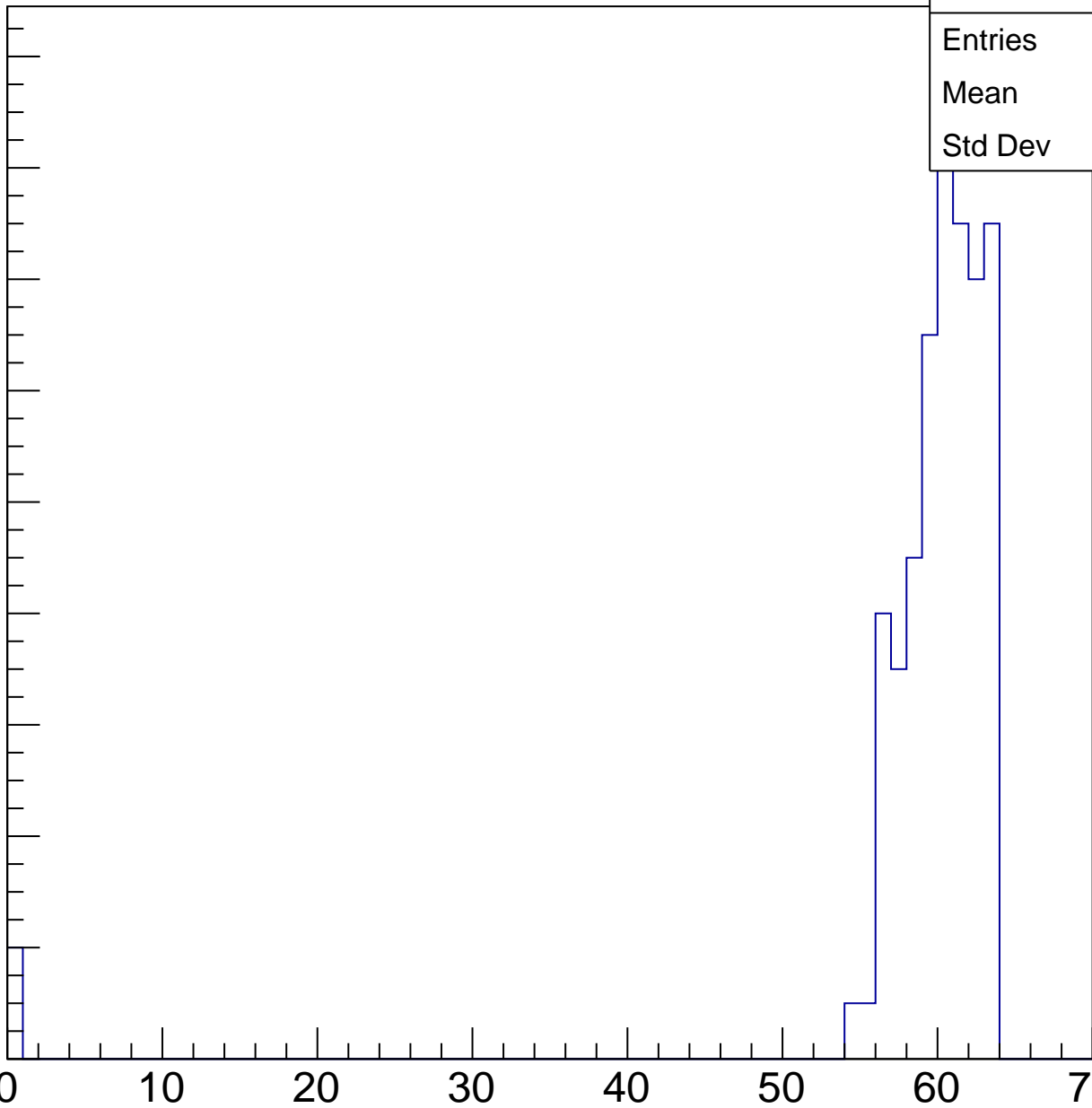
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.77
Std Dev	8.562

ampl

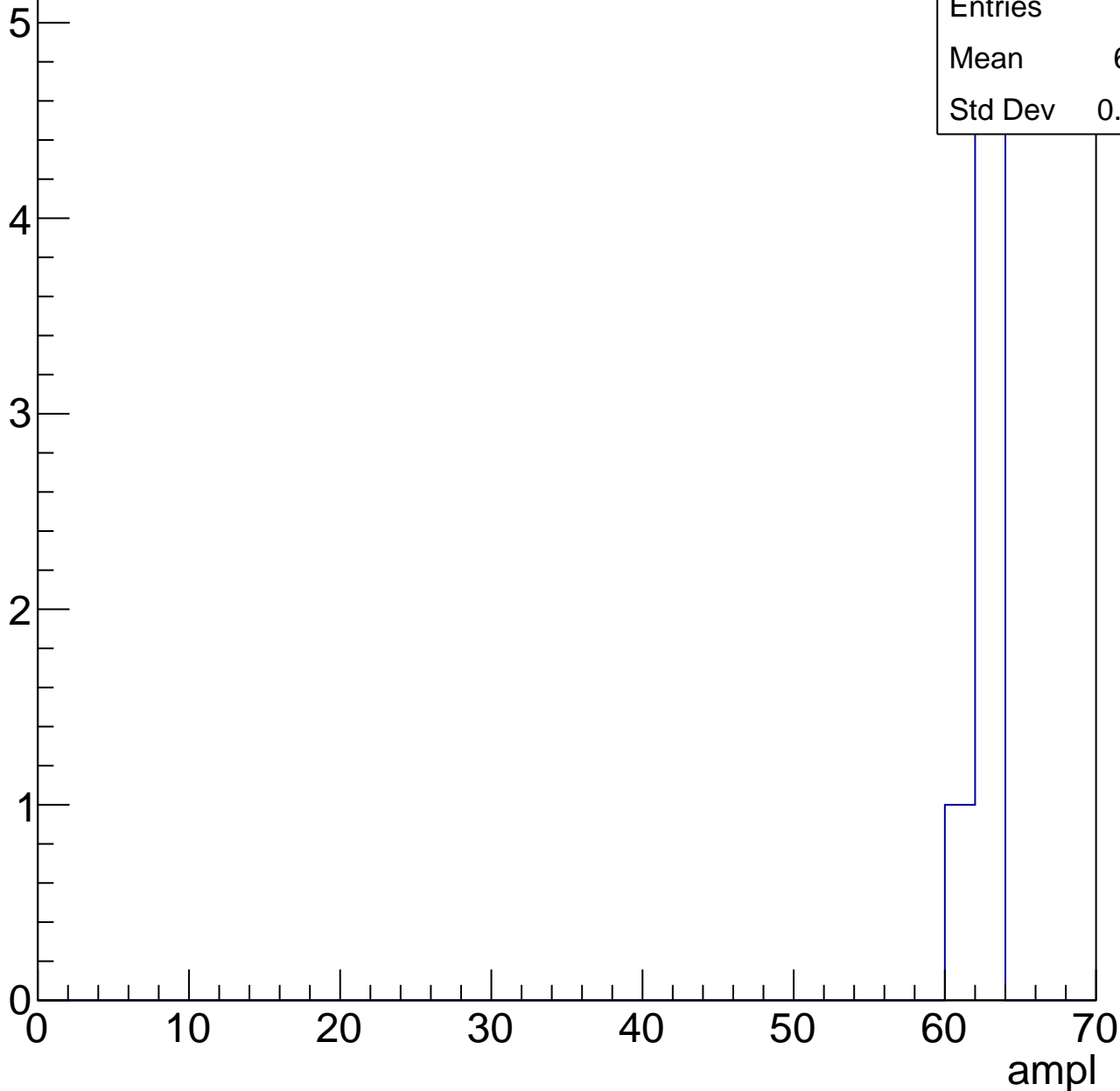


B1L001S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

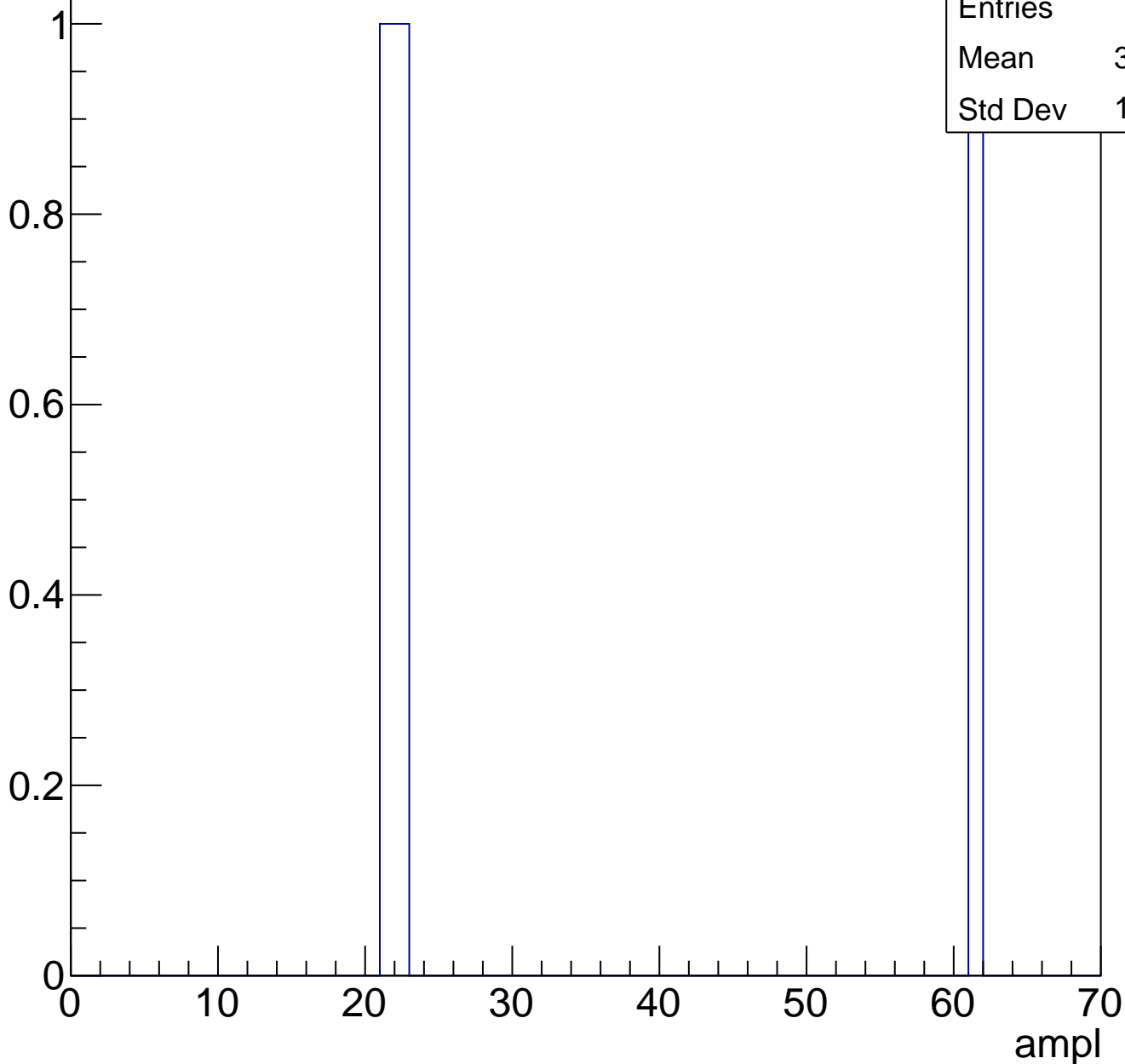
Entries	12
Mean	62.17
Std Dev	0.8975



B1L001S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



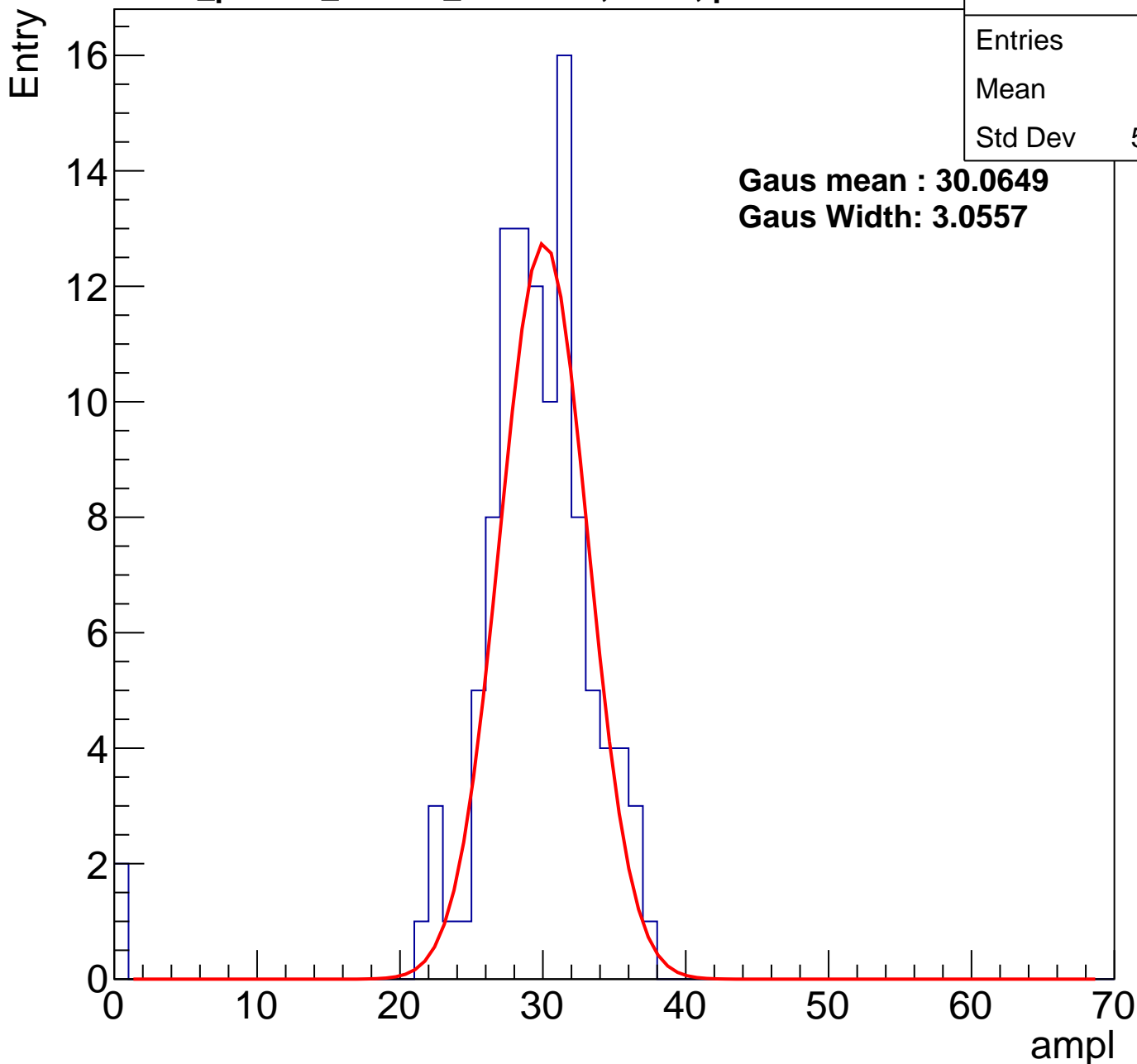
B1L001S, U14-ch12, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	28.8
Std Dev	5.088

Gaus mean : 30.0649

Gaus Width: 3.0557



B1L001S, U14-ch12, adc1

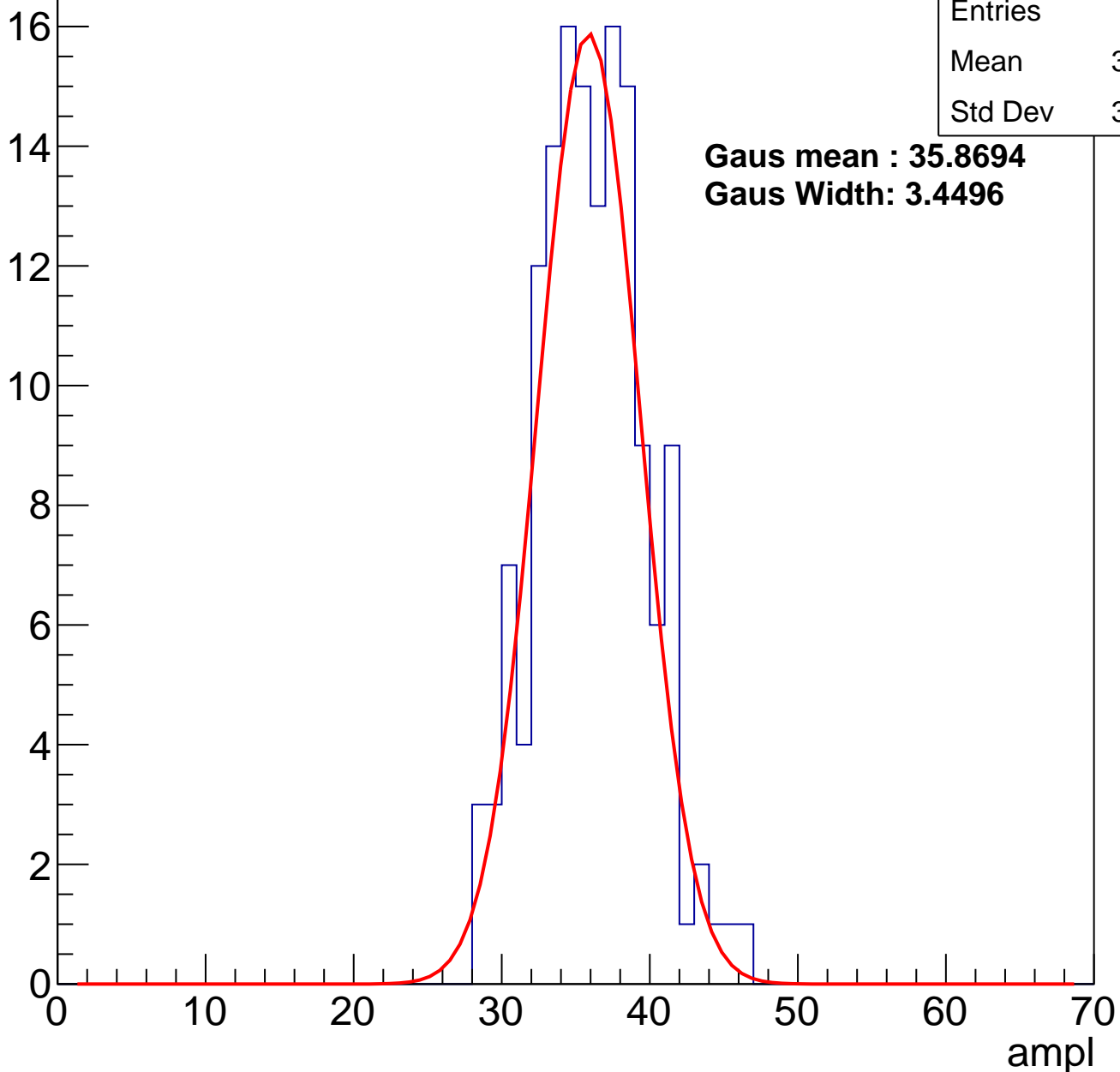
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.63
Std Dev	3.606

Gaus mean : 35.8694

Gaus Width: 3.4496

Entry



B1L001S, U14-ch12, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

107

Mean

42

Std Dev

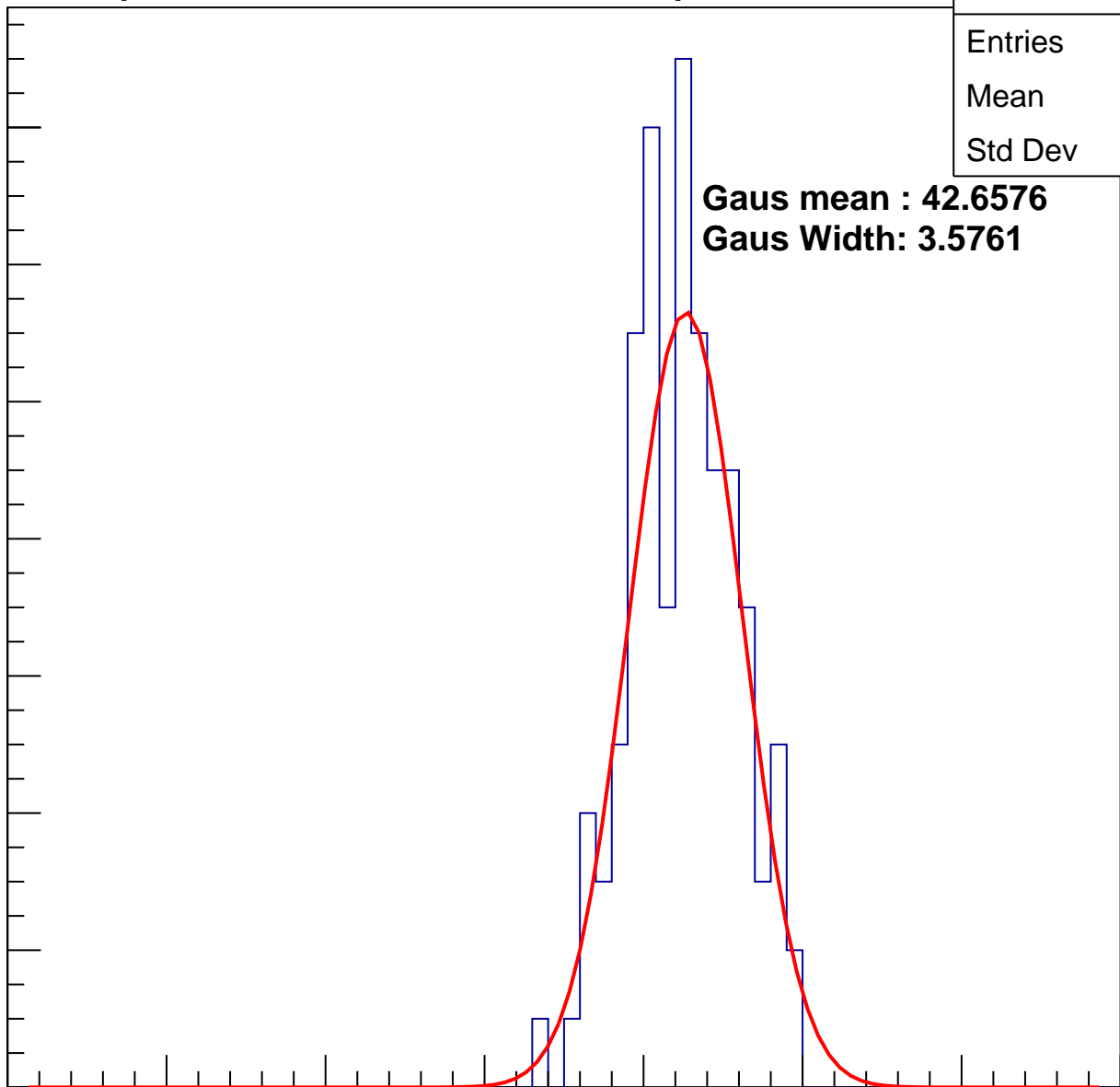
3.326

Gaus mean : 42.6576

Gaus Width: 3.5761

0 10 20 30 40 50 60 70

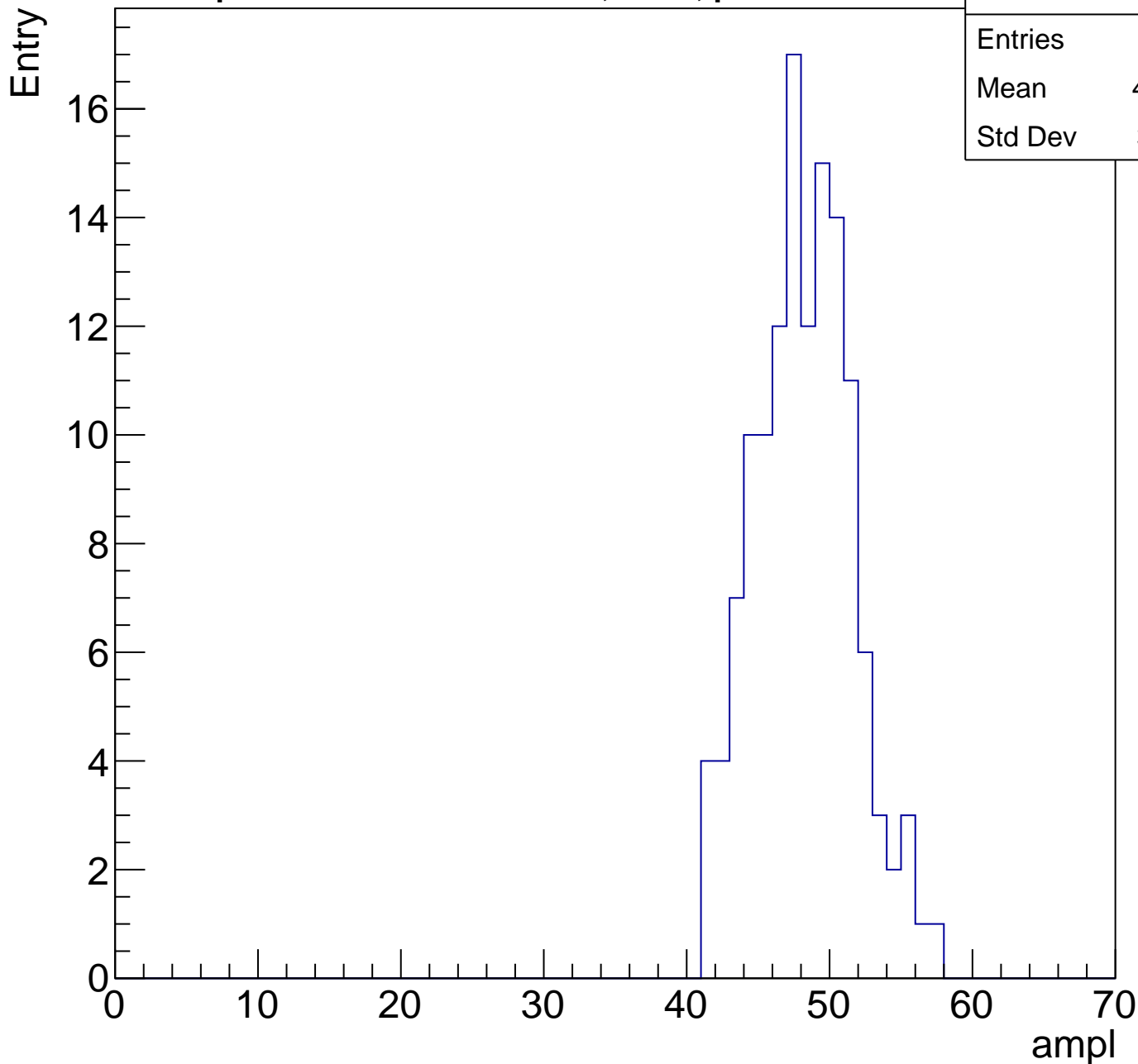
ampl



B1L001S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#2, port C2

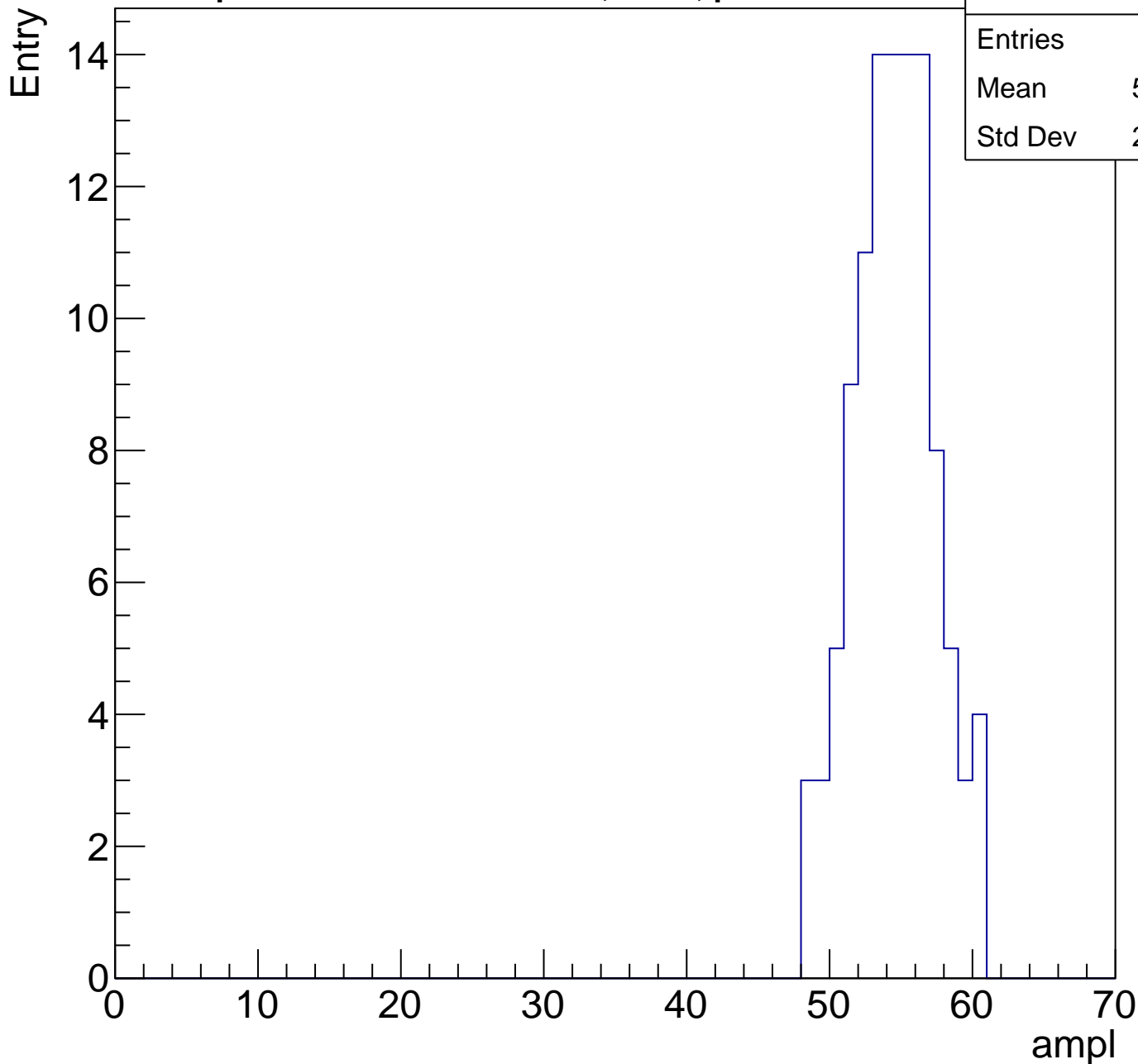
Entries	132
Mean	47.75
Std Dev	3.401



B1L001S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	54.08
Std Dev	2.806



B1L001S, U14-ch12, adc5

calib_packv5_042523_0143.root, FC#2, port C2

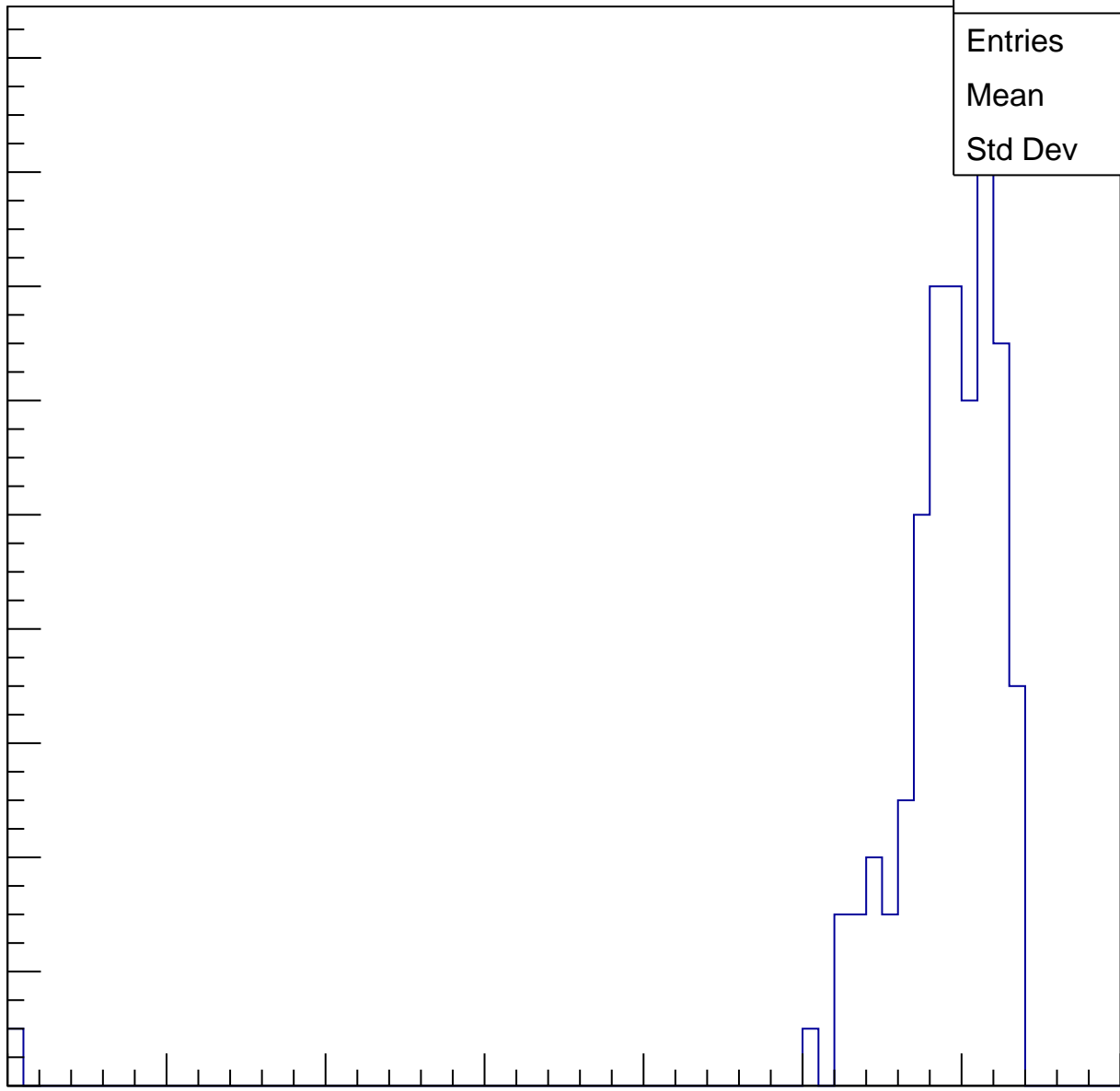
Entries	108
Mean	58.32
Std Dev	6.33

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	31
Mean	59.52
Std Dev	11

B1L001S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L001S, U14-ch13, adc0

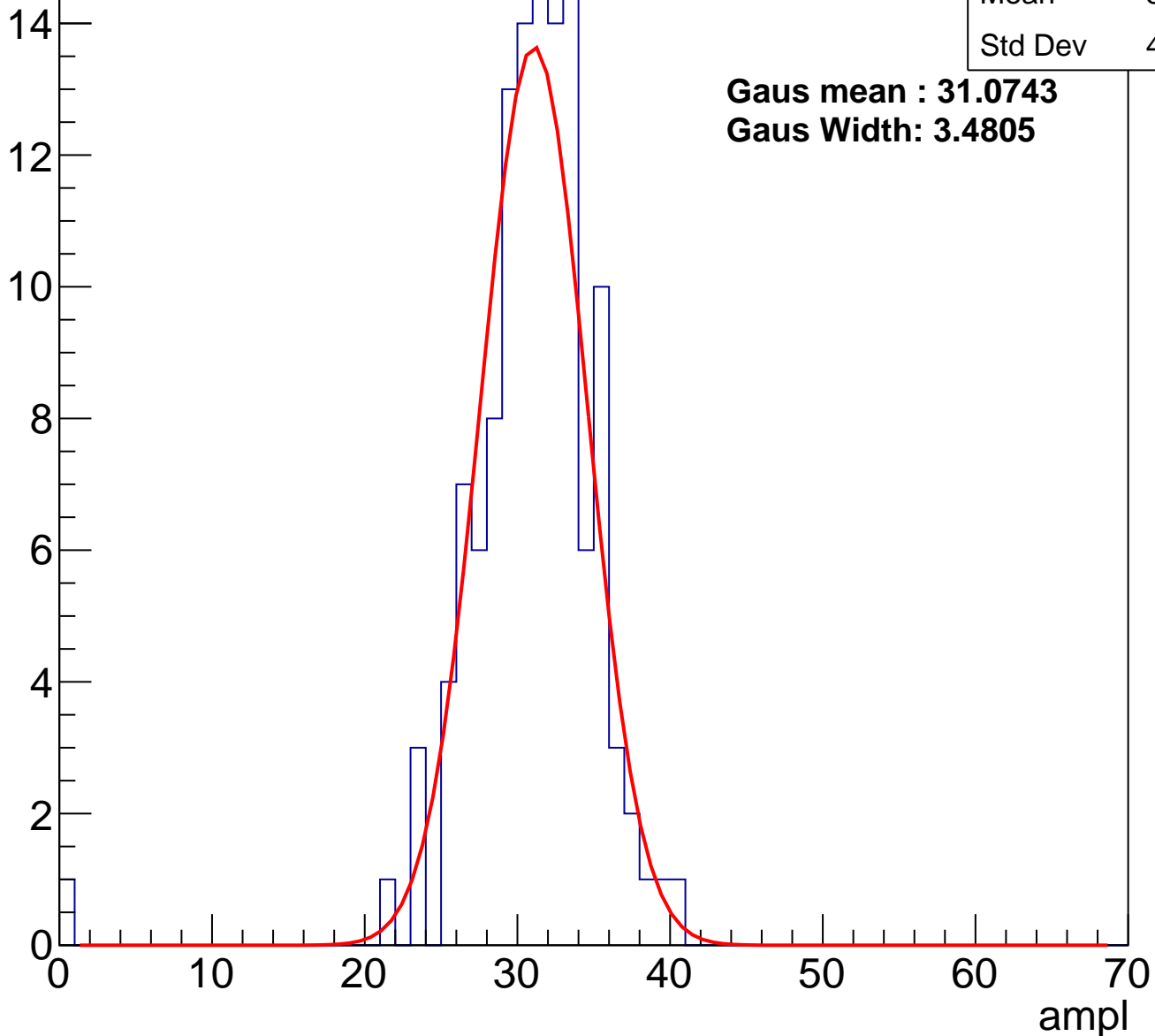
calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	30.53
Std Dev	4.388

Gaus mean : 31.0743

Gaus Width: 3.4805

Entry

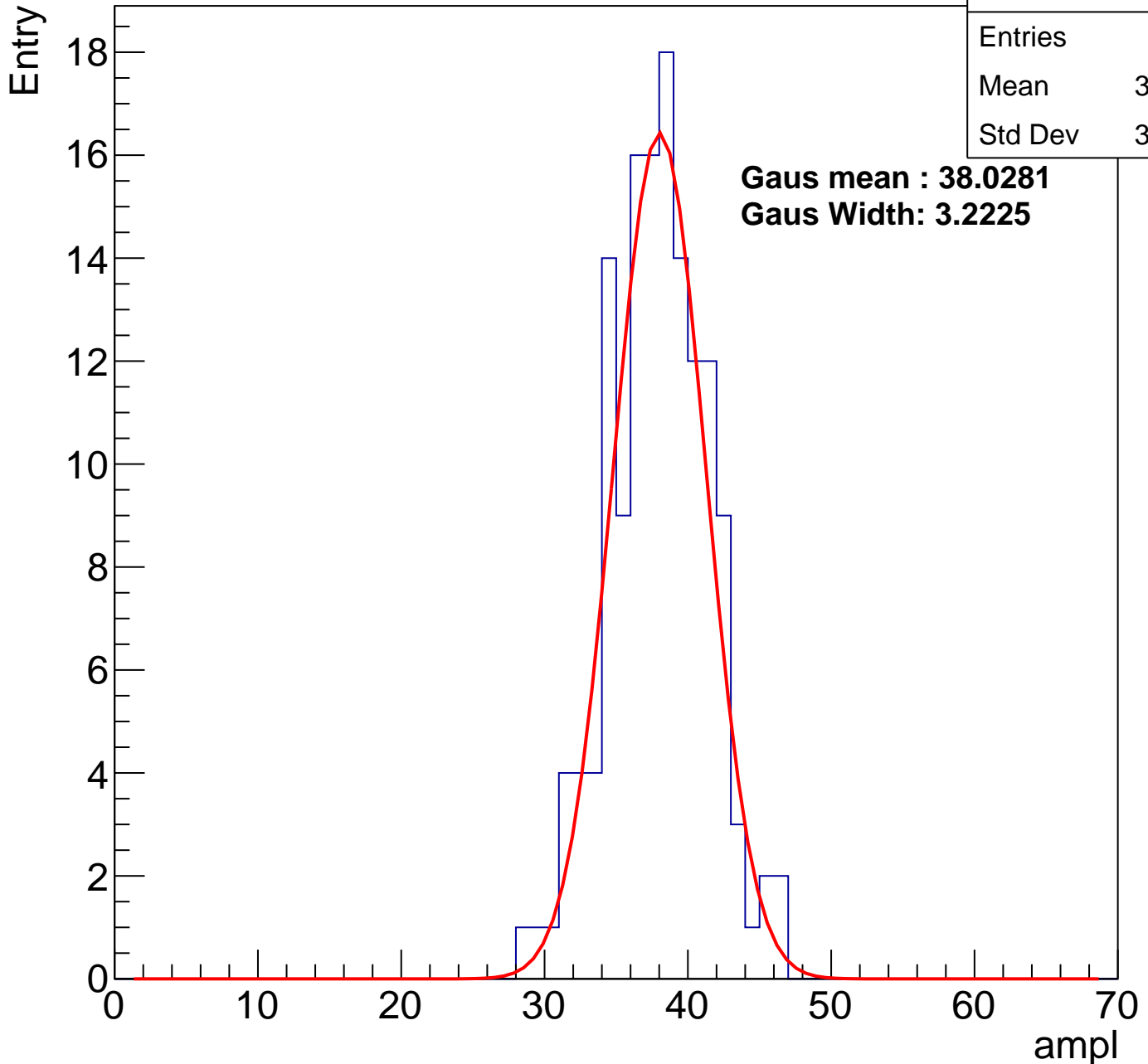


B1L001S, U14-ch13, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	37.52
Std Dev	3.435

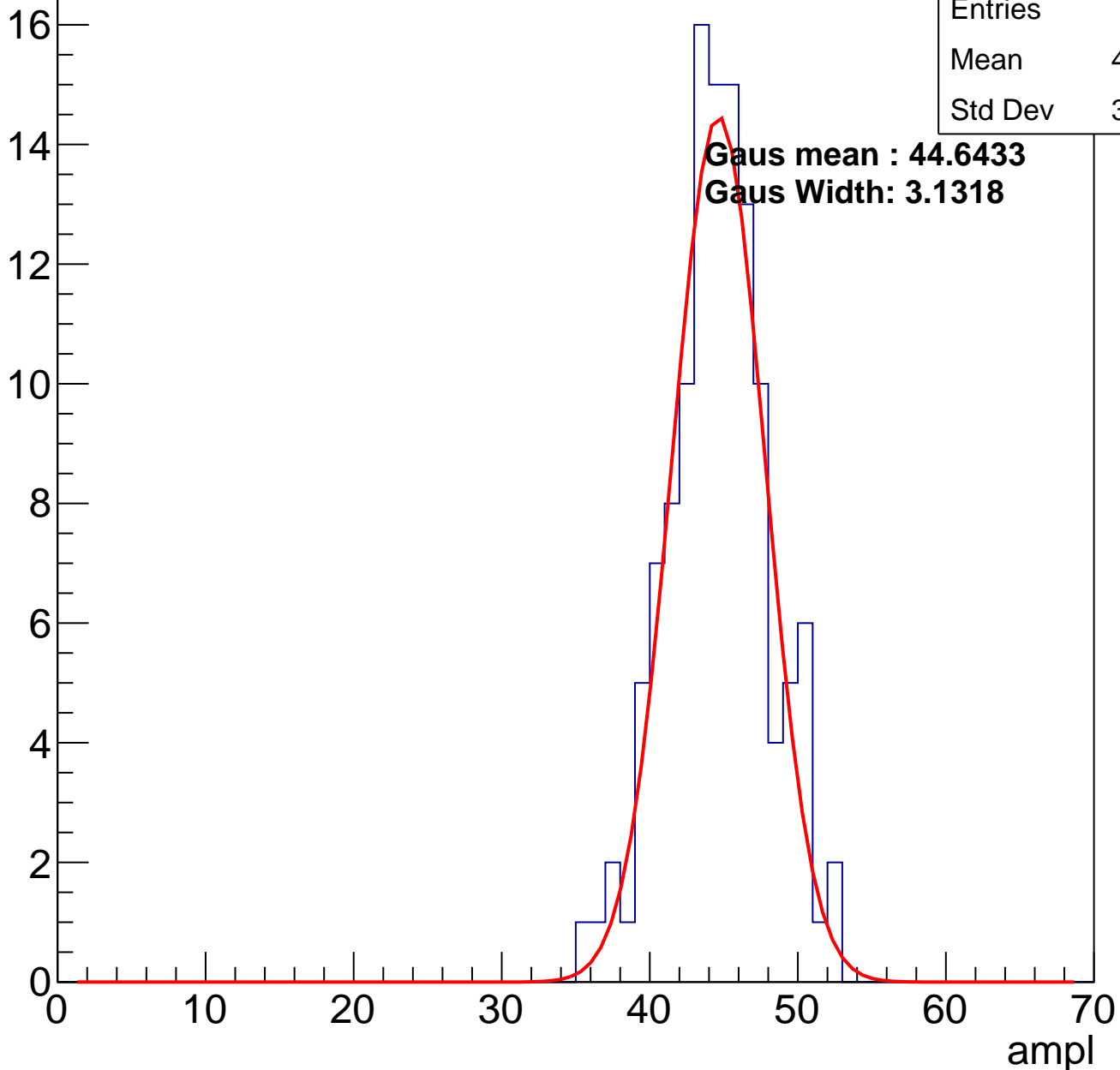
Gaus mean : 38.0281
Gaus Width: 3.2225



B1L001S, U14-ch13, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#2, port C2

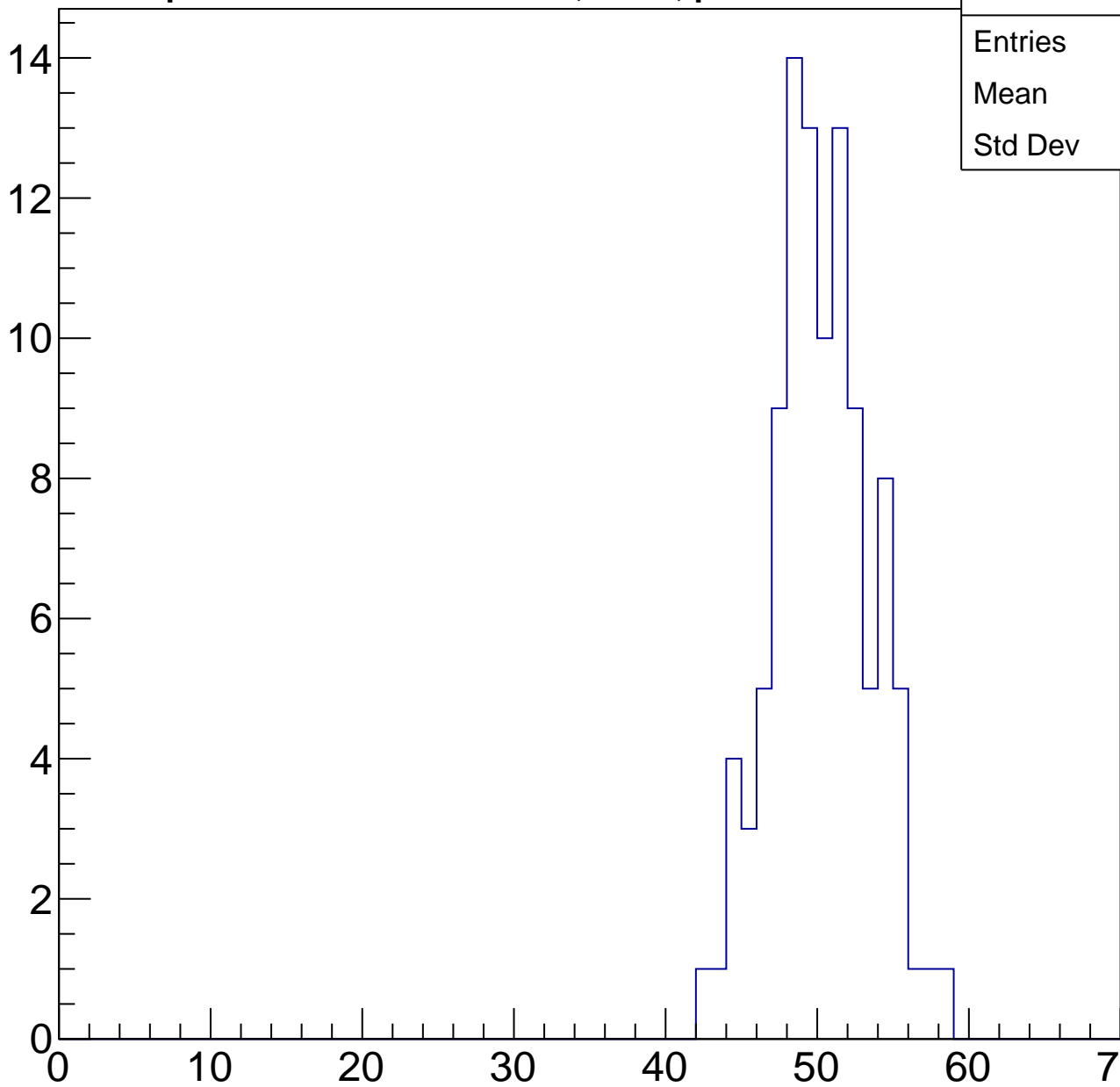
Entries	103
Mean	49.83
Std Dev	3.2

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

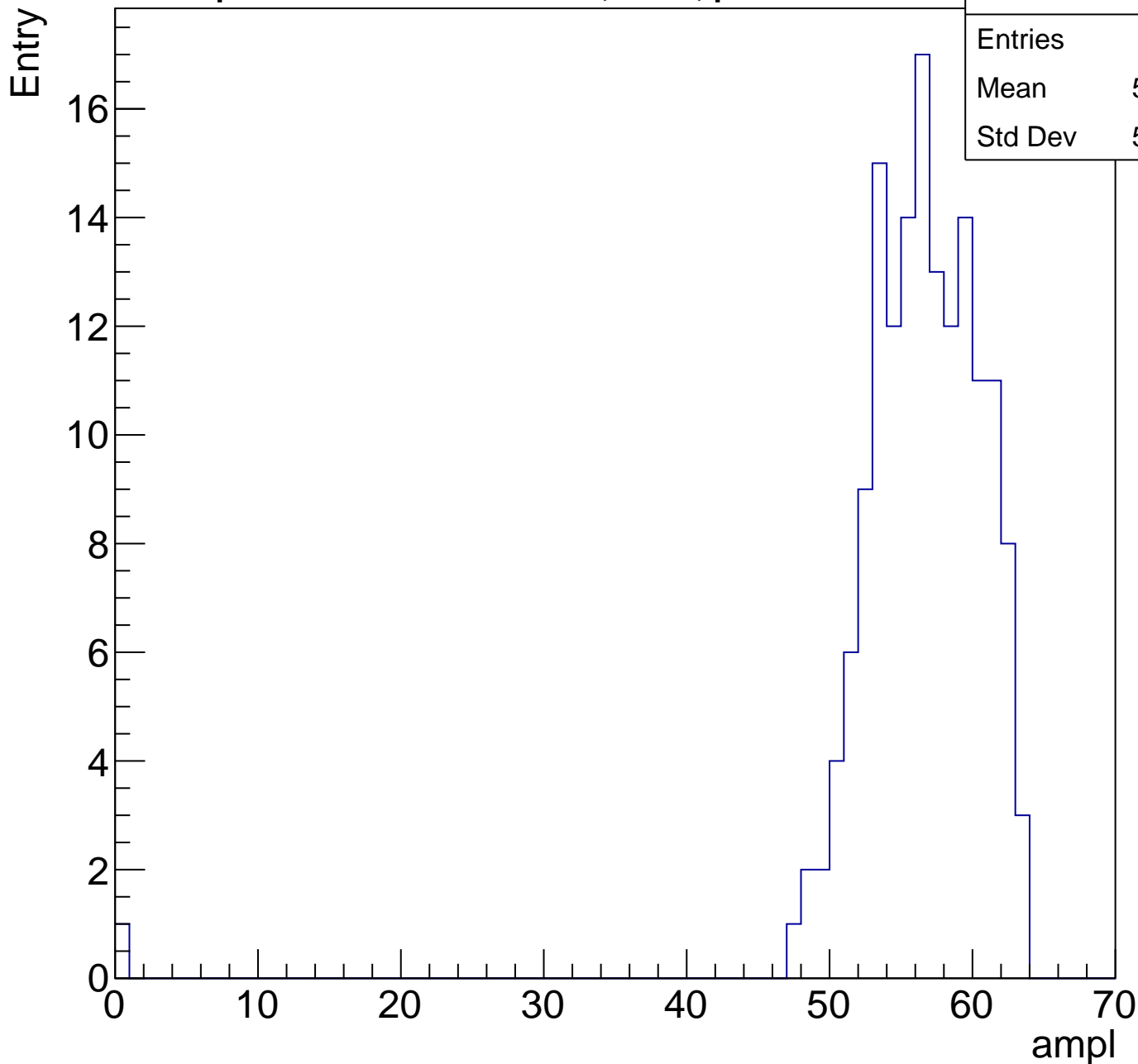
ampl



B1L001S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	55.86
Std Dev	5.763



B1L001S, U14-ch13, adc5

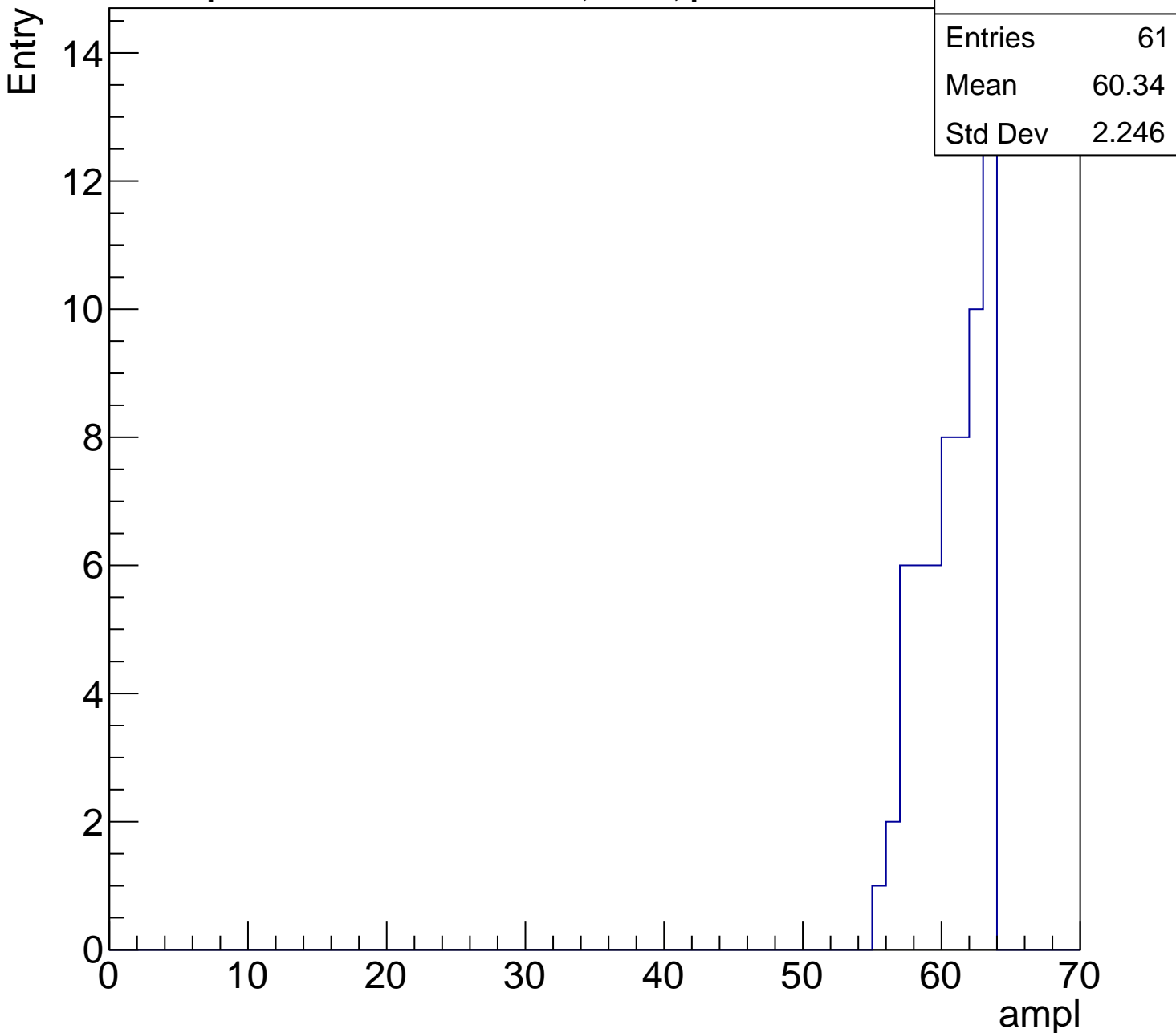
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	61
Mean	60.34
Std Dev	2.246

ampl



B1L001S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

3

2.5

2

1.5

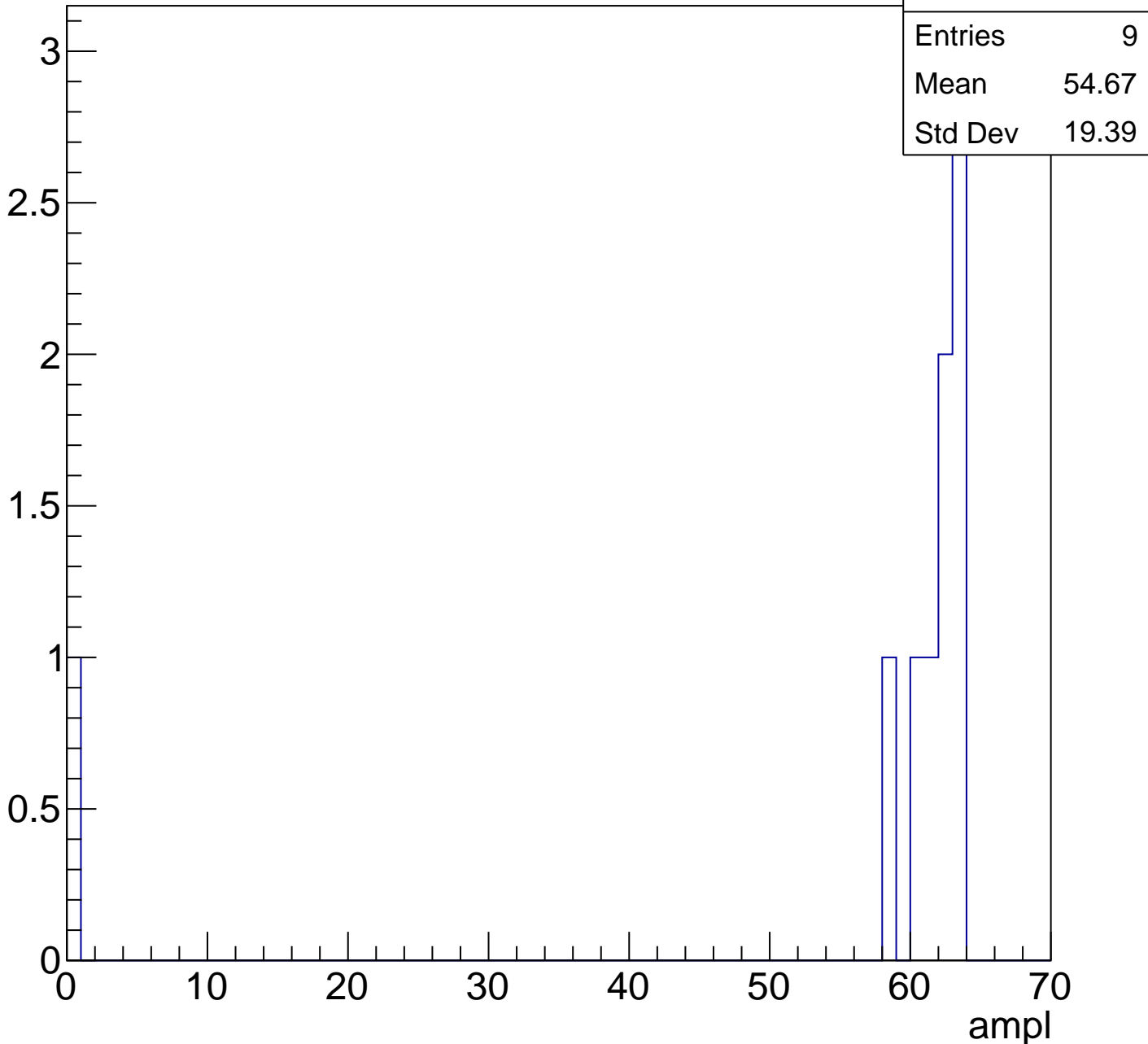
1

0.5

0

ampl

Entries	9
Mean	54.67
Std Dev	19.39



B1L001S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U14-ch14, adc0

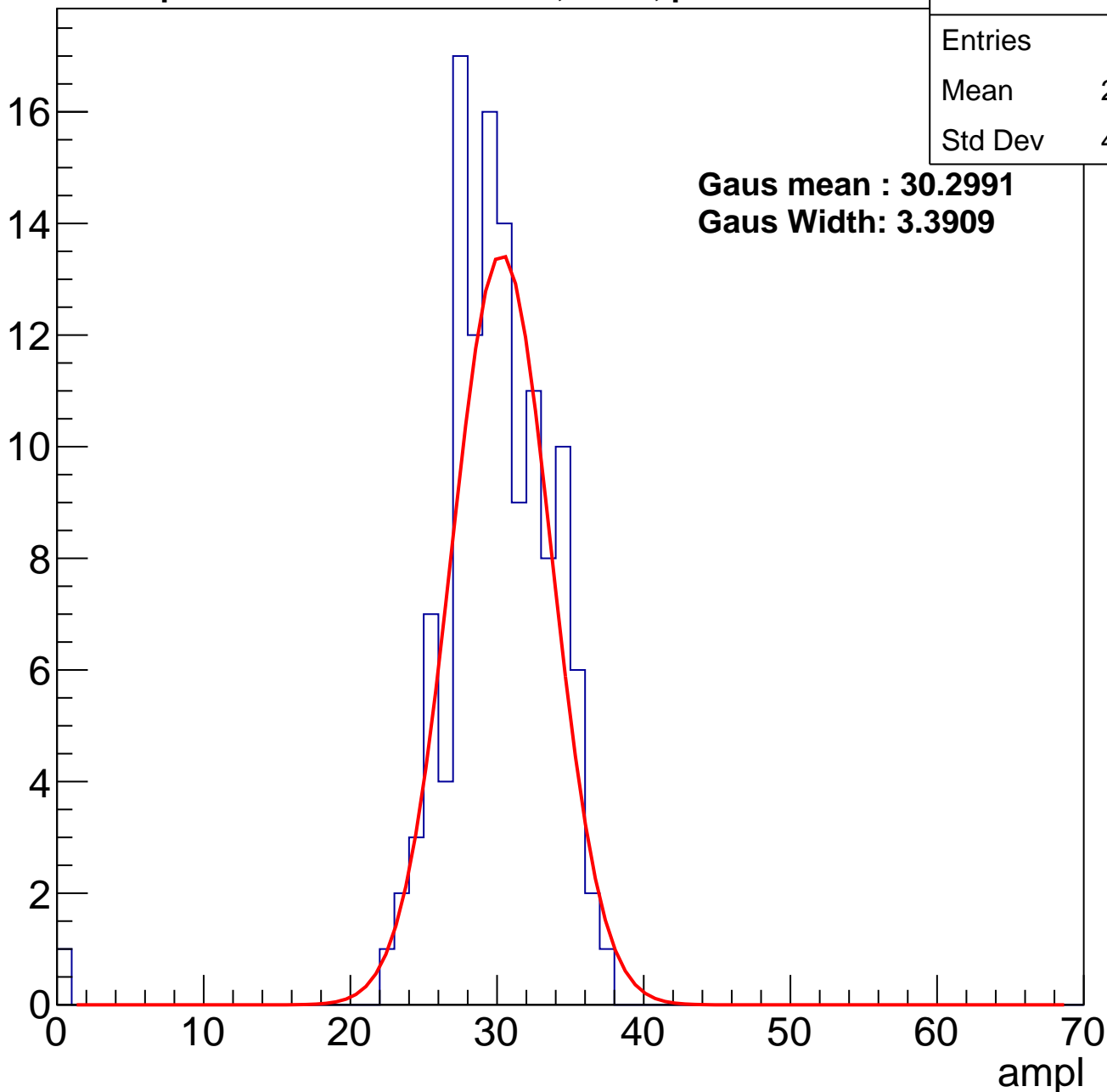
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.45
Std Dev	4.159

Gaus mean : 30.2991

Gaus Width: 3.3909

Entry



B1L001S, U14-ch14, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	35.3
Std Dev	3.318

Gaus mean : 35.6860

Gaus Width: 3.0702

Entry

12

10

8

6

4

2

0

0

10

20

30

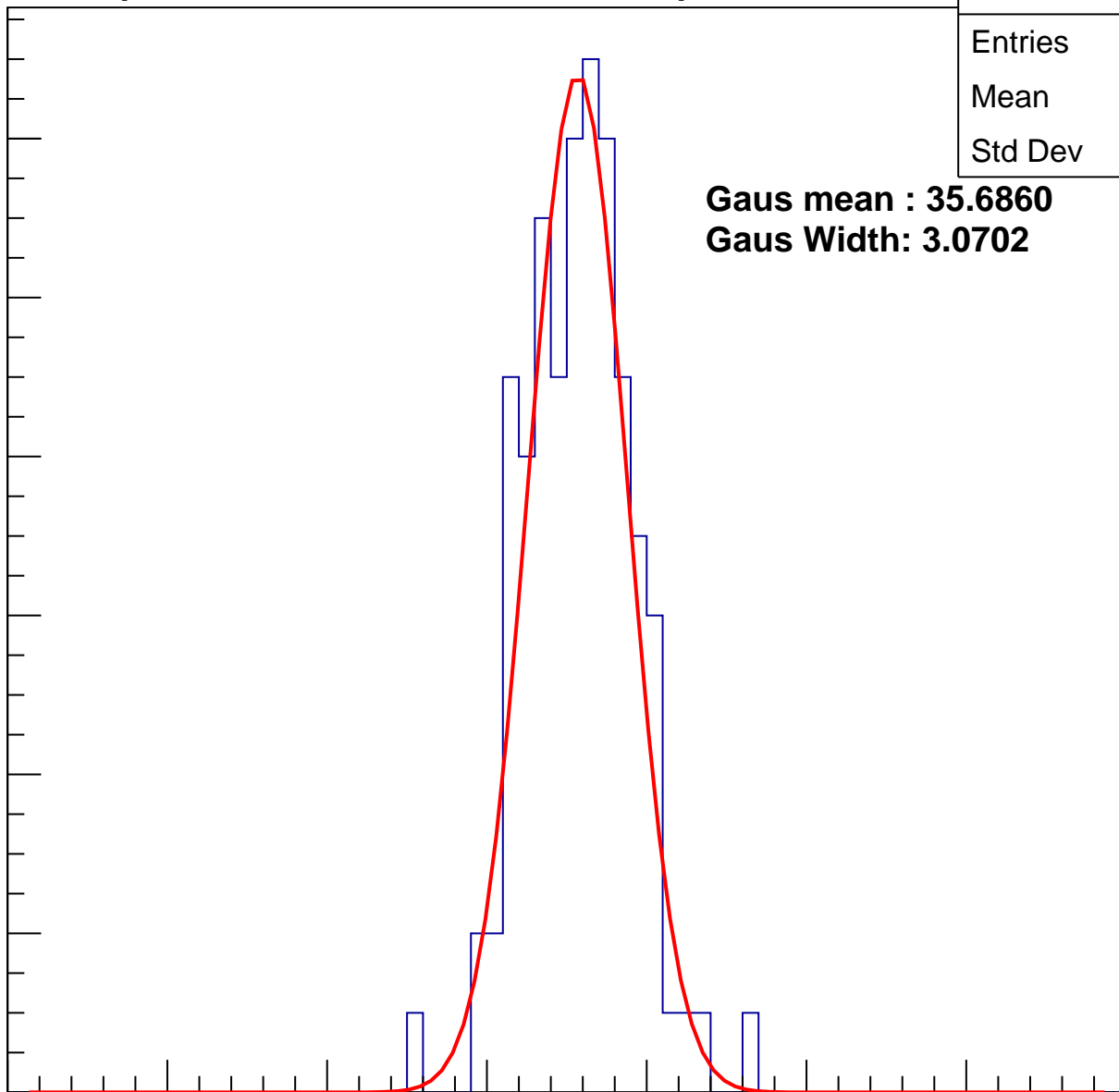
40

50

60

70

ampl



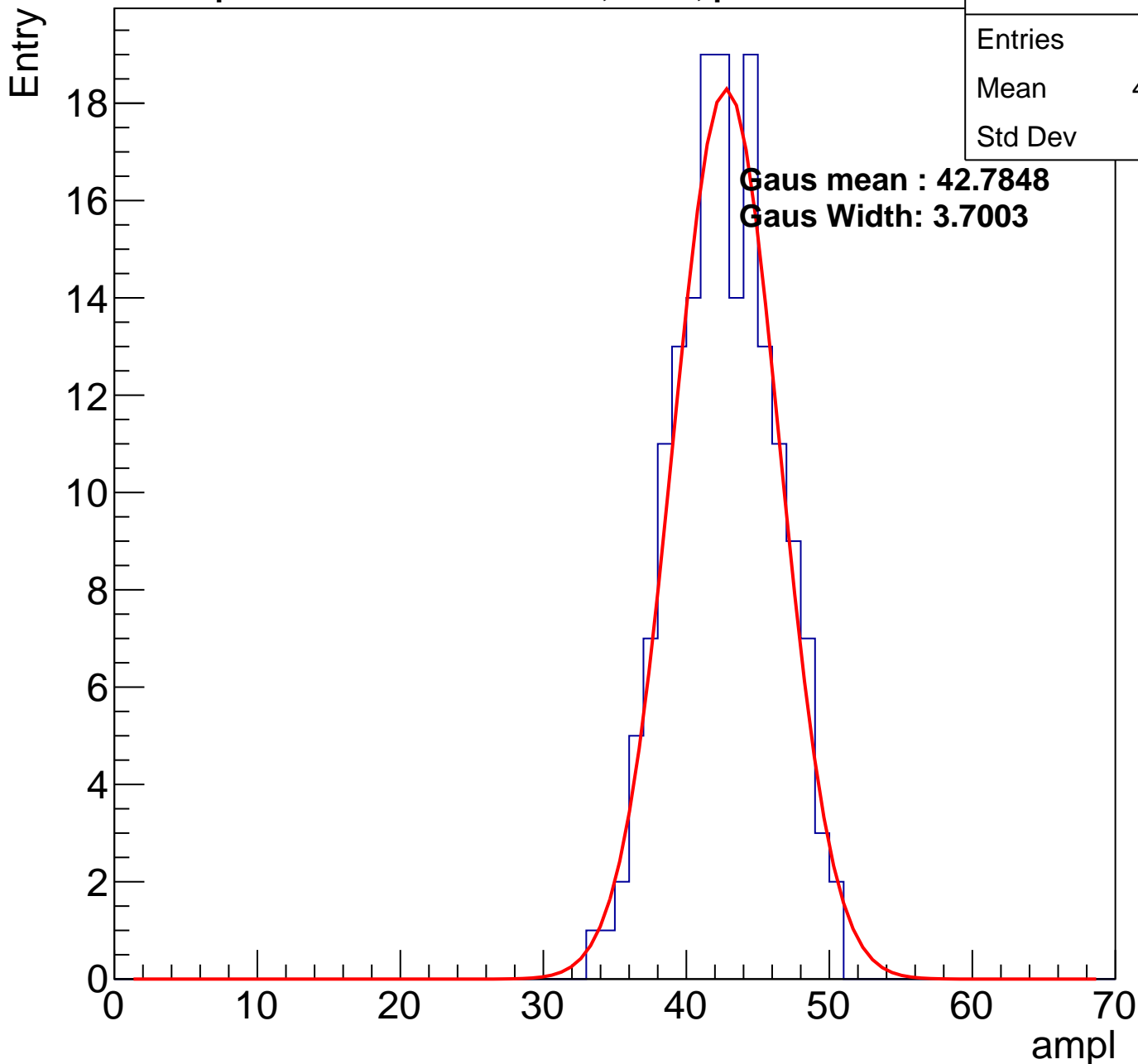
B1L001S, U14-ch14, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	170
Mean	42.19
Std Dev	3.51

Gaus mean : 42.7848

Gaus Width: 3.7003



B1L001S, U14-ch14, adc3

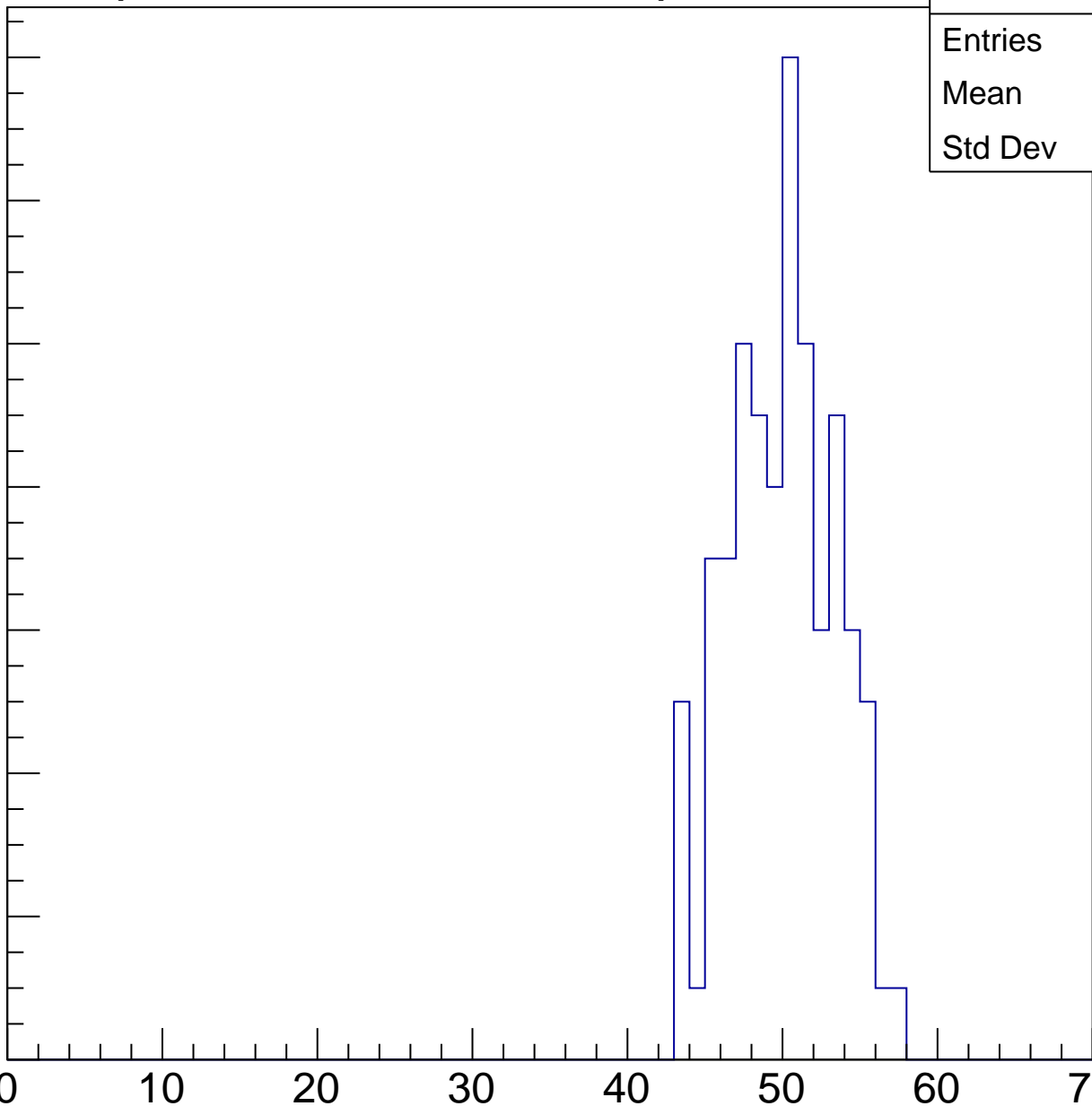
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	99
Mean	49.51
Std Dev	3.331

ampl

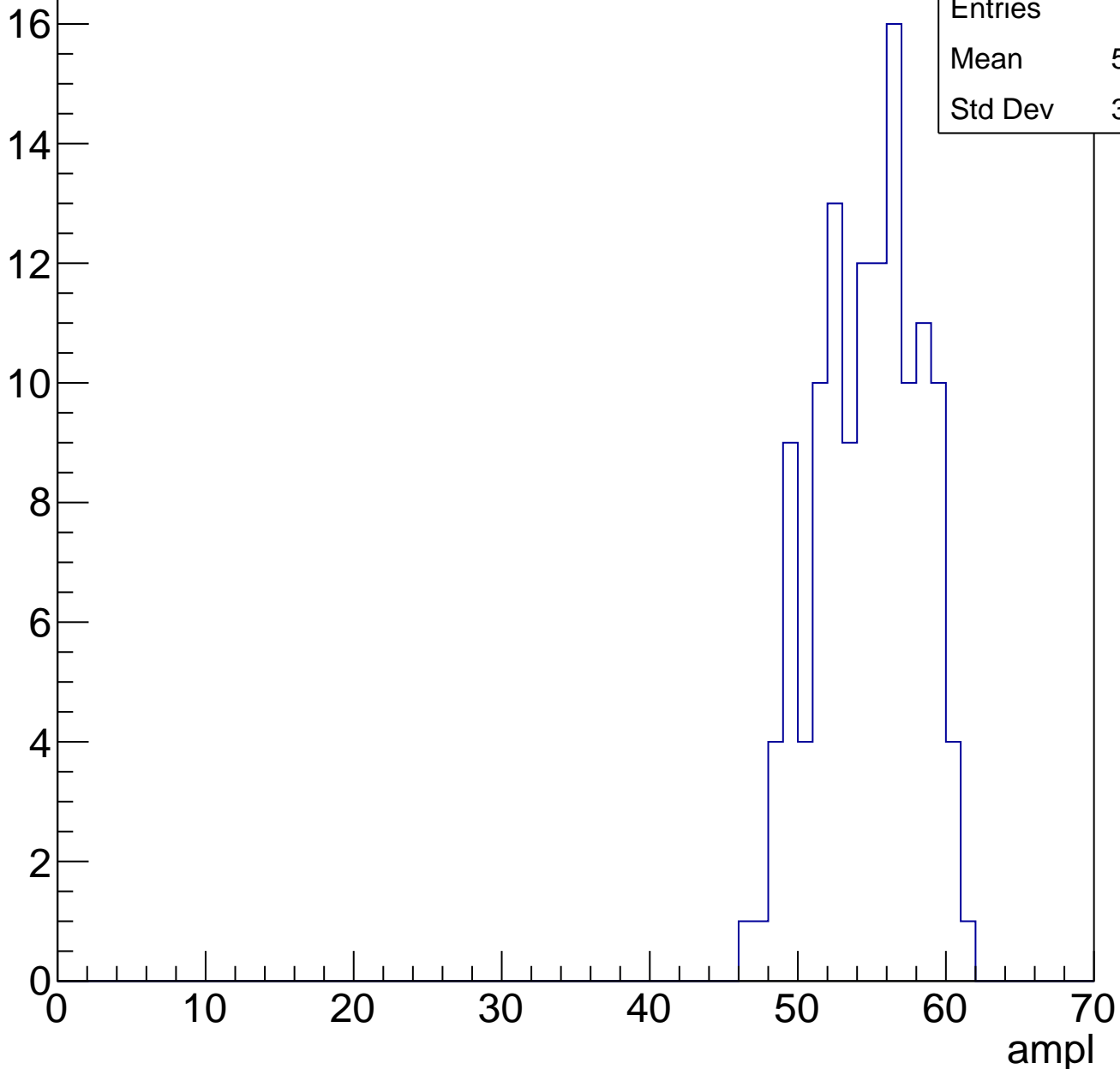


B1L001S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	54.27
Std Dev	3.406

Entry



B1L001S, U14-ch14, adc5

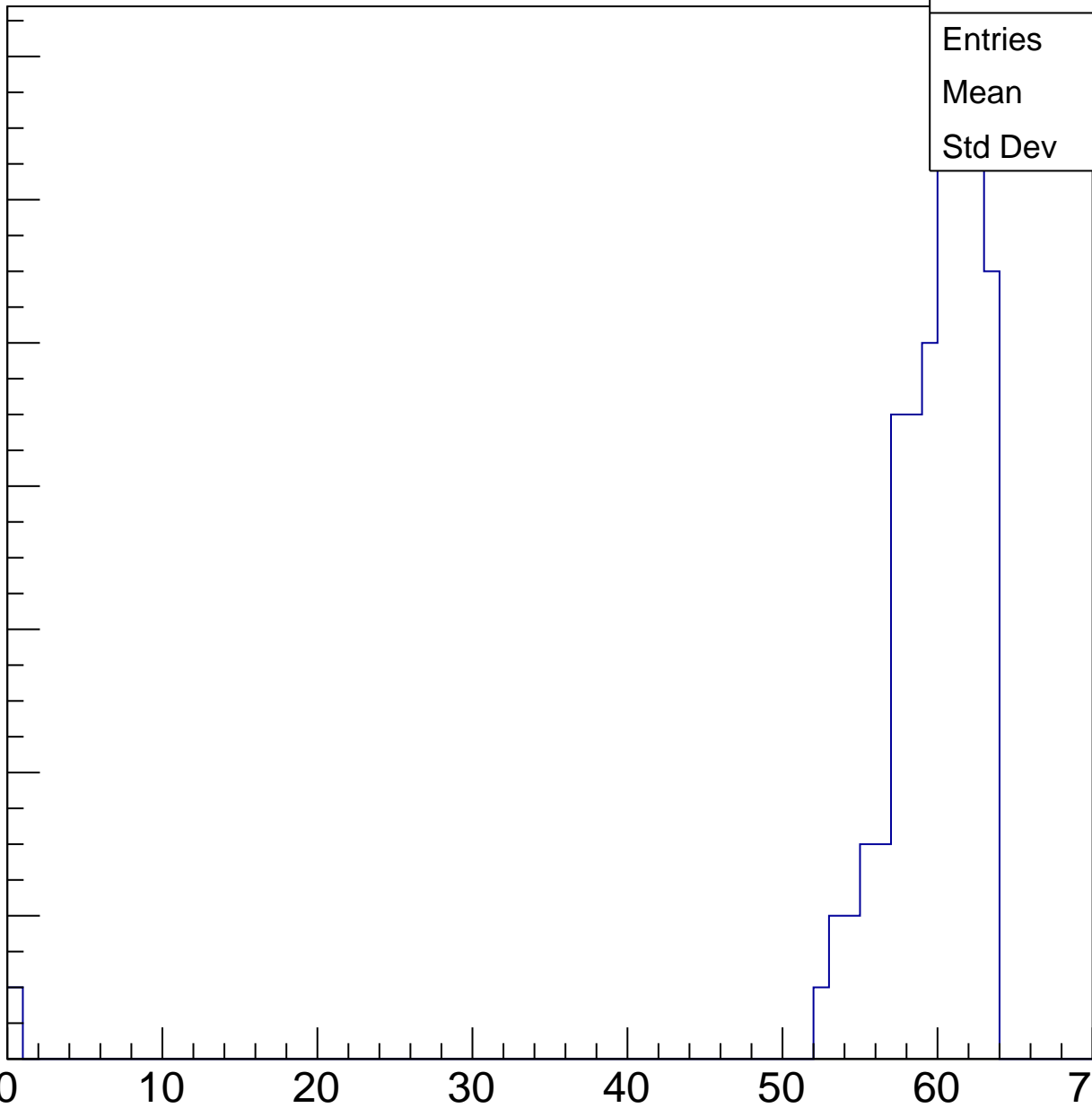
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	58.86
Std Dev	6.736

ampl

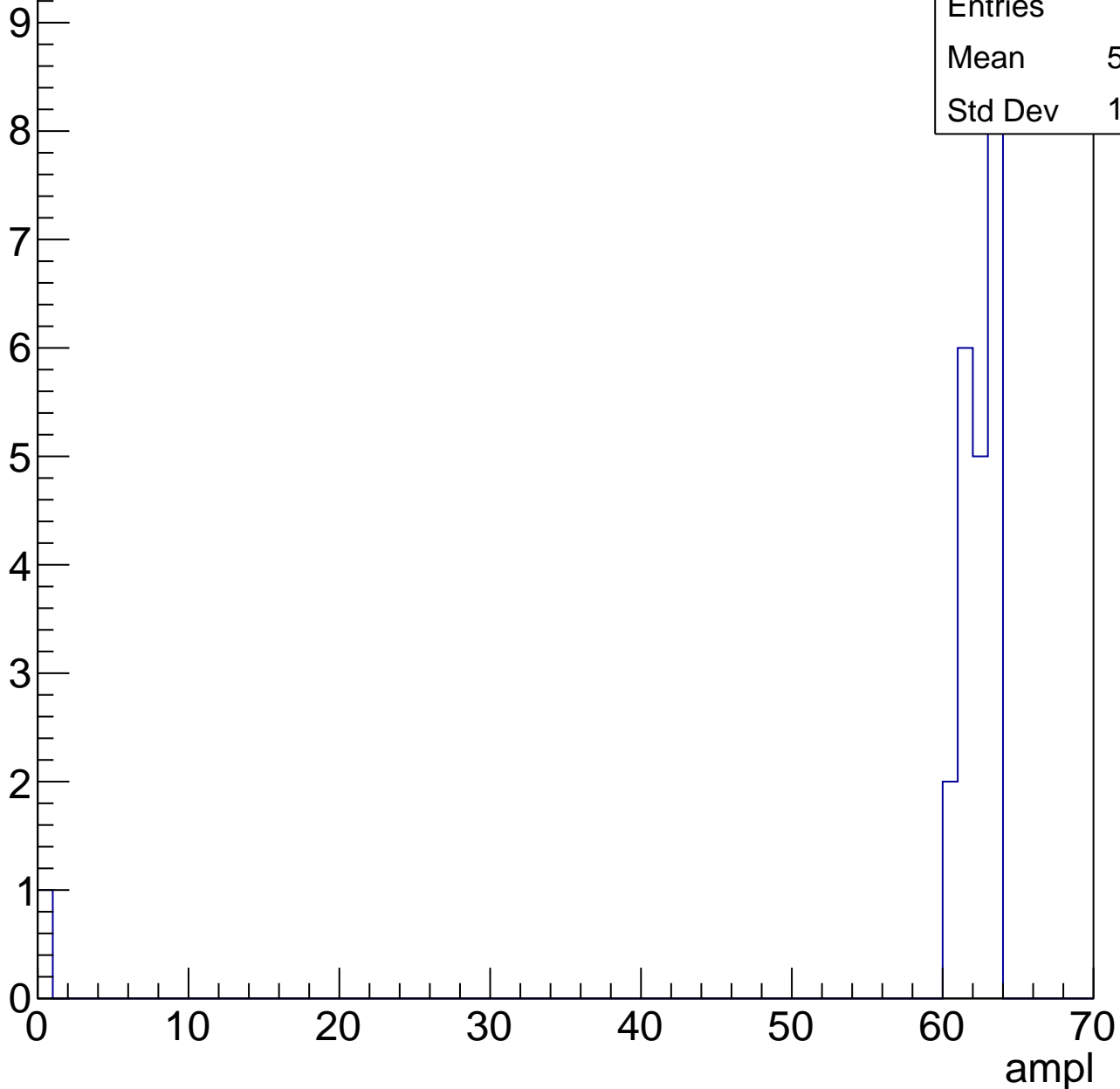


B1L001S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

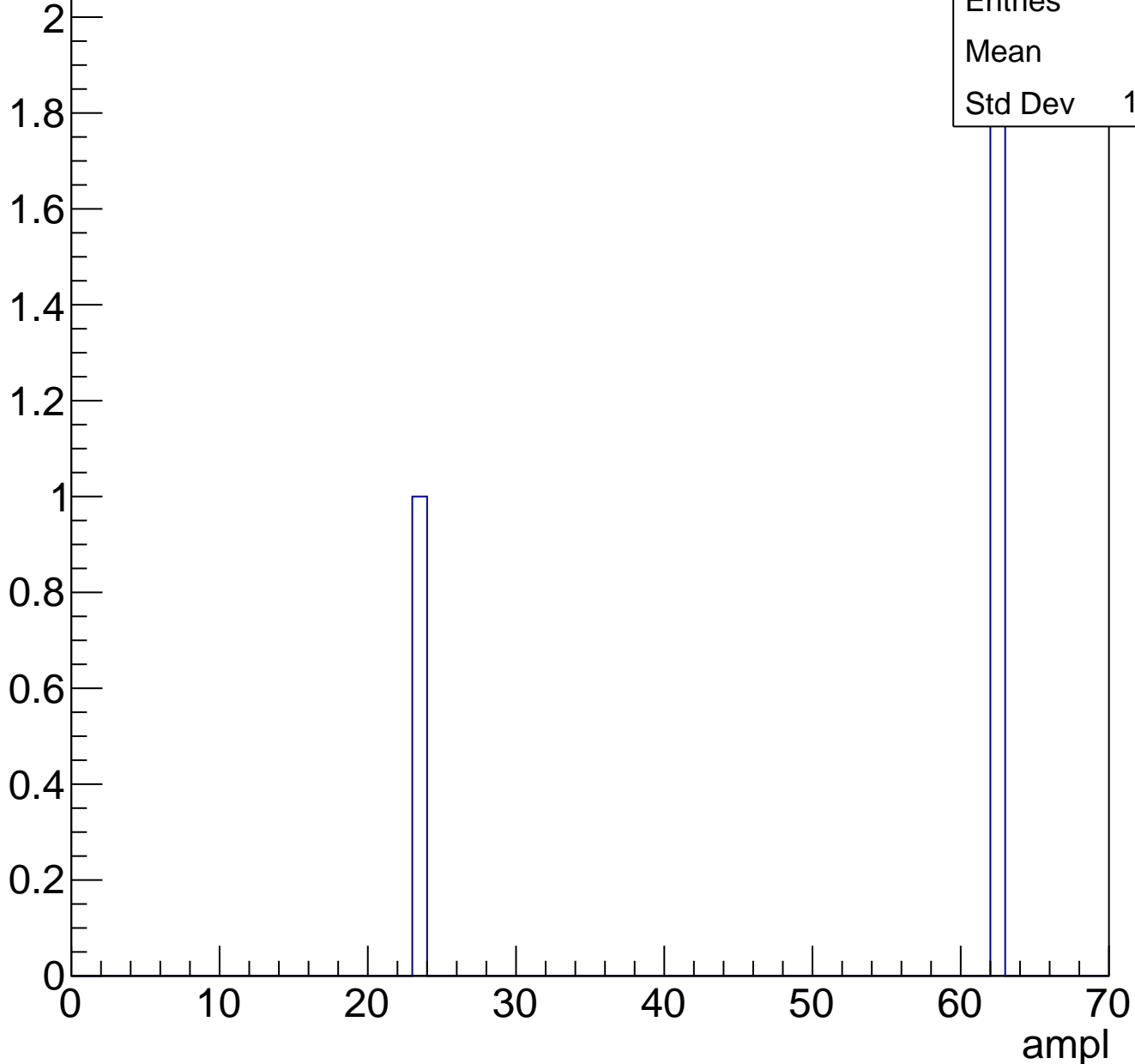
Entries	23
Mean	59.26
Std Dev	12.67



B1L001S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	49
Std Dev	18.38

B1L001S, U14-ch15, adc0

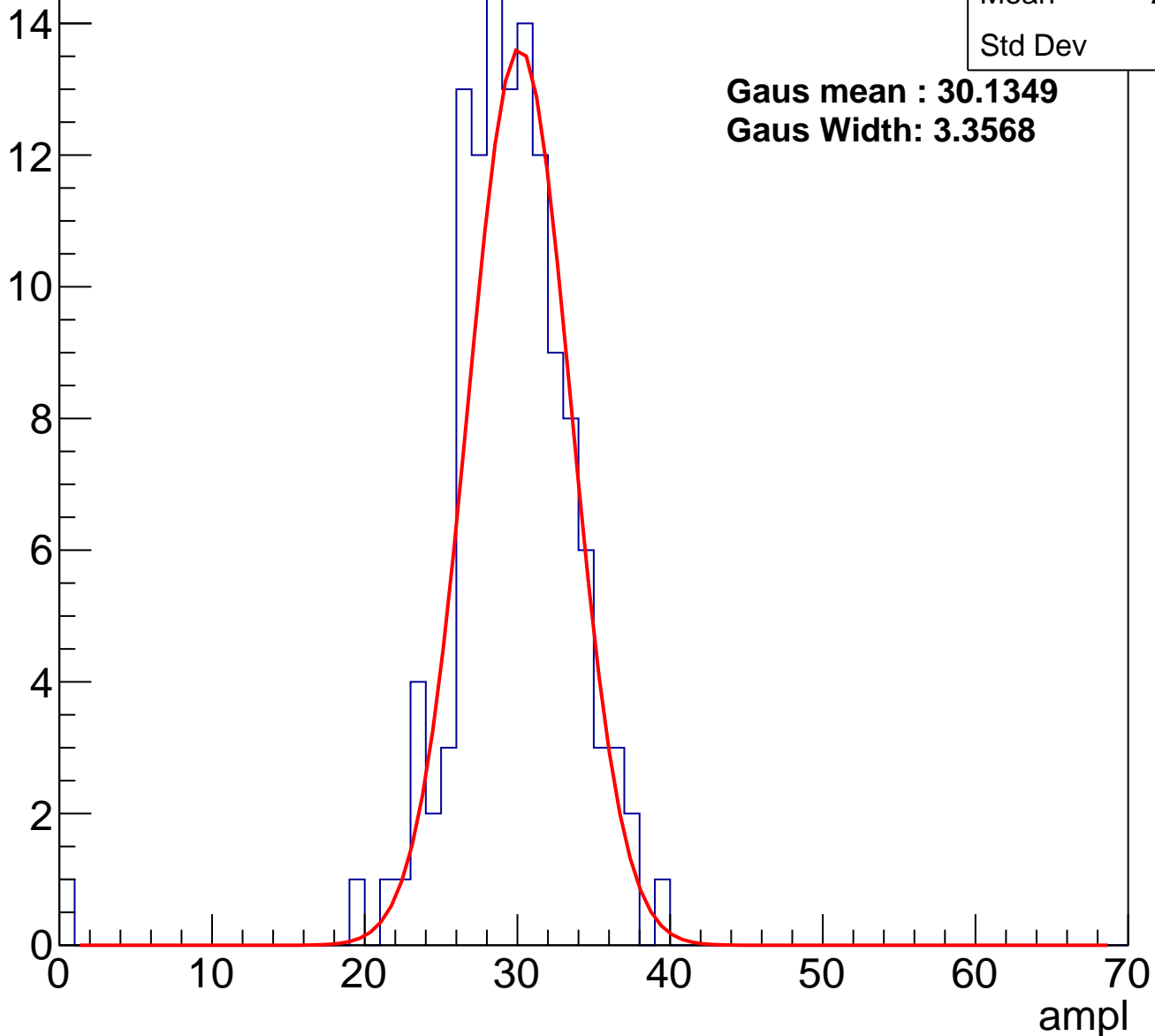
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.11
Std Dev	4.38

Gaus mean : 30.1349

Gaus Width: 3.3568

Entry



B1L001S, U14-ch15, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	35.76
Std Dev	3.597

Gaus mean : 36.0687

Gaus Width: 3.5764

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

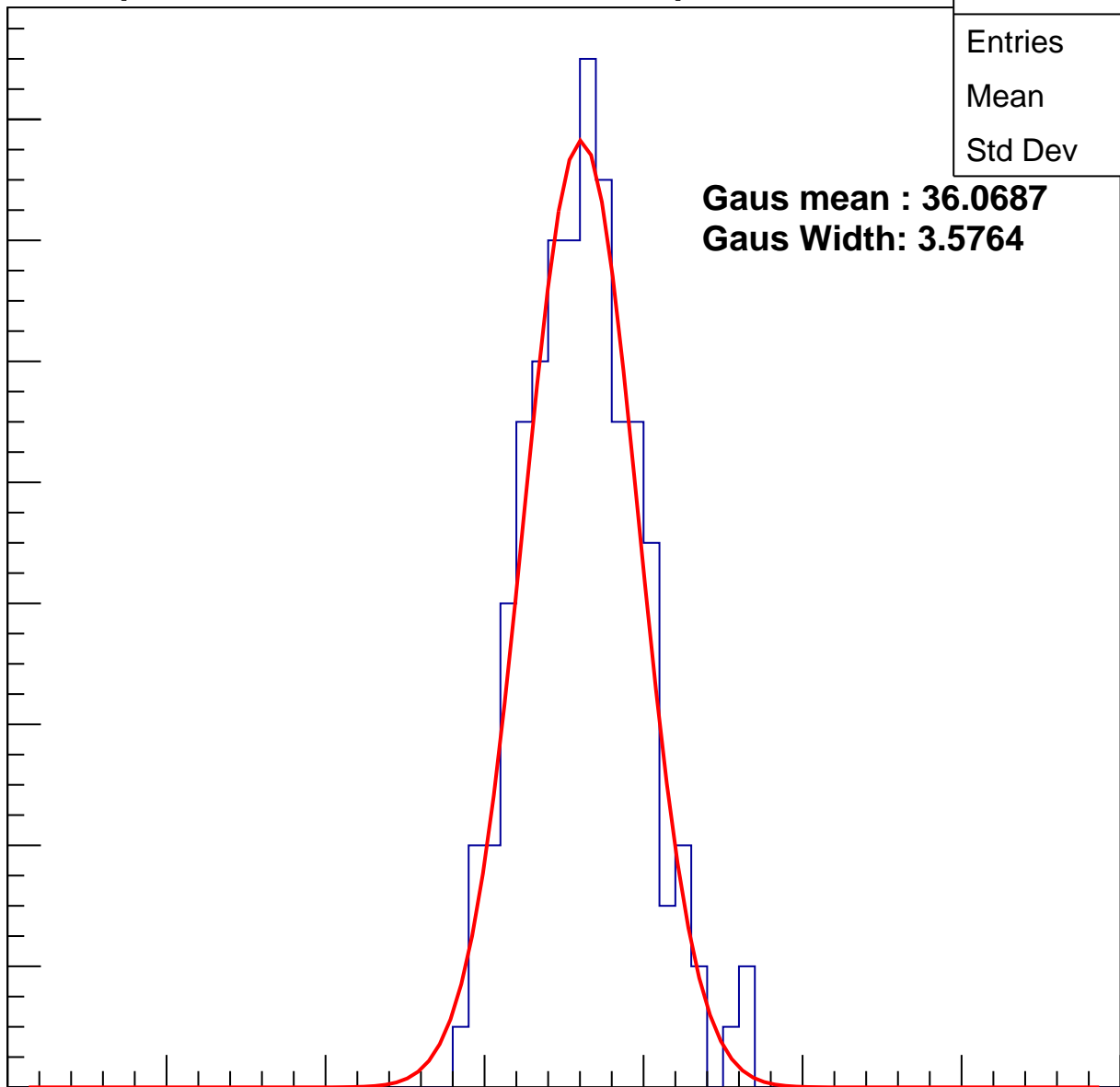
40

50

60

70

ampl



B1L001S, U14-ch15, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	41.92
Std Dev	2.92

Gaus mean : 42.4154

Gaus Width: 3.2739

0

10

20

30

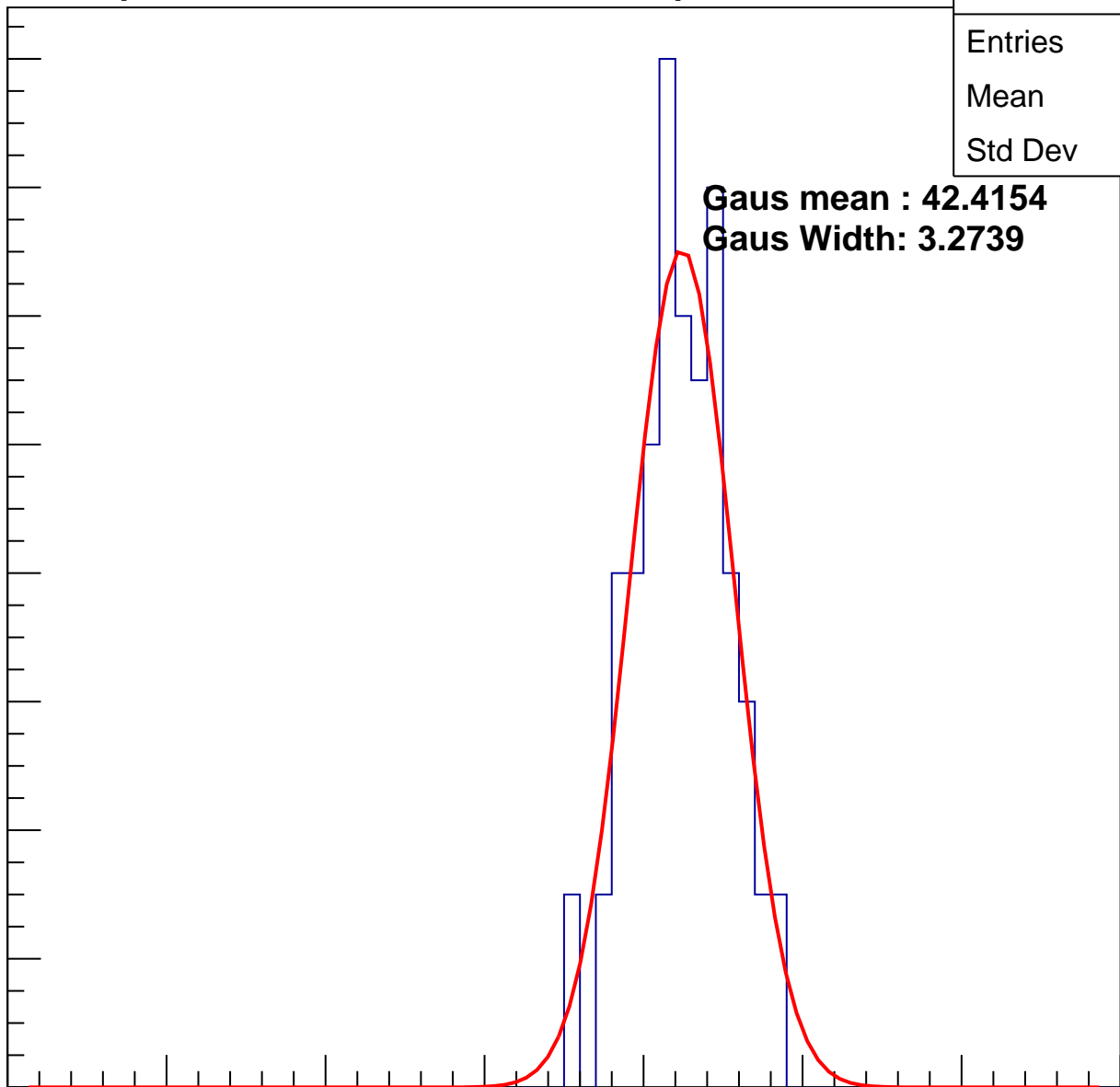
40

50

60

70

ampl



B1L001S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#2, port C2

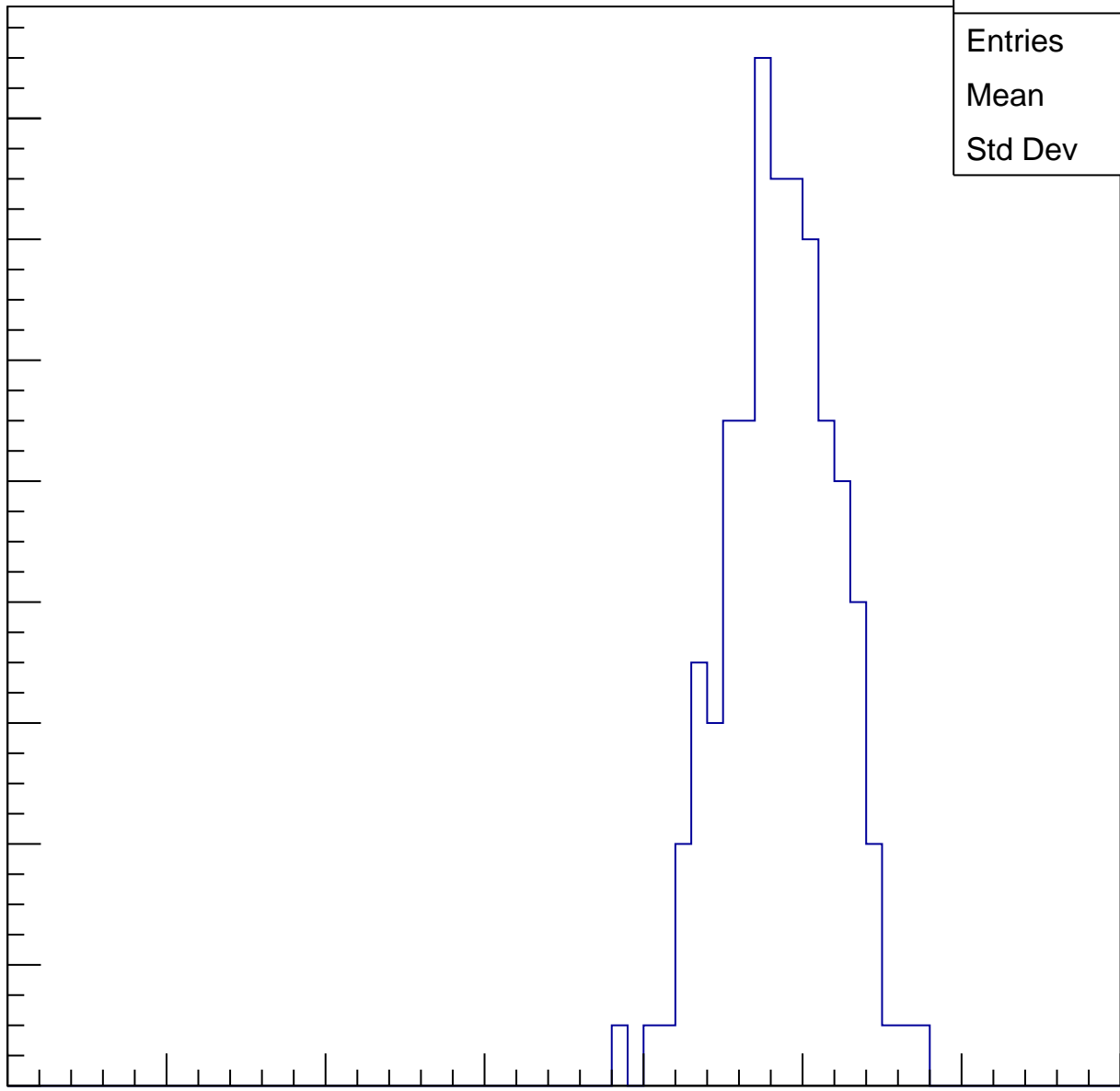
Entries	139
Mean	48.17
Std Dev	3.447

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

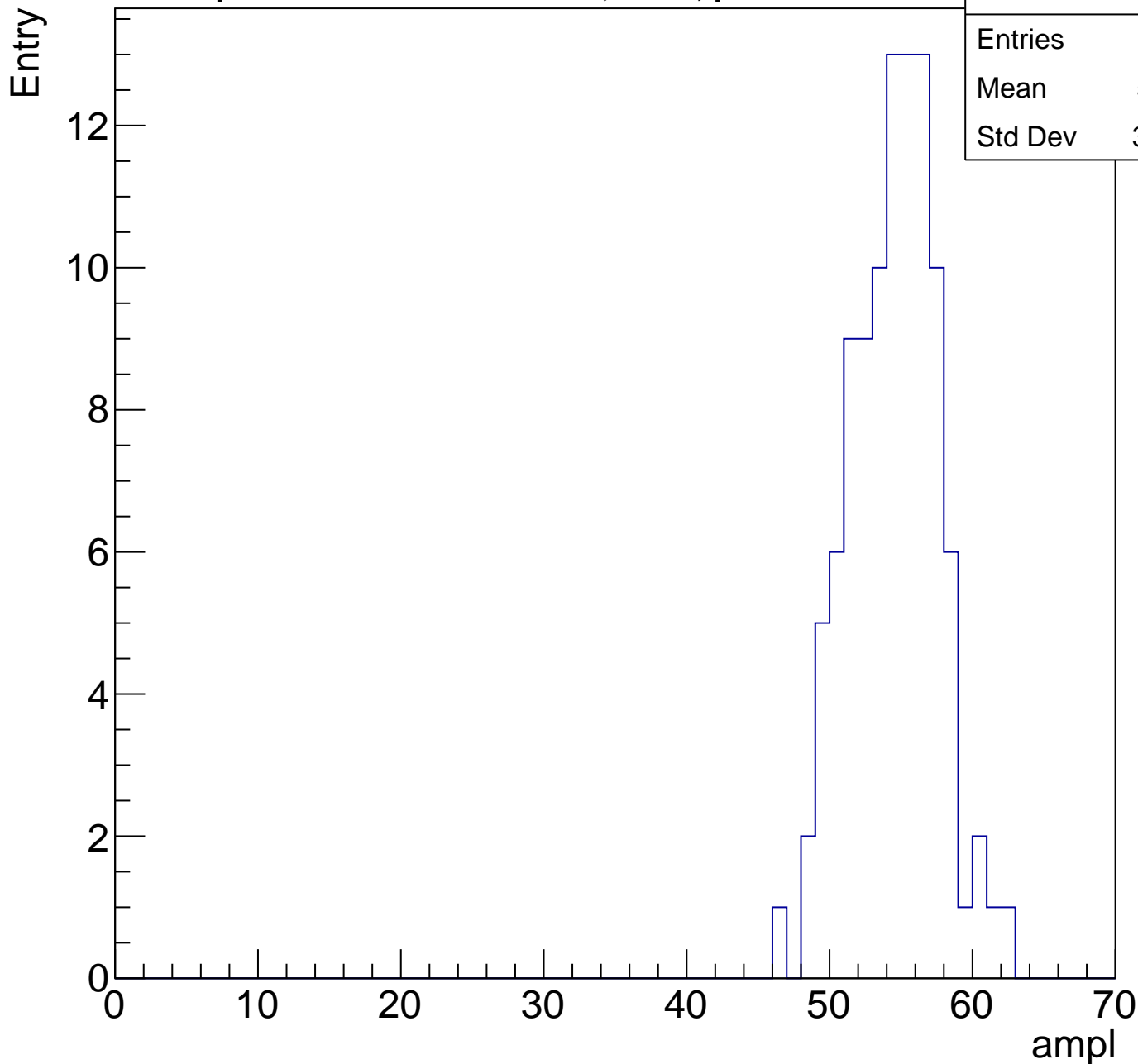
ampl



B1L001S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	54.01
Std Dev	3.044



B1L001S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	58.75
Std Dev	6.117

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

30

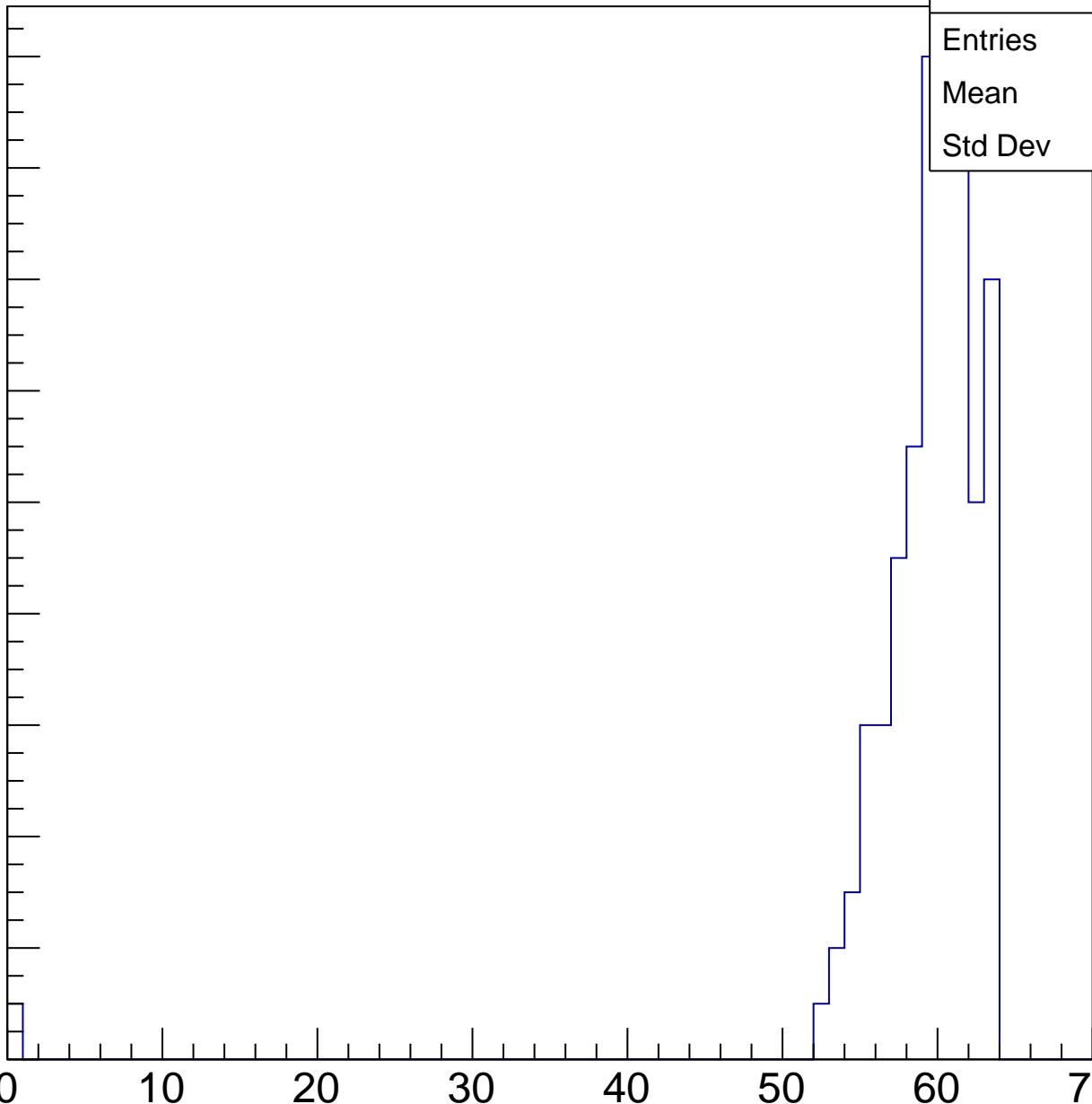
40

50

60

70

ampl

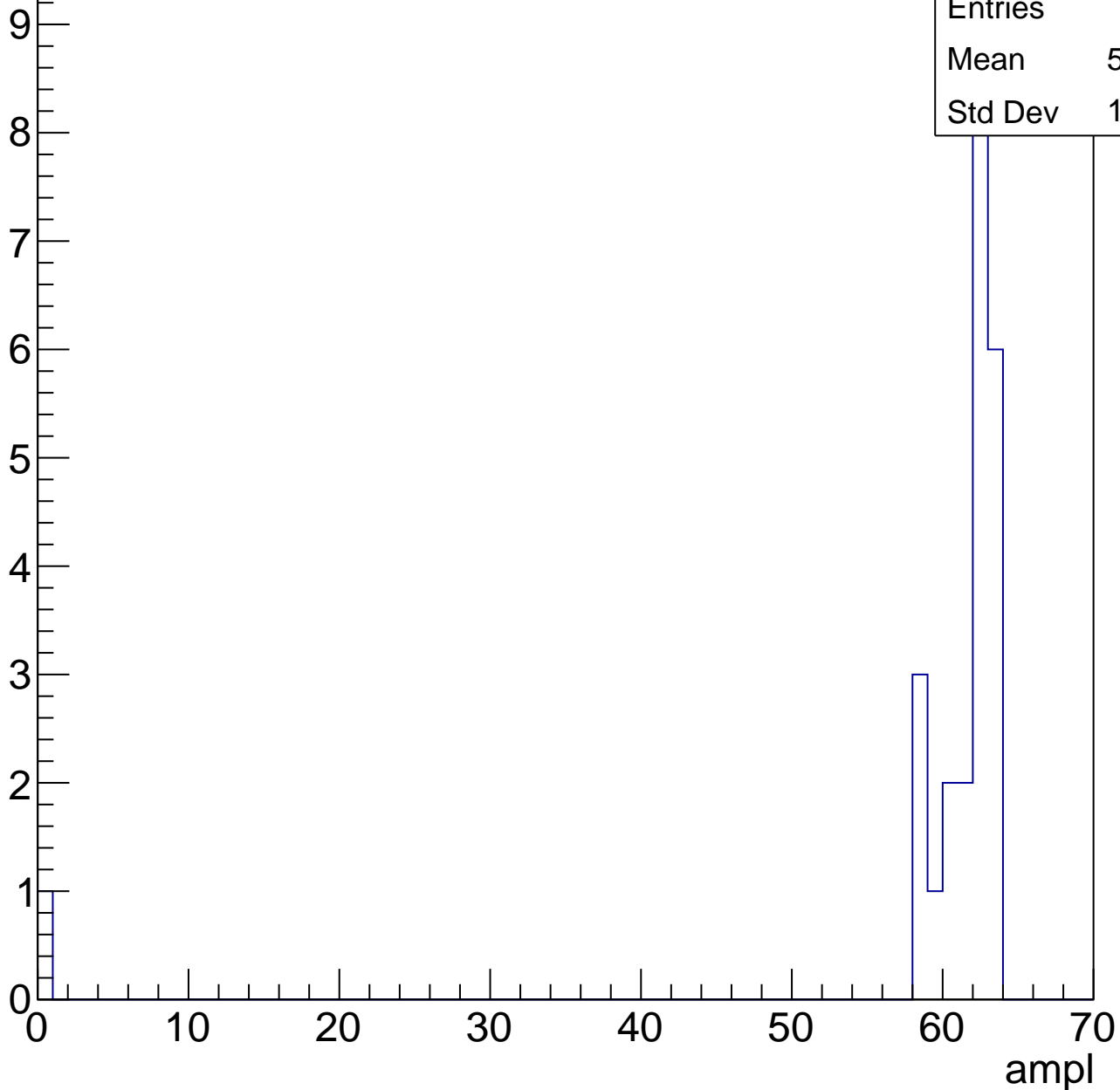


B1L001S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	24
Mean	58.79
Std Dev	12.37



B1L001S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch16, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

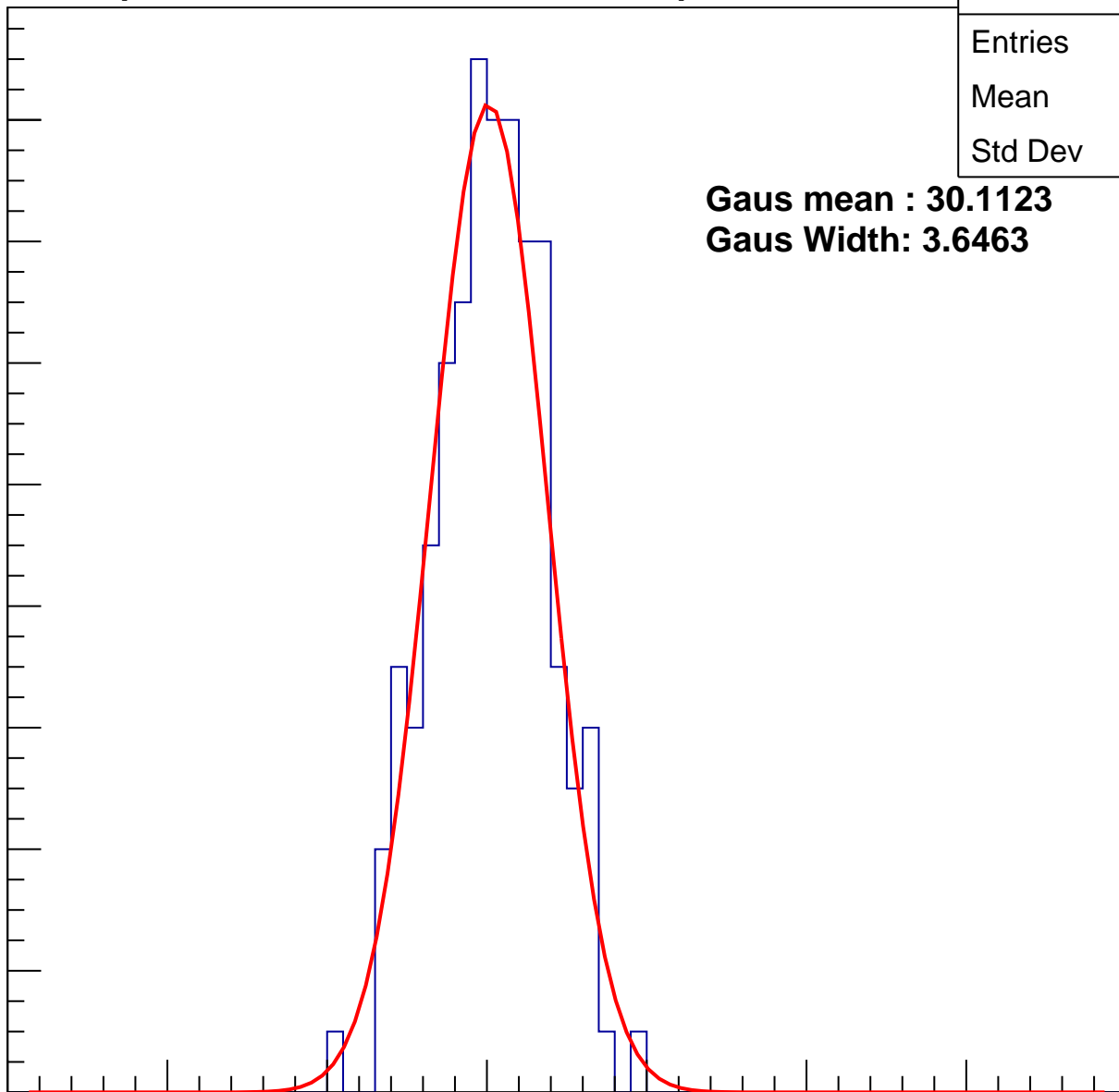
Entries	149
Mean	29.77
Std Dev	3.468

Gaus mean : 30.1123

Gaus Width: 3.6463

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch16, adc1

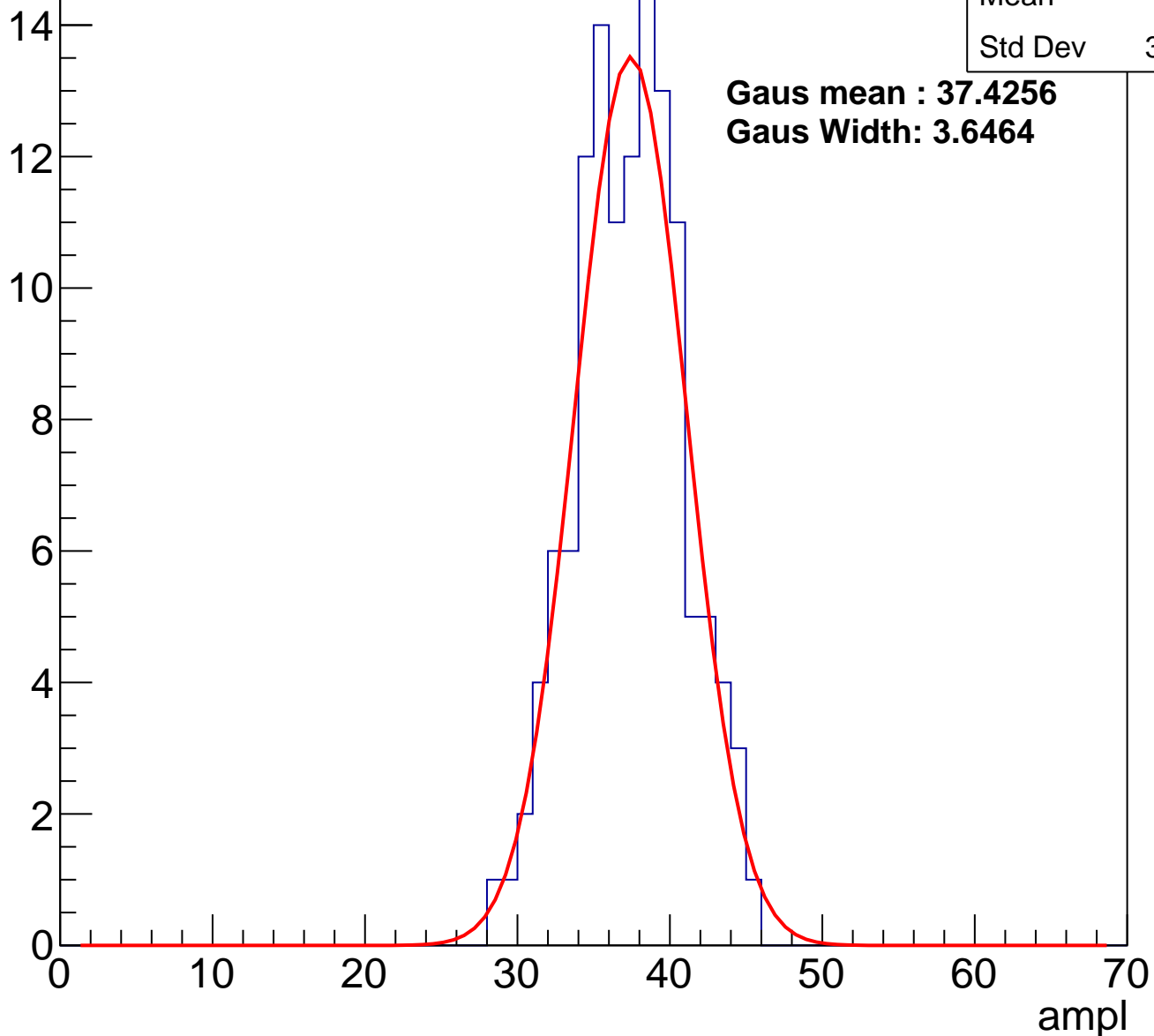
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	36.9
Std Dev	3.488

Gaus mean : 37.4256

Gaus Width: 3.6464

Entry



B1L001S, U14-ch16, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

103

Mean

41.98

Std Dev

3.297

Gaus mean : 42.4090

Gaus Width: 3.3271

0

10

20

30

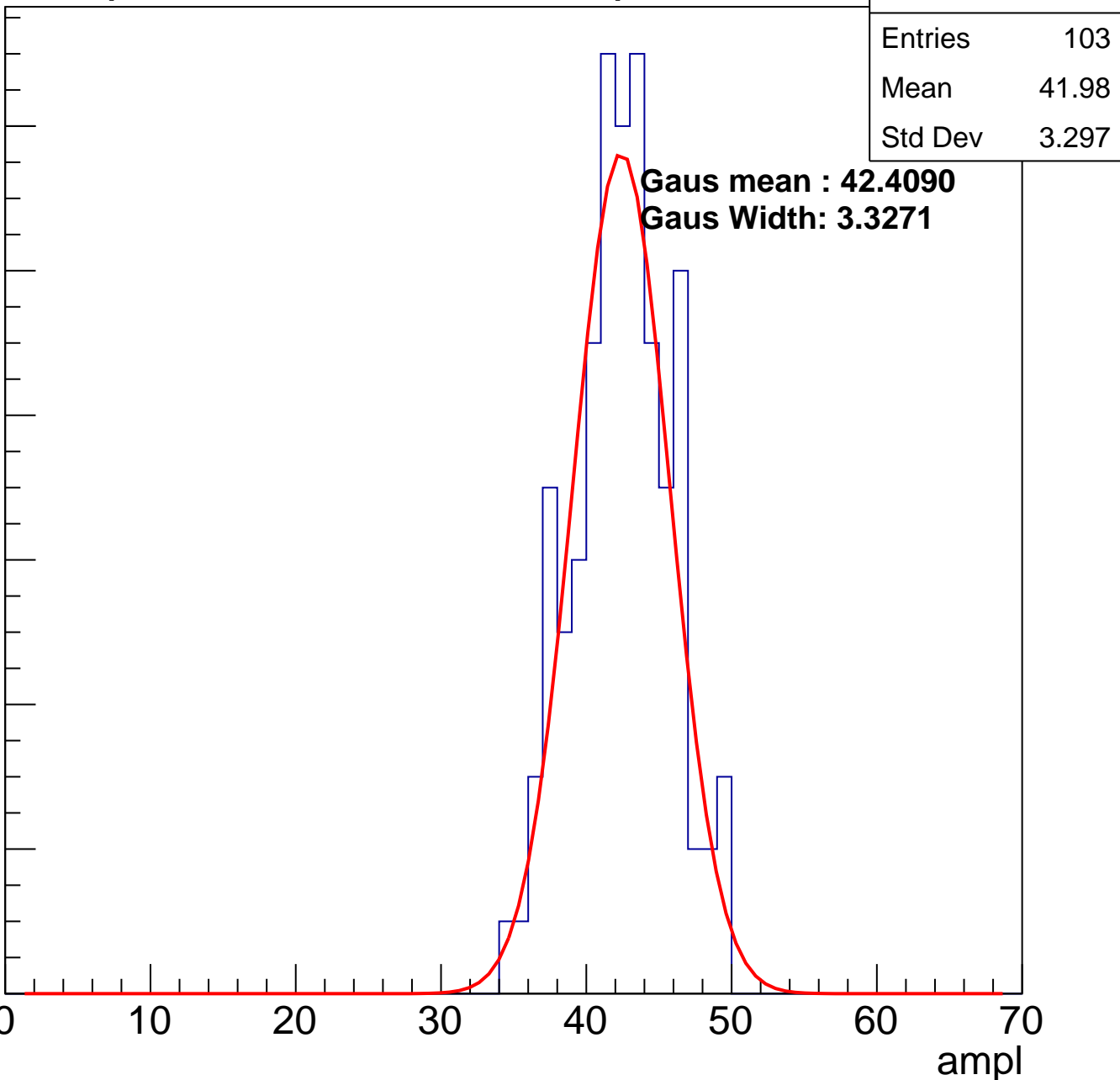
40

50

60

70

ampl



B1L001S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	47.92
Std Dev	2.999

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

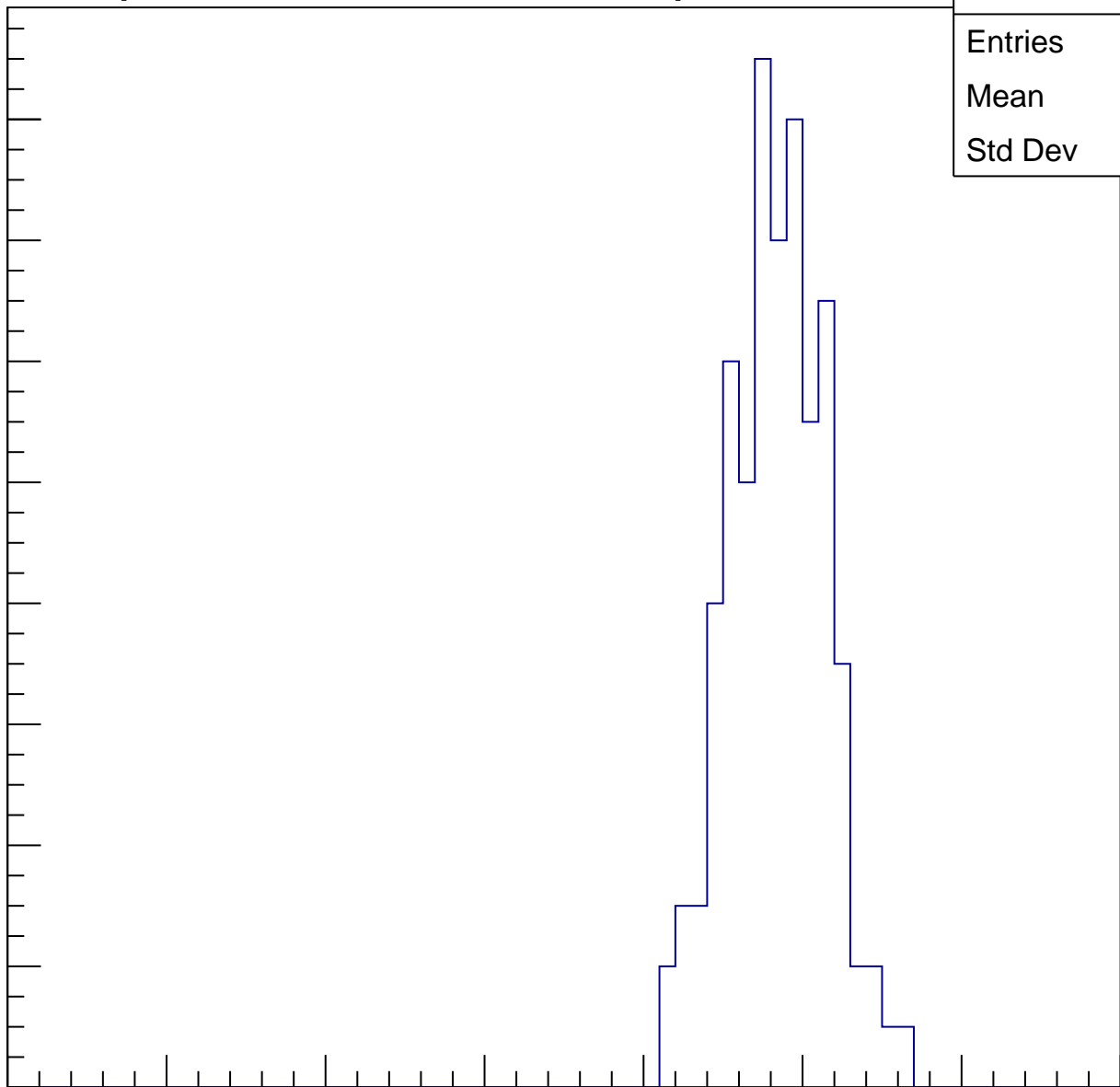
40

50

60

70

ampl



B1L001S, U14-ch16, adc4

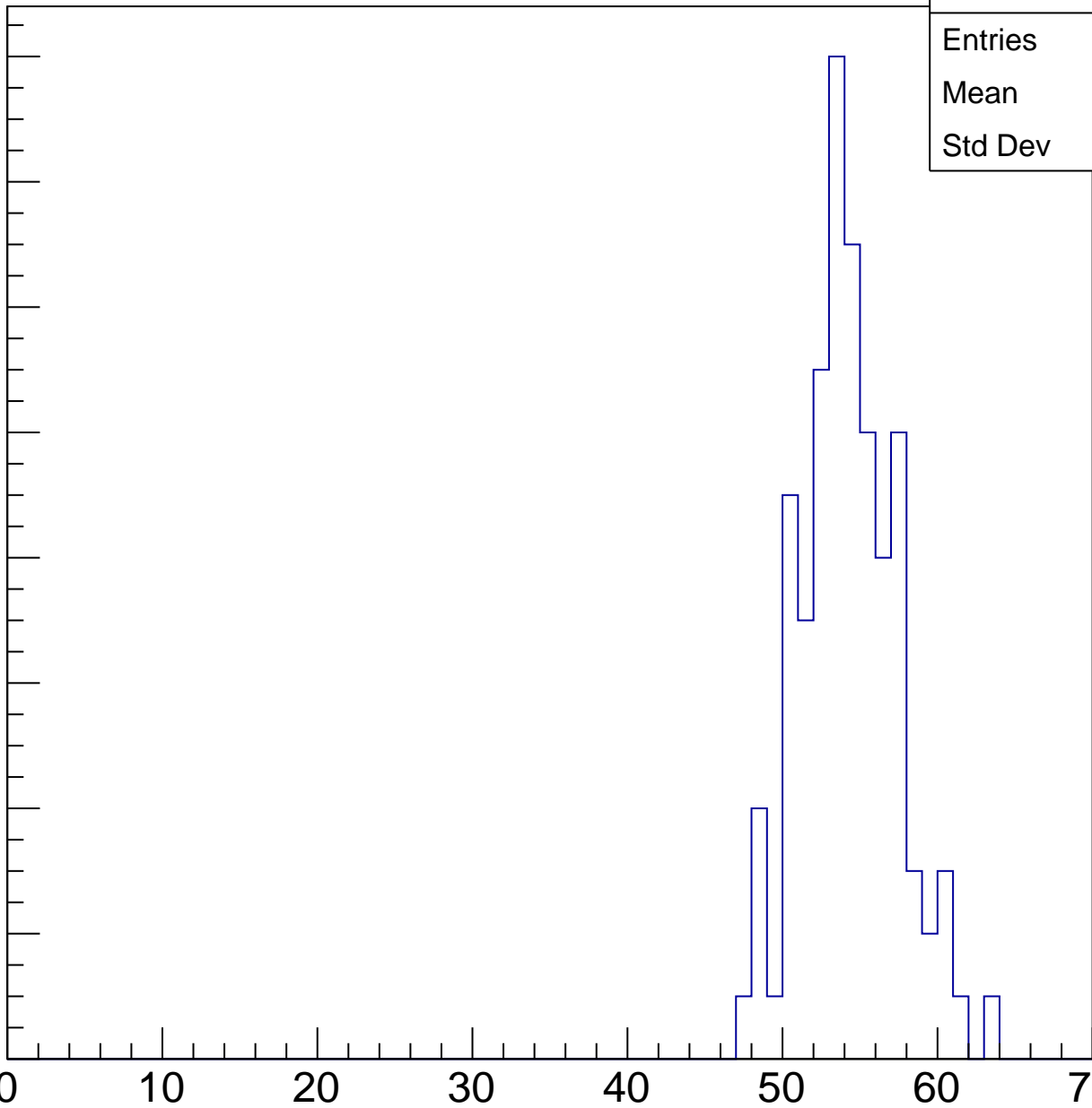
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	53.81
Std Dev	3.081

ampl



B1L001S, U14-ch16, adc5

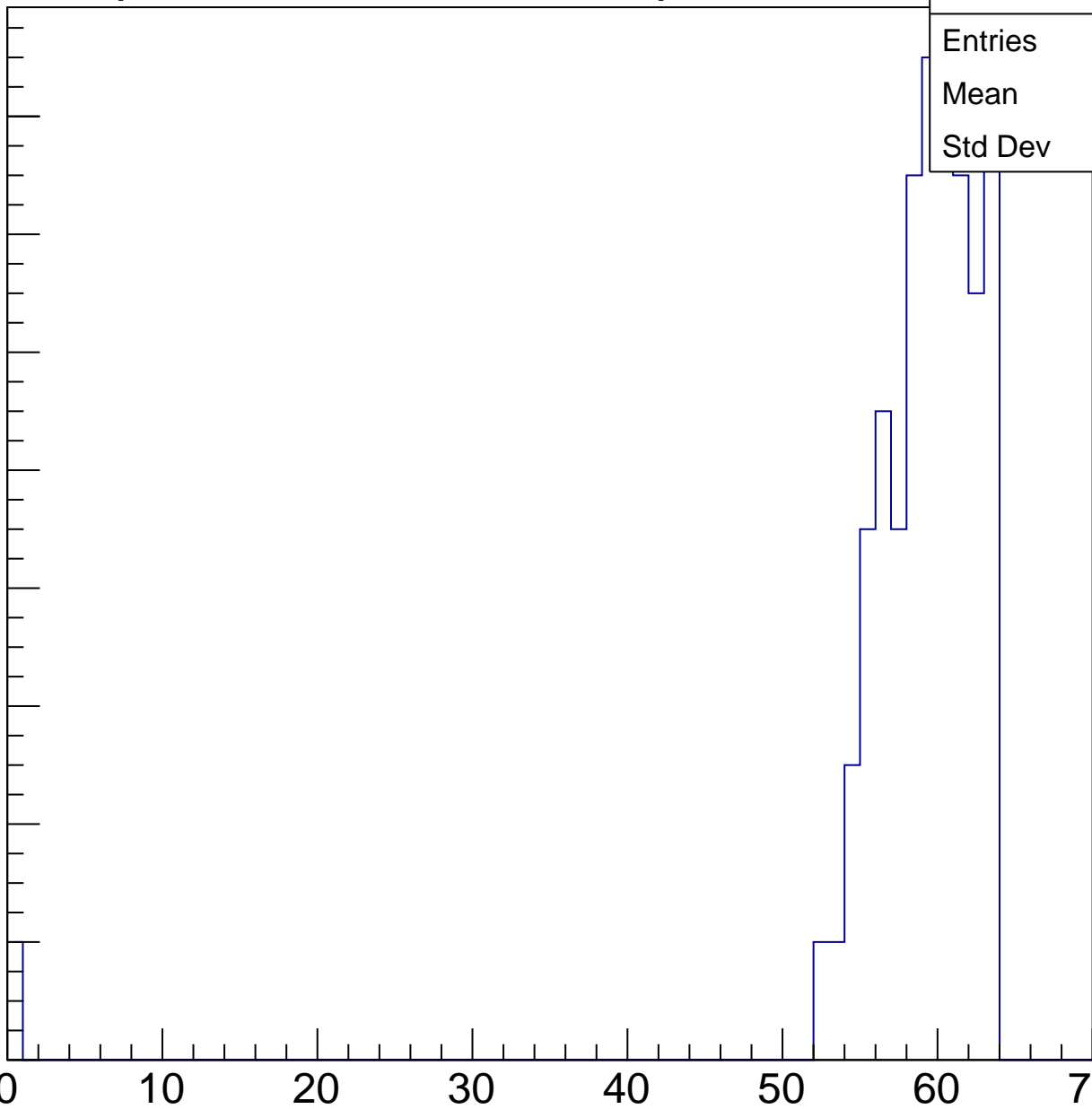
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	58.11
Std Dev	7.713

ampl

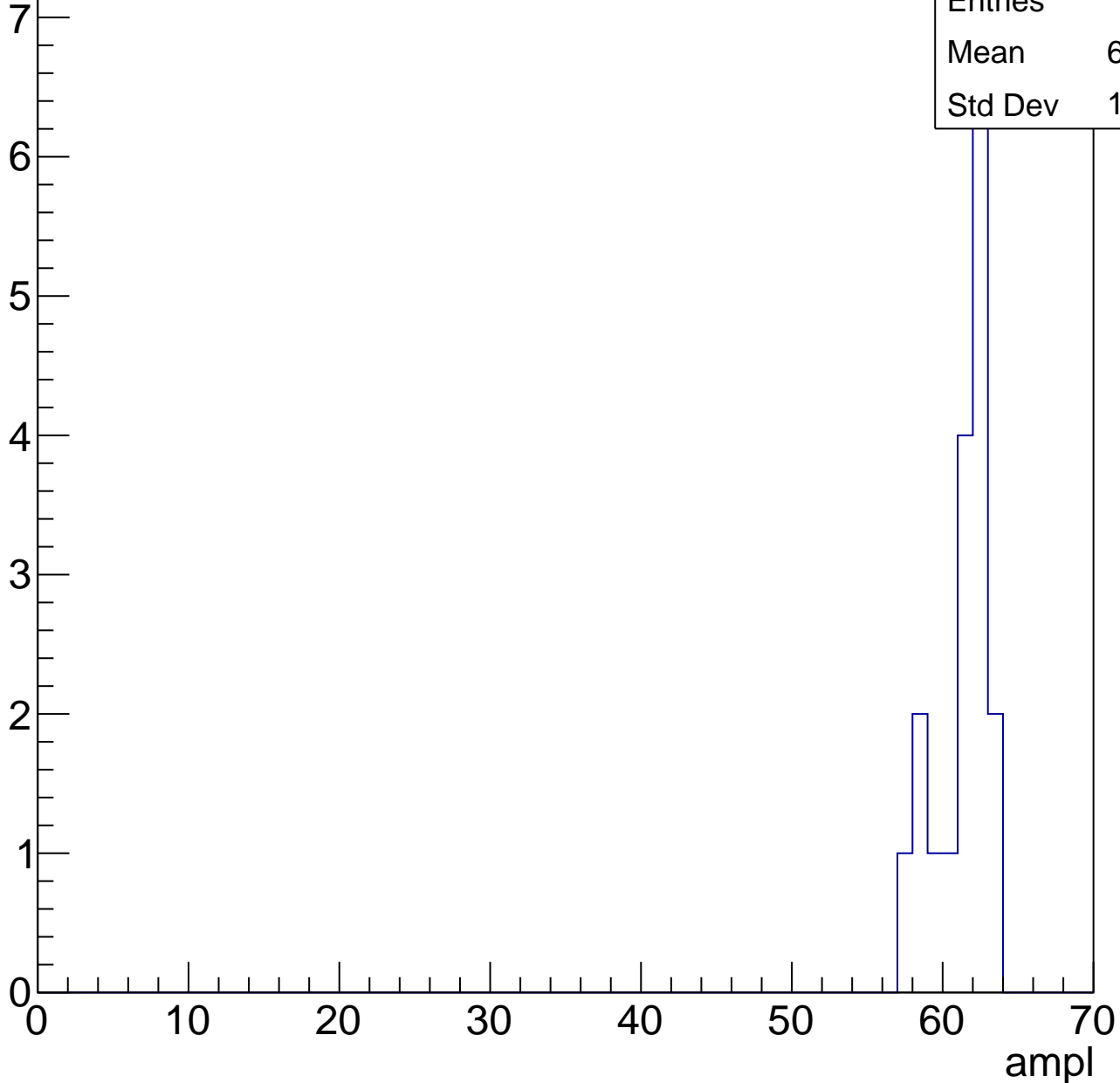


B1L001S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	60.89
Std Dev	1.728



B1L001S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L001S, U14-ch17, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	29.35
Std Dev	2.933

Gaus mean : 29.7736

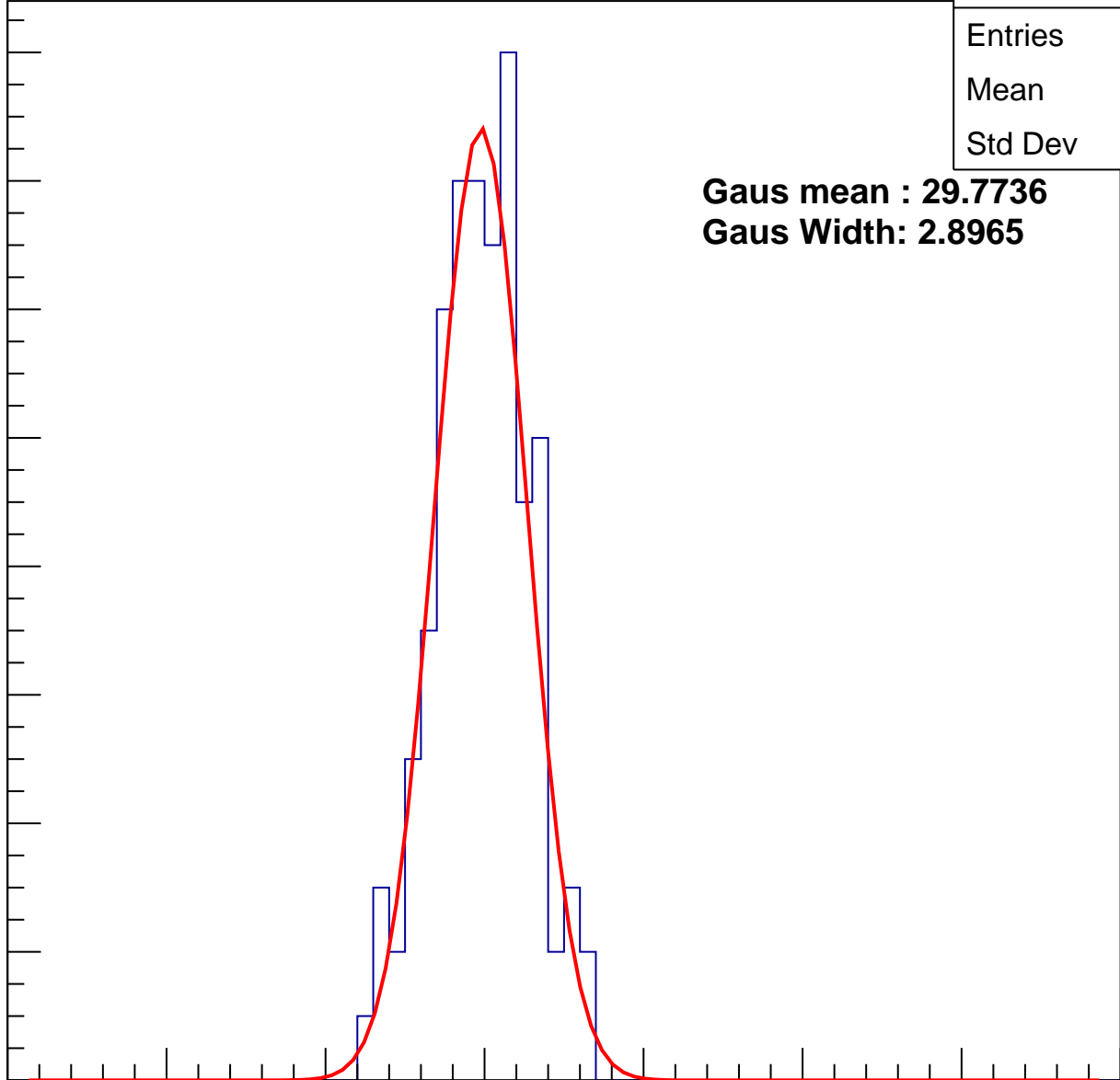
Gaus Width: 2.8965

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



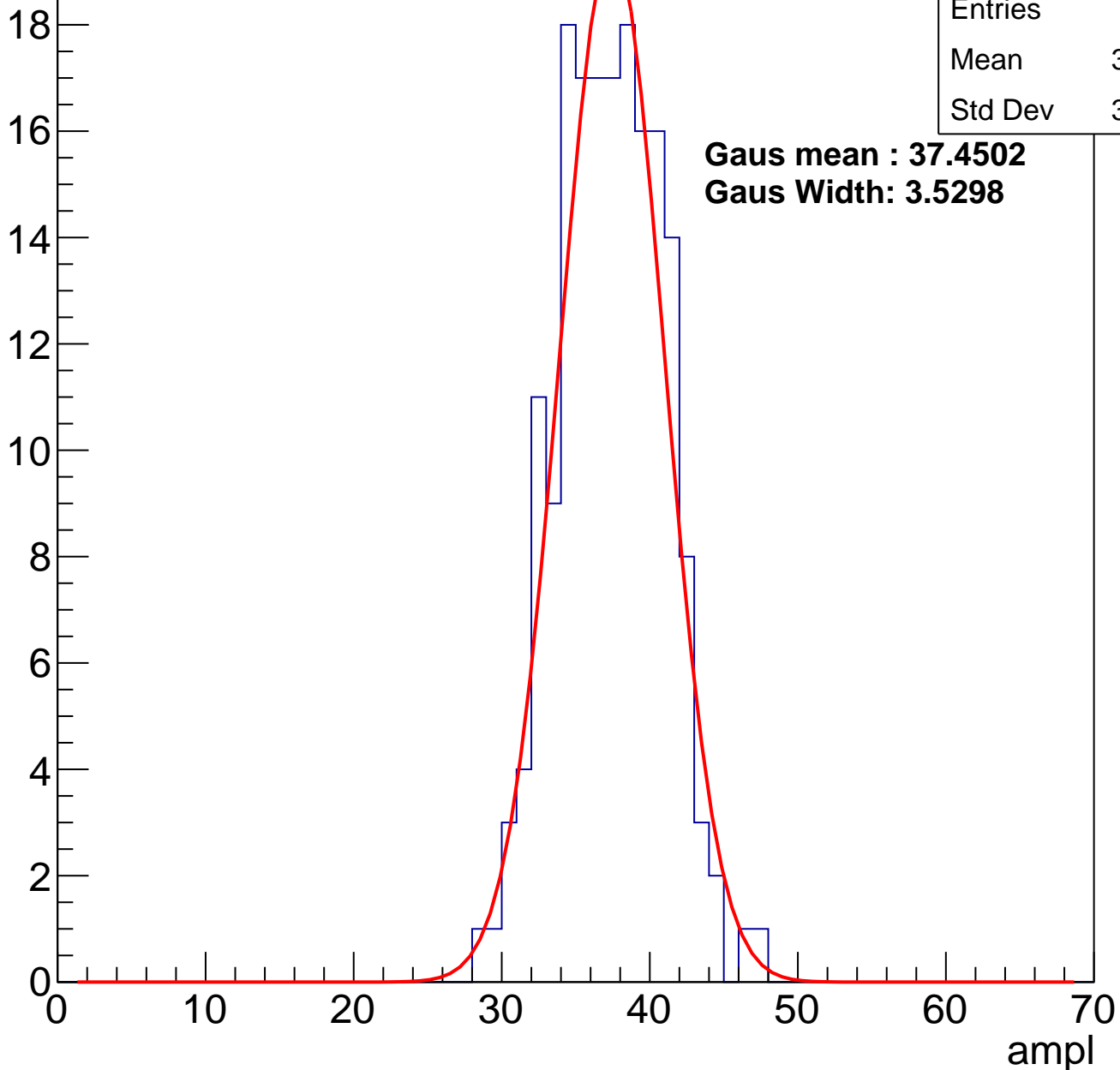
B1L001S, U14-ch17, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	177
Mean	36.93
Std Dev	3.476

Gaus mean : 37.4502
Gaus Width: 3.5298

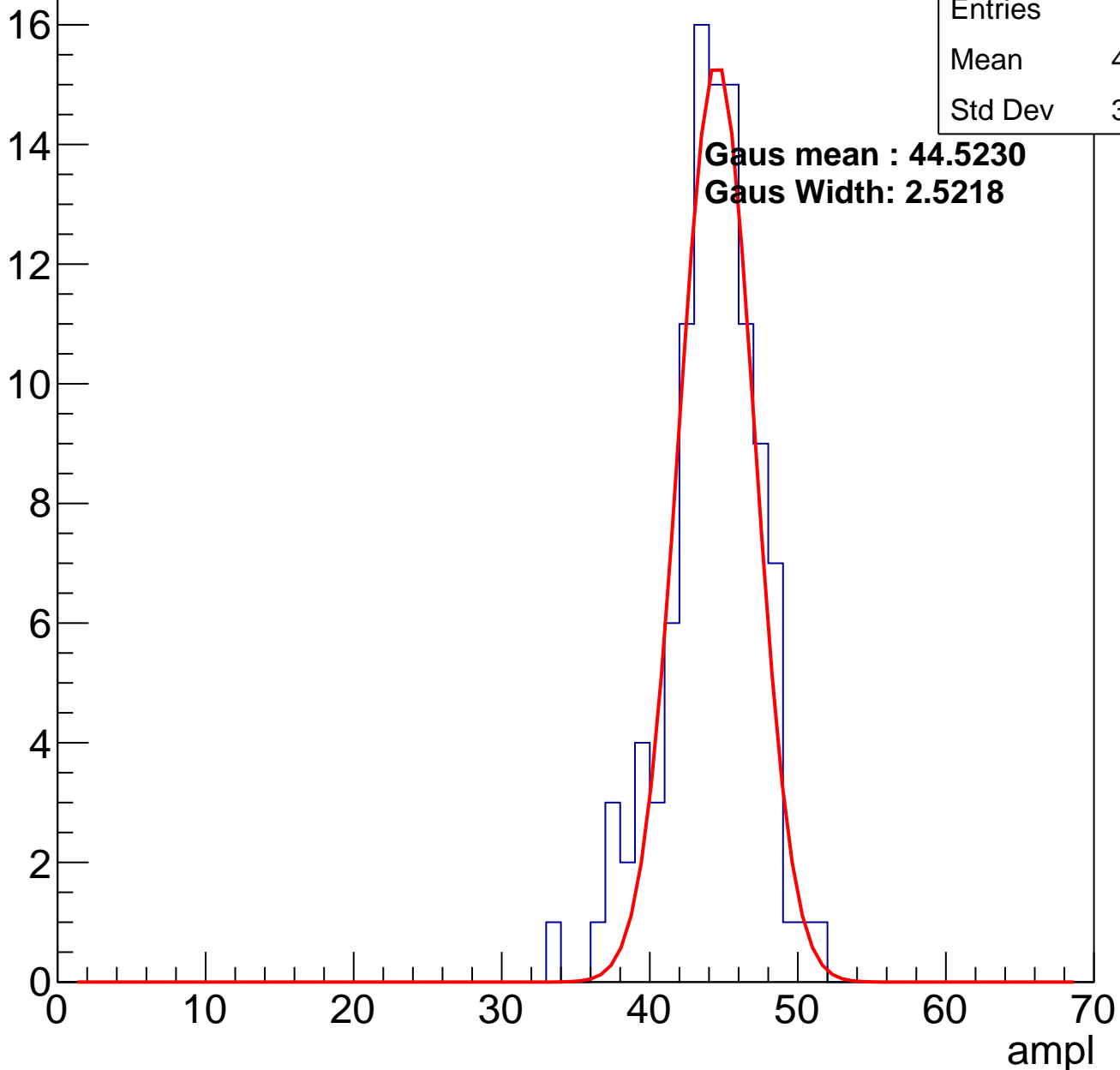
Entry



B1L001S, U14-ch17, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#2, port C2

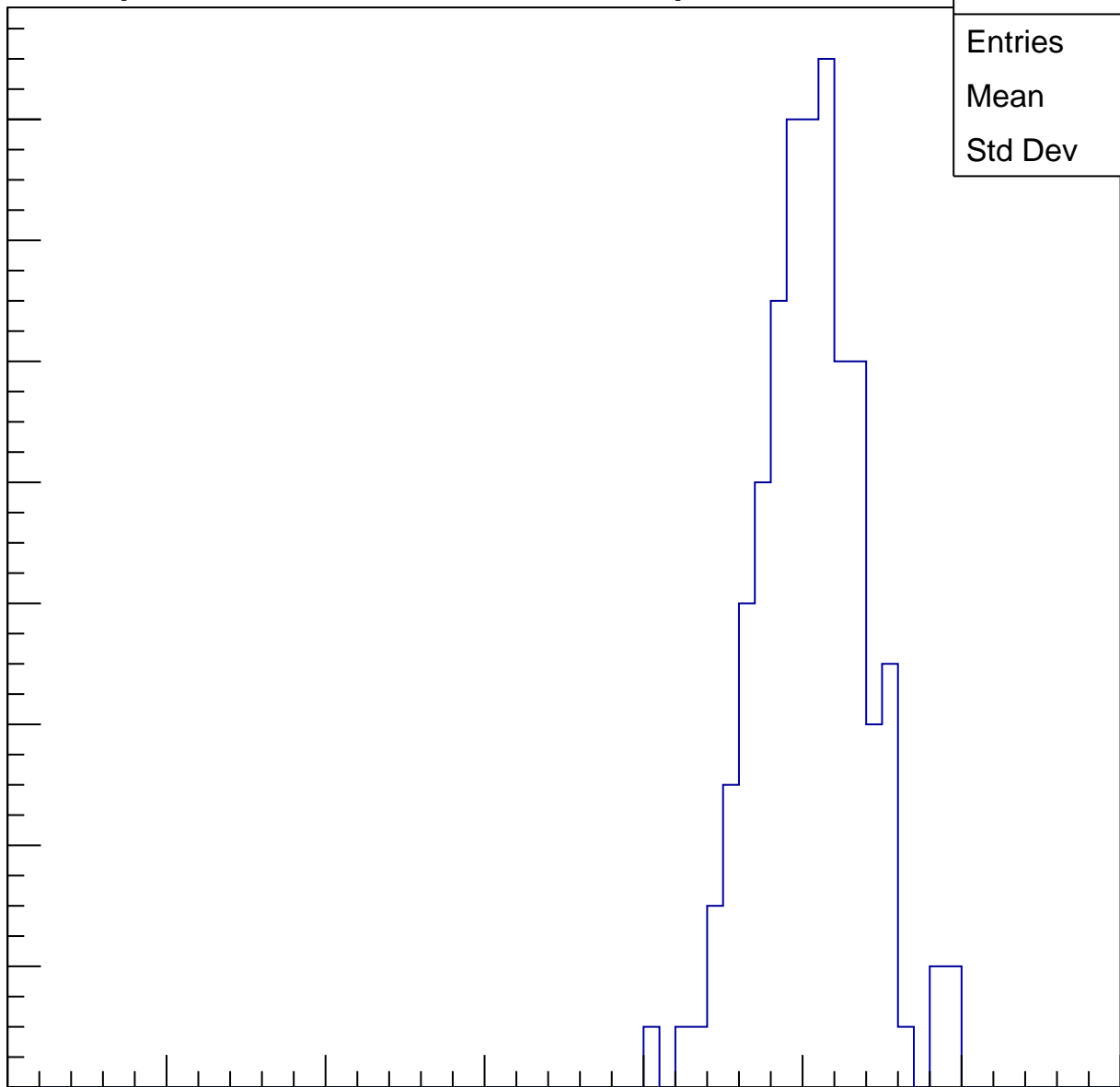
Entries	133
Mean	50.03
Std Dev	3.36

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

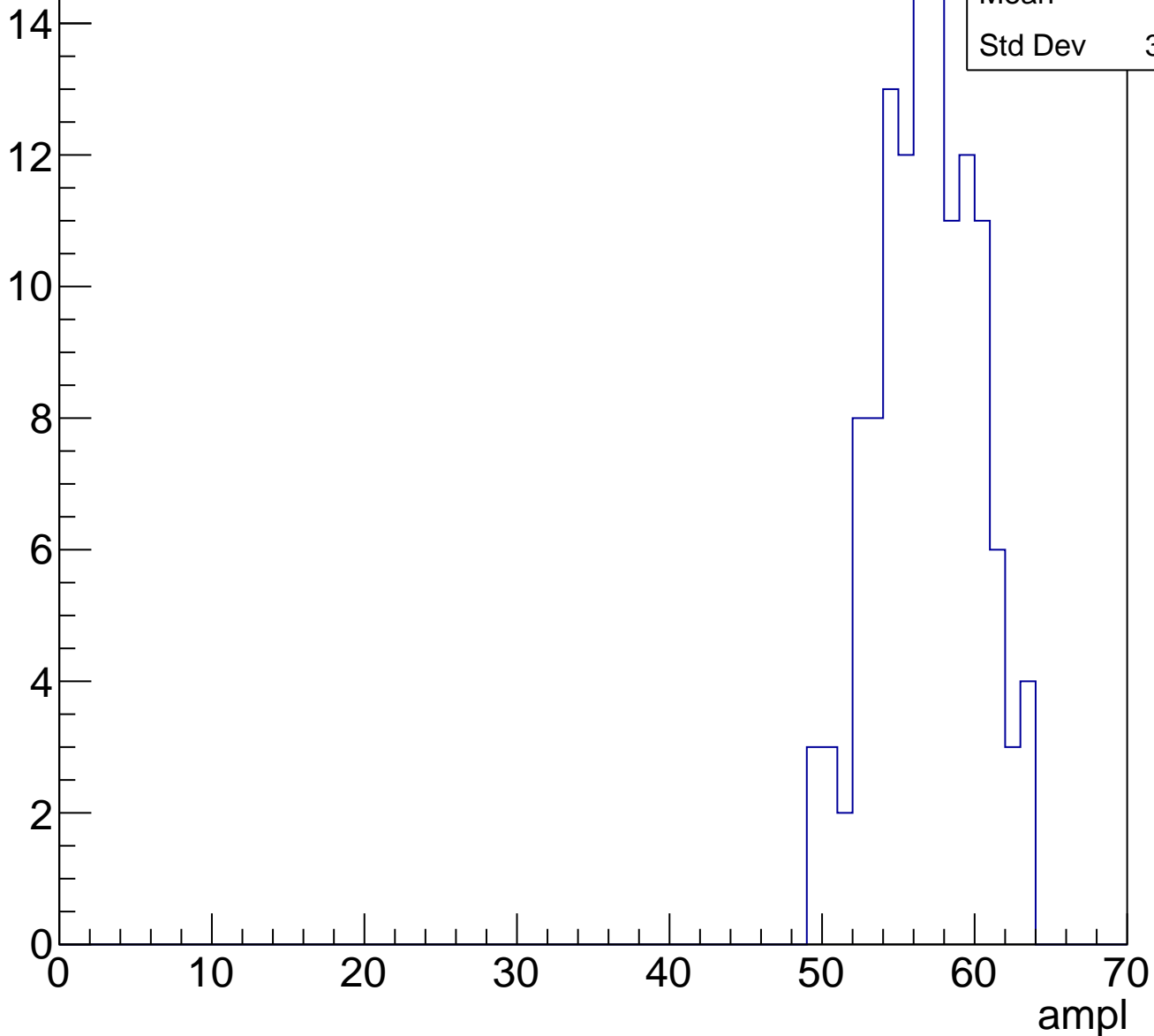


B1L001S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.4
Std Dev	3.269

Entry



B1L001S, U14-ch17, adc5

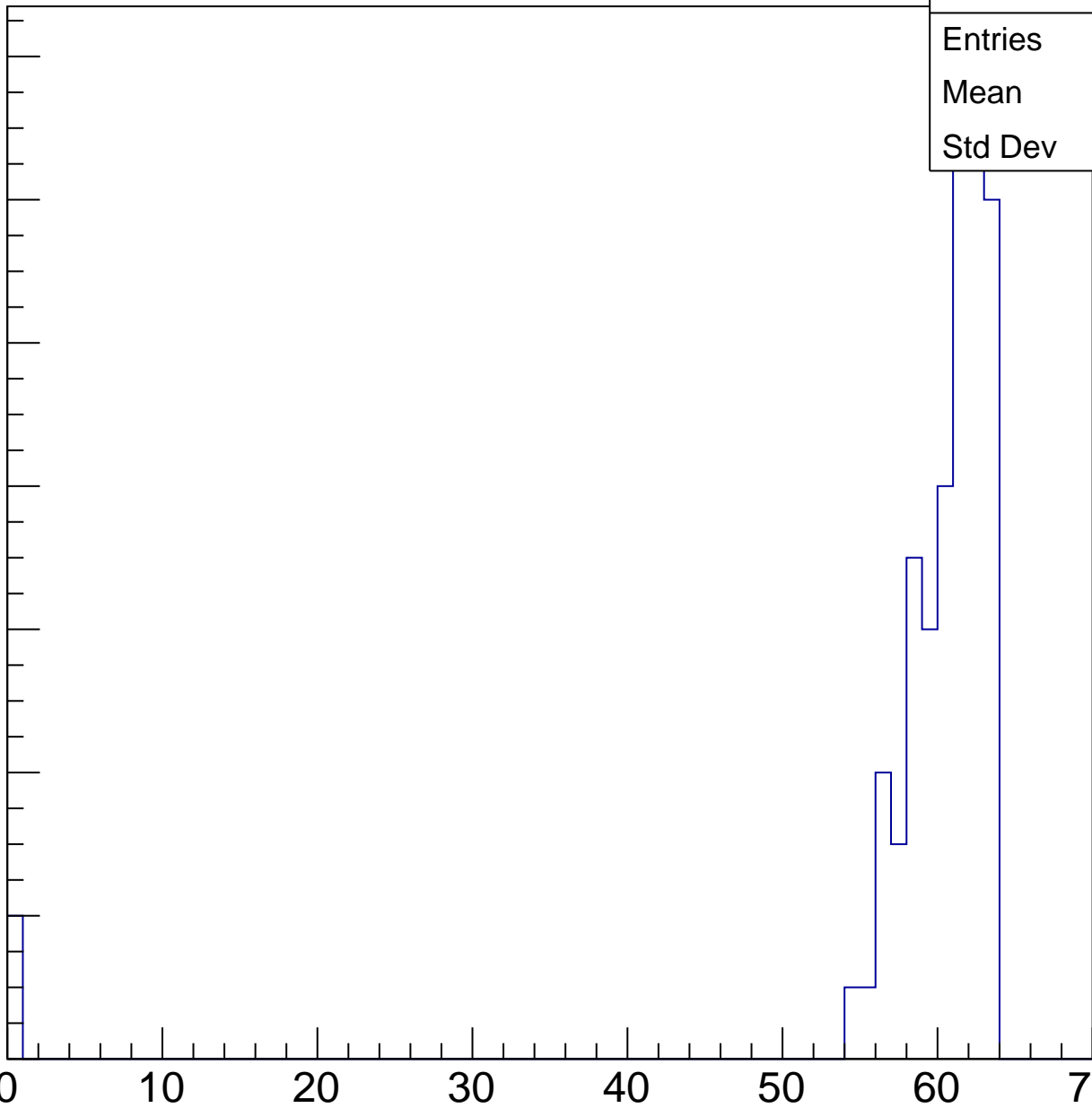
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	71
Mean	58.61
Std Dev	10.23

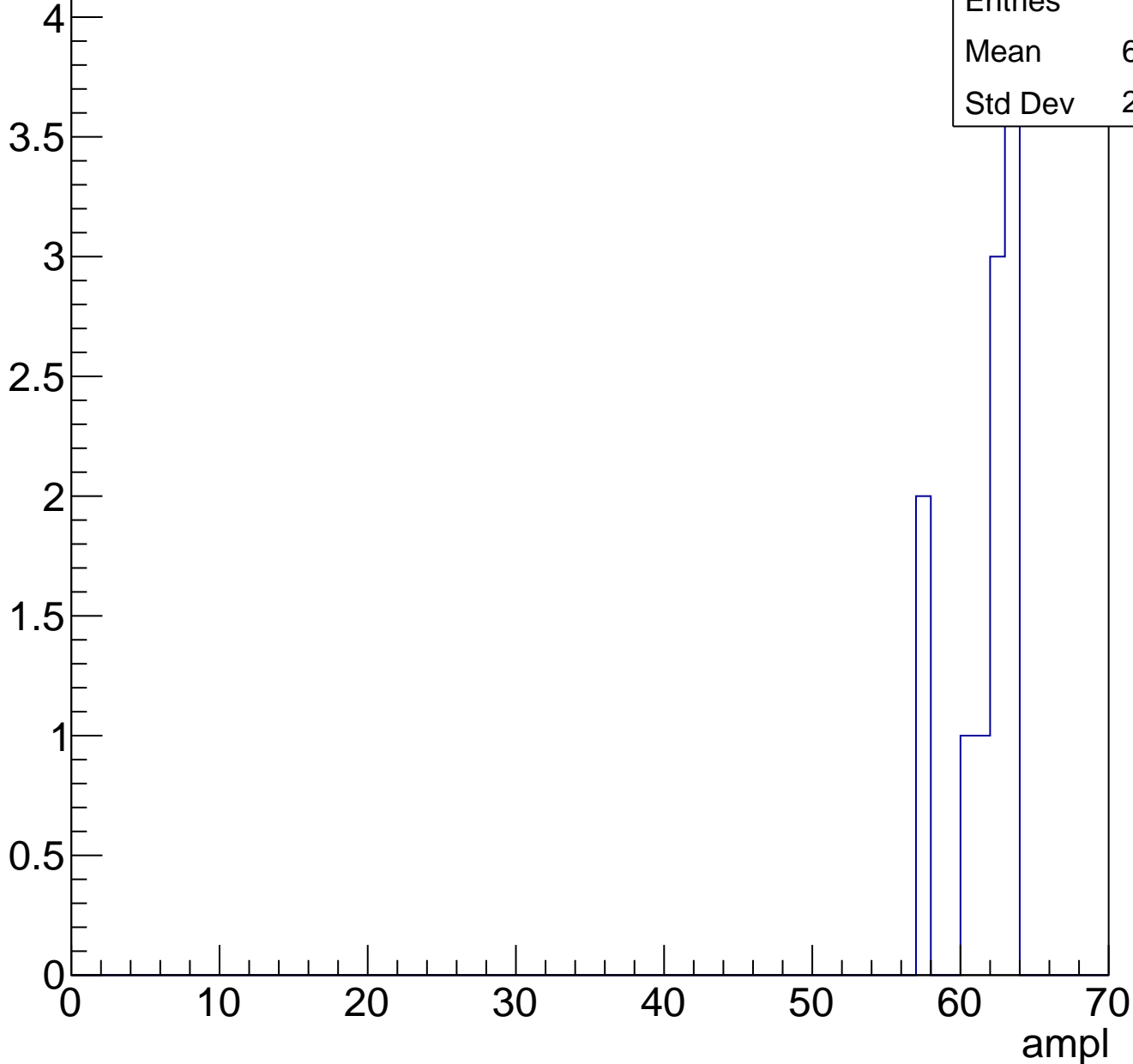
ampl



B1L001S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	11
Mean	61.18
Std Dev	2.167

B1L001S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

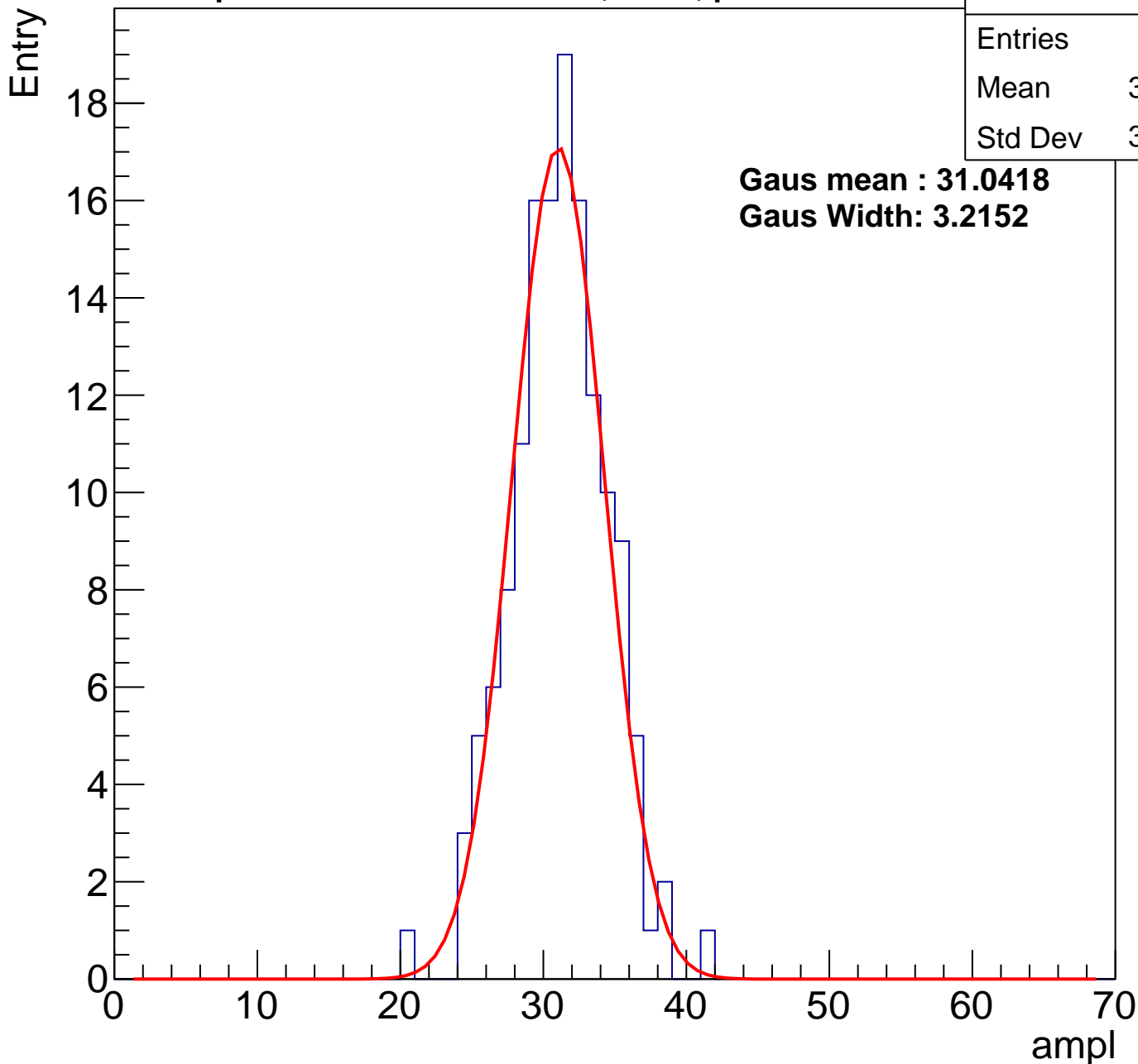
B1L001S, U14-ch18, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	30.69
Std Dev	3.314

Gaus mean : 31.0418

Gaus Width: 3.2152



B1L001S, U14-ch18, adc1

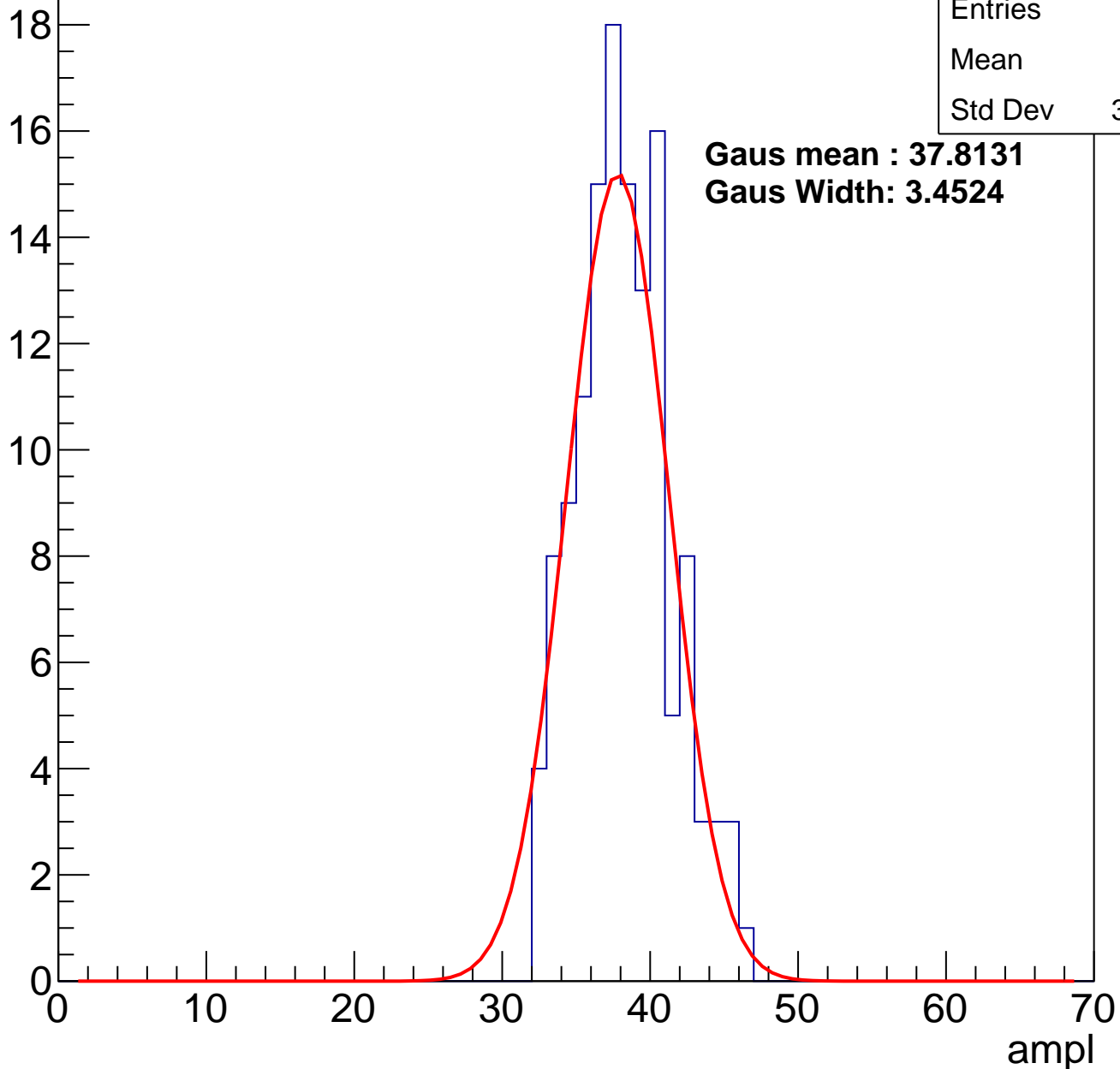
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	37.8
Std Dev	3.142

Gaus mean : 37.8131

Gaus Width: 3.4524

Entry



B1L001S, U14-ch18, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

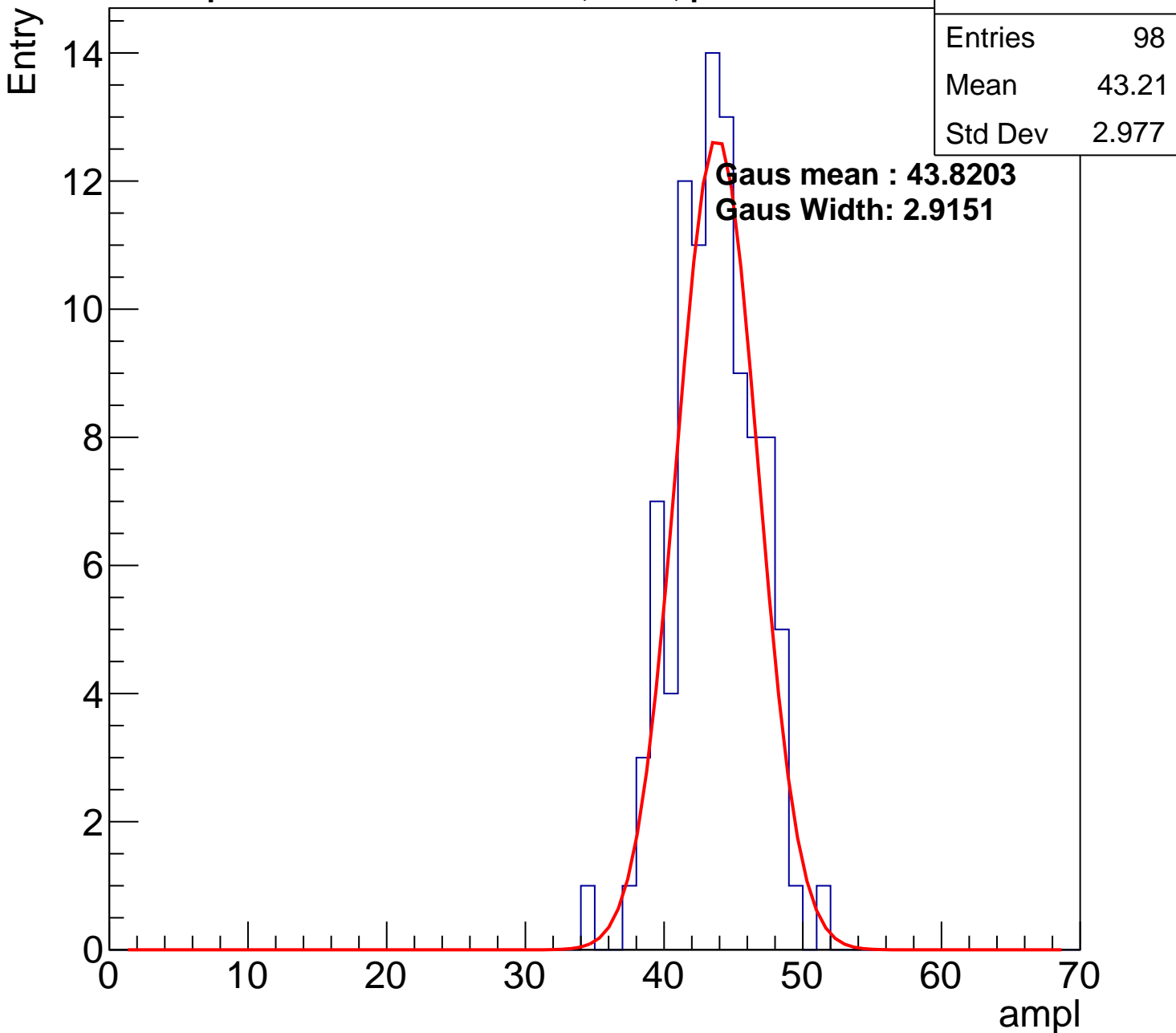
14
12
10
8
6
4
2
0

Entries	98
Mean	43.21
Std Dev	2.977

Gaus mean : 43.8203
Gaus Width: 2.9151

ampl

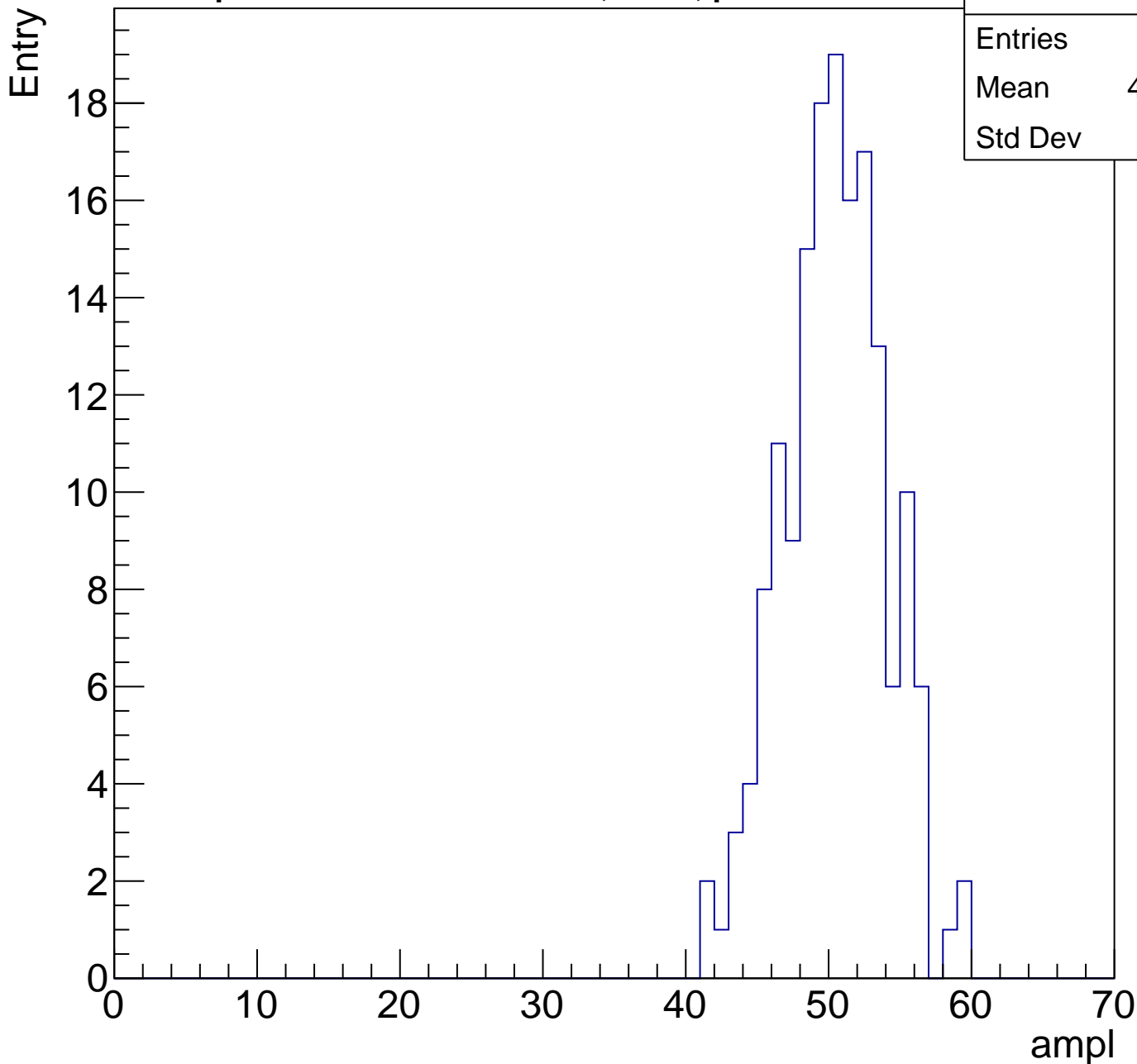
0 10 20 30 40 50 60 70



B1L001S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	161
Mean	49.97
Std Dev	3.56



B1L001S, U14-ch18, adc4

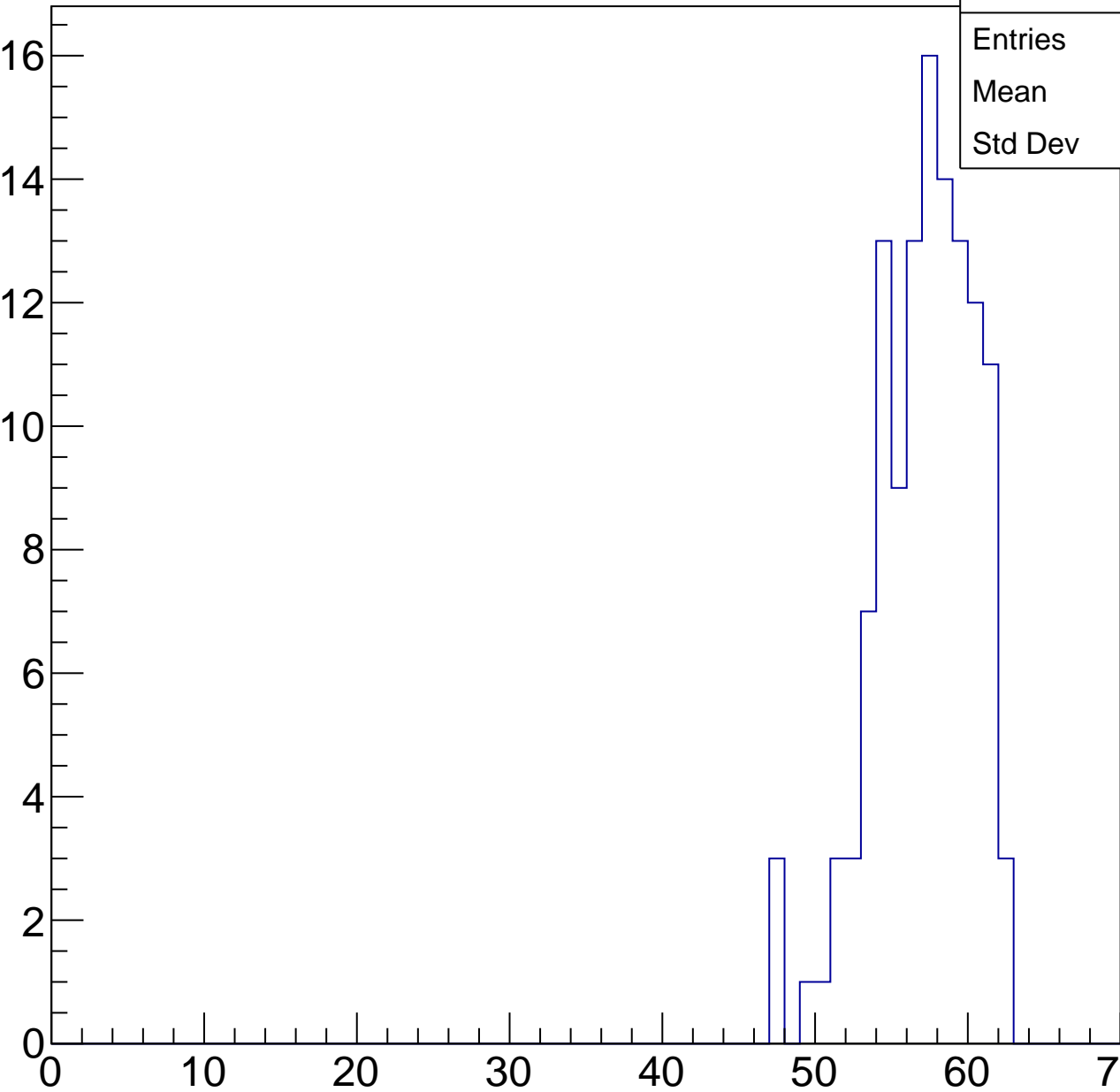
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	122
Mean	56.66
Std Dev	3.246

ampl



B1L001S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#2, port C2

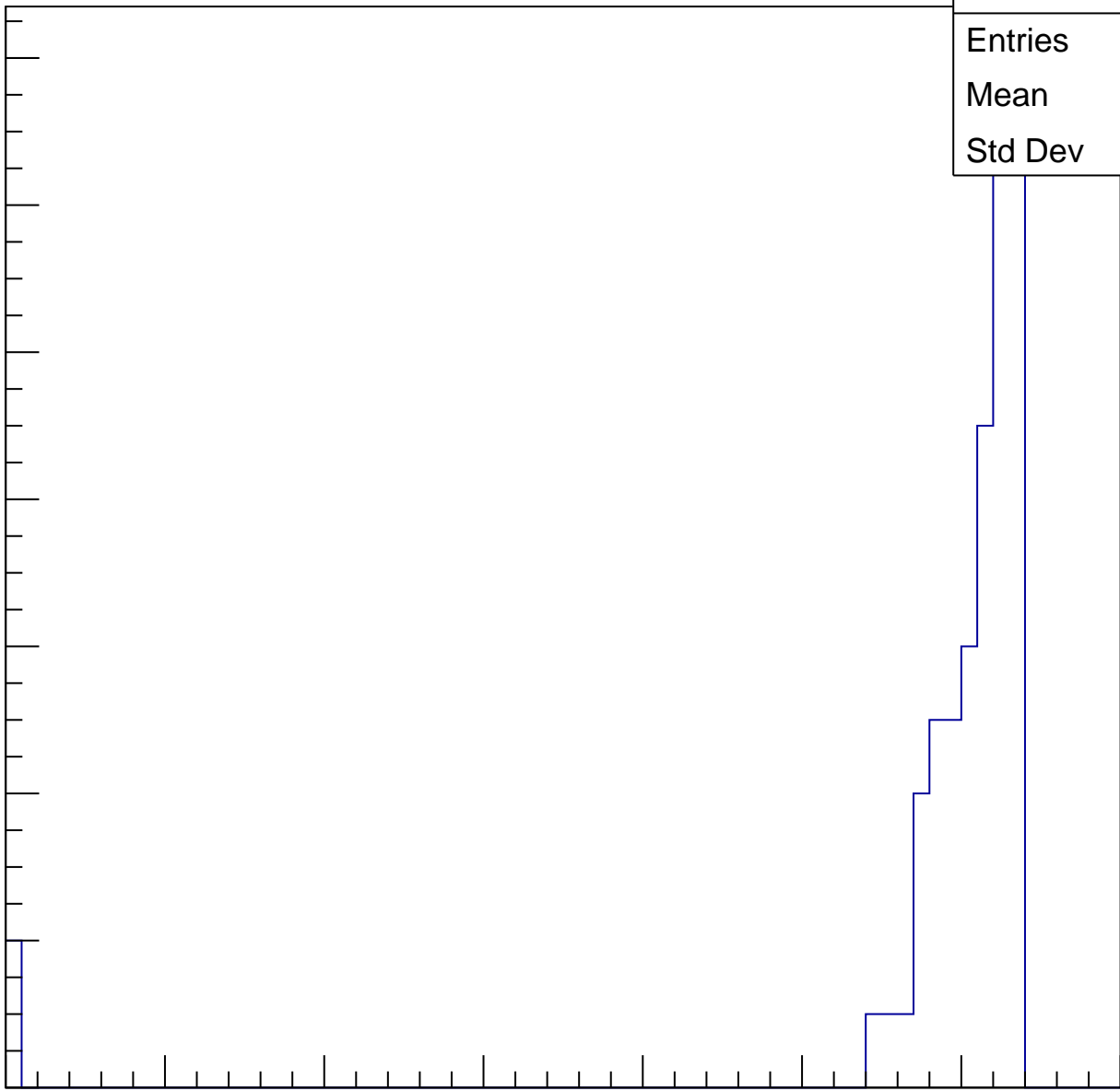
Entries	62
Mean	58.66
Std Dev	10.94

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

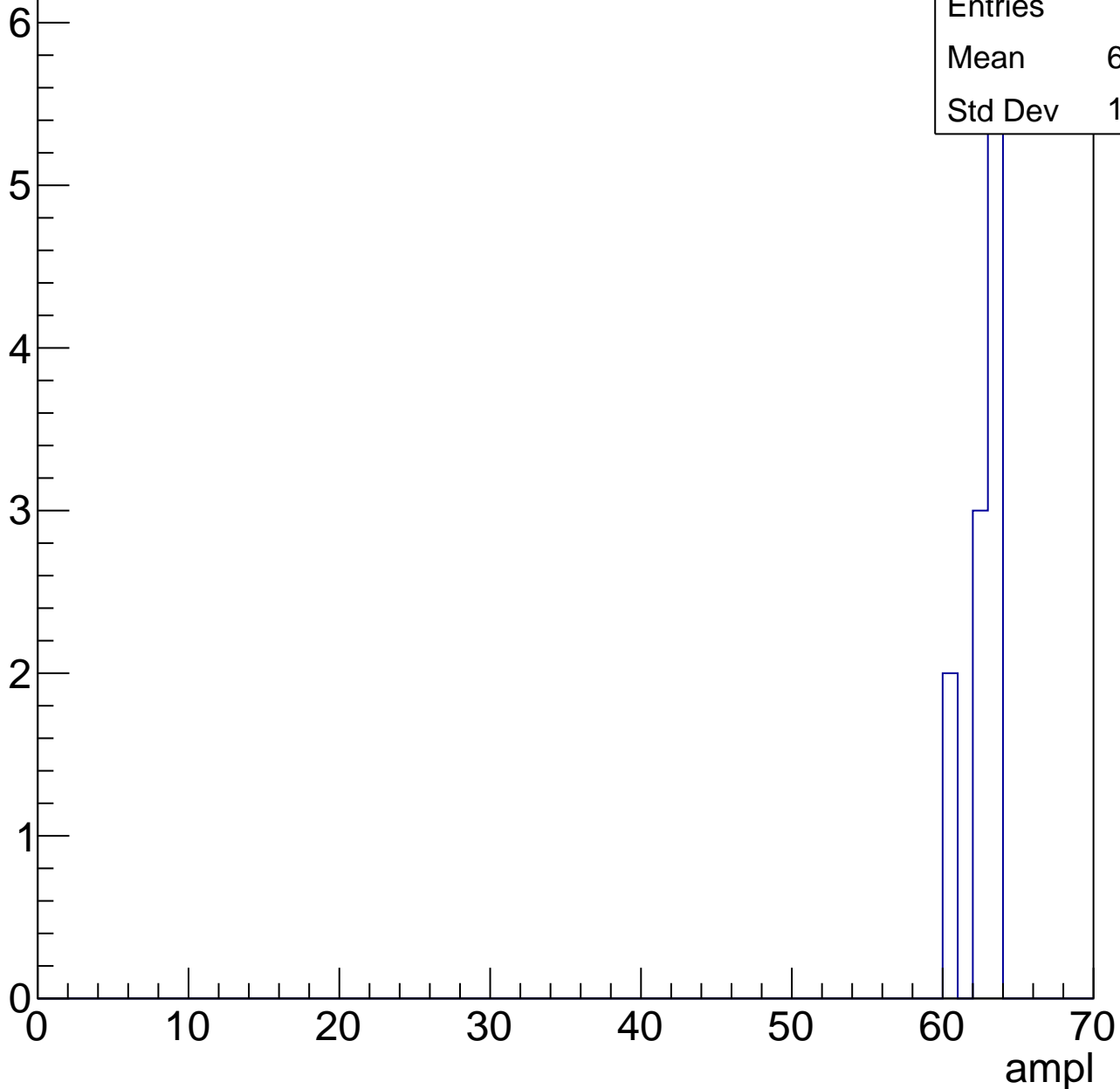


B1L001S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.18
Std Dev	1.113



B1L001S, U14-ch18, adc7

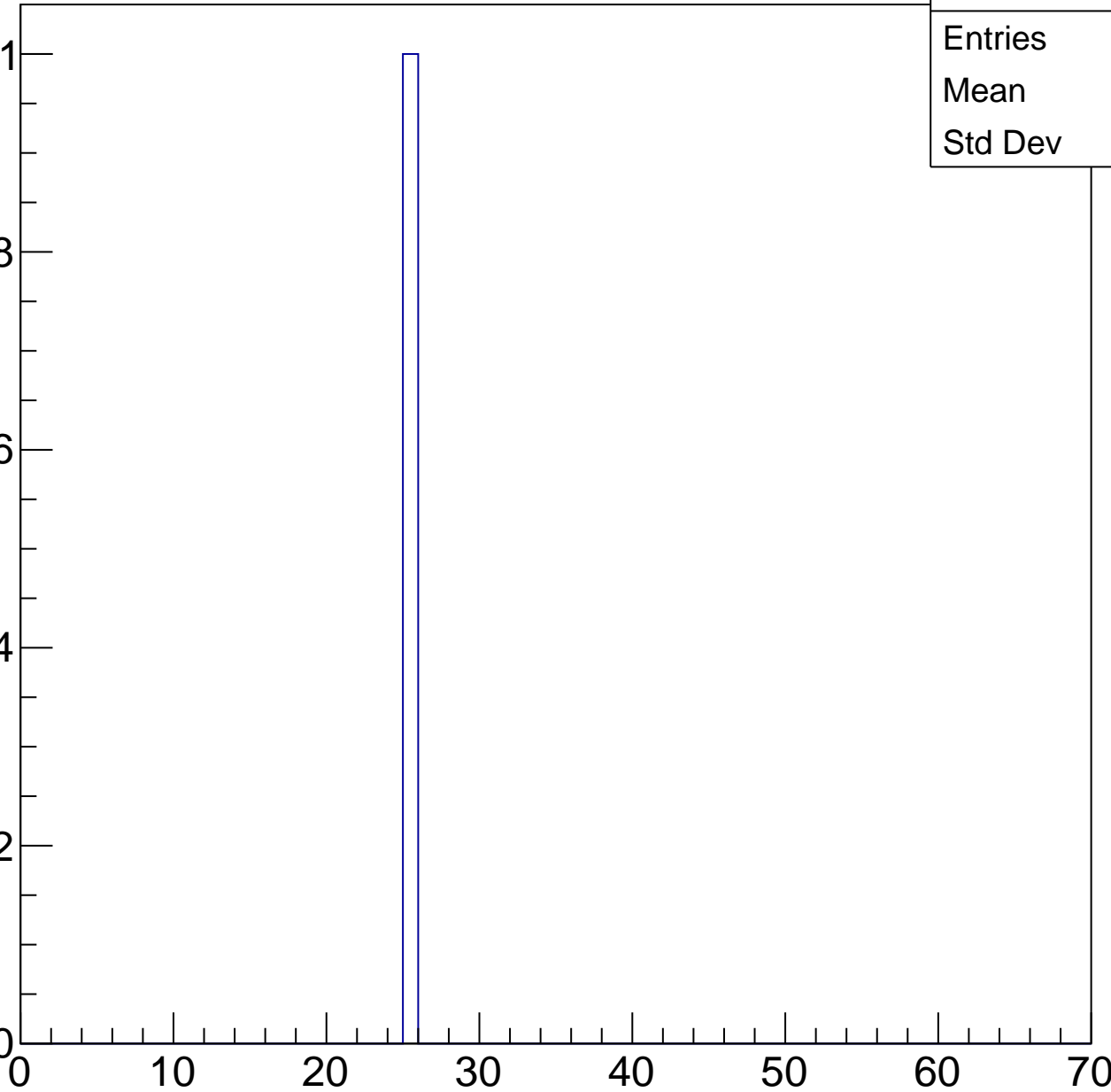
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl



B1L001S, U14-ch19, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	30.42
Std Dev	3.618

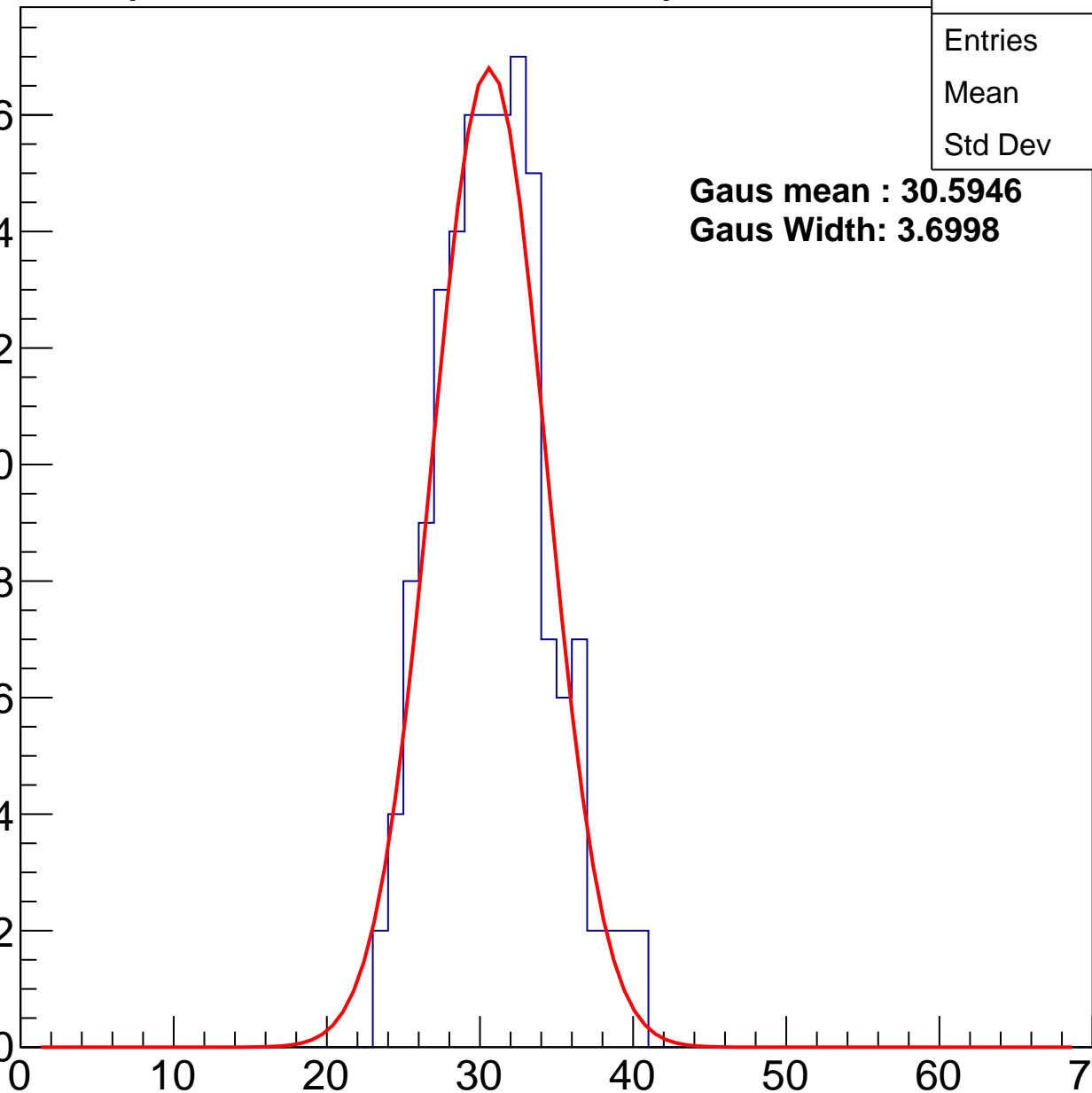
Gaus mean : 30.5946

Gaus Width: 3.6998

Entry

16
14
12
10
8
6
4
2
0

ampl

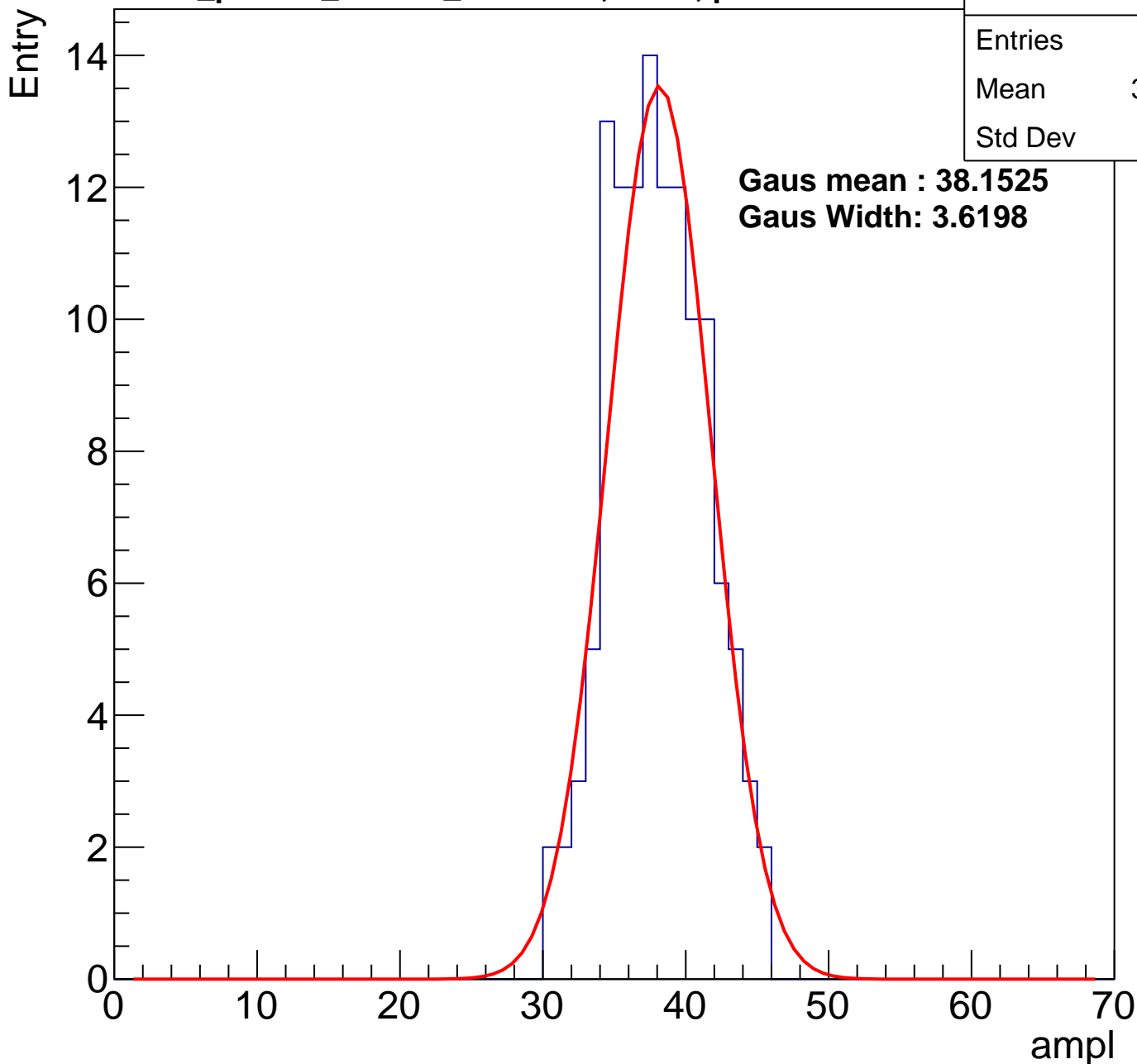


B1L001S, U14-ch19, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	37.54
Std Dev	3.35

Gaus mean : 38.1525
Gaus Width: 3.6198



B1L001S, U14-ch19, adc2

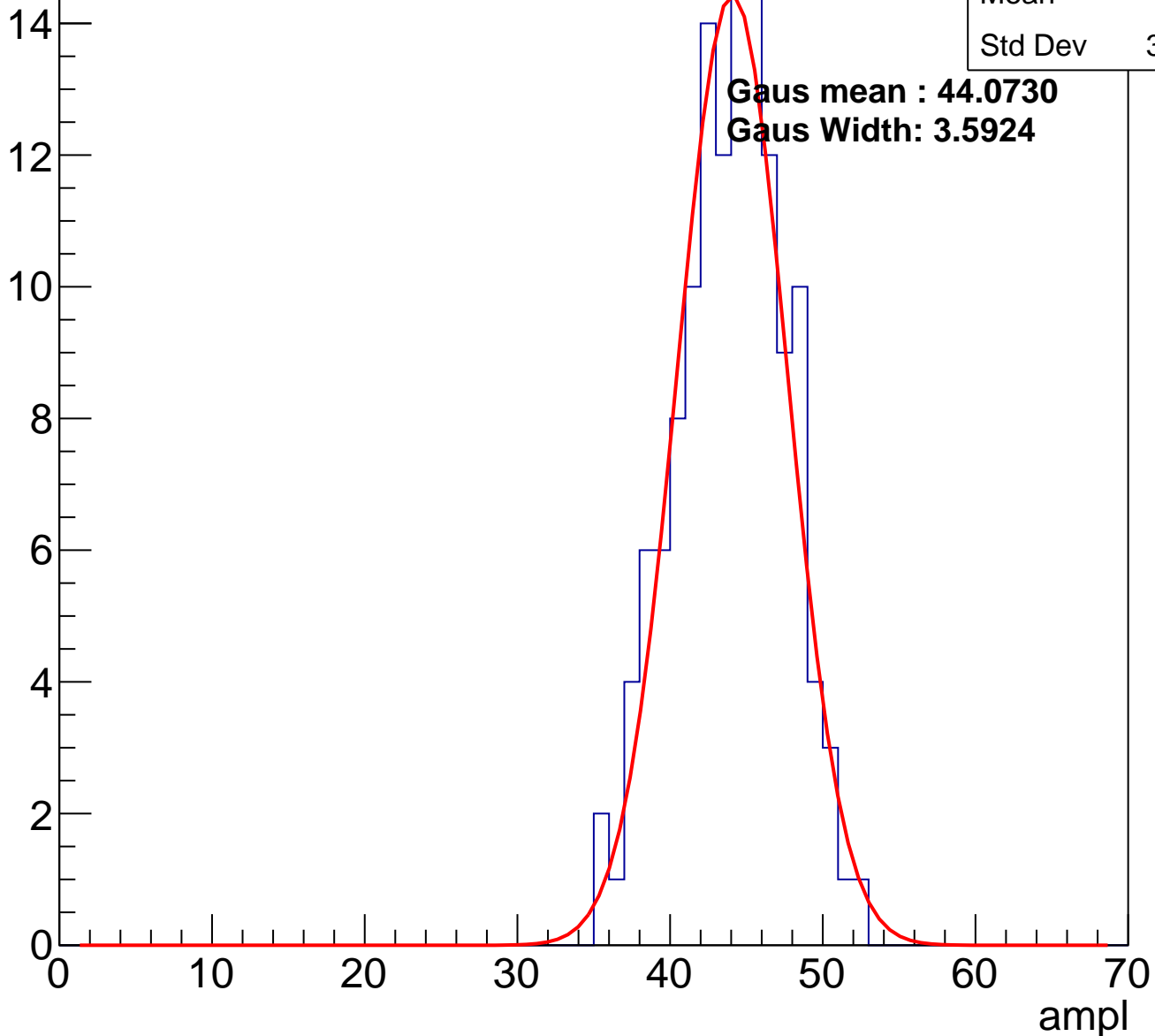
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	43.53
Std Dev	3.543

Gaus mean : 44.0730

Gaus Width: 3.5924

Entry



B1L001S, U14-ch19, adc3

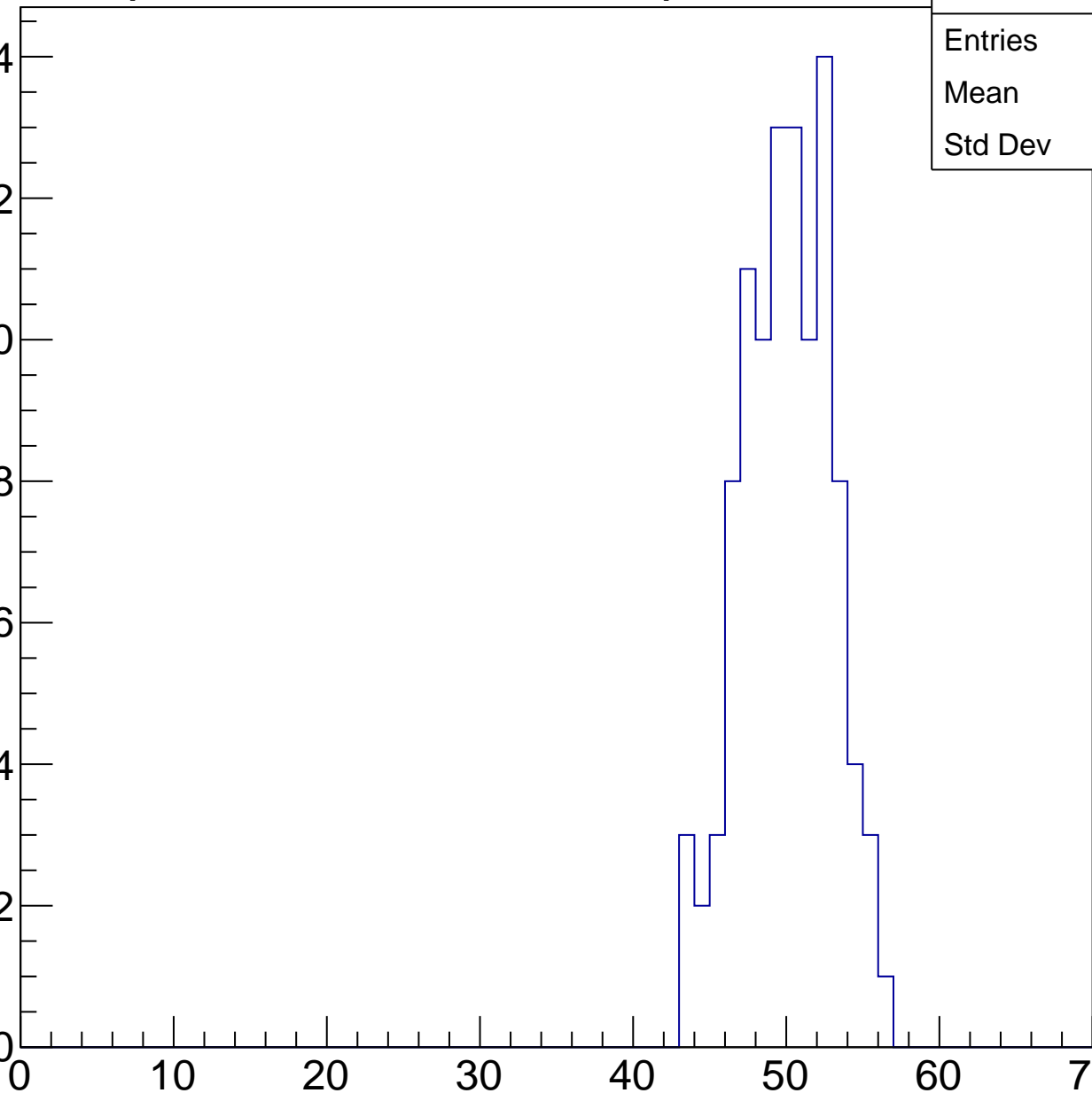
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	49.54
Std Dev	2.899

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	56.14
Std Dev	3.256

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

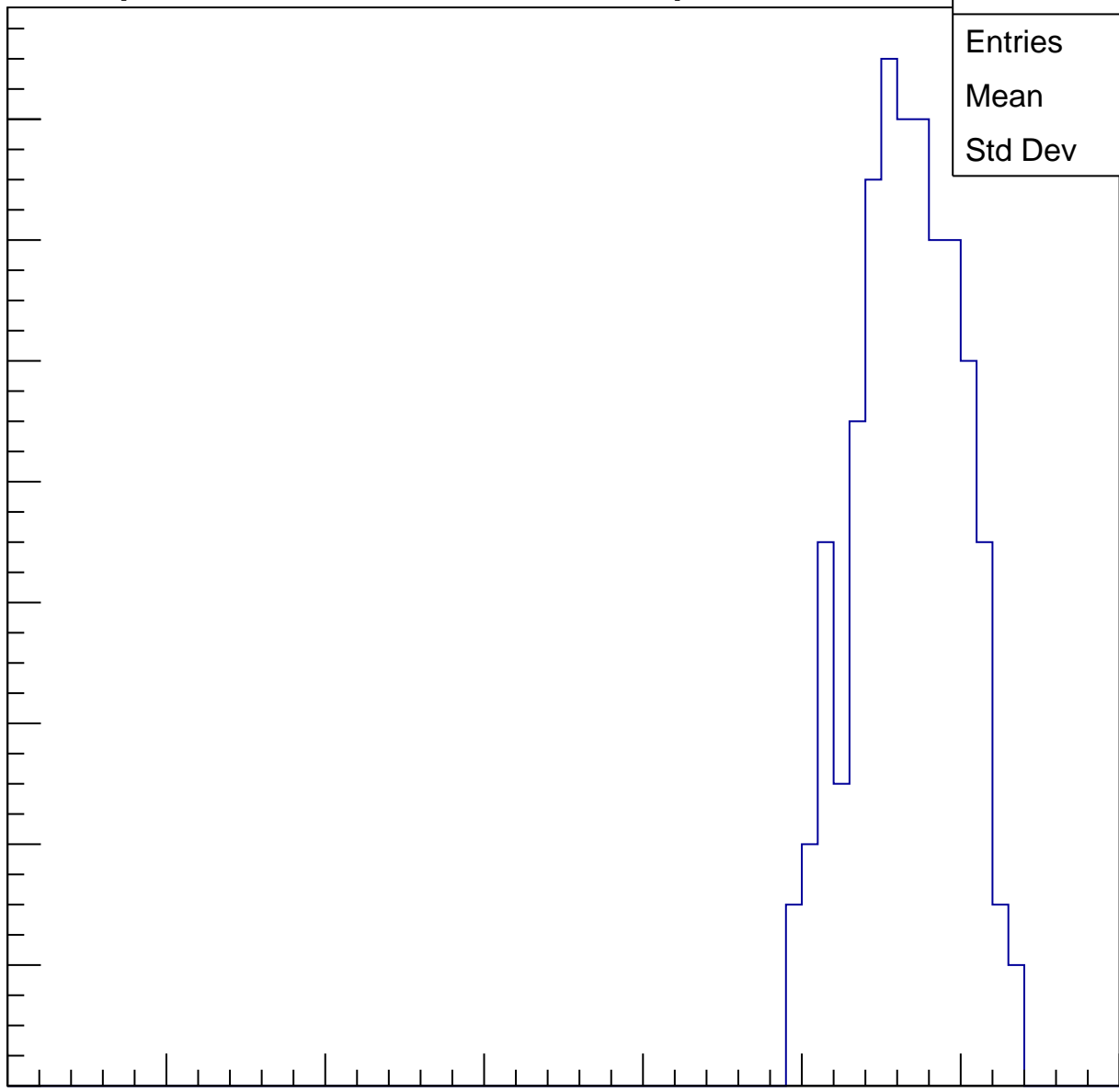
40

50

60

70

ampl



B1L001S, U14-ch19, adc5

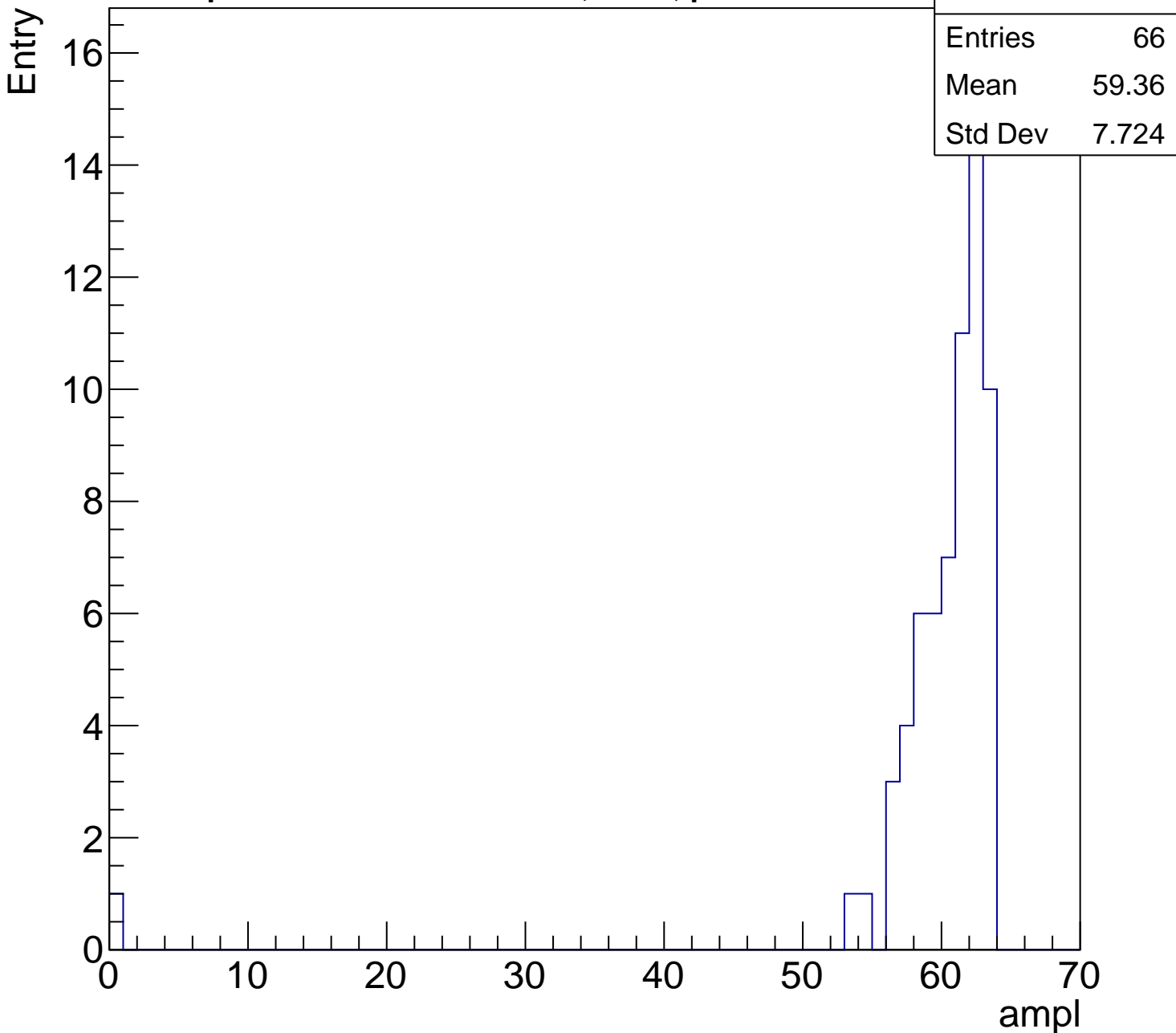
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	66
Mean	59.36
Std Dev	7.724

ampl

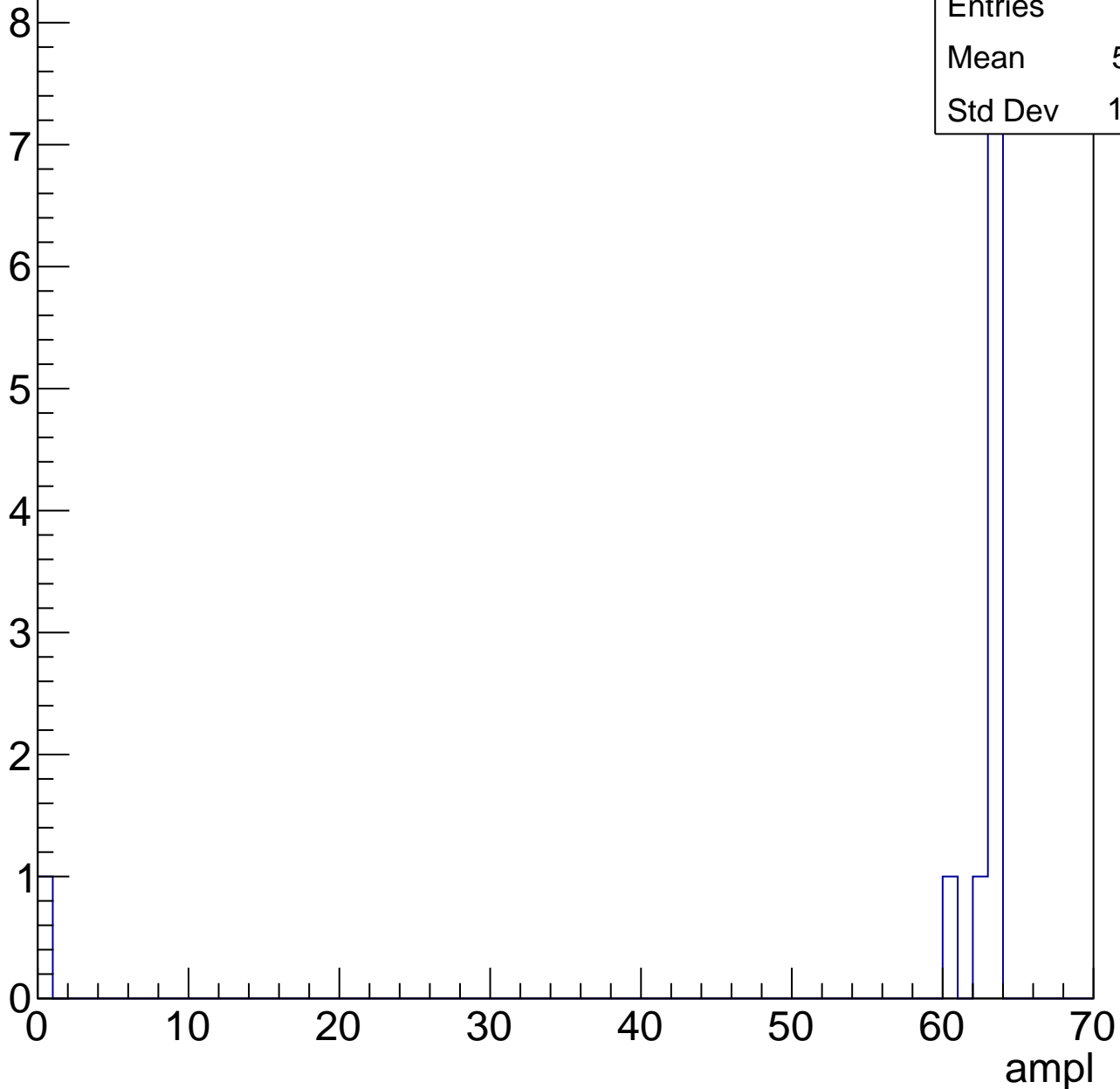


B1L001S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.91
Std Dev	18.02



B1L001S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L001S, U14-ch20, adc0

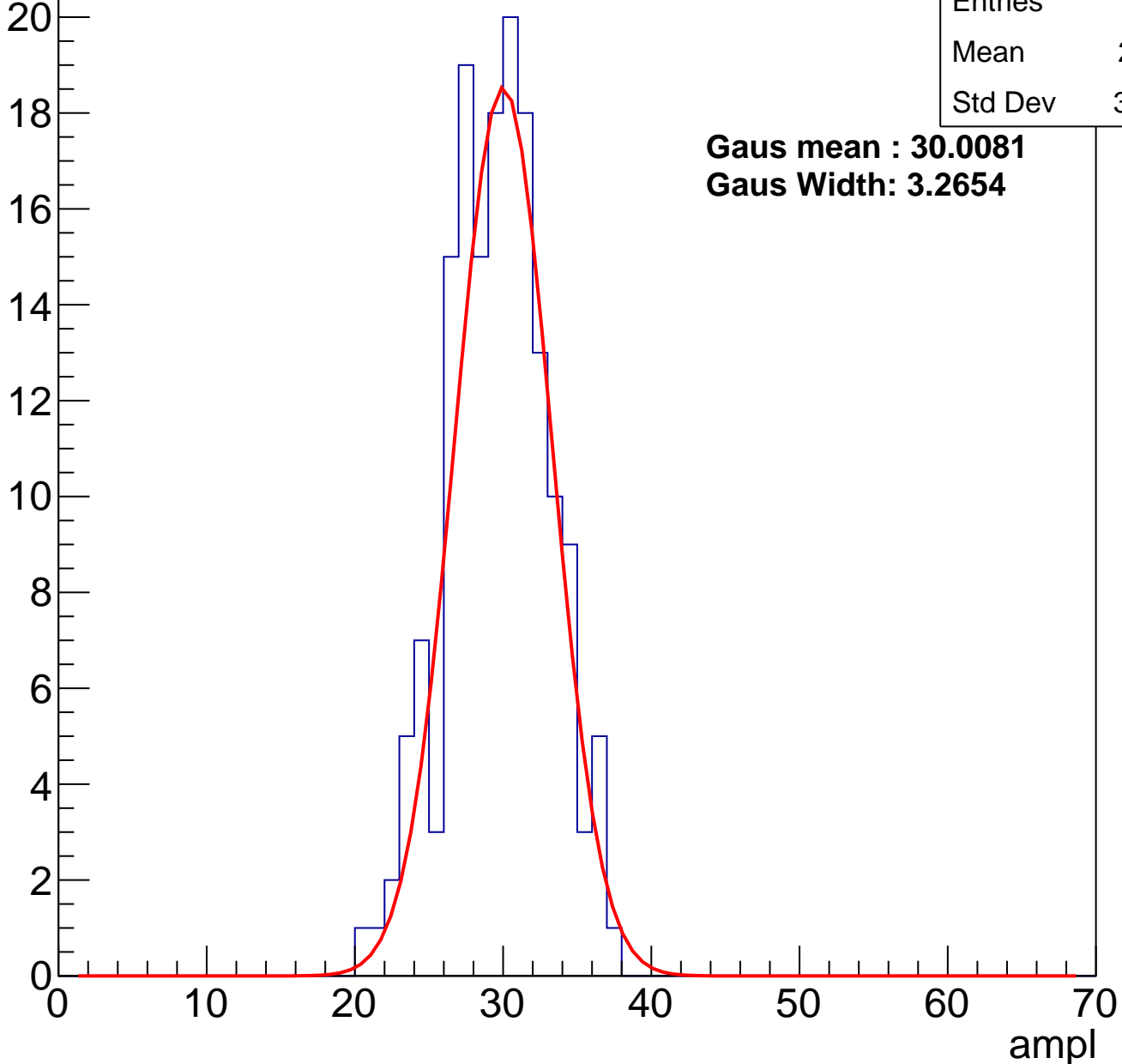
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	29.21
Std Dev	3.372

Gaus mean : 30.0081

Gaus Width: 3.2654

Entry



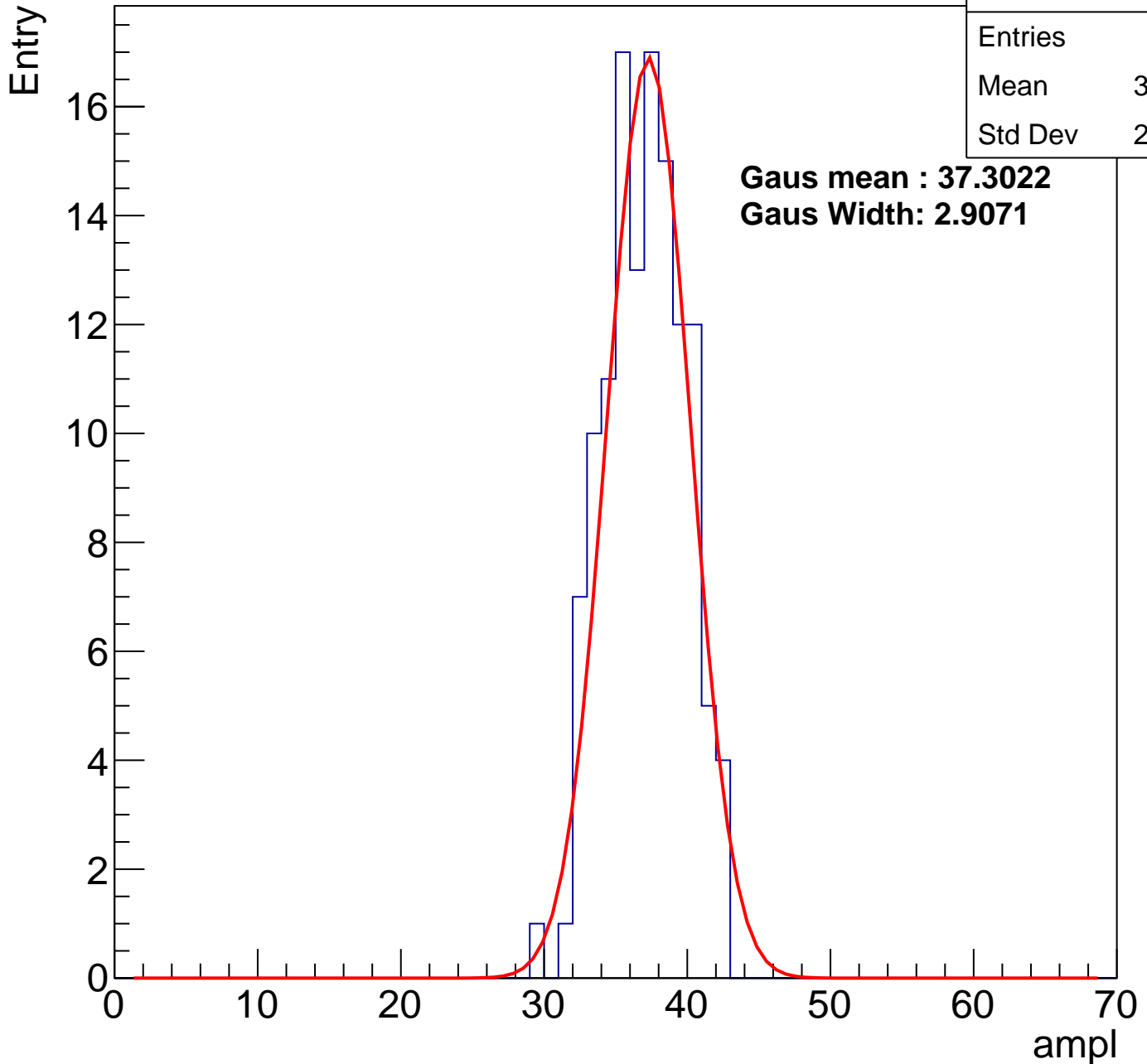
B1L001S, U14-ch20, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.57
Std Dev	2.746

Gaus mean : 37.3022

Gaus Width: 2.9071



B1L001S, U14-ch20, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	42.83
Std Dev	2.876

Gaus mean : 43.3840

Gaus Width: 3.0654

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

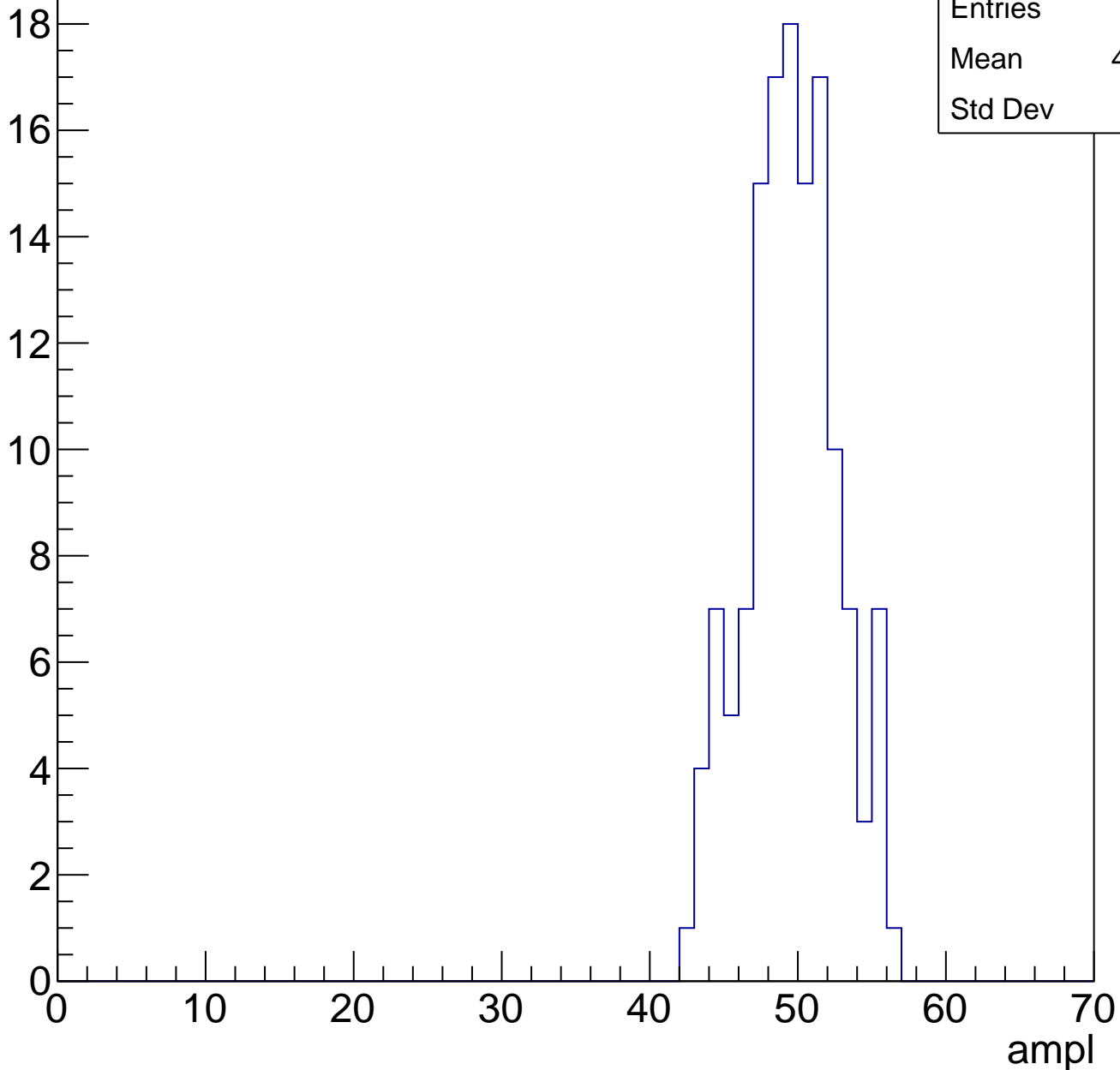
70

B1L001S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	49.13
Std Dev	3.05

Entry



B1L001S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#2, port C2

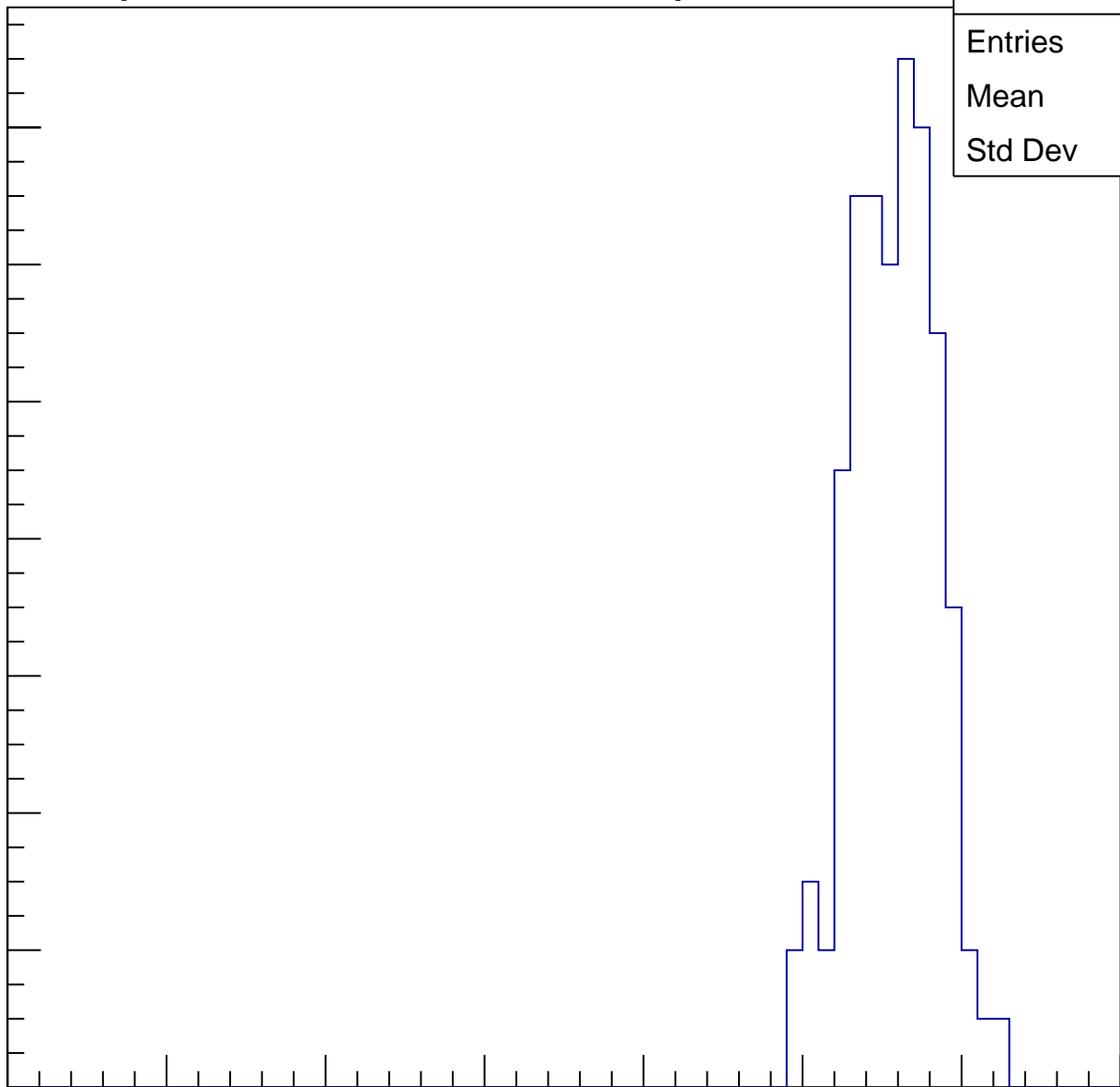
Entries	105
Mean	55.25
Std Dev	2.647

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

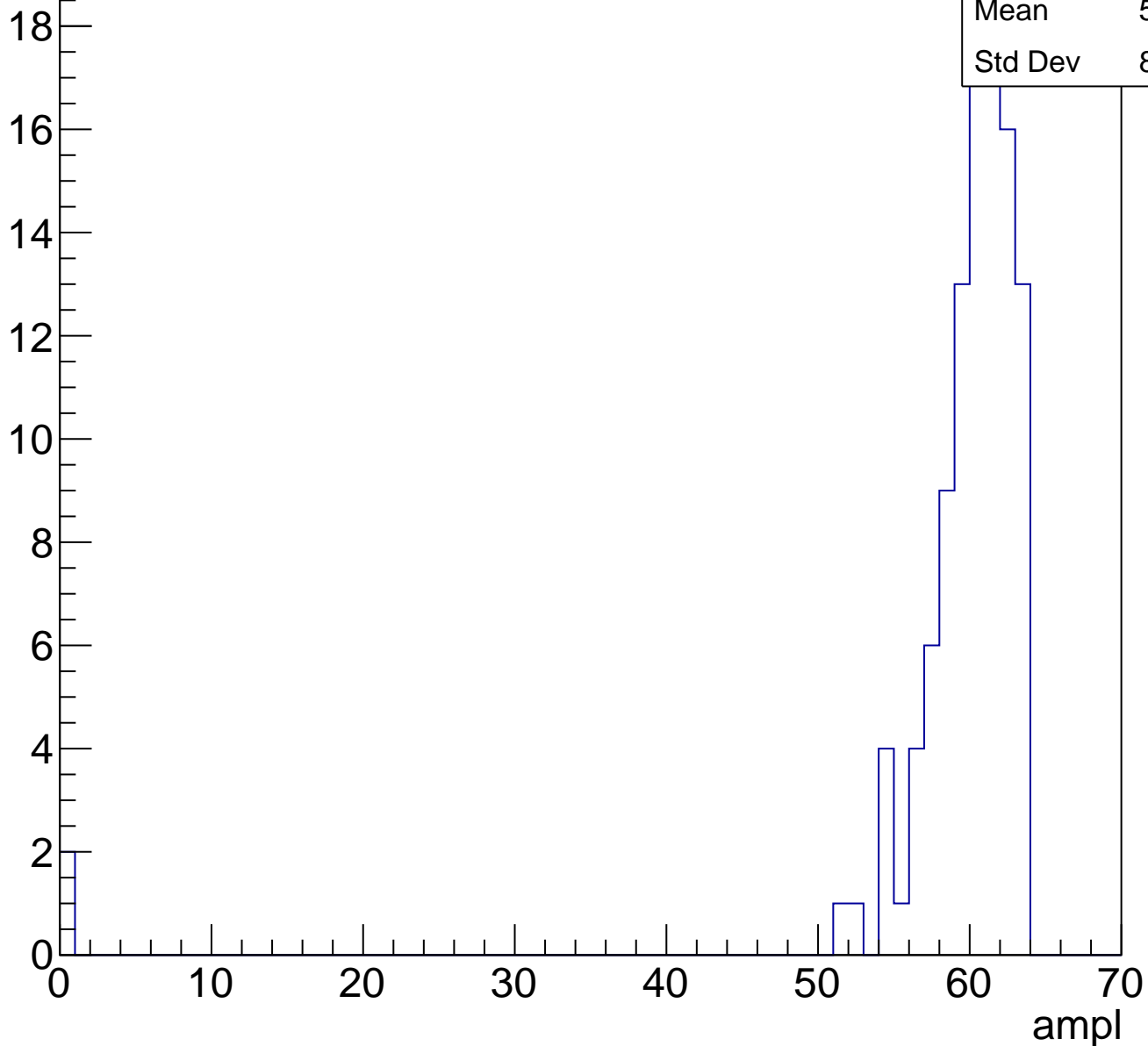
ampl



B1L001S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

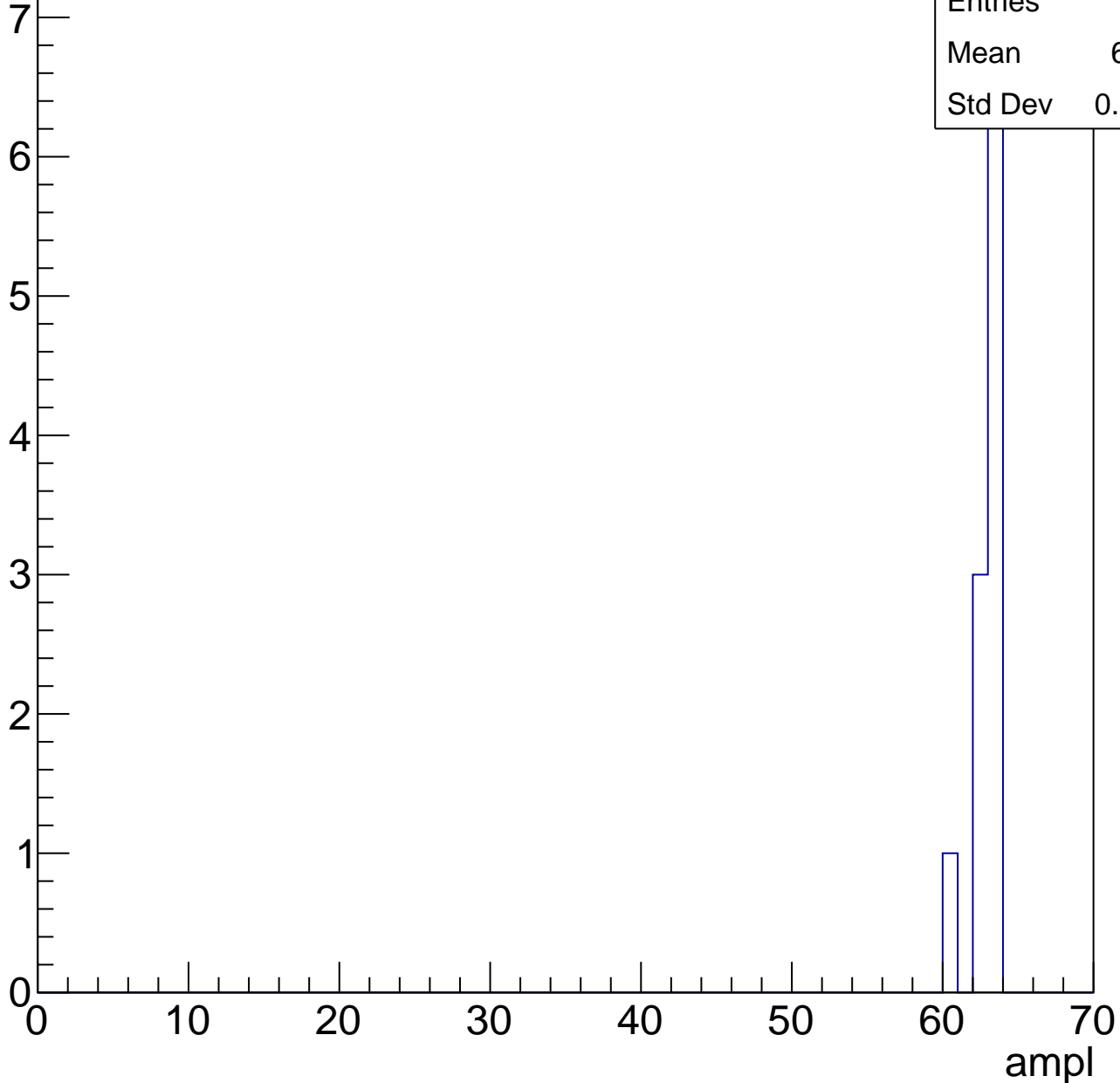


B1L001S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	62.45
Std Dev	0.8907



B1L001S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch21, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	29.63
Std Dev	4.112

Gaus mean : 30.5368

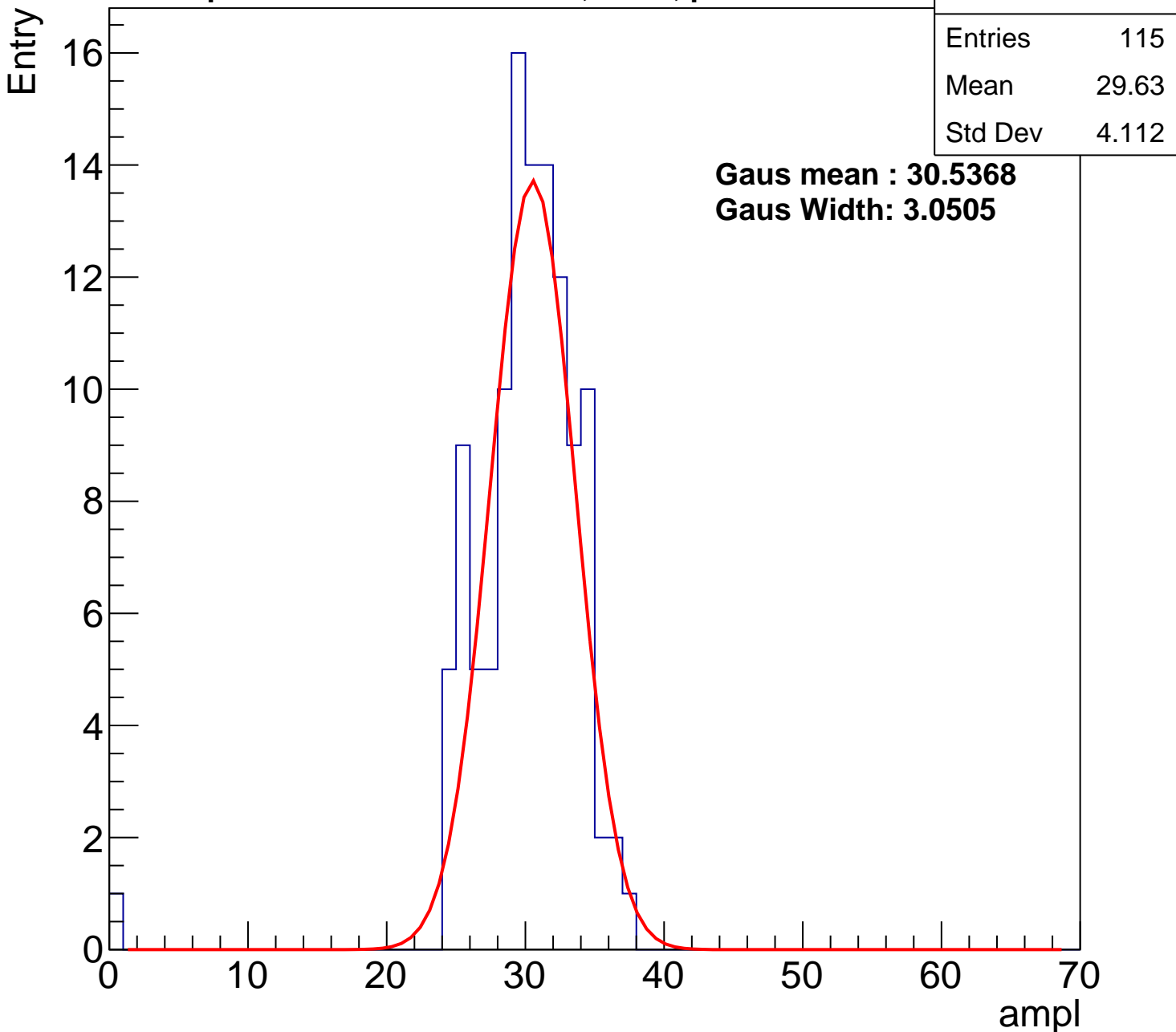
Gaus Width: 3.0505

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch21, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.48
Std Dev	3.044

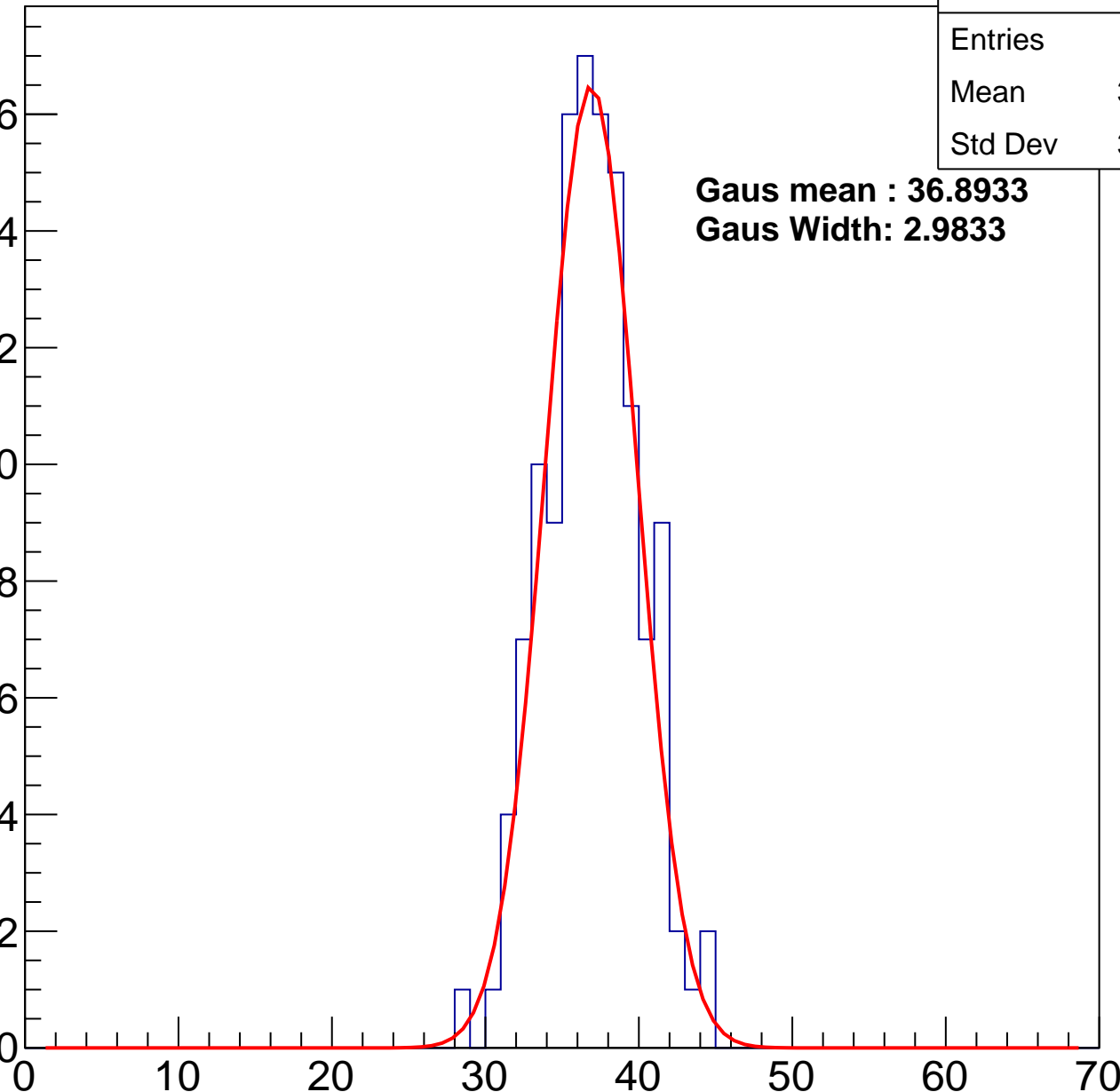
Gaus mean : 36.8933

Gaus Width: 2.9833

Entry

16
14
12
10
8
6
4
2
0

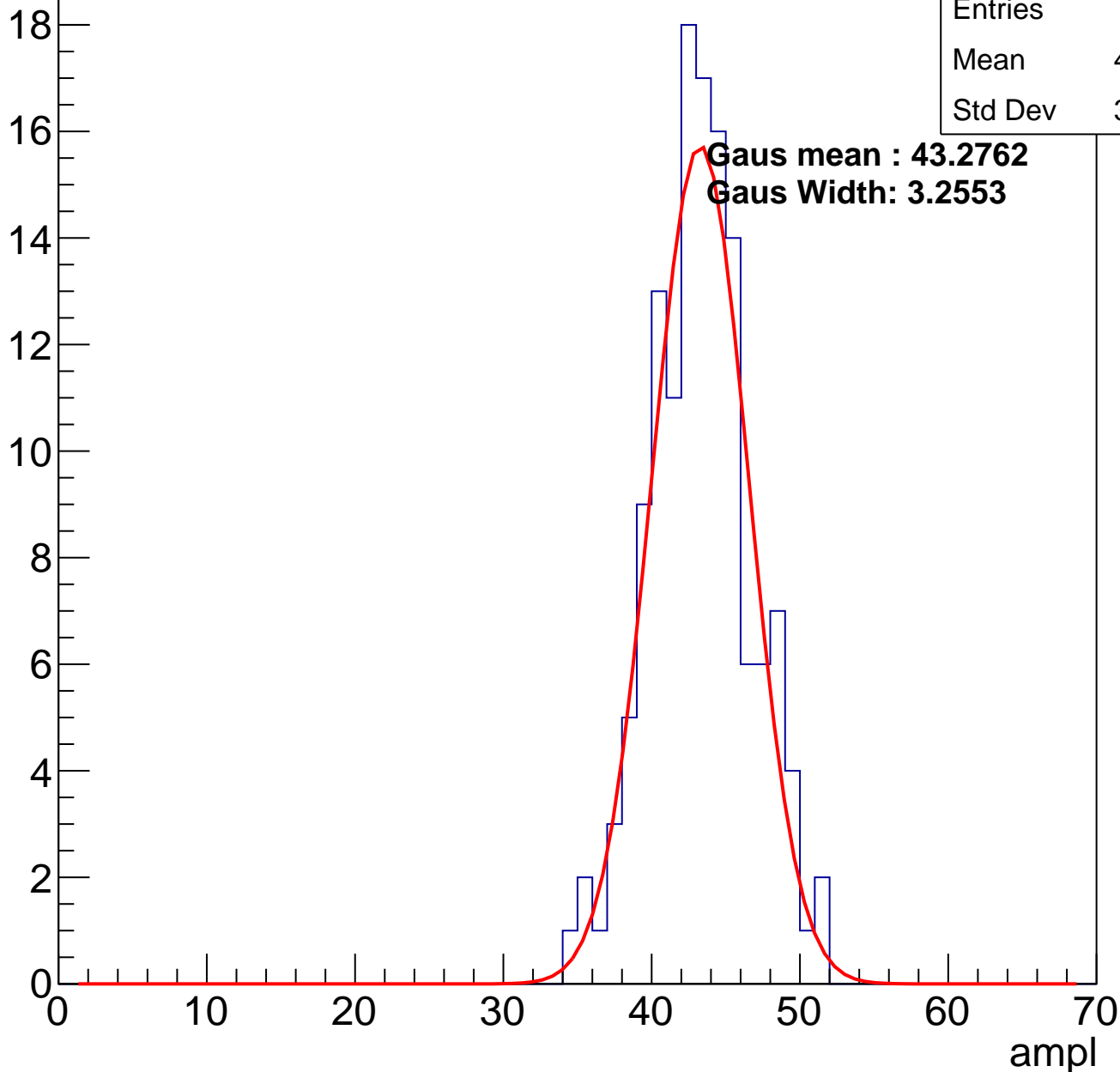
ampl



B1L001S, U14-ch21, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#2, port C2

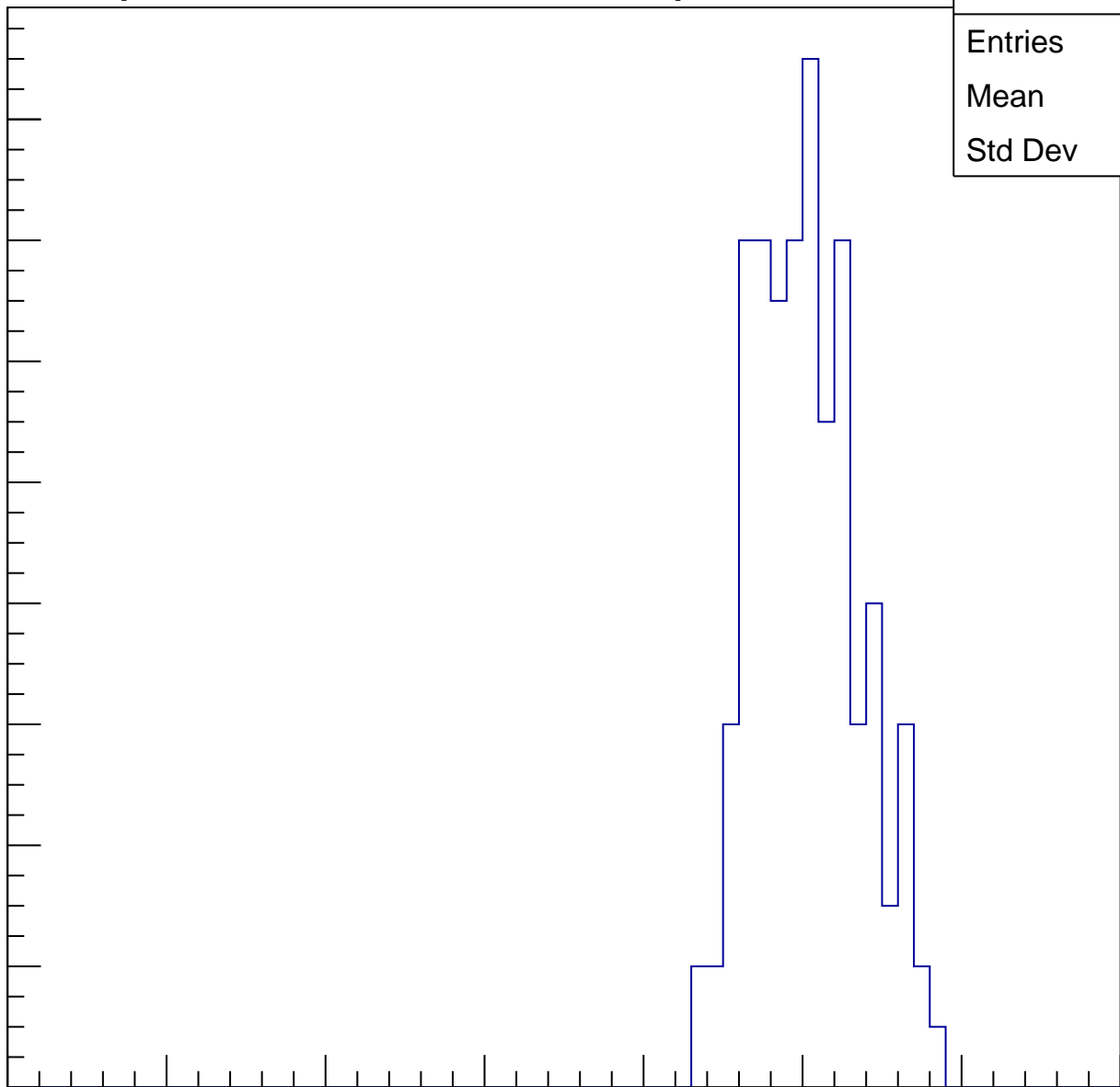
Entries	133
Mean	49.76
Std Dev	3.279

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

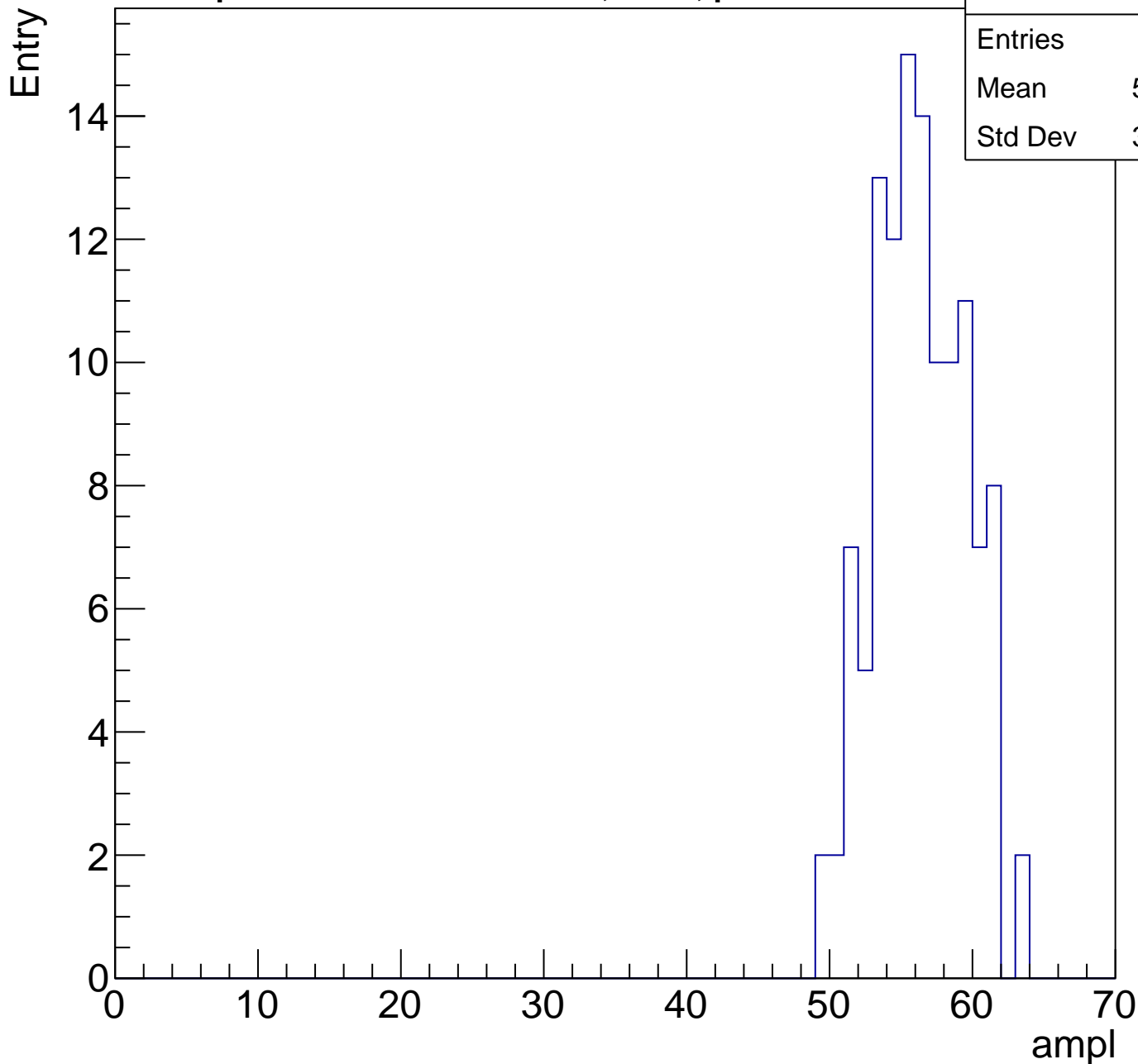
ampl



B1L001S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#2, port C2

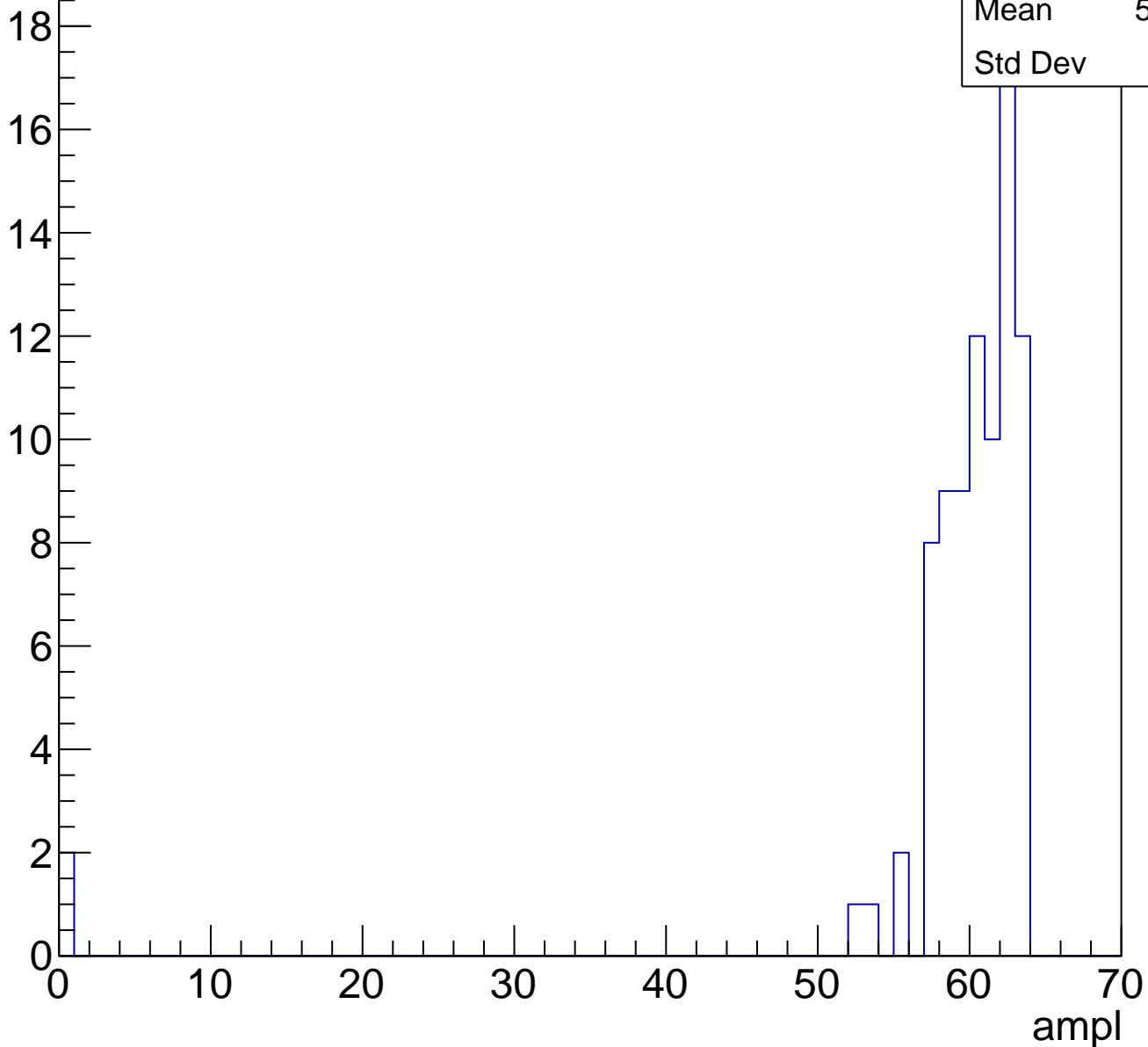
Entries	118
Mean	55.88
Std Dev	3.139



B1L001S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

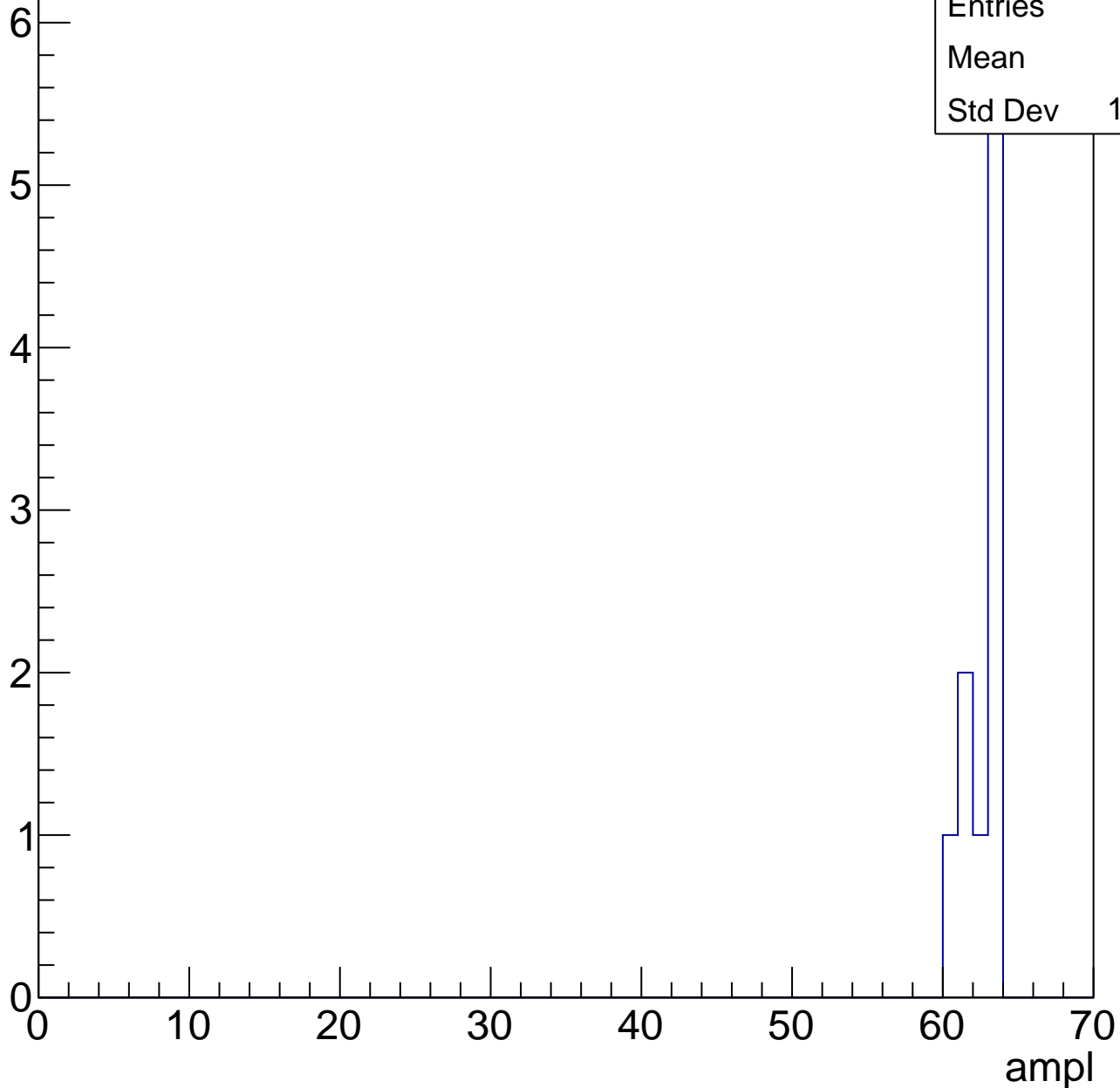


B1L001S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	10
Mean	62.2
Std Dev	1.077



B1L001S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L001S, U14-ch22, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	28.34
Std Dev	2.909

Gaus mean : 29.2179

Gaus Width: 2.7405

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

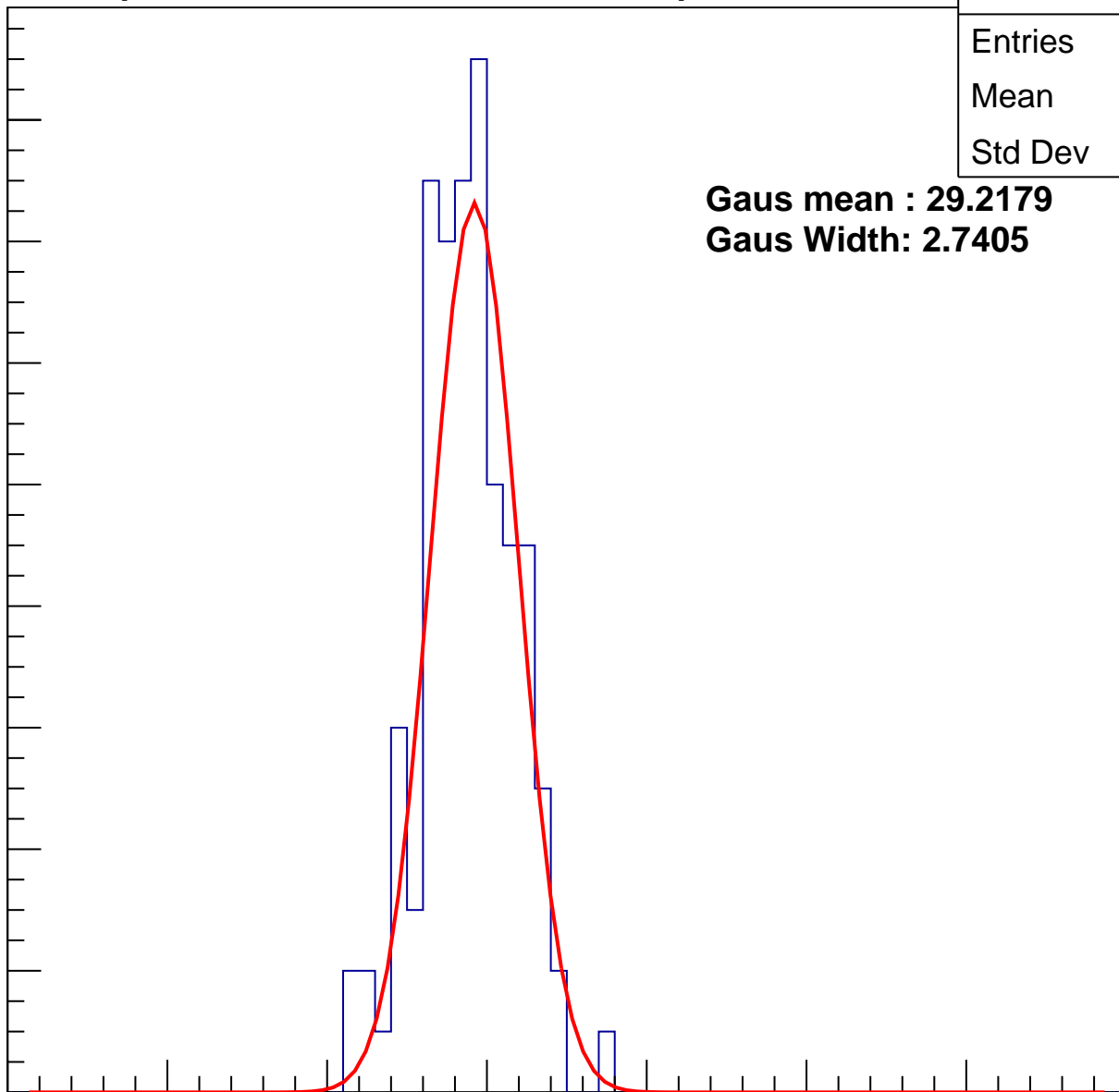
40

50

60

70

ampl



B1L001S, U14-ch22, adc1

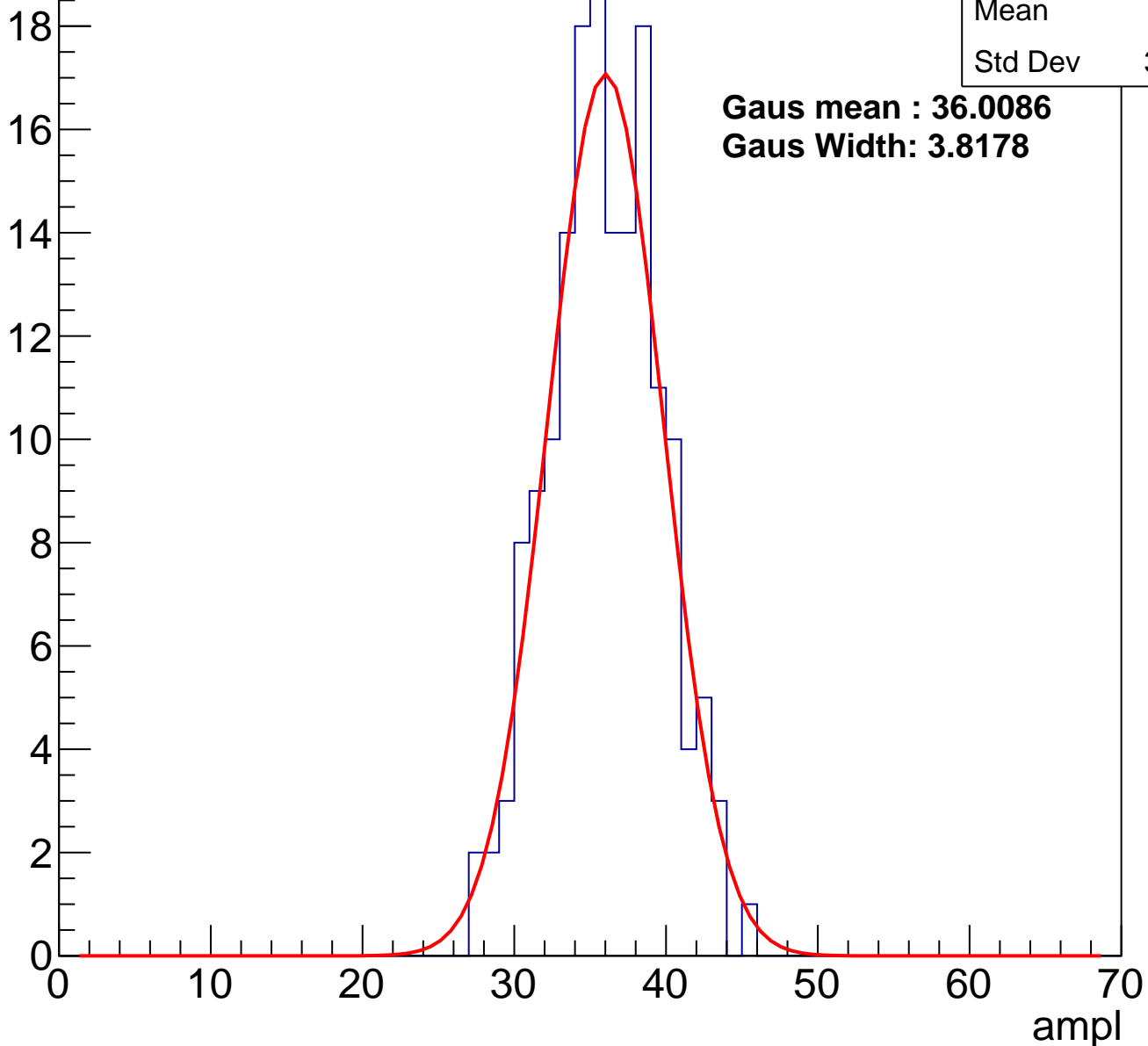
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	35.5
Std Dev	3.601

Gaus mean : 36.0086

Gaus Width: 3.8178

Entry



B1L001S, U14-ch22, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

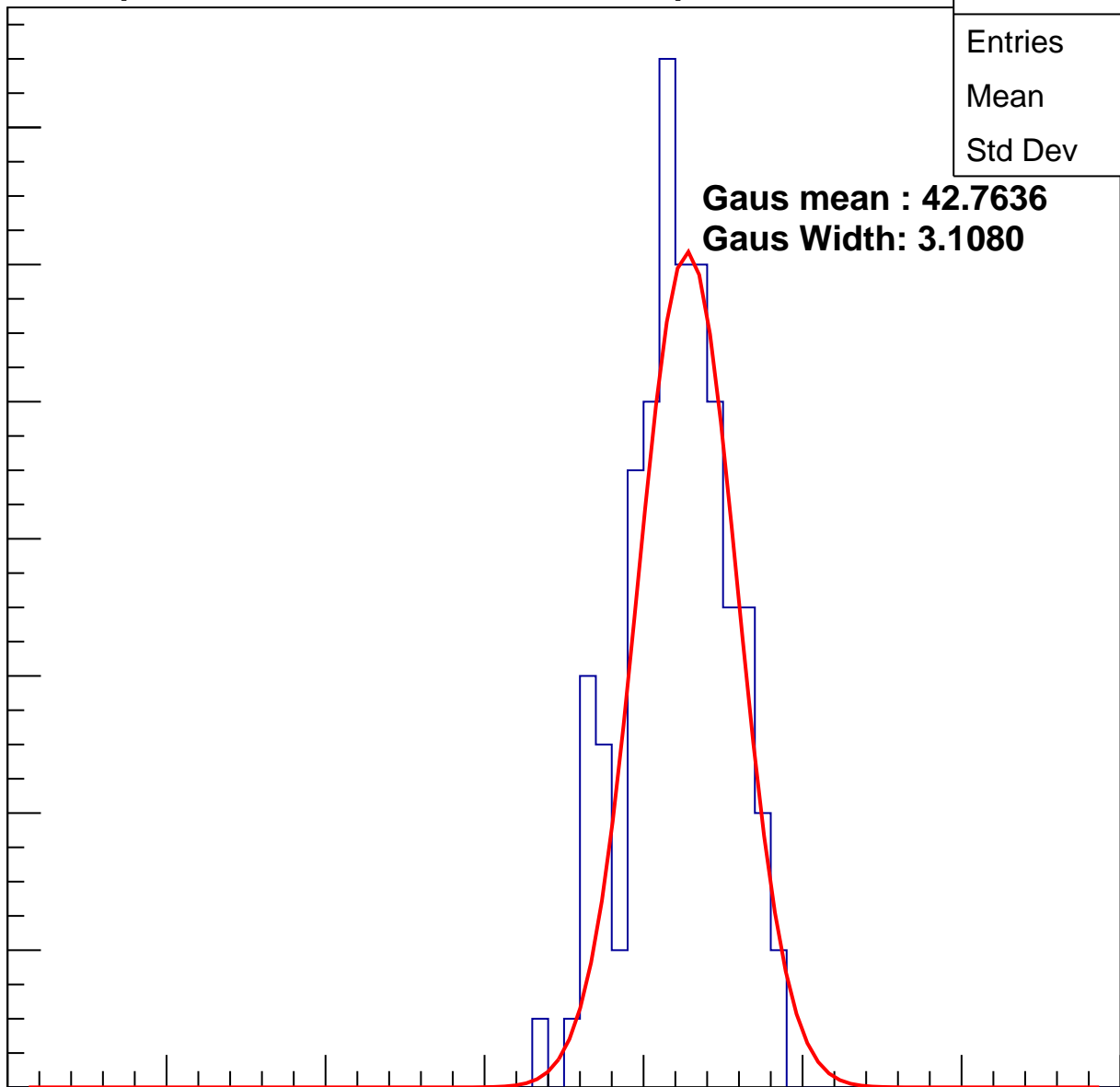
70

ampl

Entries	103
Mean	41.67
Std Dev	3.157

Gaus mean : 42.7636

Gaus Width: 3.1080



B1L001S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	47.72
Std Dev	2.891

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

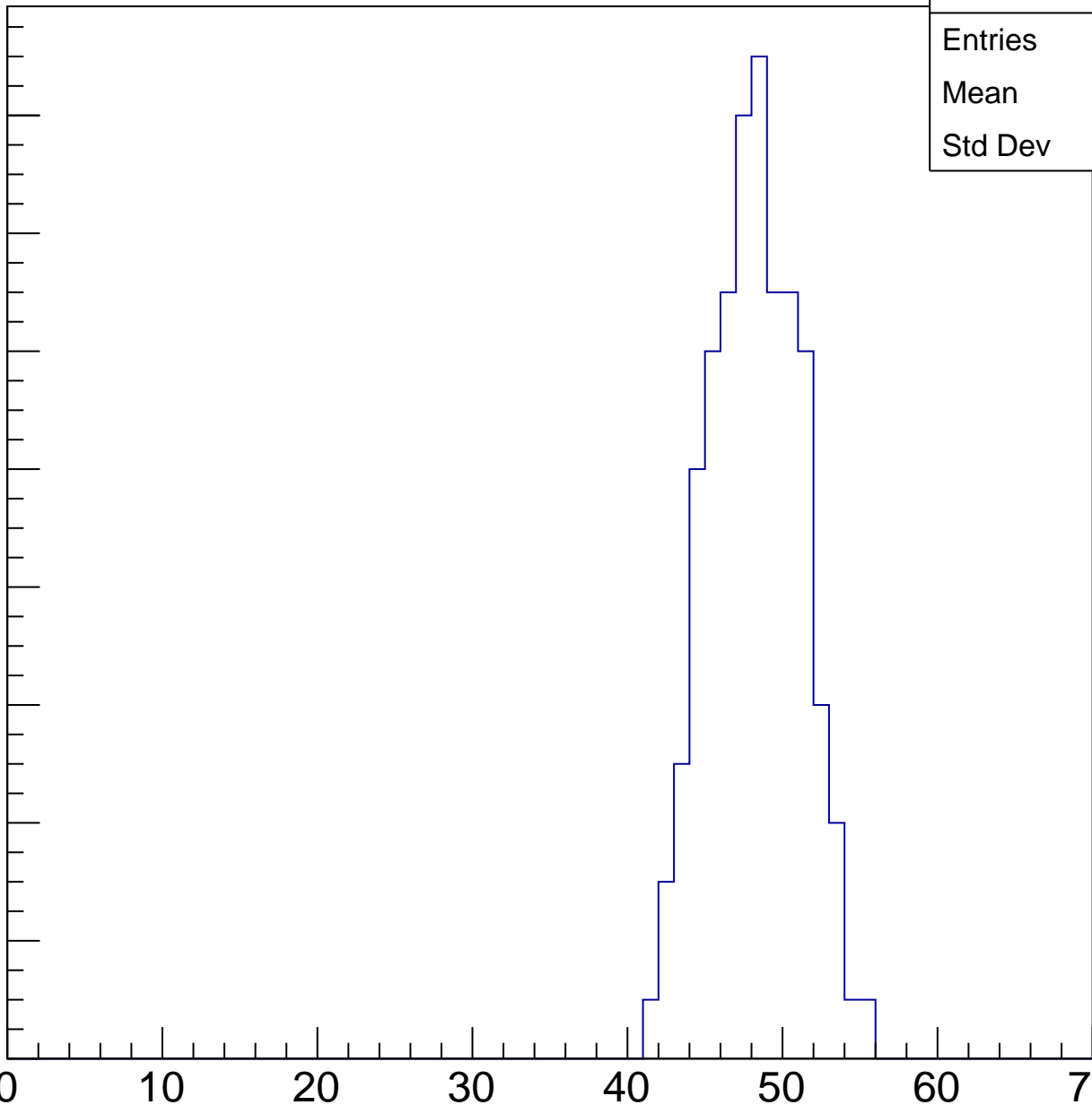
40

50

60

70

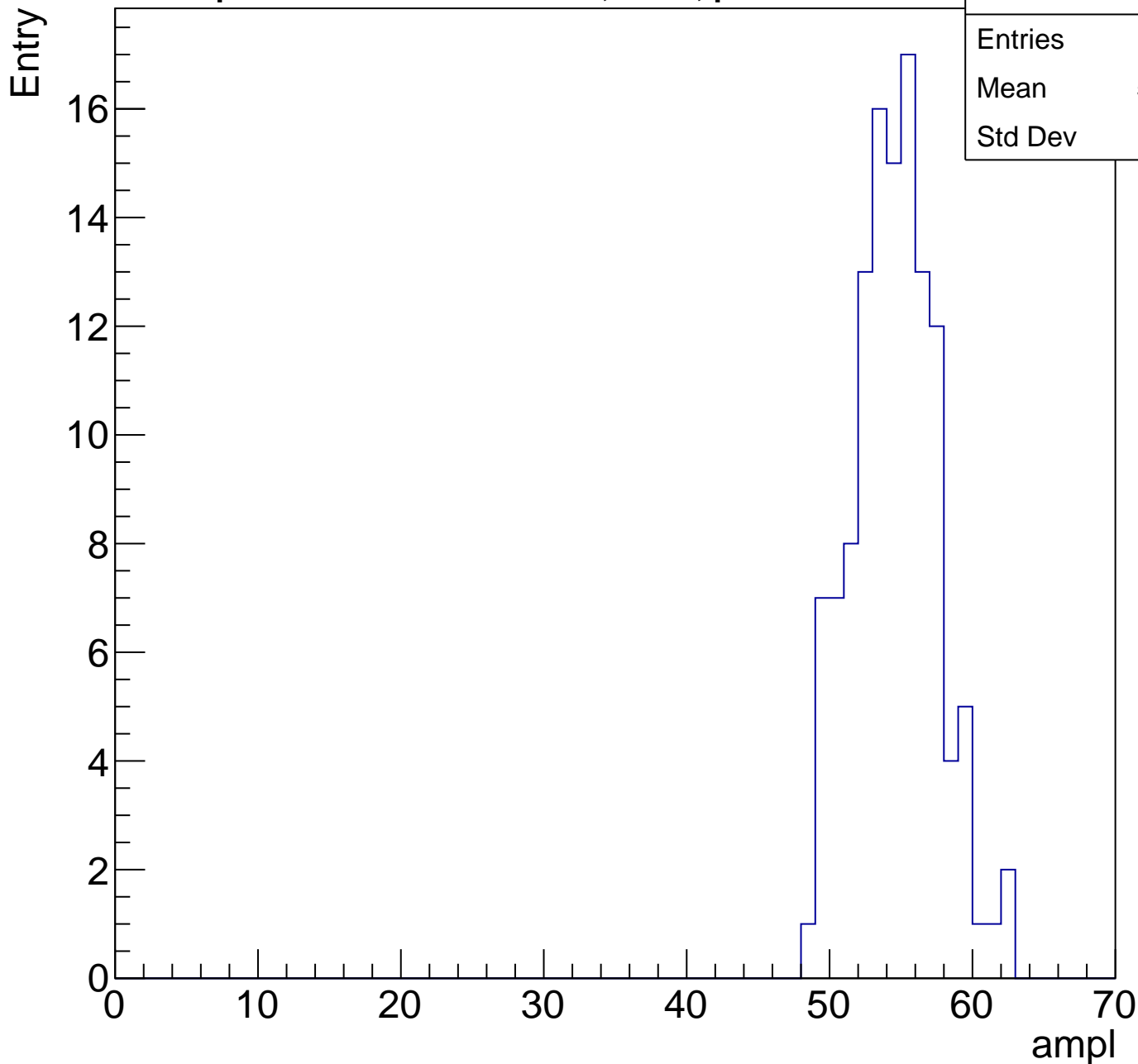
ampl



B1L001S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	54.11
Std Dev	2.92



B1L001S, U14-ch22, adc5

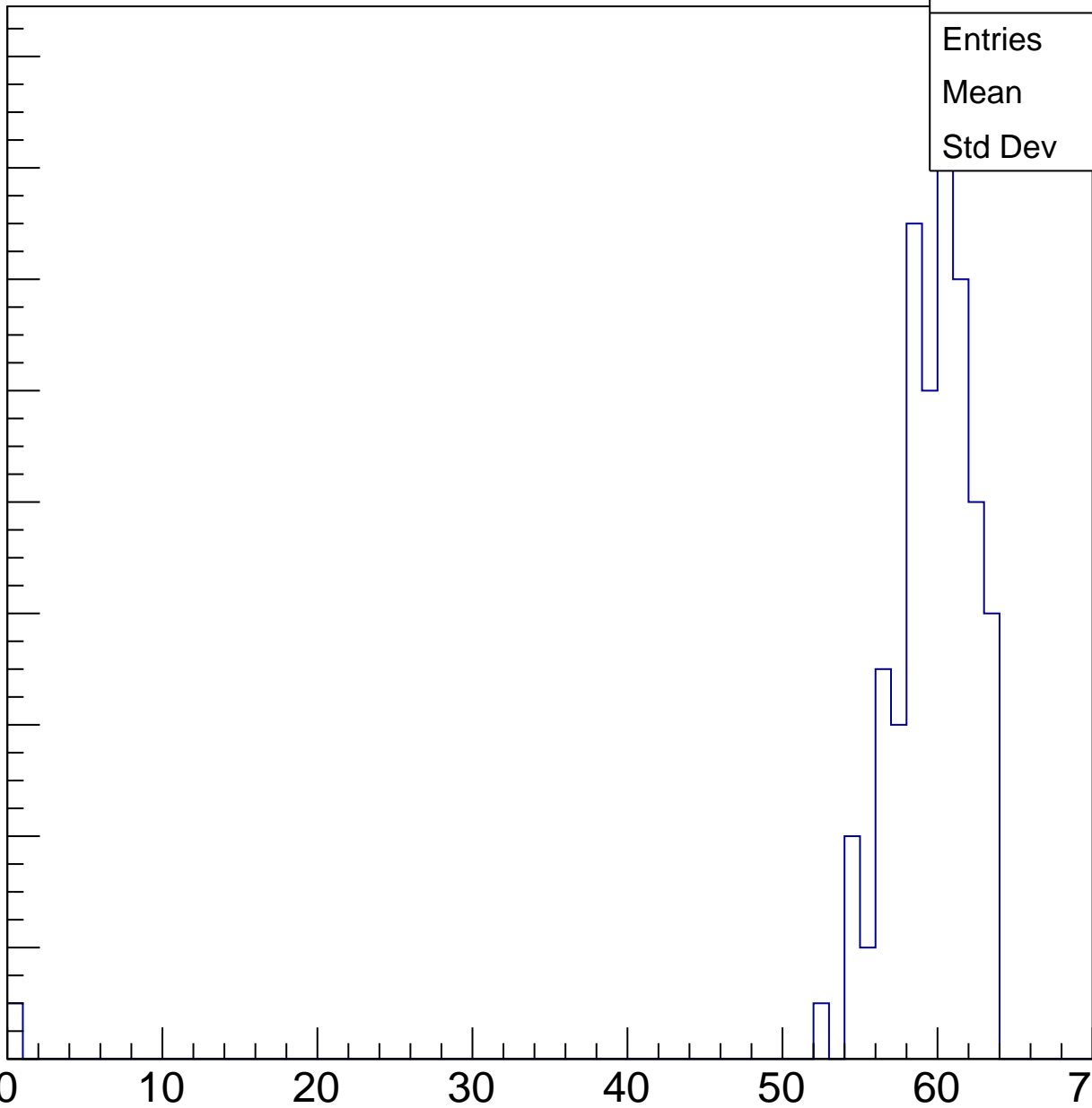
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	98
Mean	58.65
Std Dev	6.424

ampl



B1L001S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

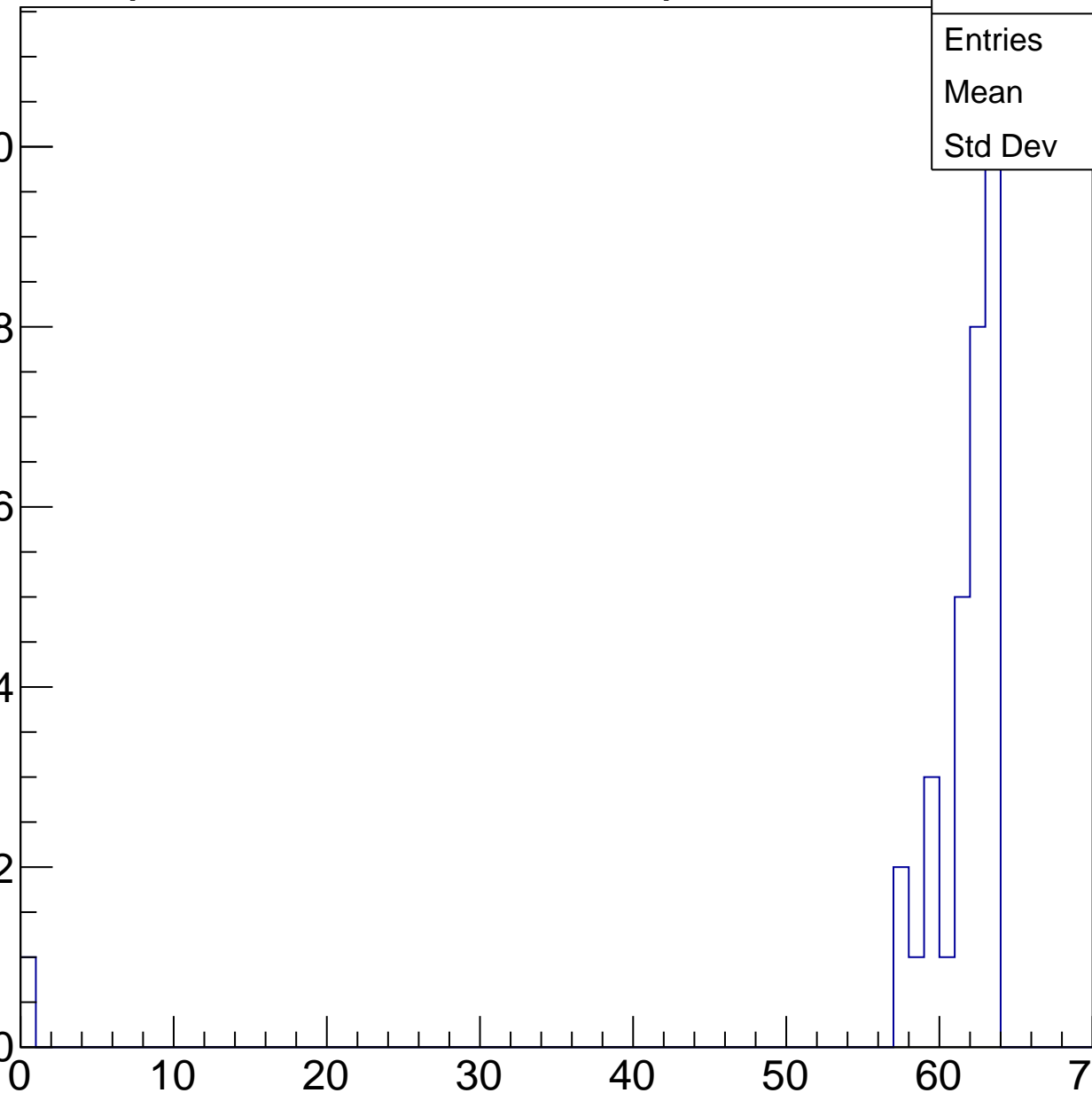
4

2

0

Entries	32
Mean	59.47
Std Dev	10.83

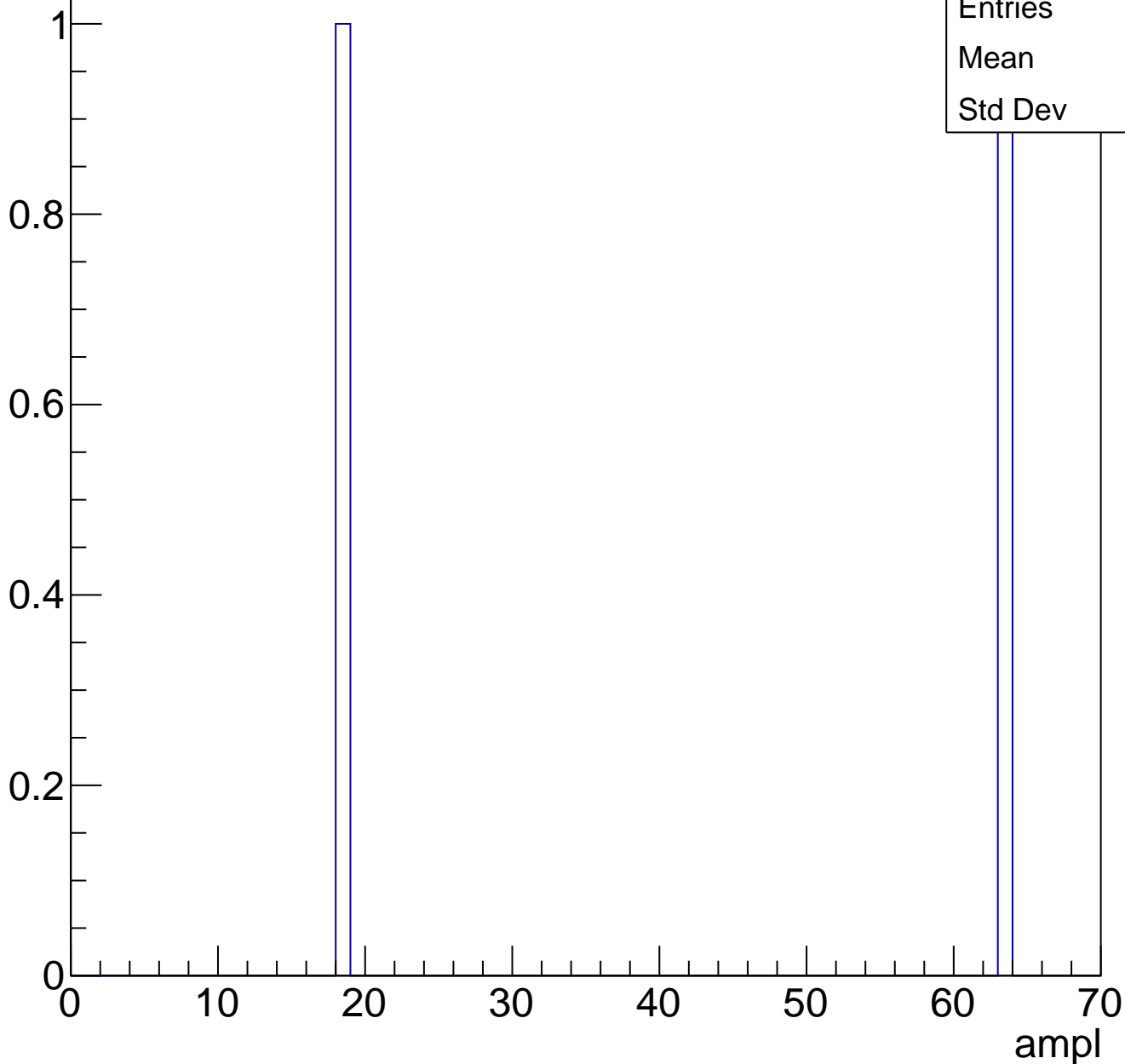
ampl



B1L001S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch23, adc0

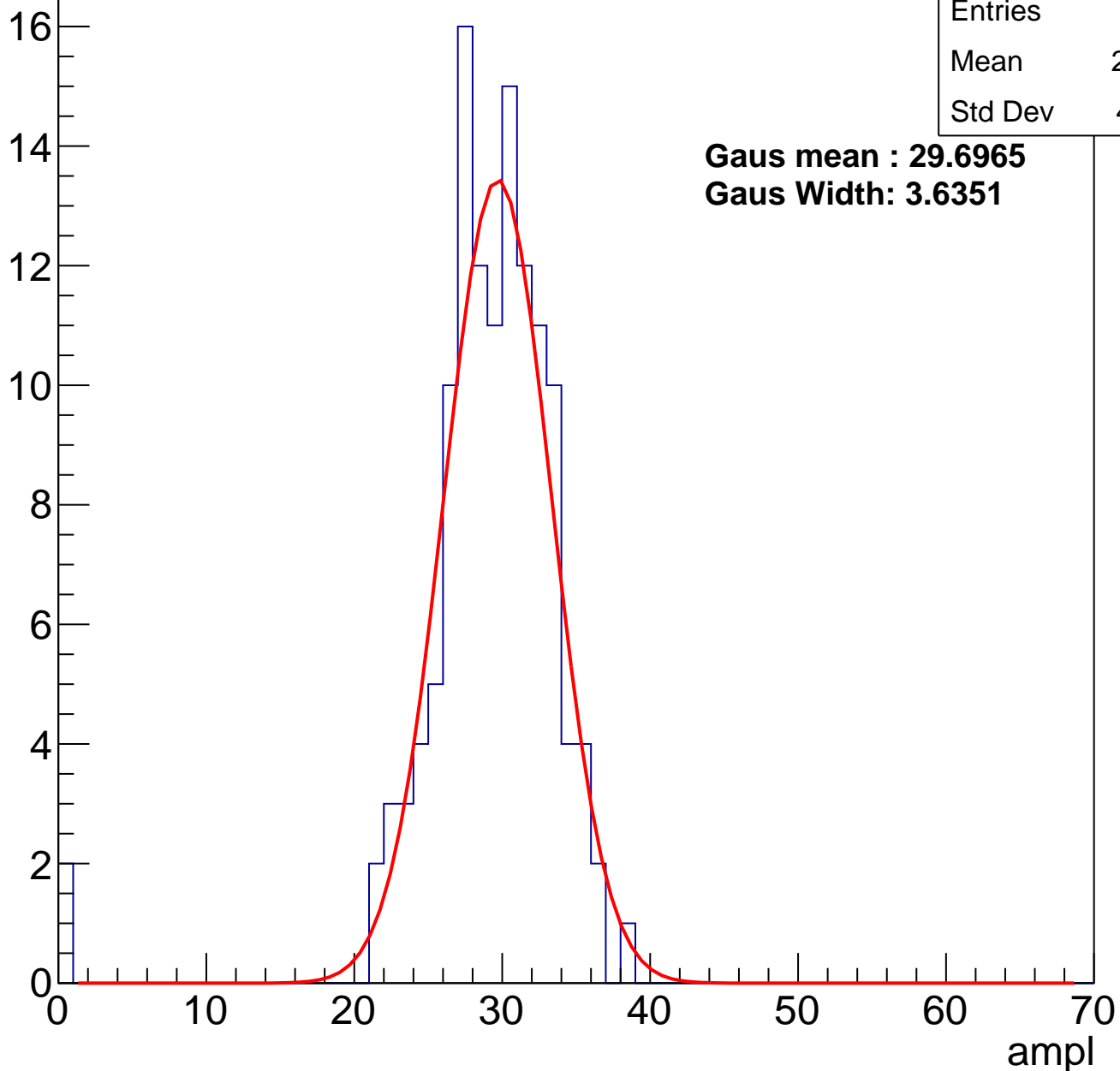
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	28.62
Std Dev	4.971

Gaus mean : 29.6965

Gaus Width: 3.6351

Entry



B1L001S, U14-ch23, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	35.44
Std Dev	3.45

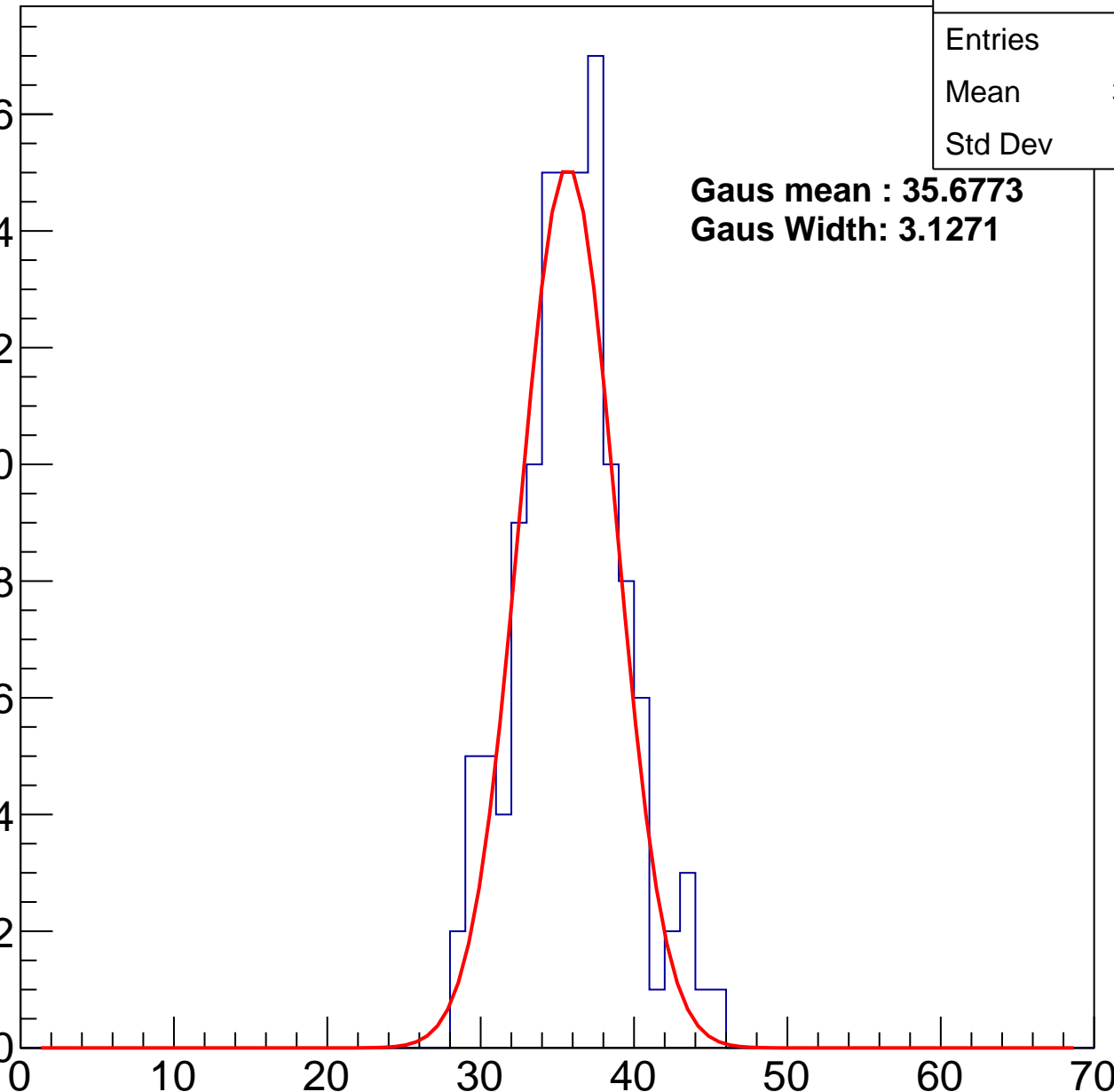
Gaus mean : 35.6773

Gaus Width: 3.1271

Entry

16
14
12
10
8
6
4
2
0

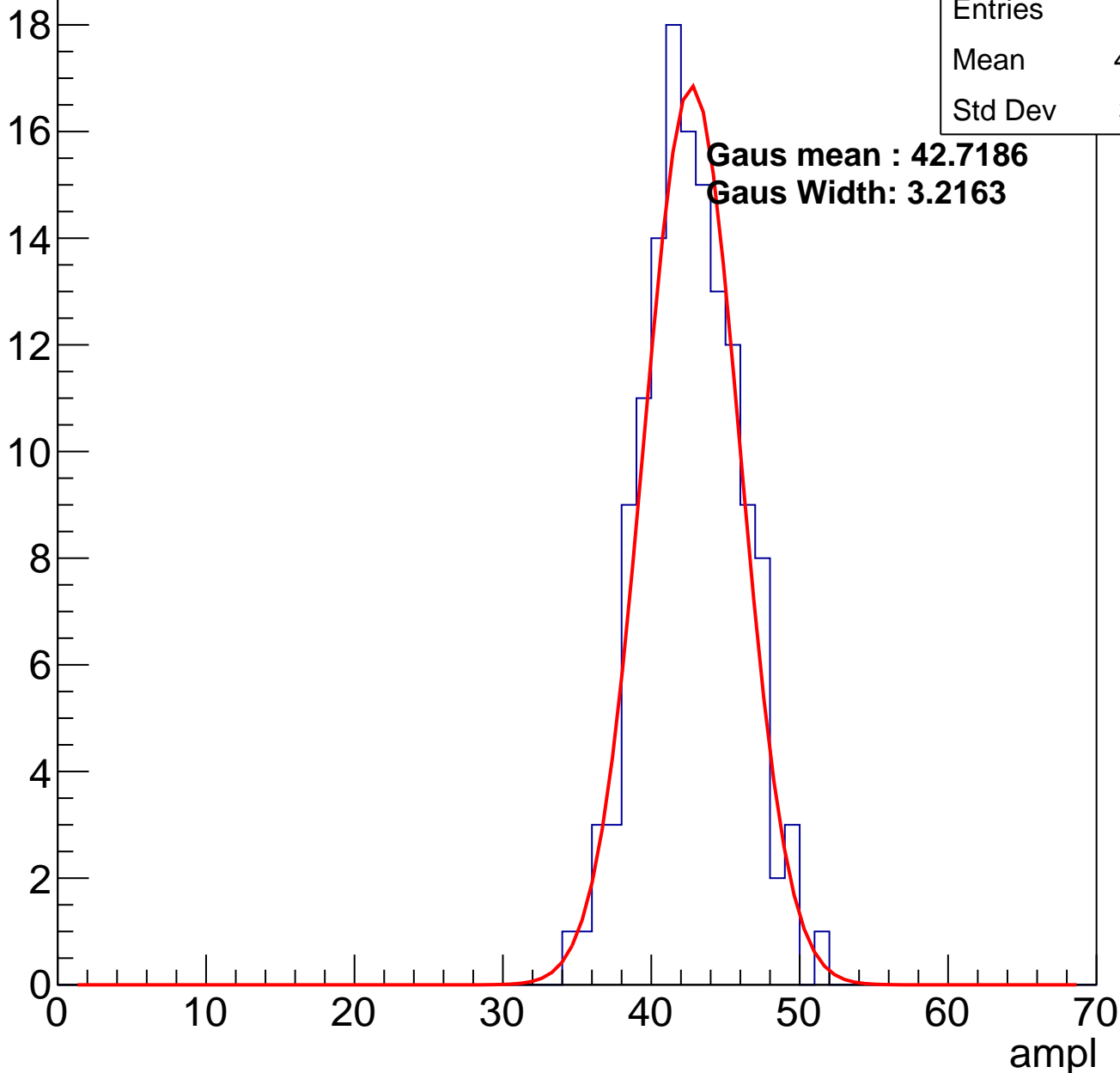
ampl



B1L001S, U14-ch23, adc2

calib_packv5_042523_0143.root, FC#2, port C2

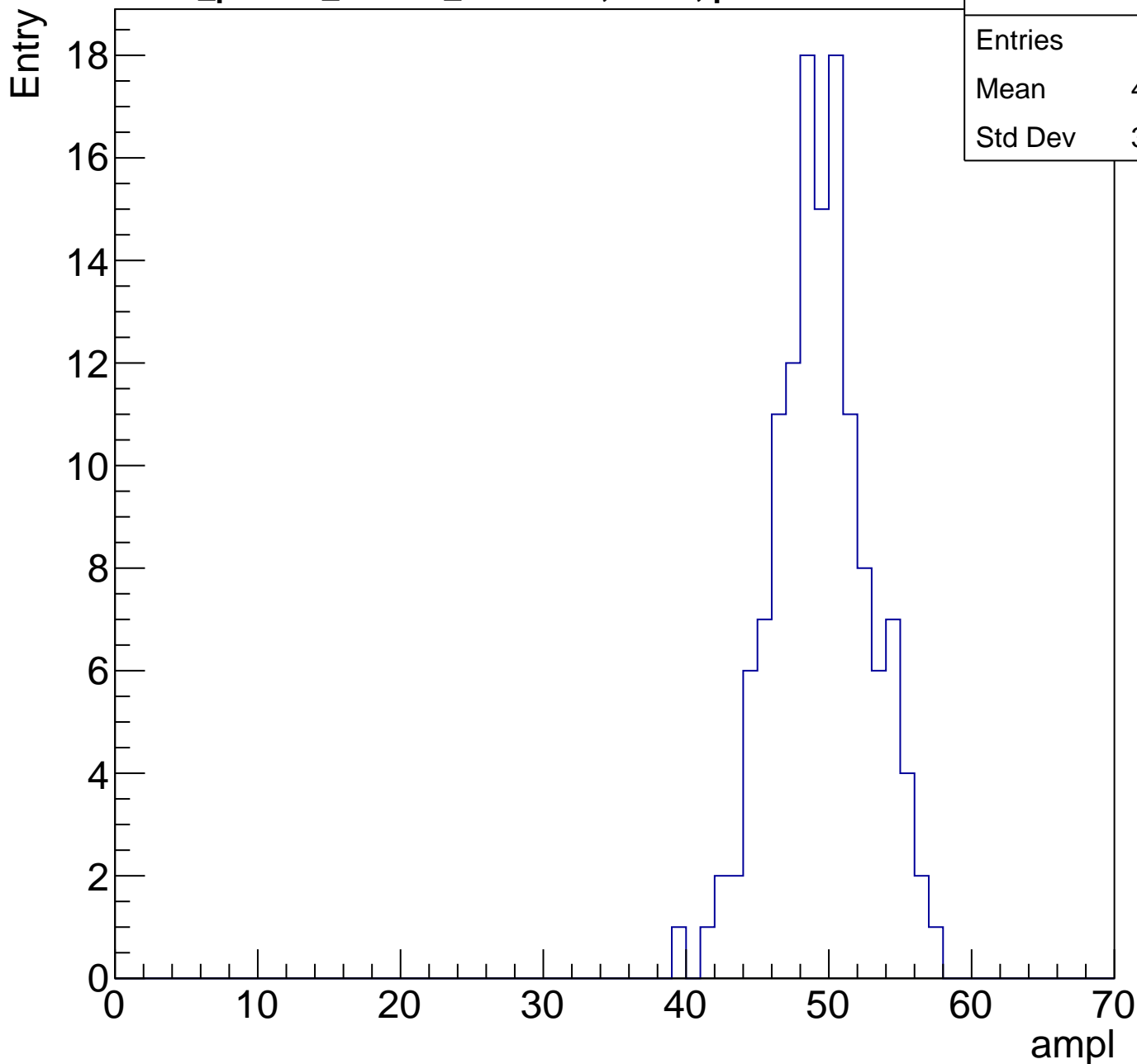
Entry



B1L001S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	48.94
Std Dev	3.339

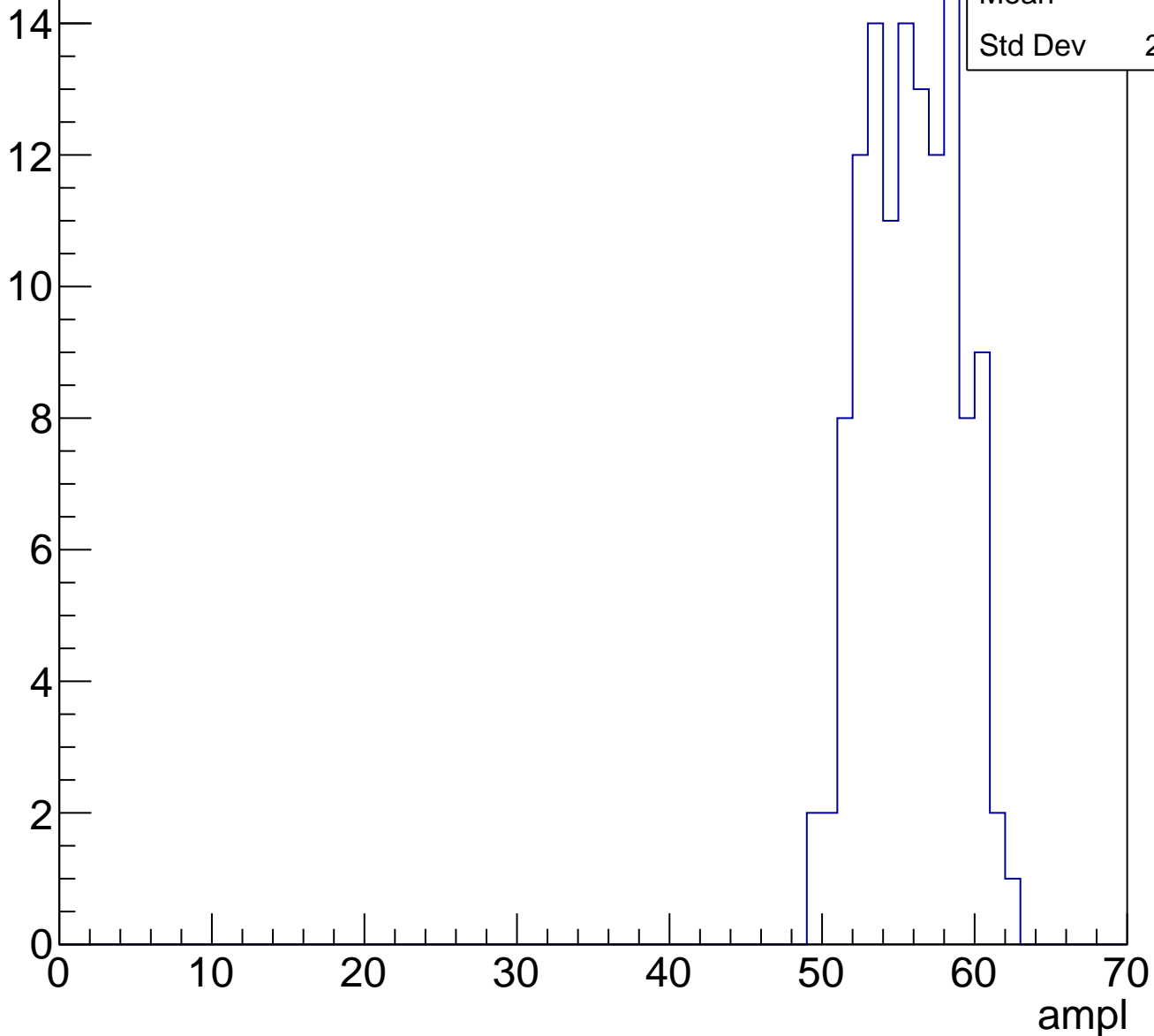


B1L001S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	55.4
Std Dev	2.957

Entry



B1L001S, U14-ch23, adc5

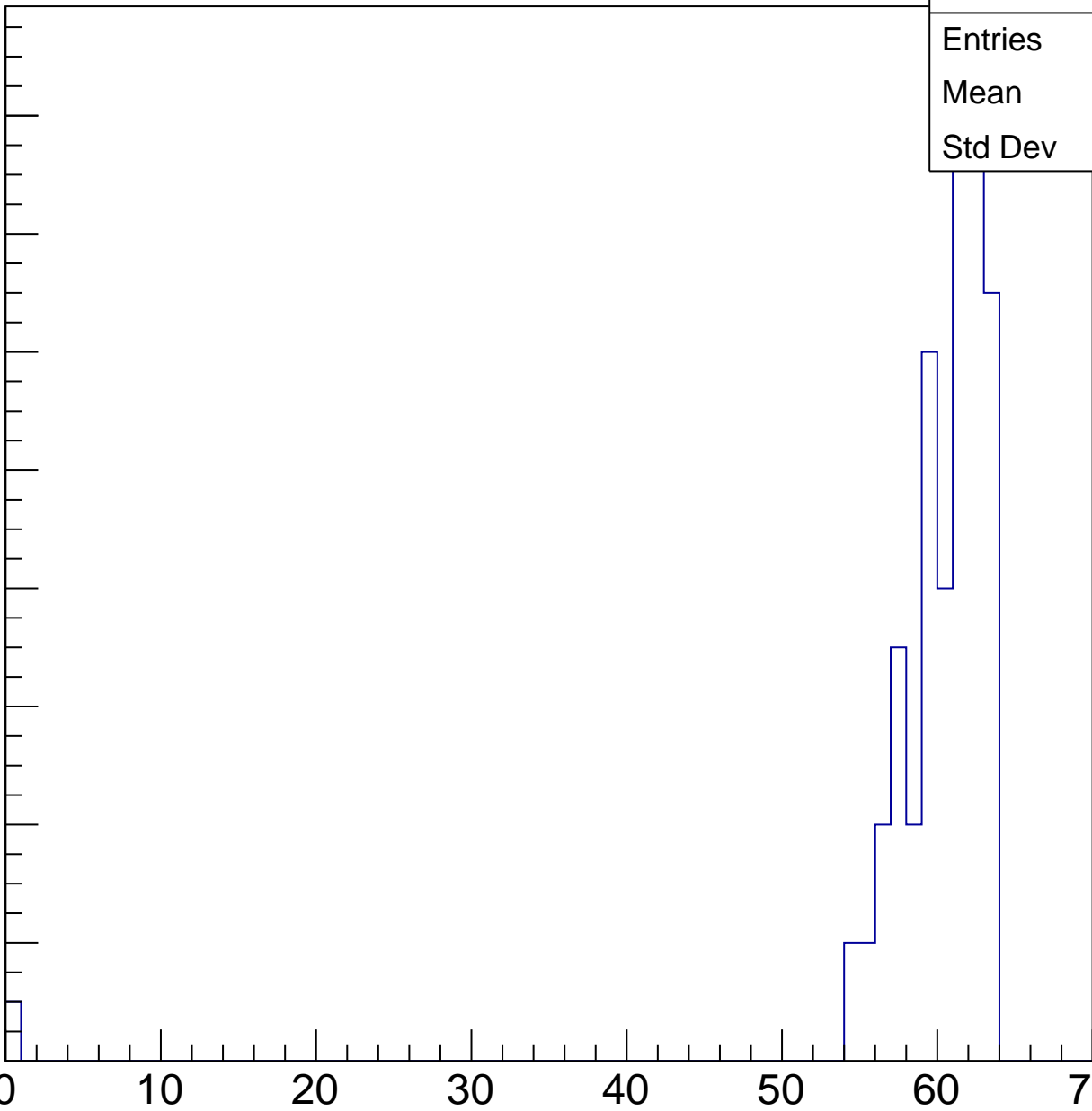
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	87
Mean	59.44
Std Dev	6.824

ampl

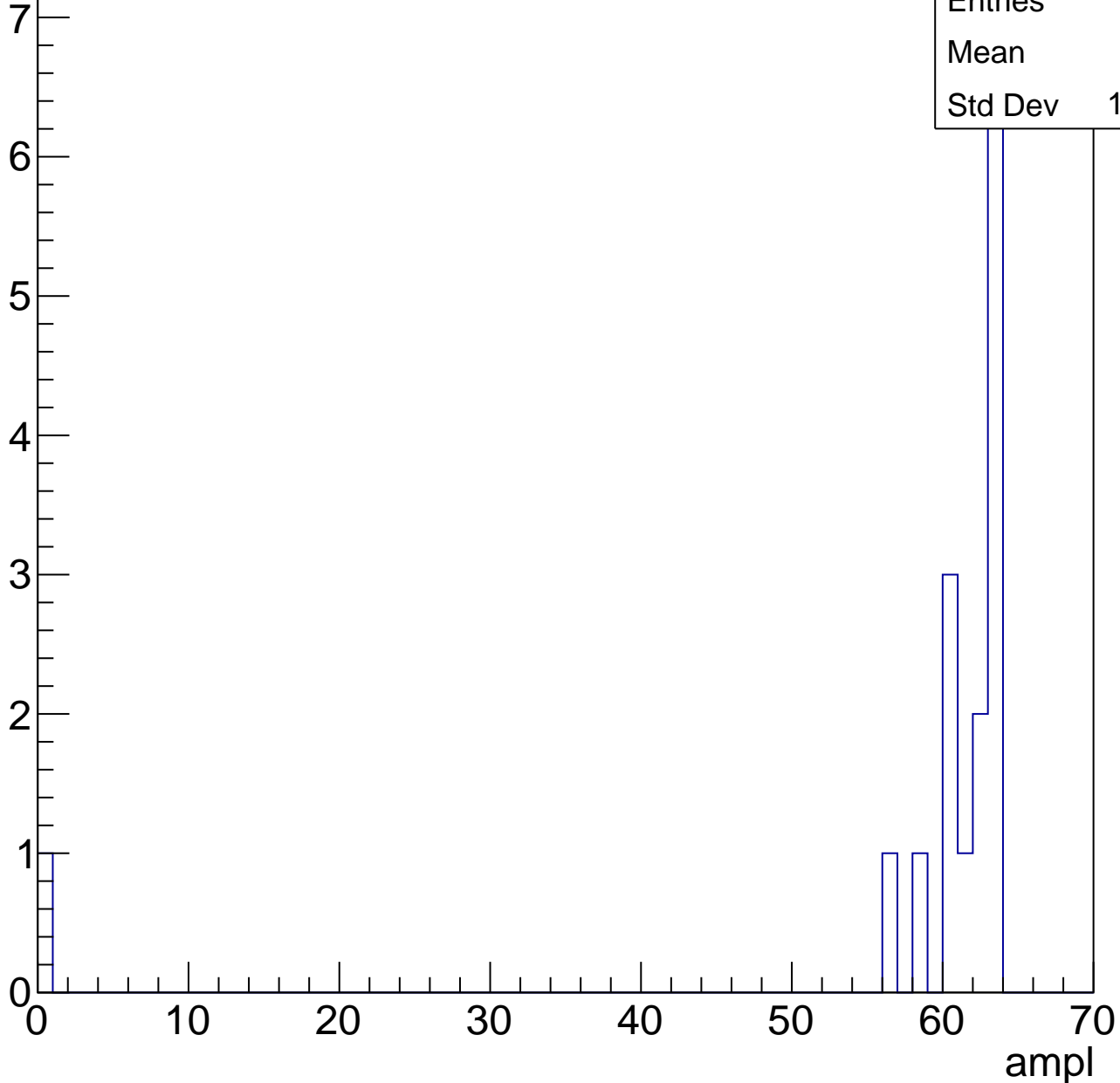


B1L001S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	57.5
Std Dev	14.98



B1L001S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch24, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	29.64
Std Dev	3.322

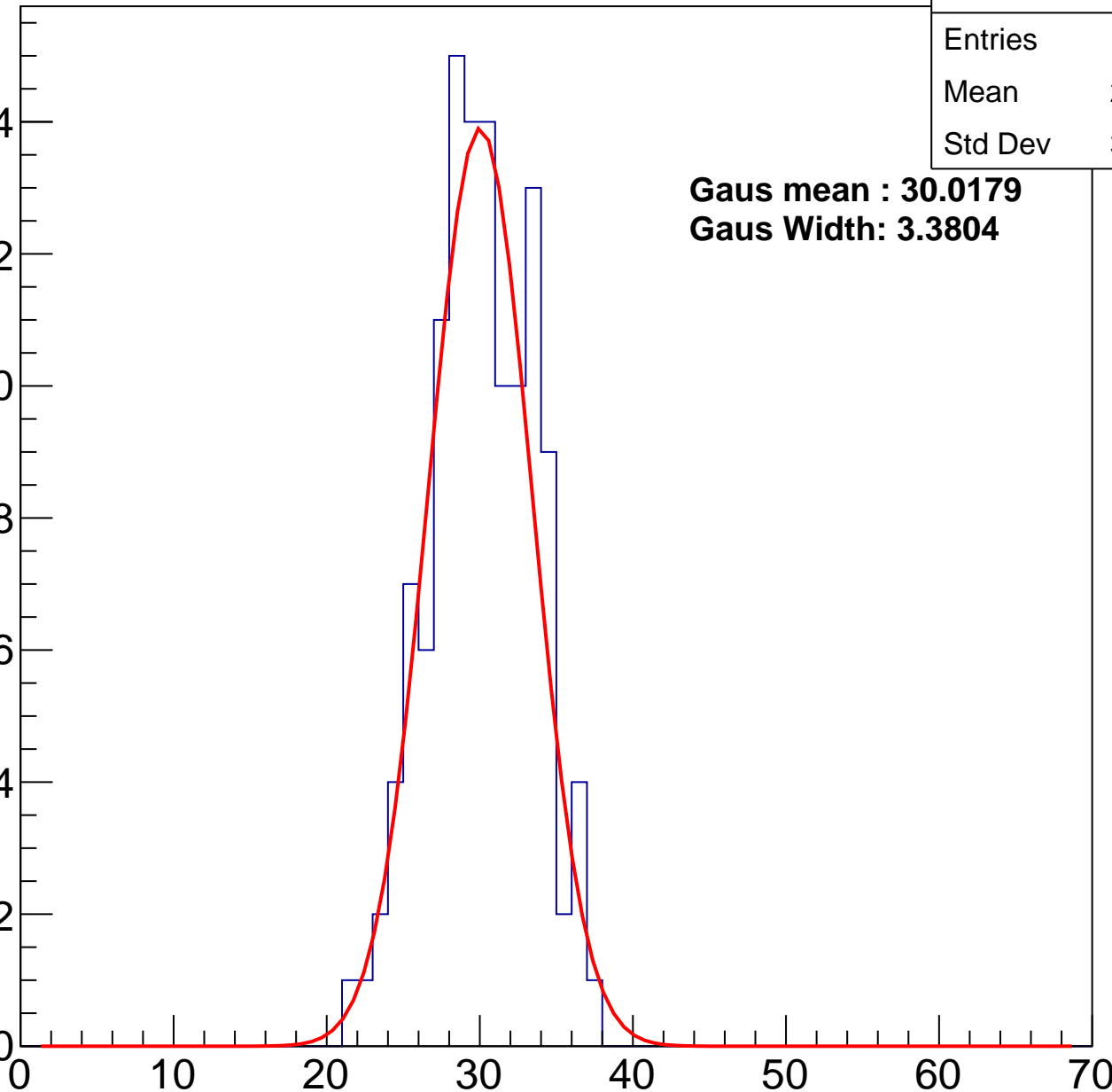
Gaus mean : 30.0179

Gaus Width: 3.3804

Entry

14
12
10
8
6
4
2
0

ampl



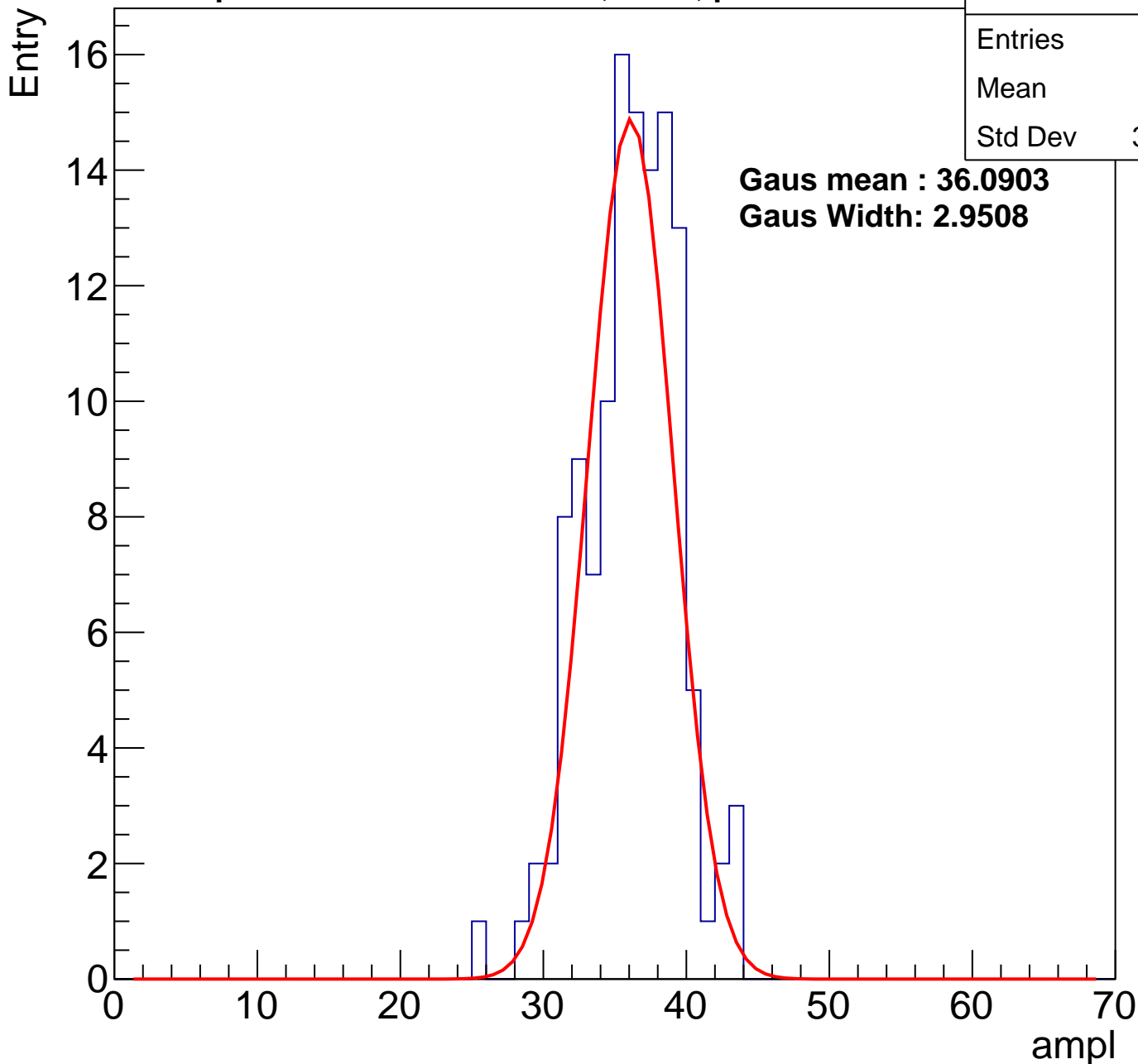
B1L001S, U14-ch24, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	35.7
Std Dev	3.265

Gaus mean : 36.0903

Gaus Width: 2.9508



B1L001S, U14-ch24, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

120

Mean

42.03

Std Dev

2.975

Gaus mean : 42.7041

Gaus Width: 2.9165

0

10

20

30

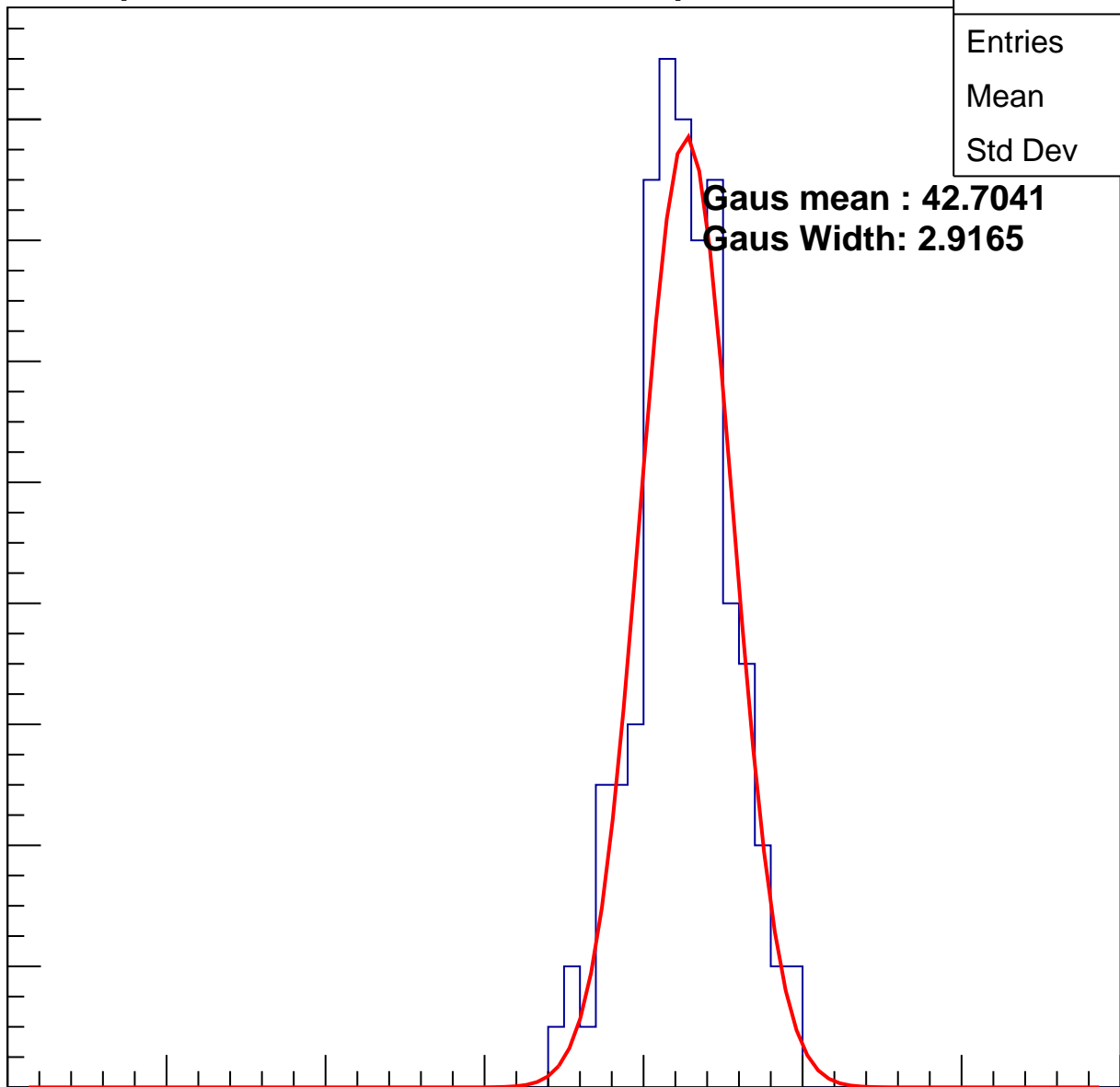
40

50

60

70

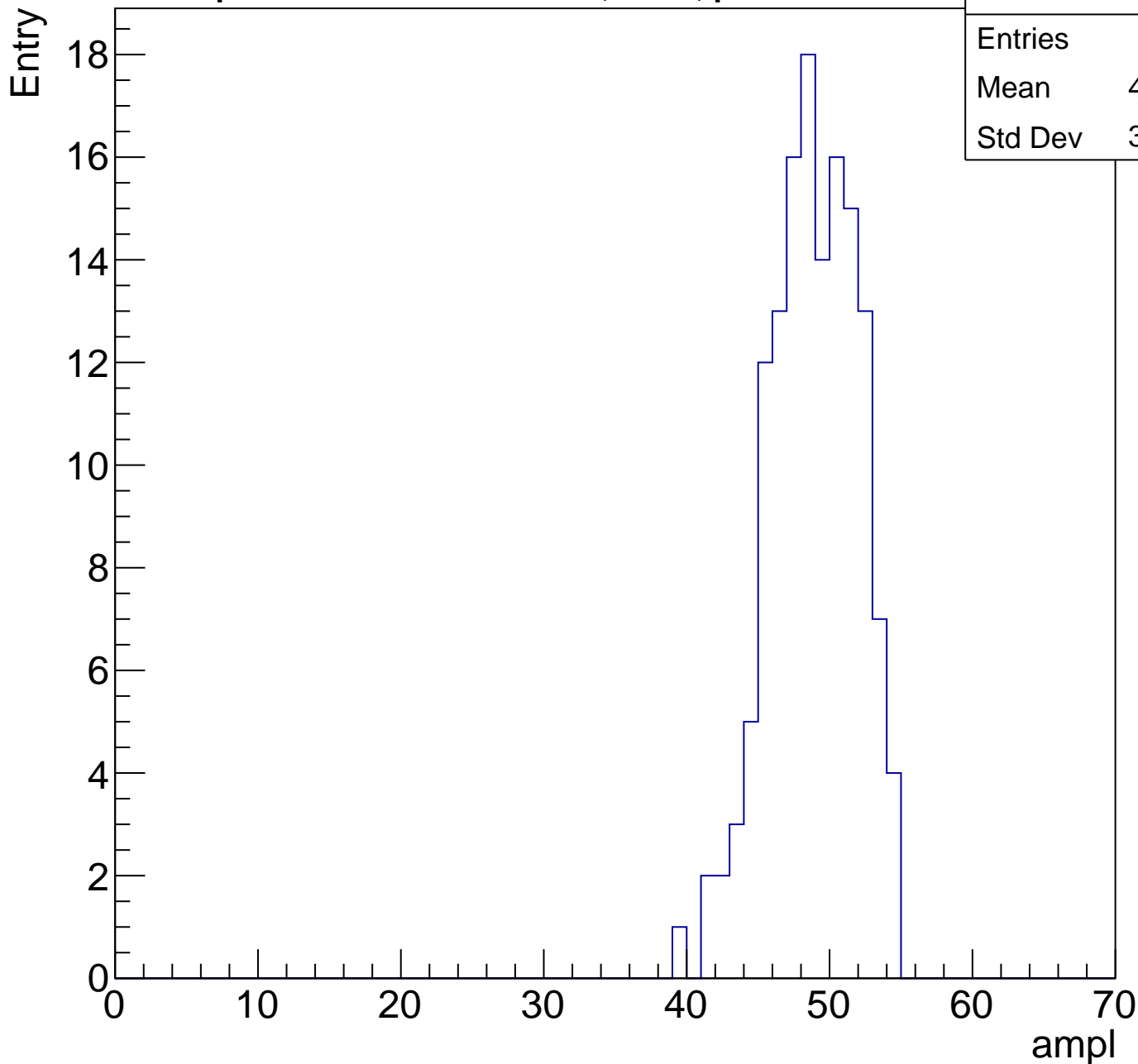
ampl



B1L001S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#2, port C2

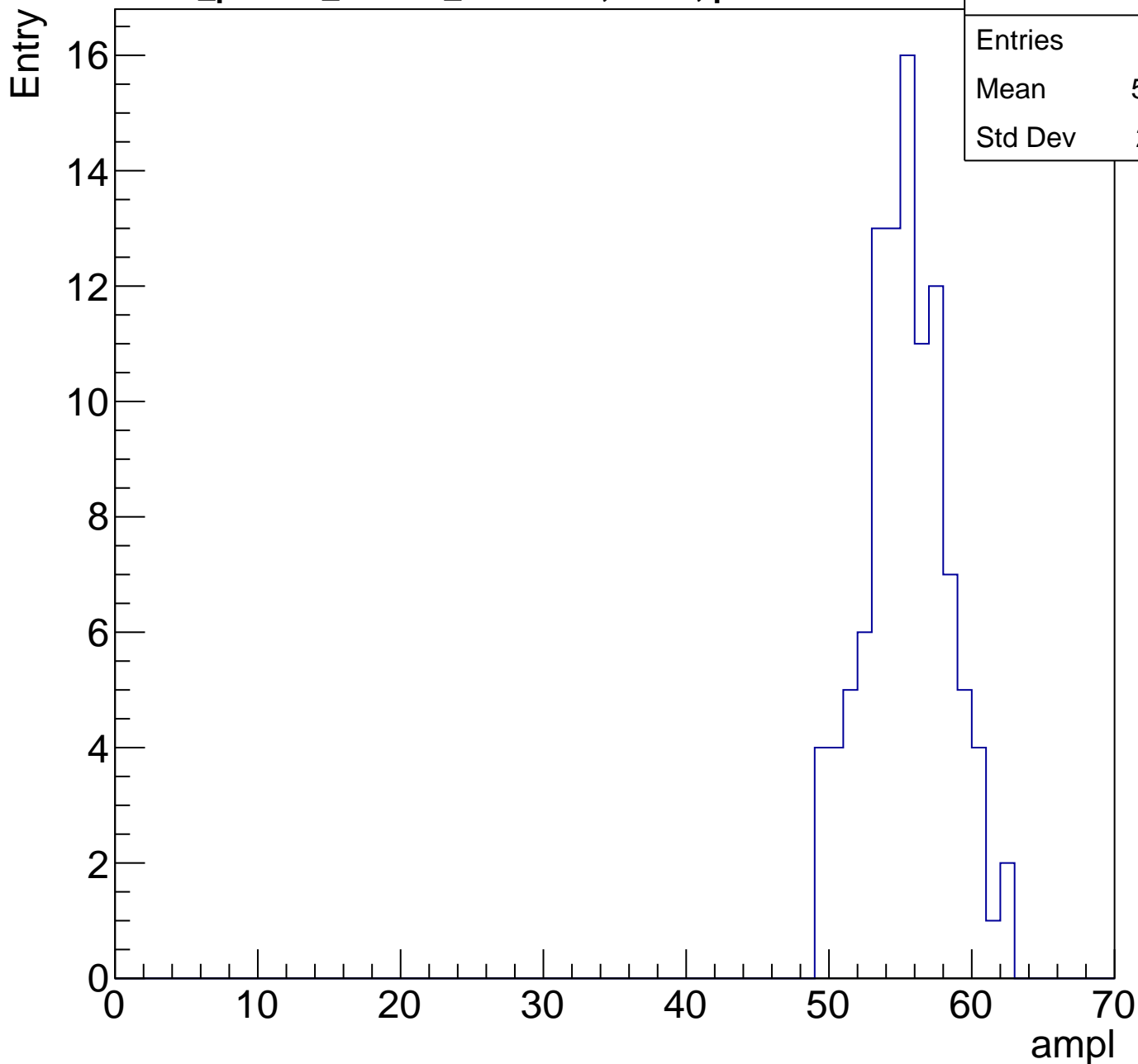
Entries	141
Mean	48.38
Std Dev	3.038



B1L001S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	54.95
Std Dev	2.921



B1L001S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#2, port C2

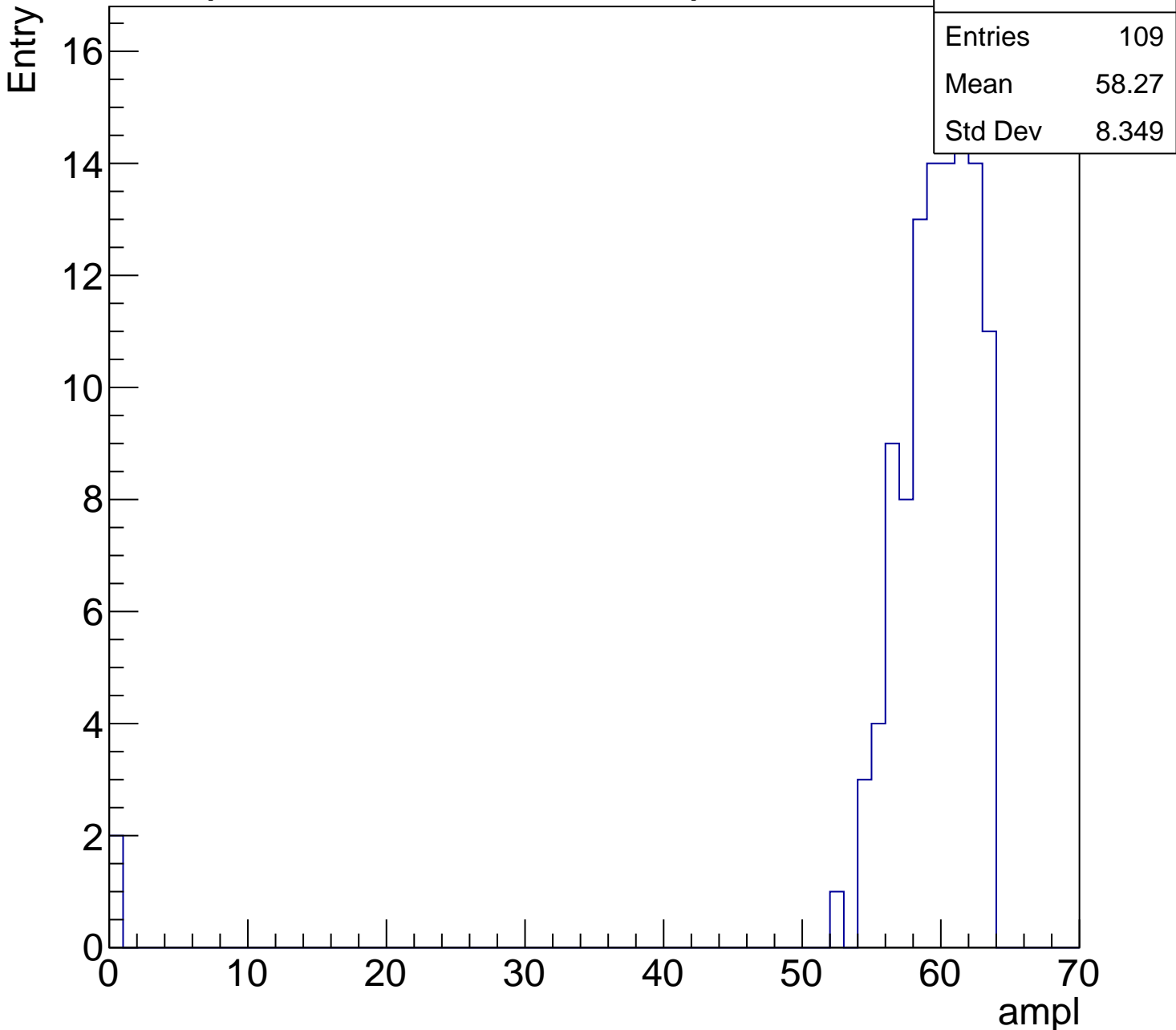
Entries	109
Mean	58.27
Std Dev	8.349

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

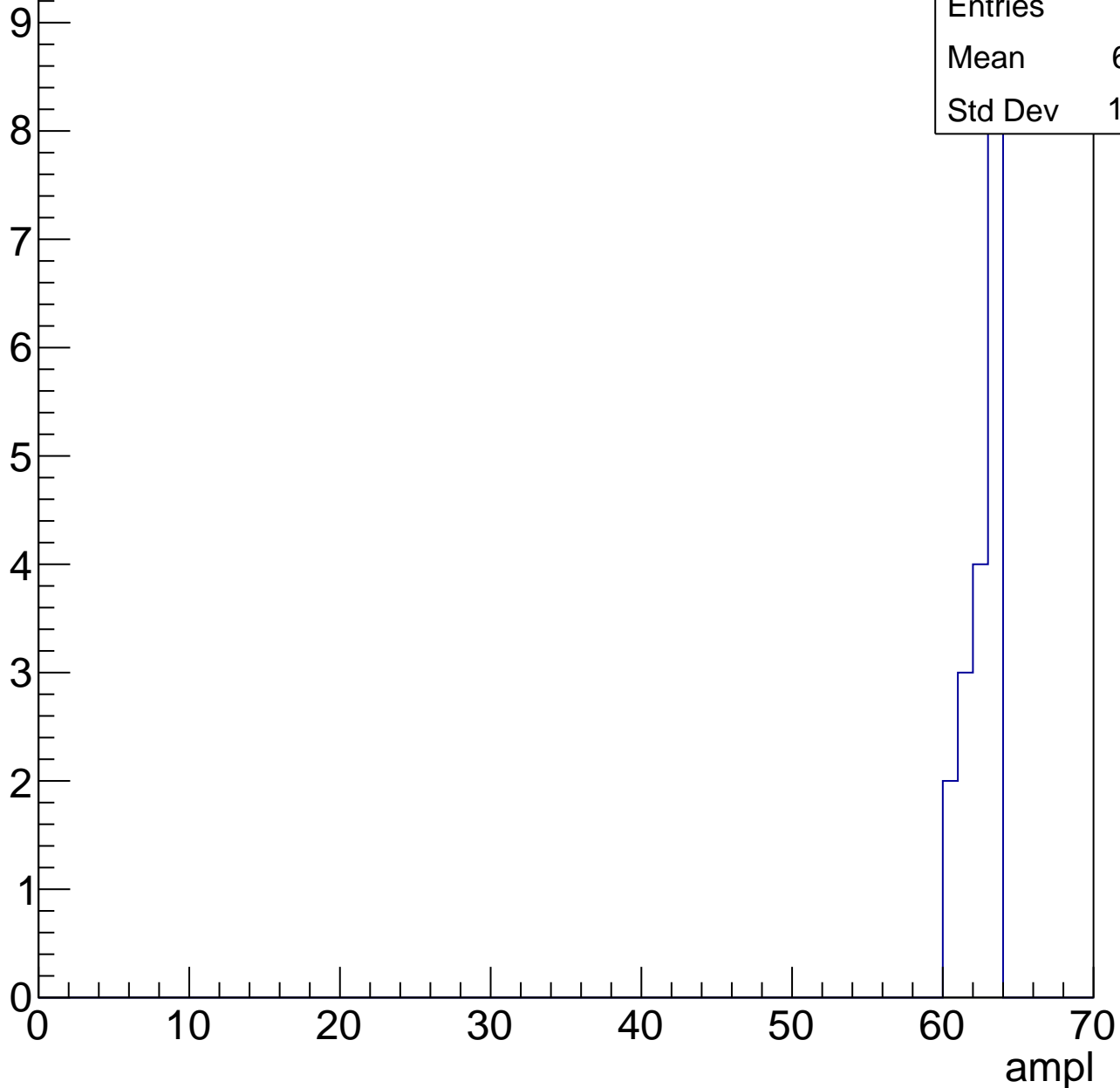
ampl



B1L001S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#2, port C2

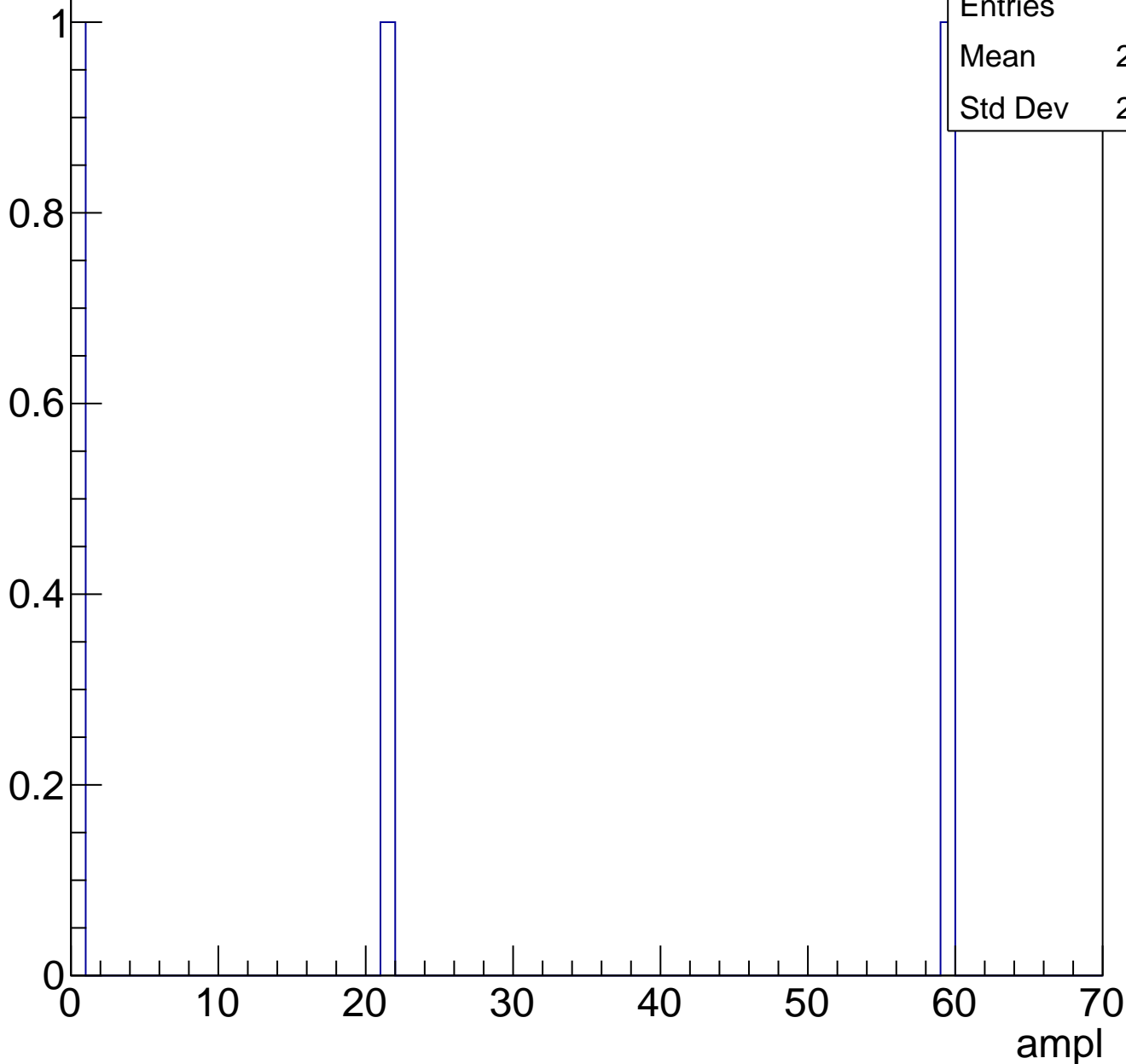
Entry



B1L001S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#2, port C2

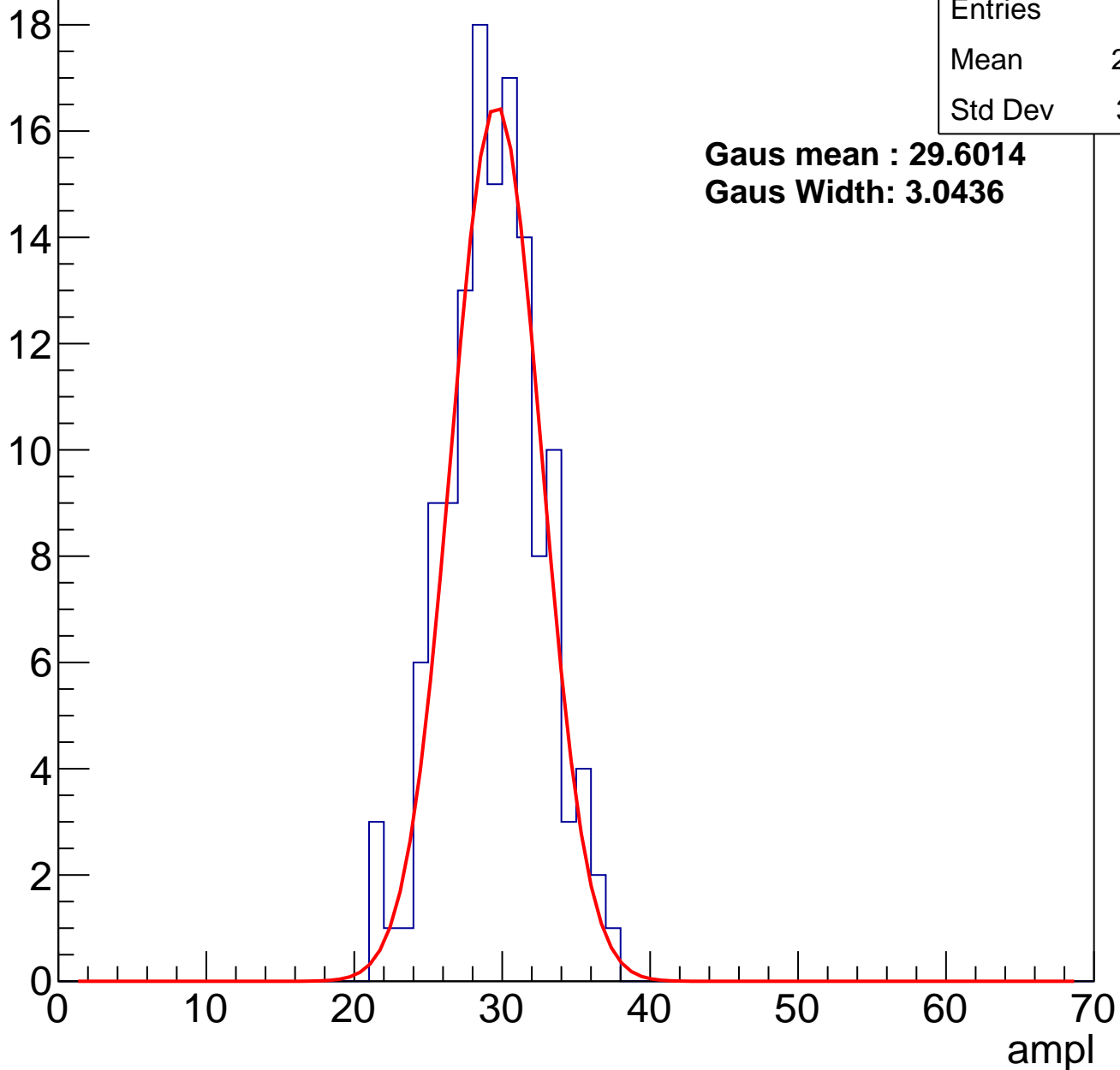
Entry



B1L001S, U14-ch25, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch25, adc1

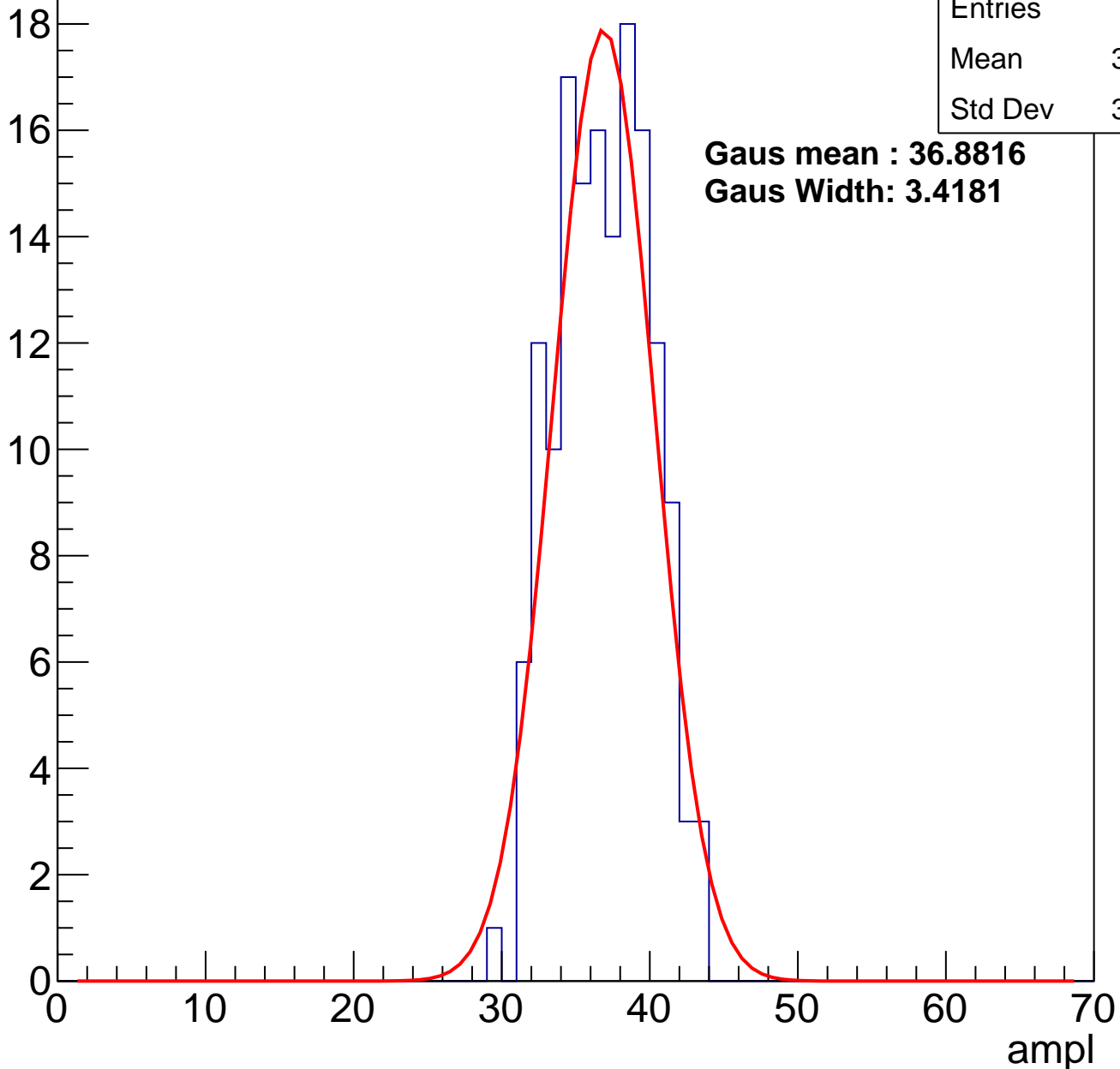
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	36.43
Std Dev	3.075

Gaus mean : 36.8816

Gaus Width: 3.4181

Entry



B1L001S, U14-ch25, adc2

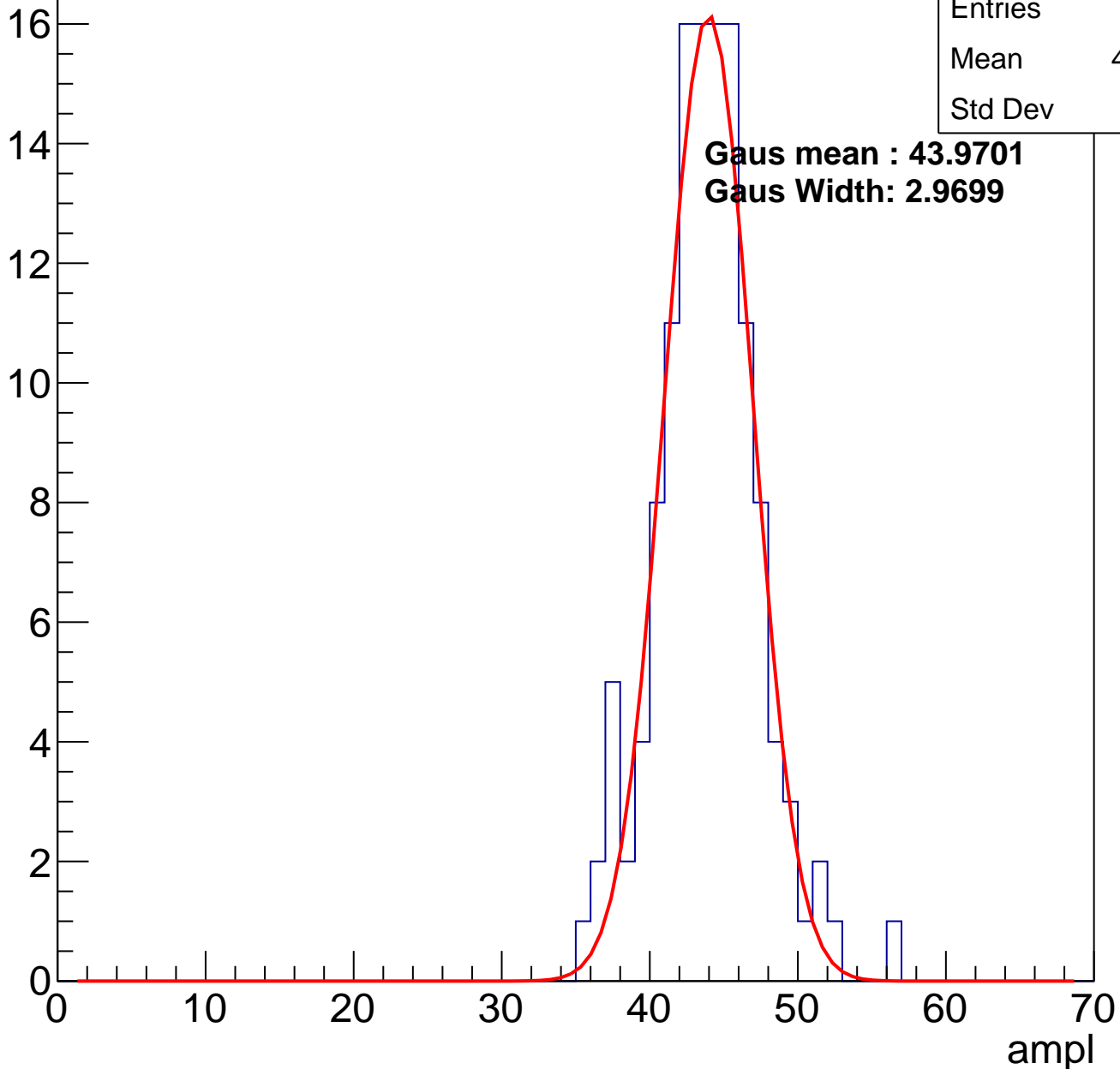
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	43.44
Std Dev	3.43

Gaus mean : 43.9701

Gaus Width: 2.9699

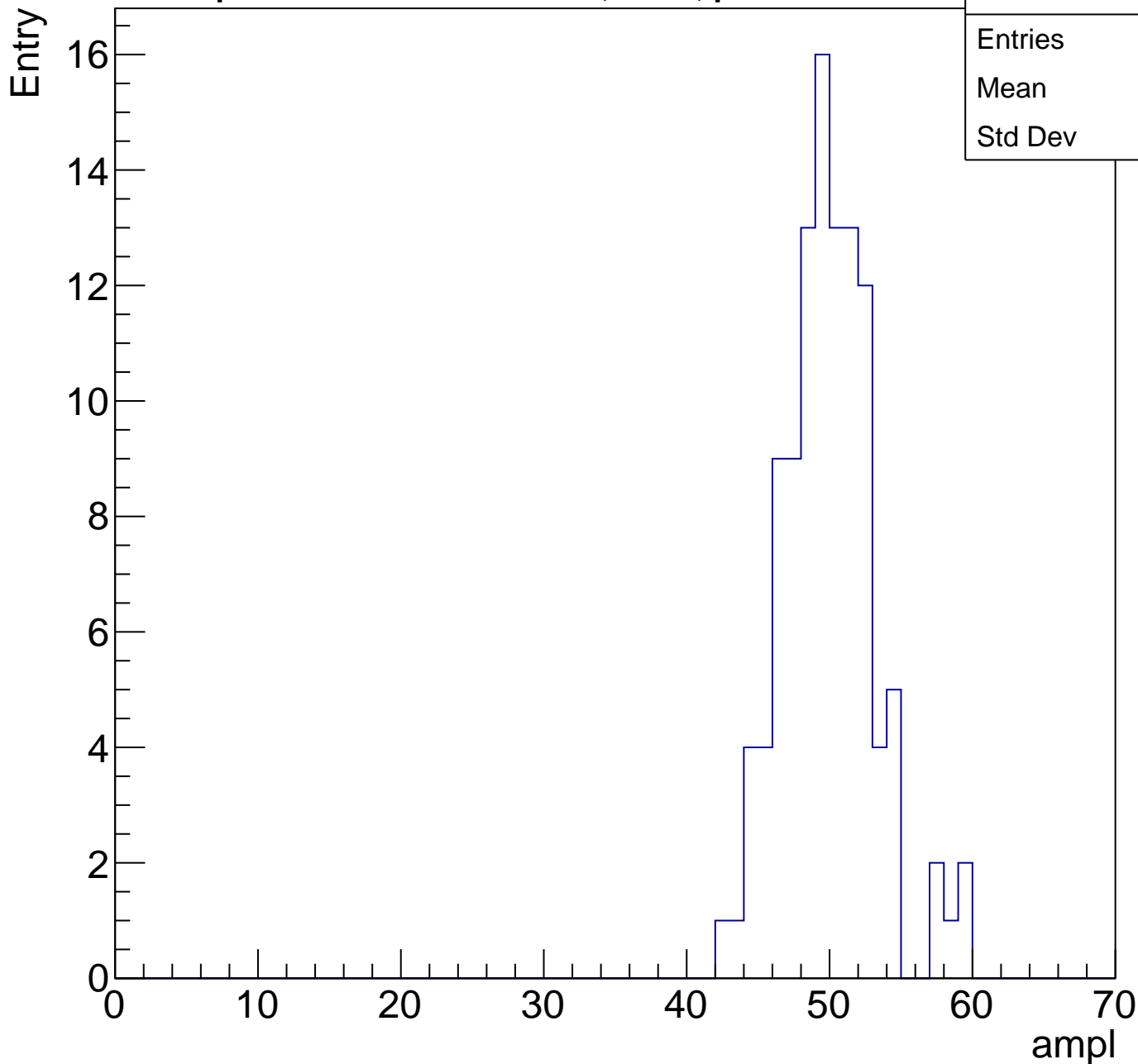
Entry



B1L001S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#2, port C2

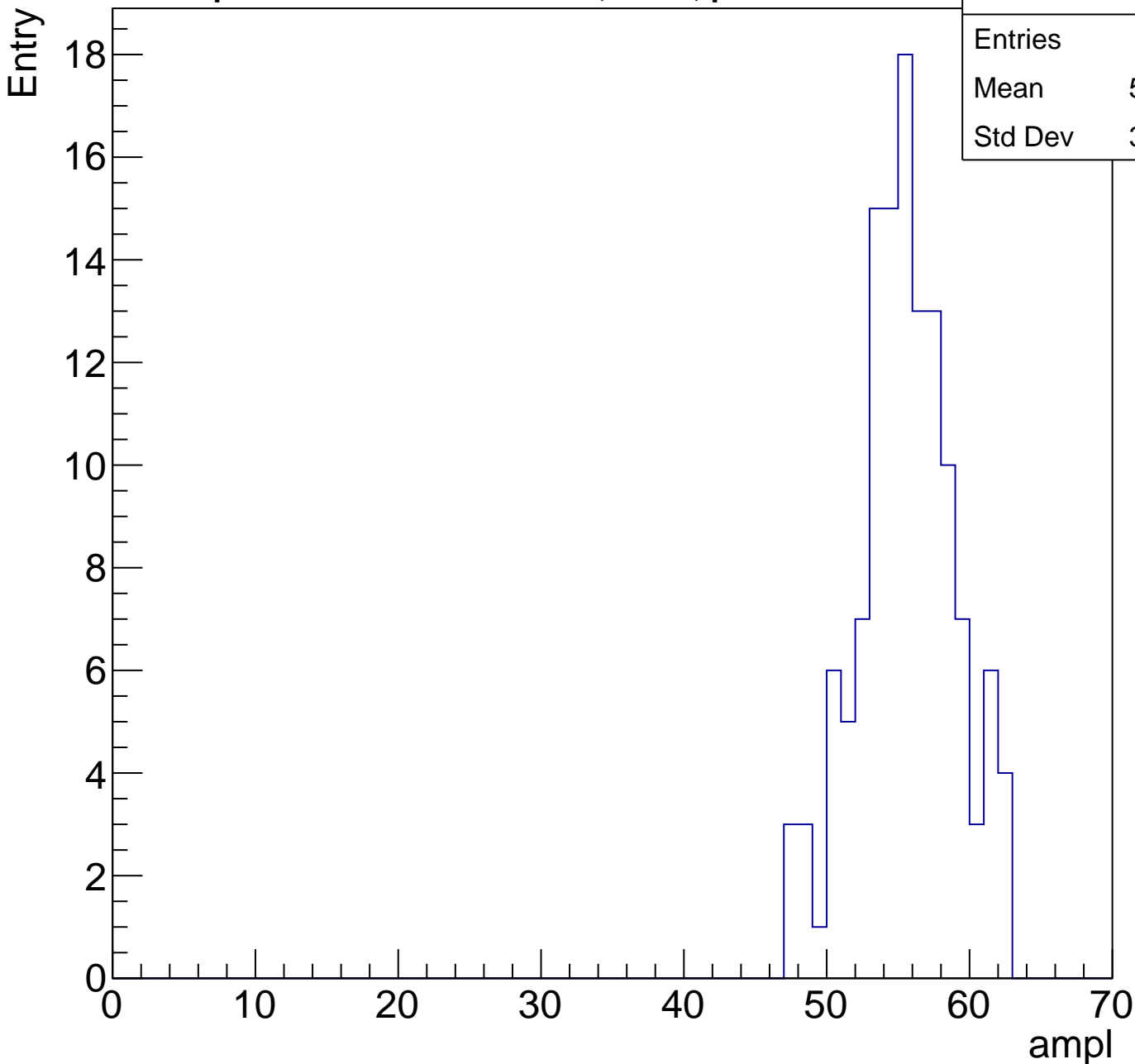
Entries	109
Mean	49.5
Std Dev	3.21



B1L001S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	55.07
Std Dev	3.406



B1L001S, U14-ch25, adc5

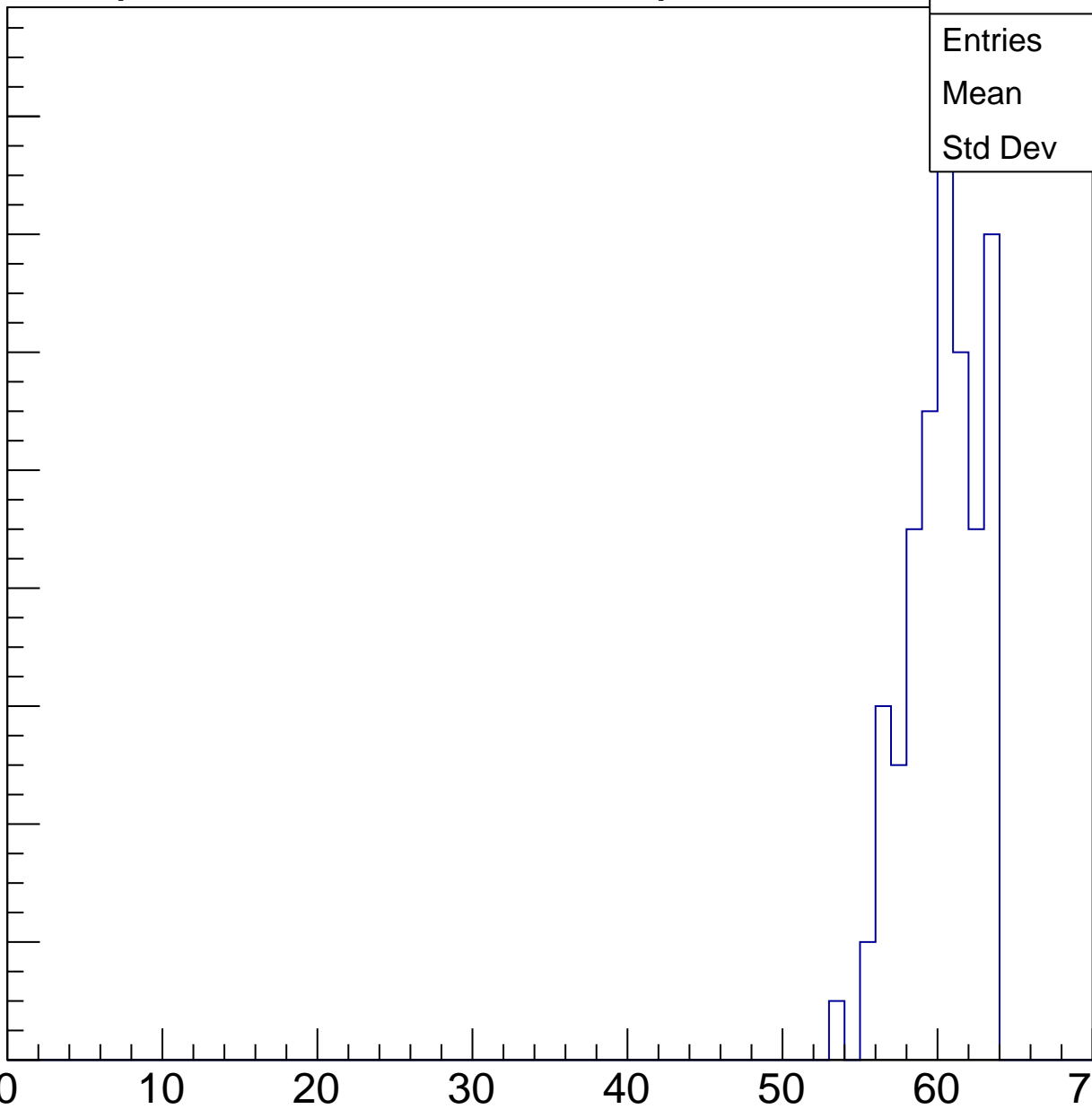
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	86
Mean	59.85
Std Dev	2.31

ampl

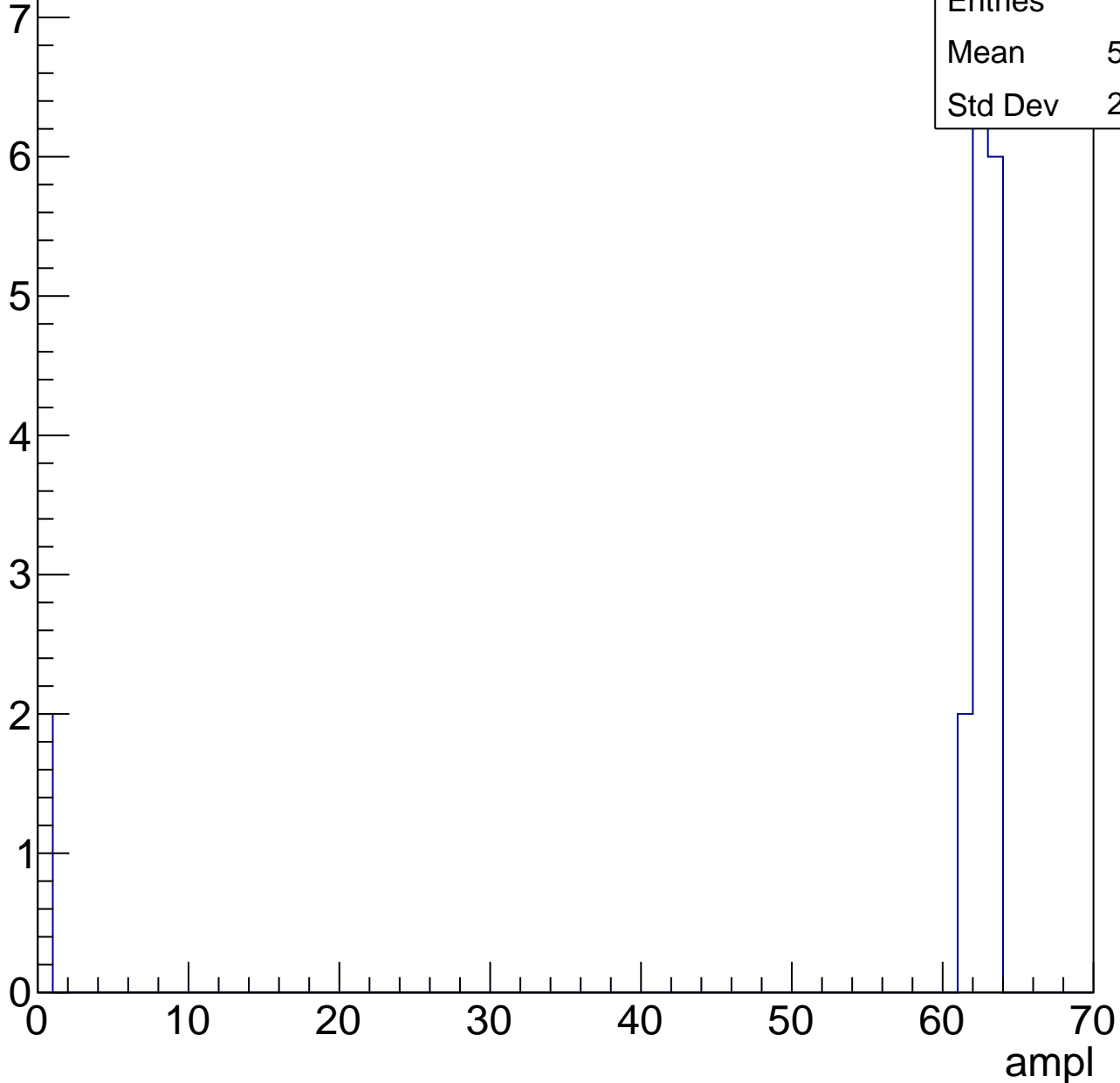


B1L001S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	17
Mean	54.94
Std Dev	20.07



B1L001S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

B1L001S, U14-ch26, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

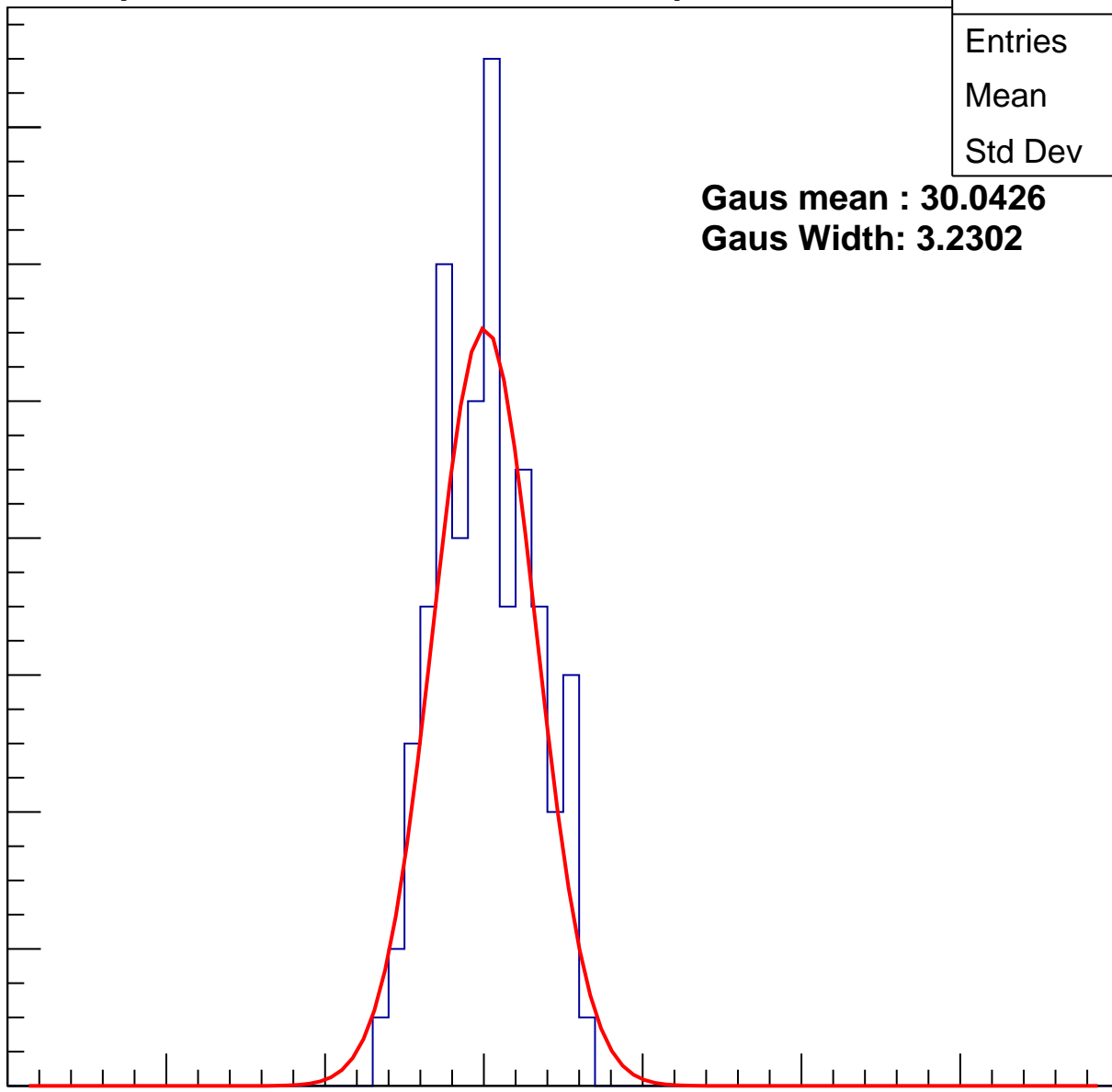
70

ampl

Entries	94
Mean	29.62
Std Dev	3.007

Gaus mean : 30.0426

Gaus Width: 3.2302



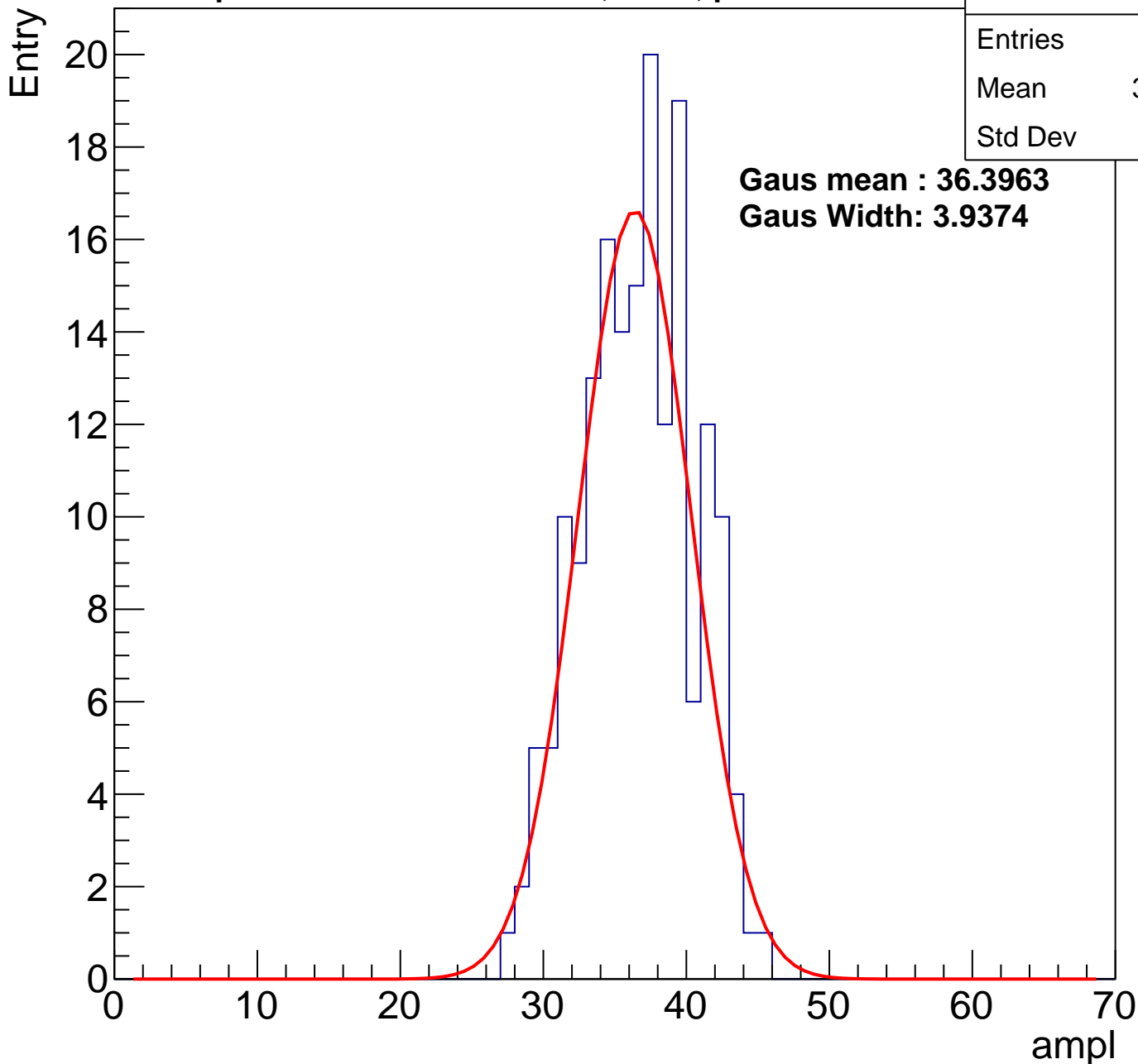
B1L001S, U14-ch26, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	175
Mean	36.17
Std Dev	3.82

Gaus mean : 36.3963

Gaus Width: 3.9374



B1L001S, U14-ch26, adc2

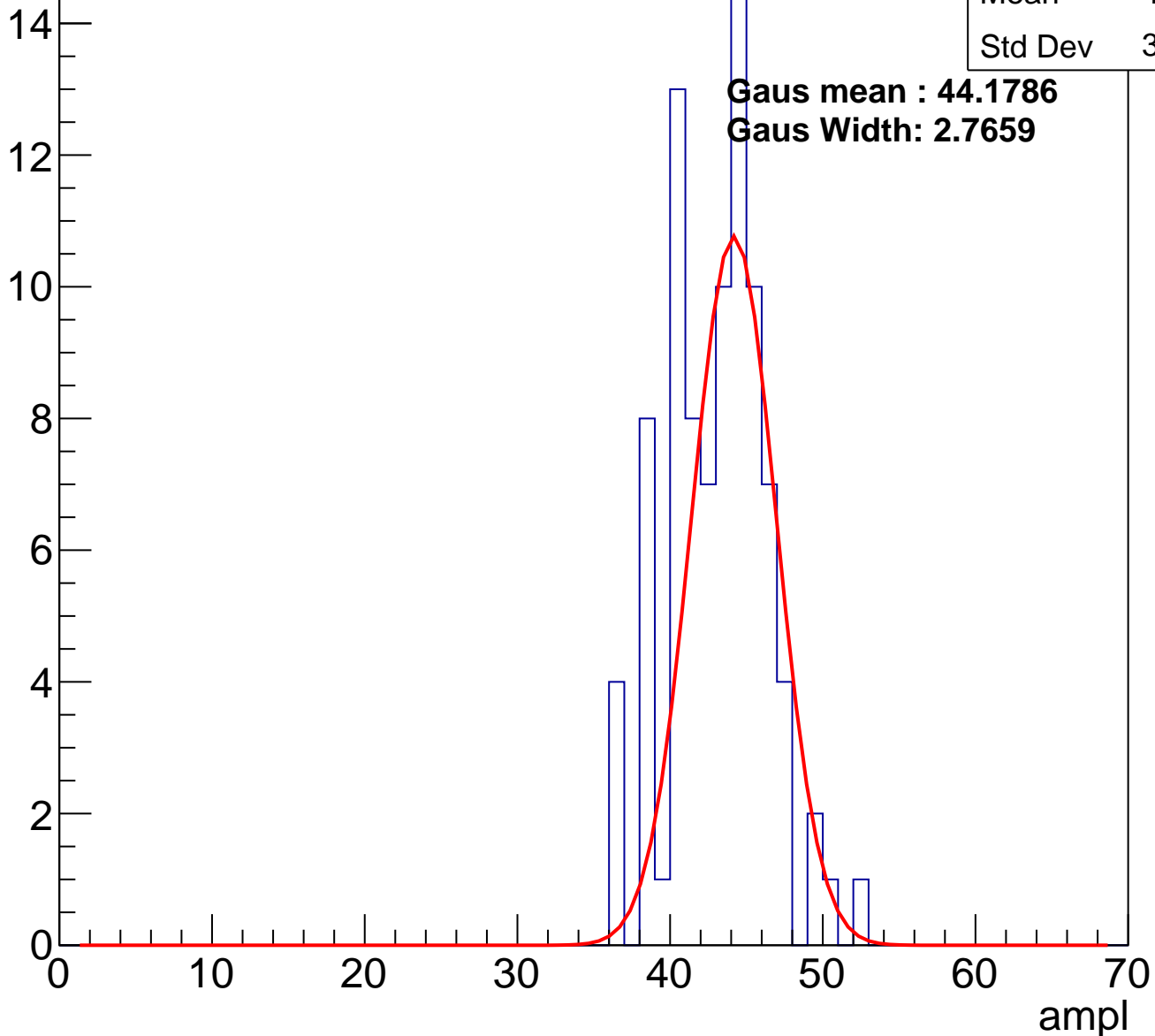
calib_packv5_042523_0143.root, FC#2, port C2

Entries	91
Mean	42.63
Std Dev	3.213

Gaus mean : 44.1786

Gaus Width: 2.7659

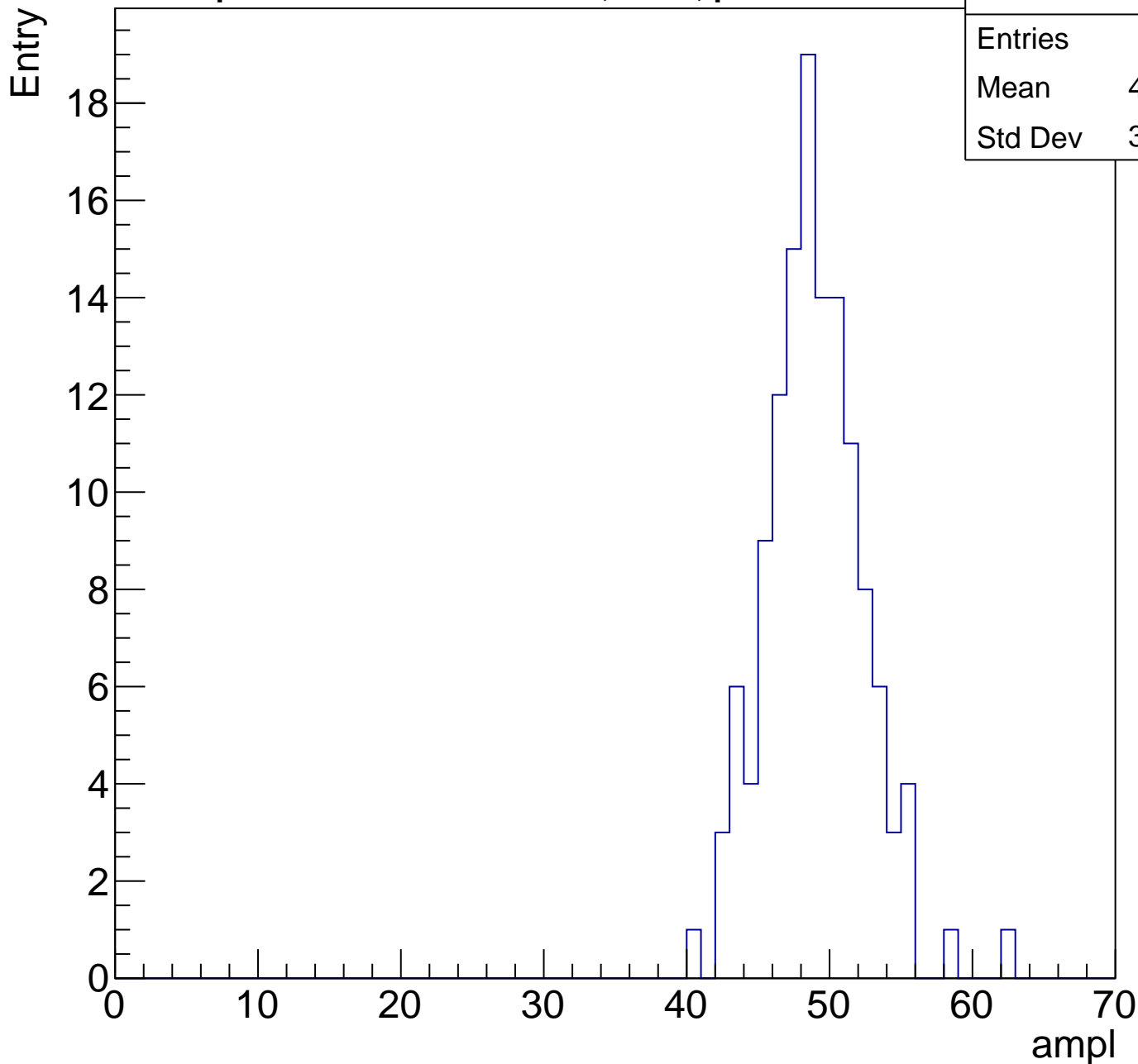
Entry



B1L001S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#2, port C2

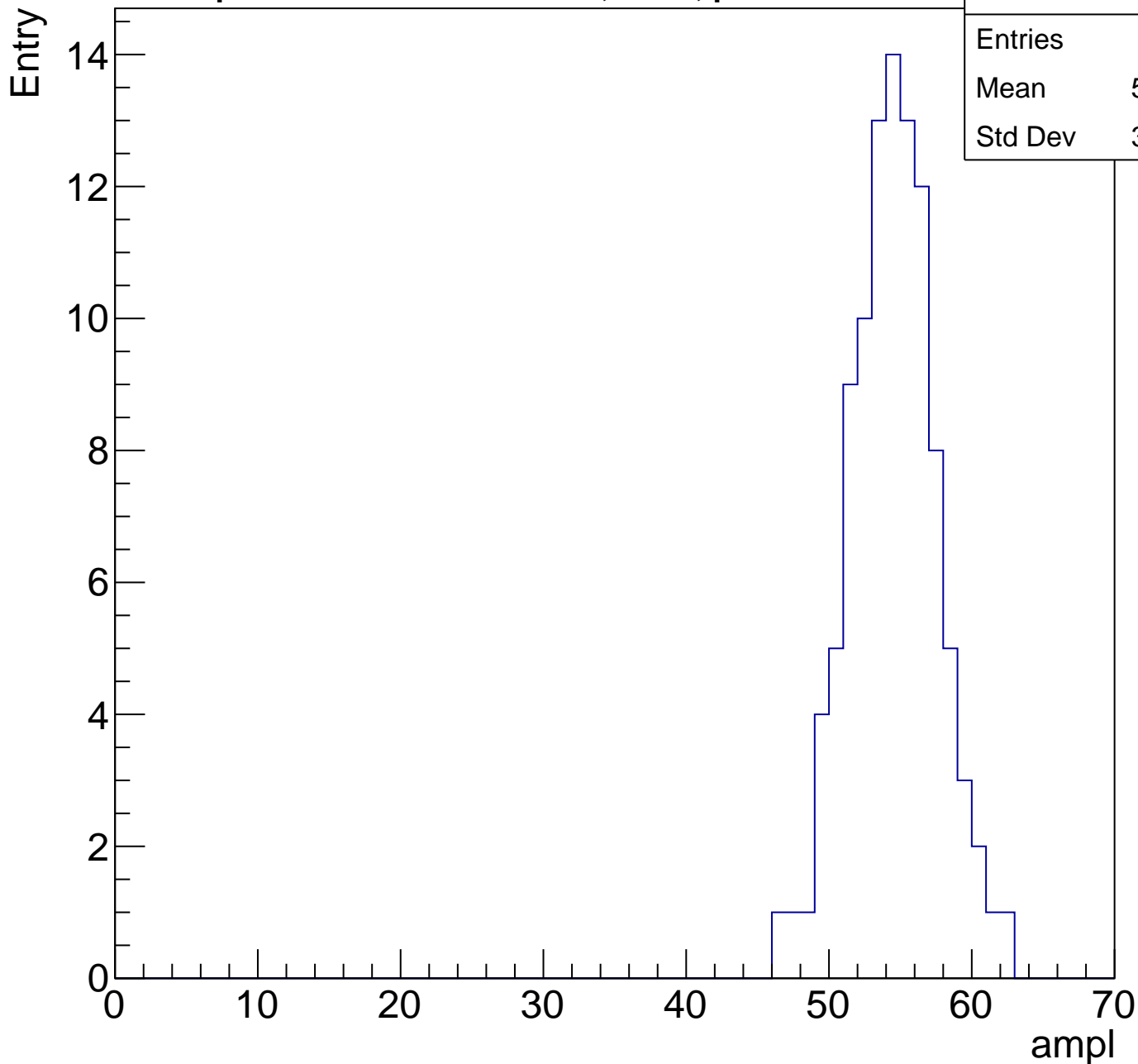
Entries	131
Mean	48.53
Std Dev	3.429



B1L001S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	54.02
Std Dev	3.014



B1L001S, U14-ch26, adc5

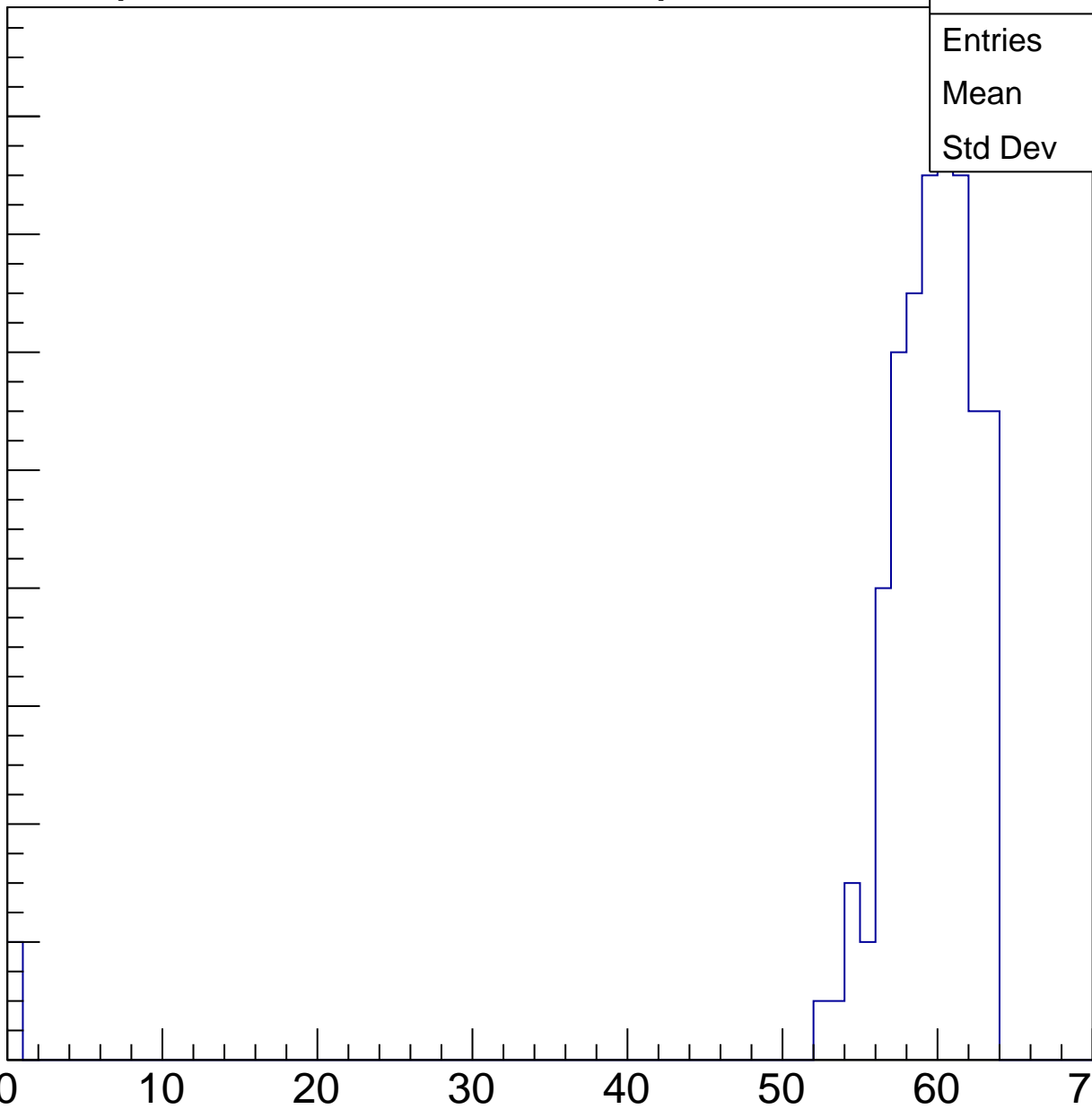
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.18
Std Dev	8.255

ampl

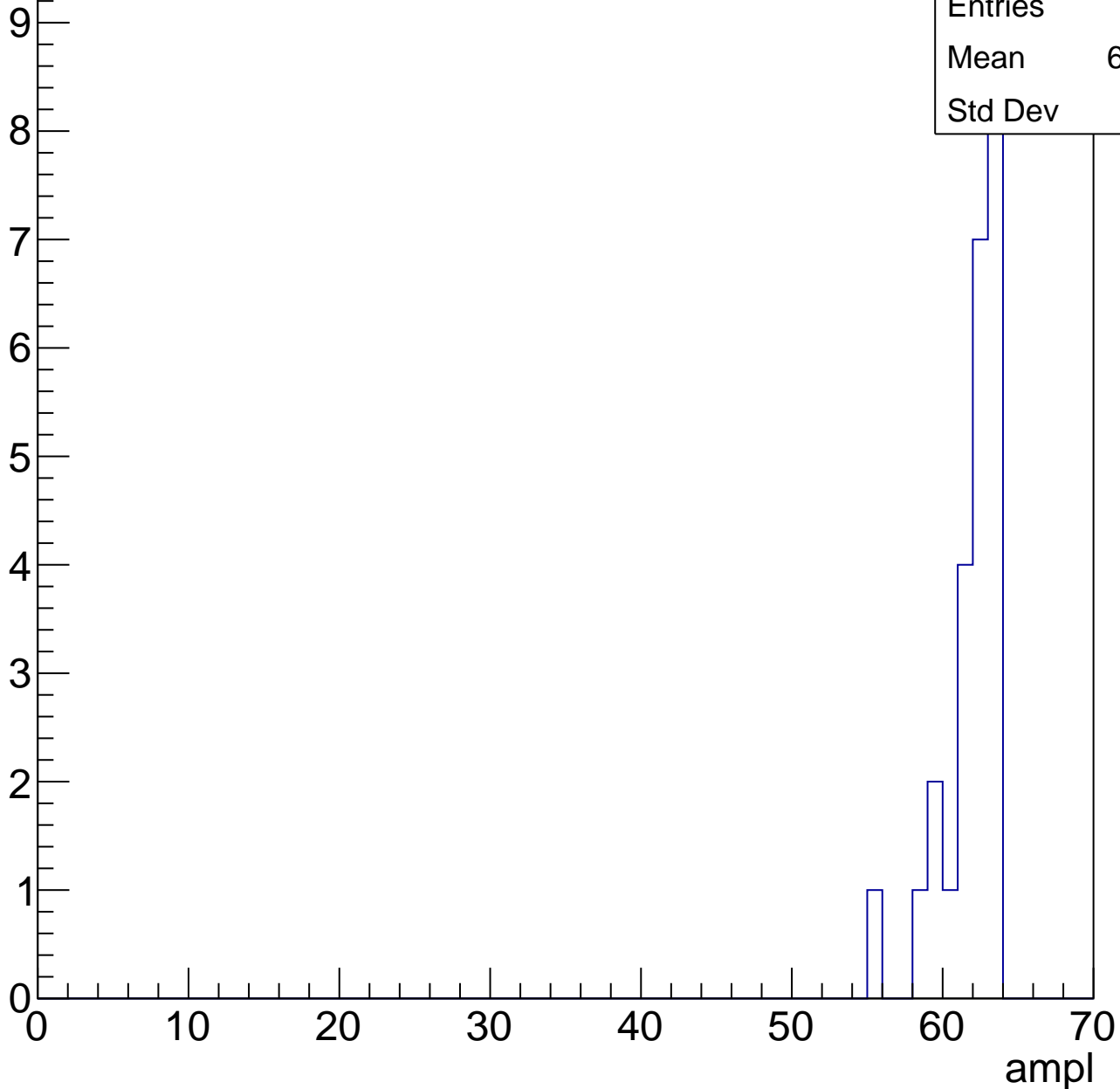


B1L001S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

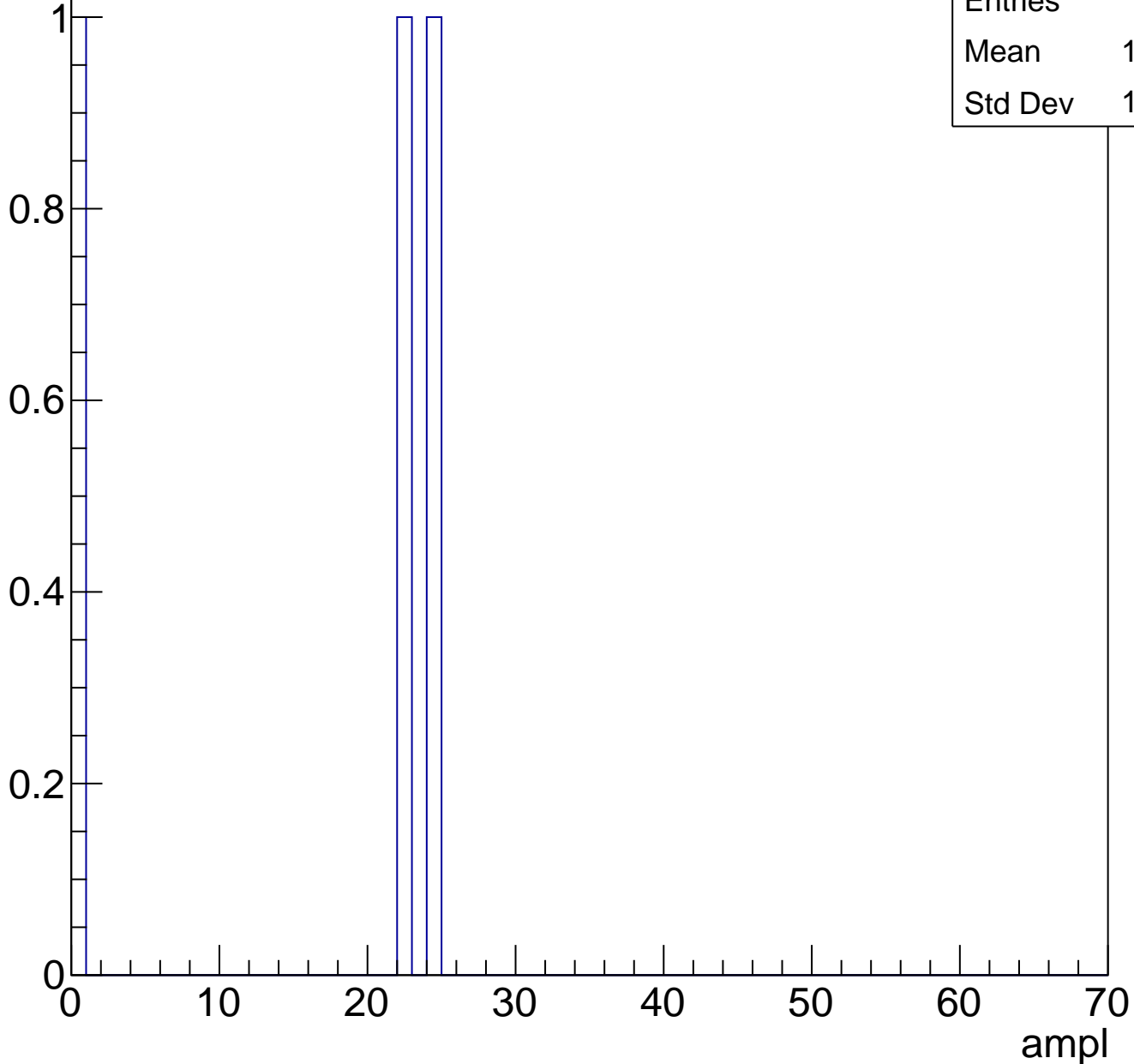
Entries	25
Mean	61.44
Std Dev	1.92



B1L001S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch27, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.29
Std Dev	4.232

Gaus mean : 29.2902

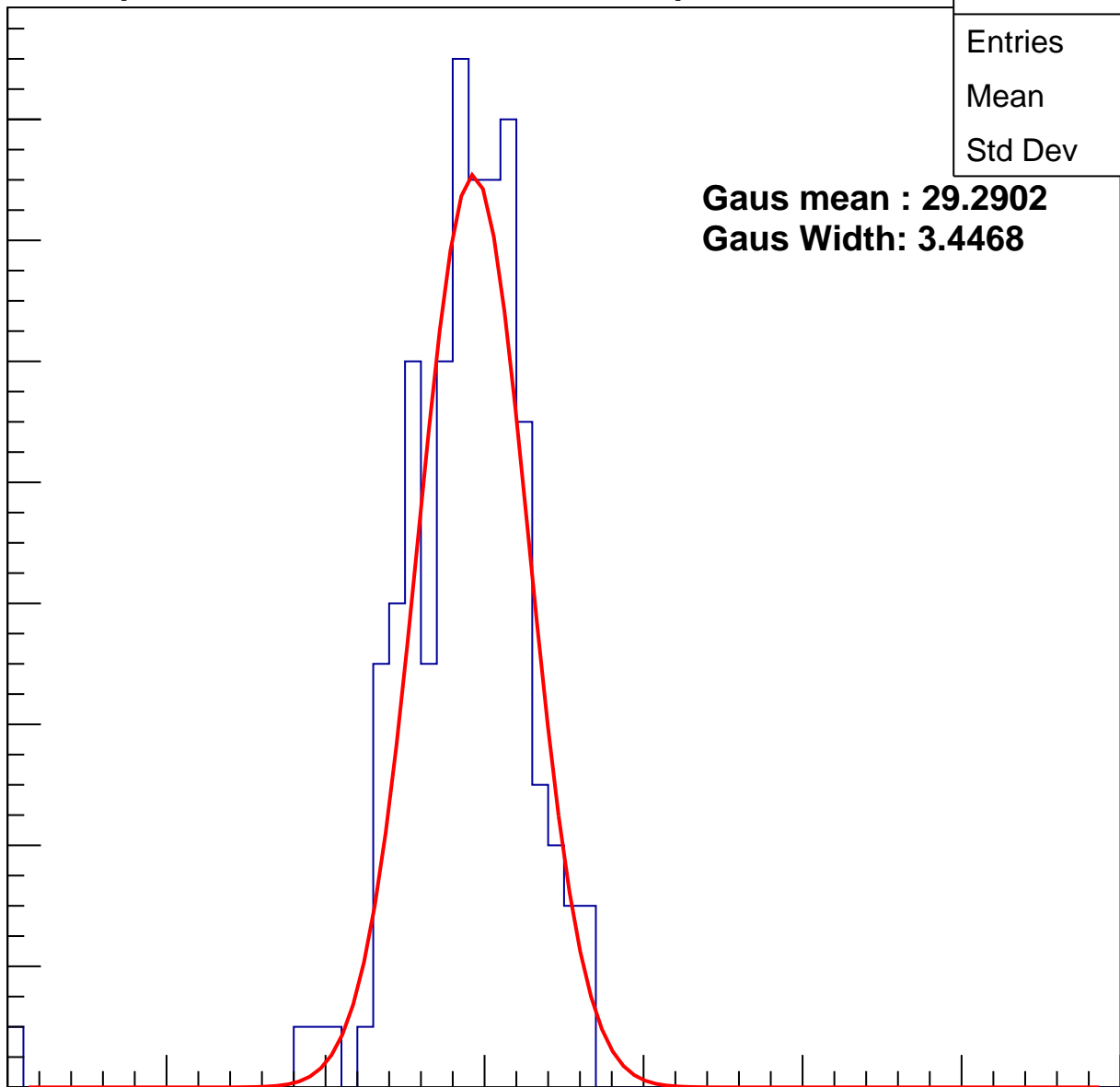
Gaus Width: 3.4468

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

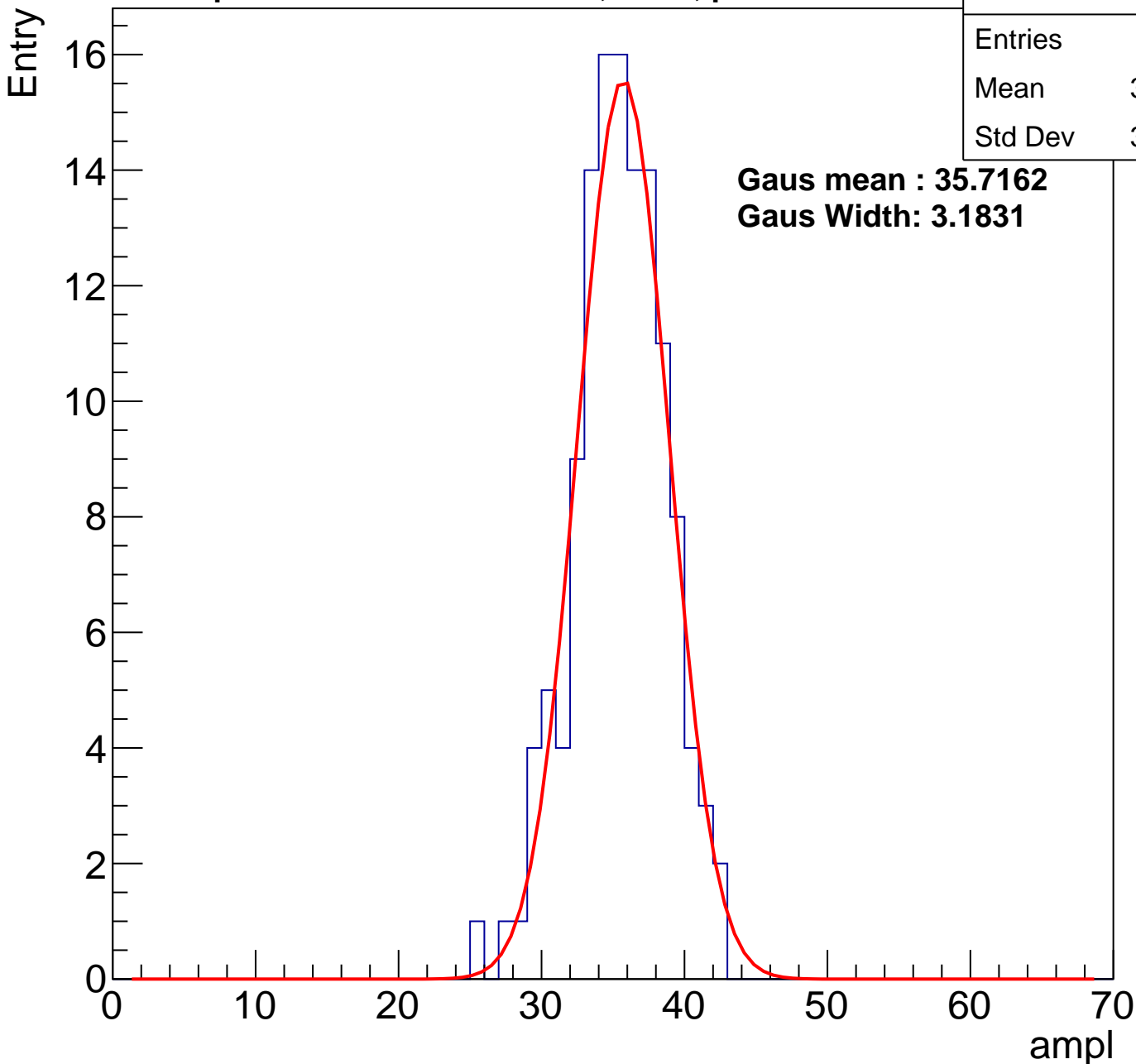


B1L001S, U14-ch27, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	34.98
Std Dev	3.202

Gaus mean : 35.7162
Gaus Width: 3.1831



B1L001S, U14-ch27, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	41.69
Std Dev	3.129

Gaus mean : 42.2246

Gaus Width: 3.2634

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

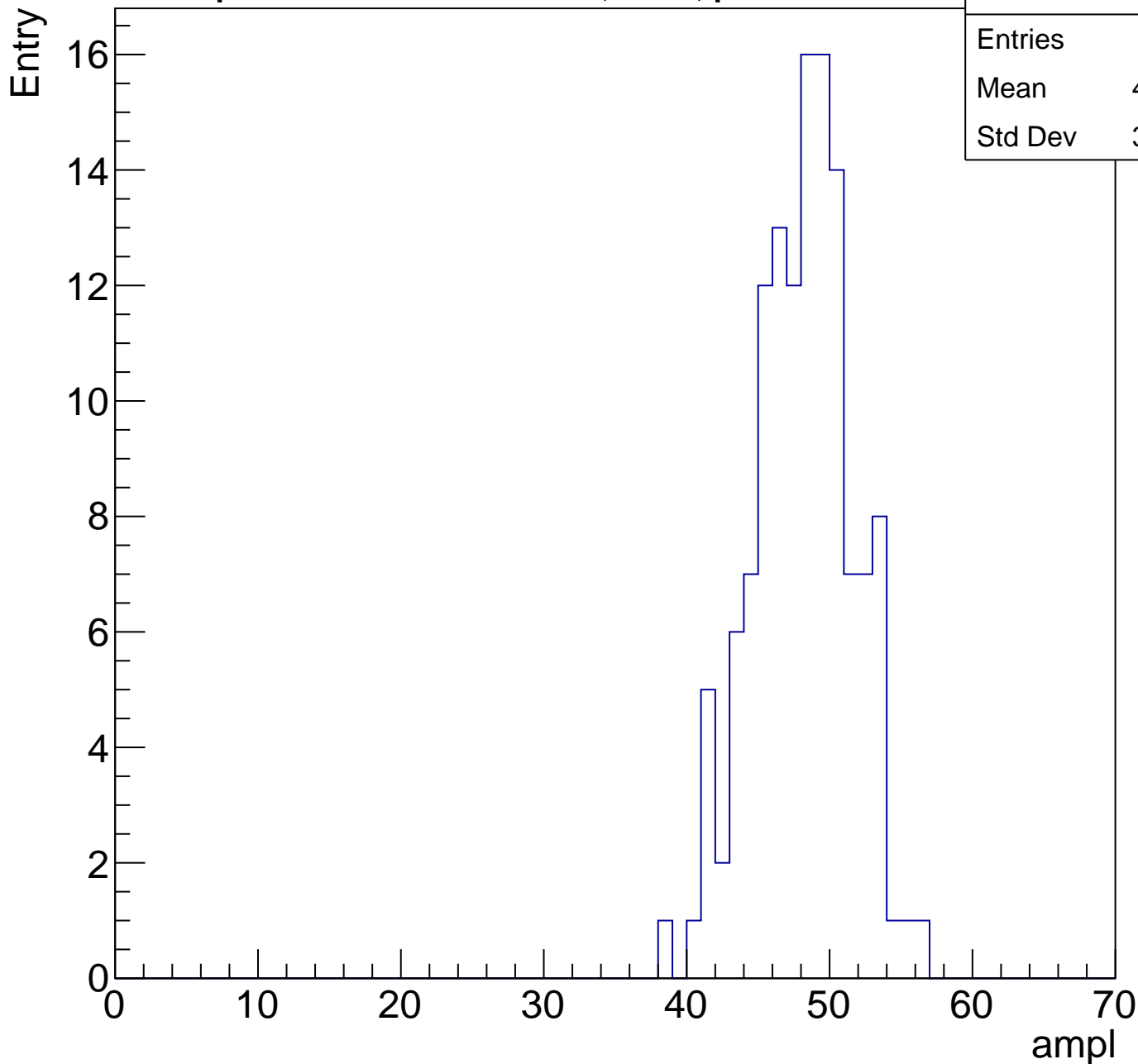
60

70

B1L001S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#2, port C2

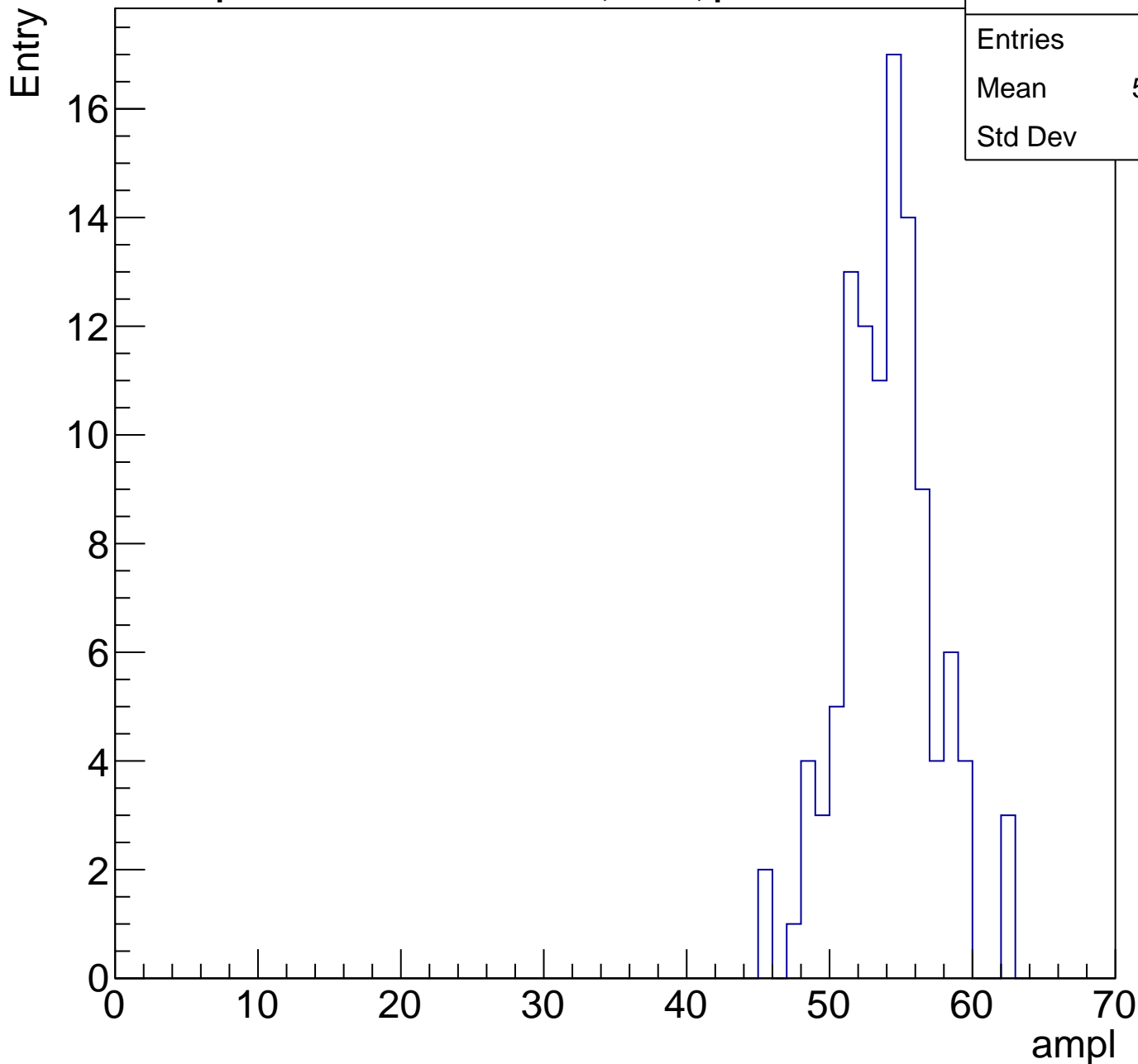
Entries	130
Mean	47.67
Std Dev	3.393



B1L001S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#2, port C2

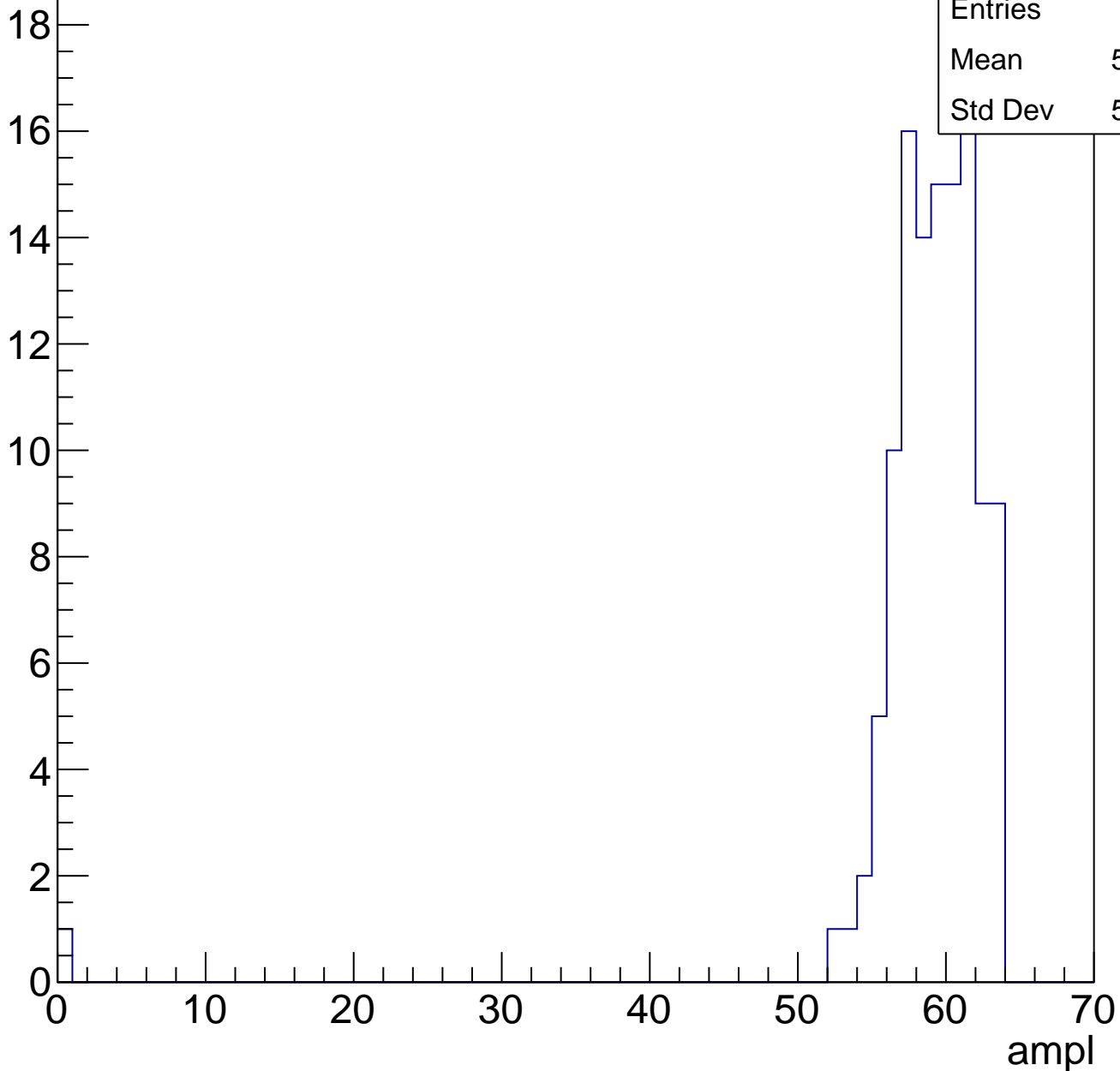
Entries	108
Mean	53.57
Std Dev	3.25



B1L001S, U14-ch27, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

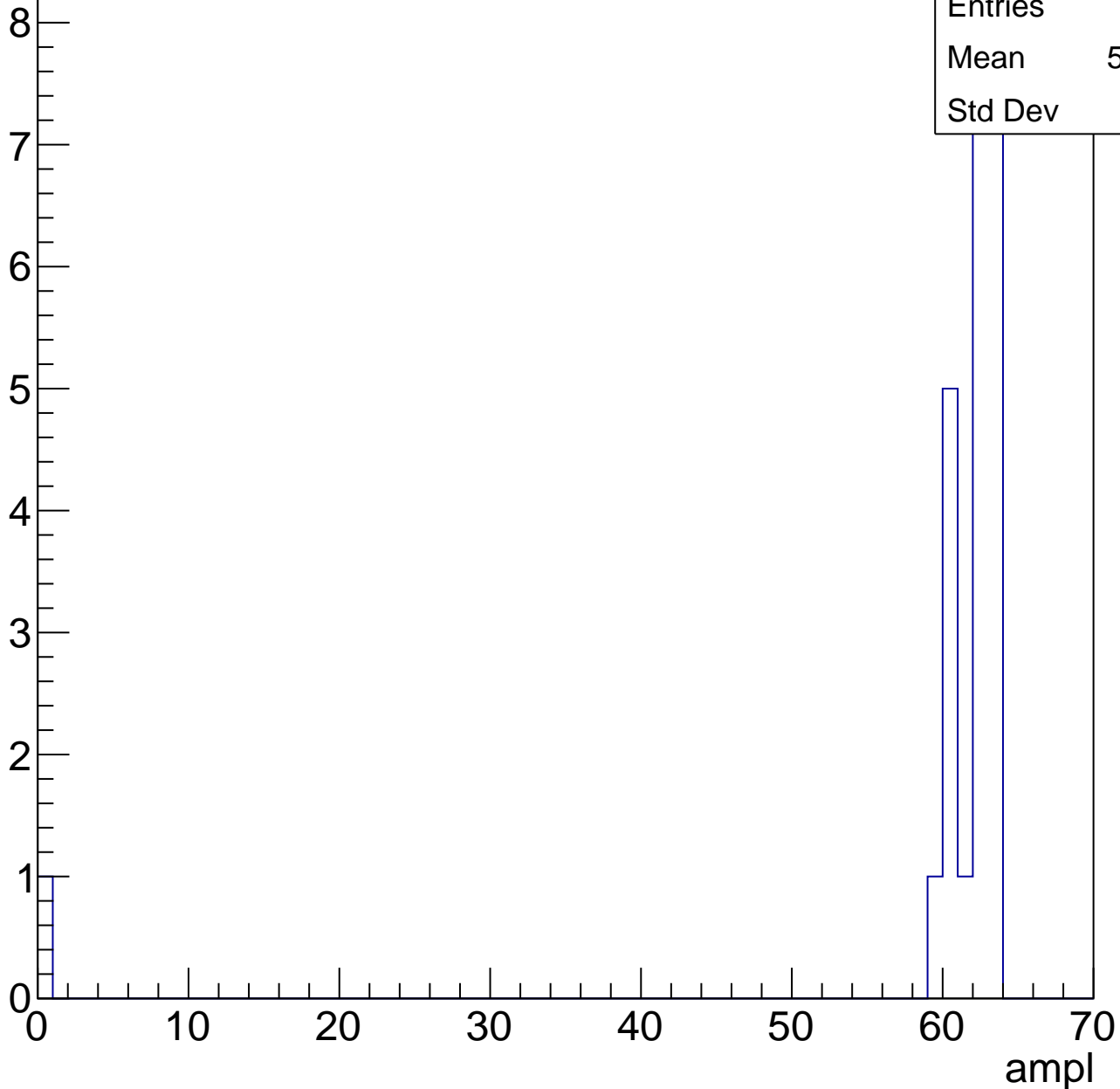


B1L001S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

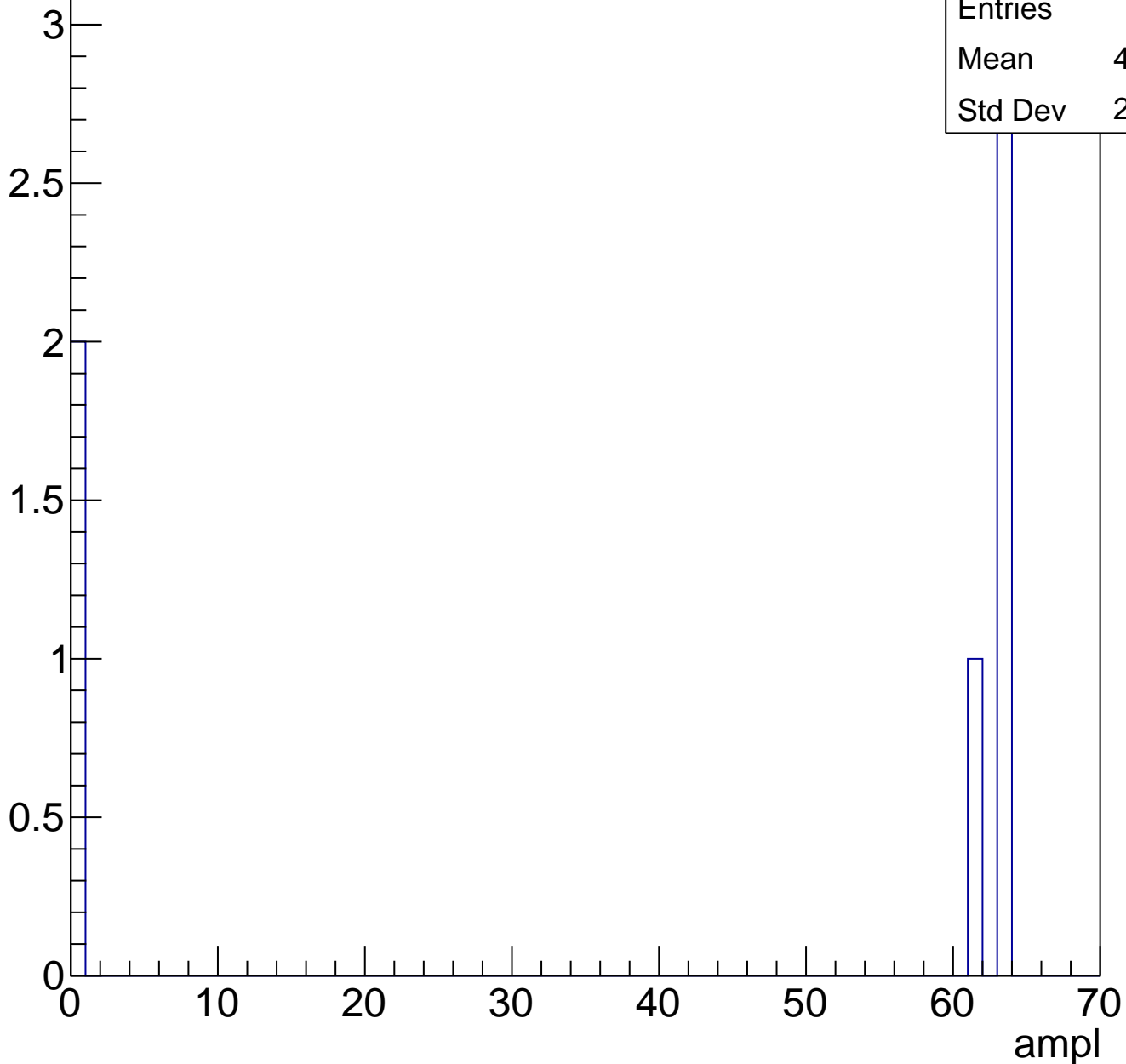
Entries	24
Mean	59.17
Std Dev	12.4



B1L001S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch28, adc0

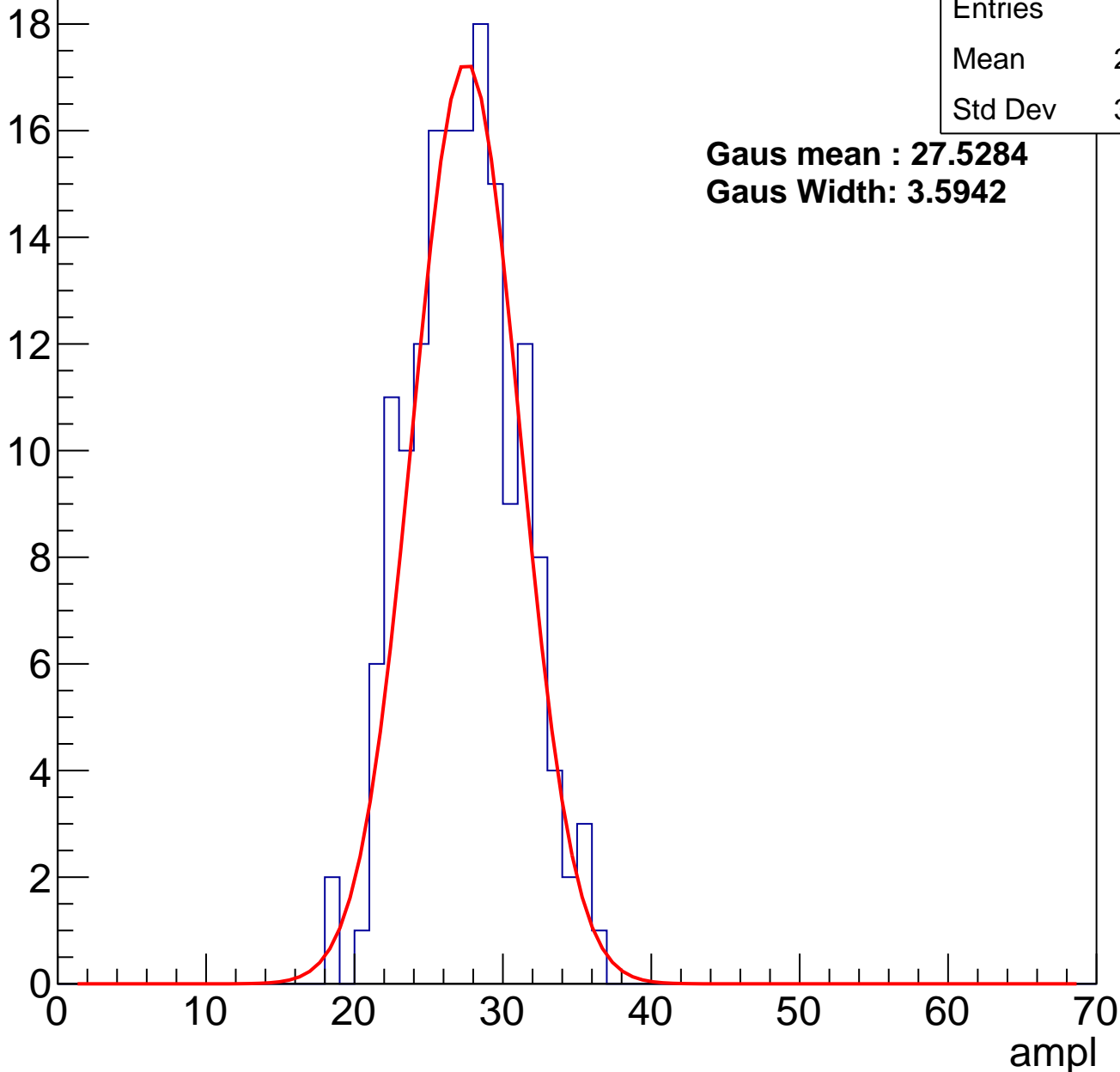
calib_packv5_042523_0143.root, FC#2, port C2

Entries	162
Mean	26.96
Std Dev	3.614

Gaus mean : 27.5284

Gaus Width: 3.5942

Entry



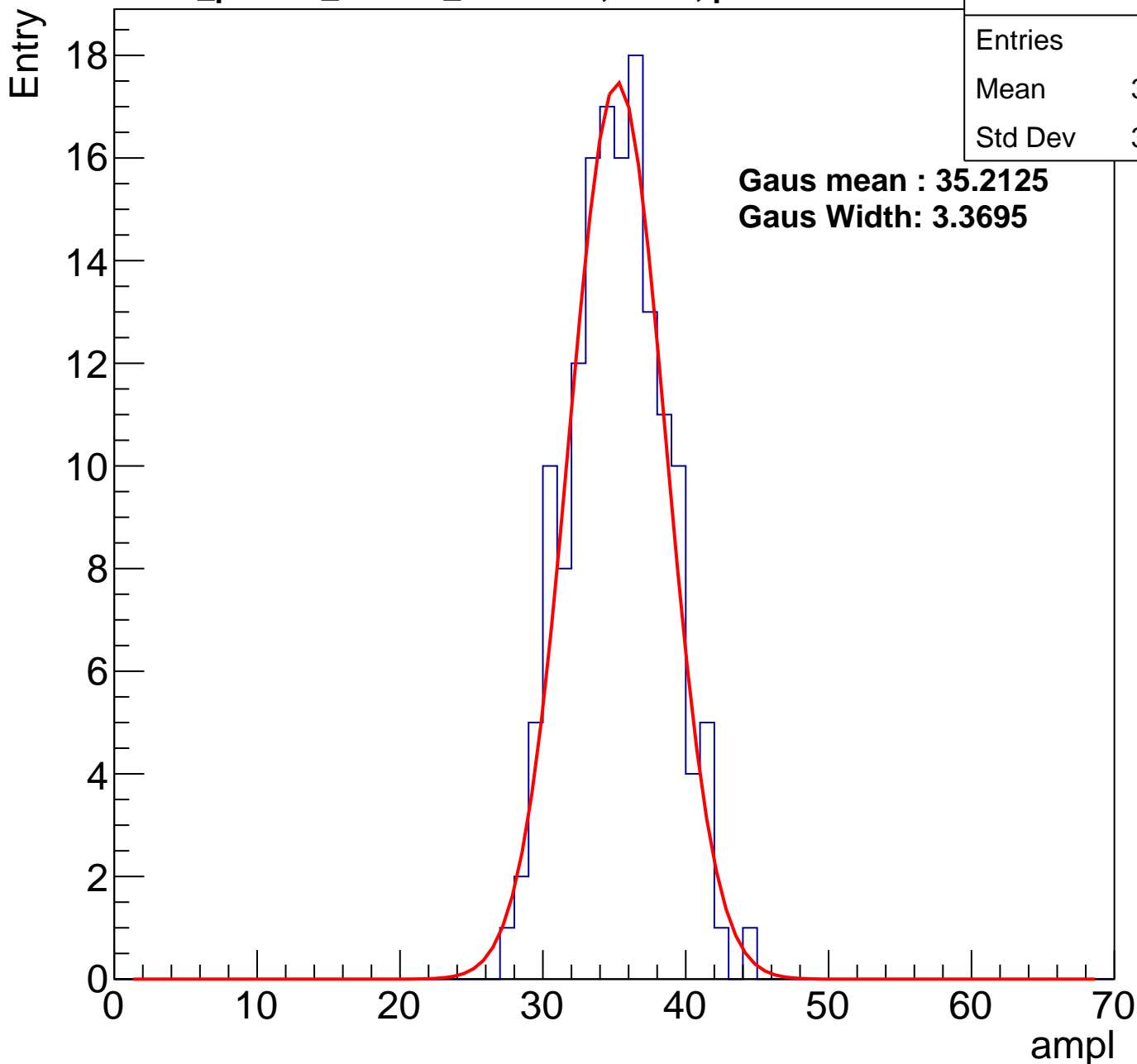
B1L001S, U14-ch28, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	34.76
Std Dev	3.306

Gaus mean : 35.2125

Gaus Width: 3.3695



B1L001S, U14-ch28, adc2

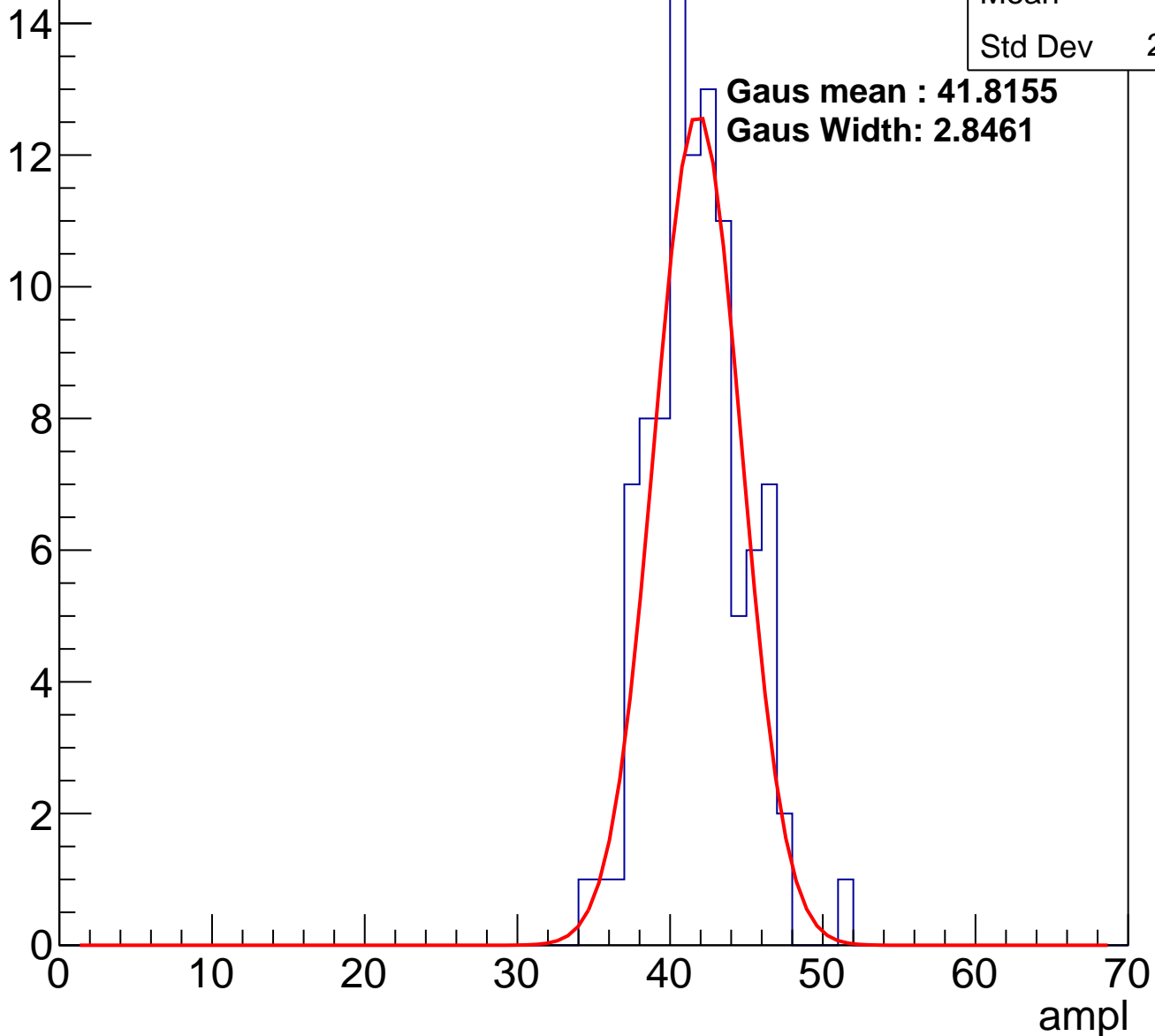
calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	41.31
Std Dev	2.991

Gaus mean : 41.8155

Gaus Width: 2.8461

Entry



B1L001S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

128

Mean

46.52

Std Dev

3.162

ampl

0

10

20

30

40

50

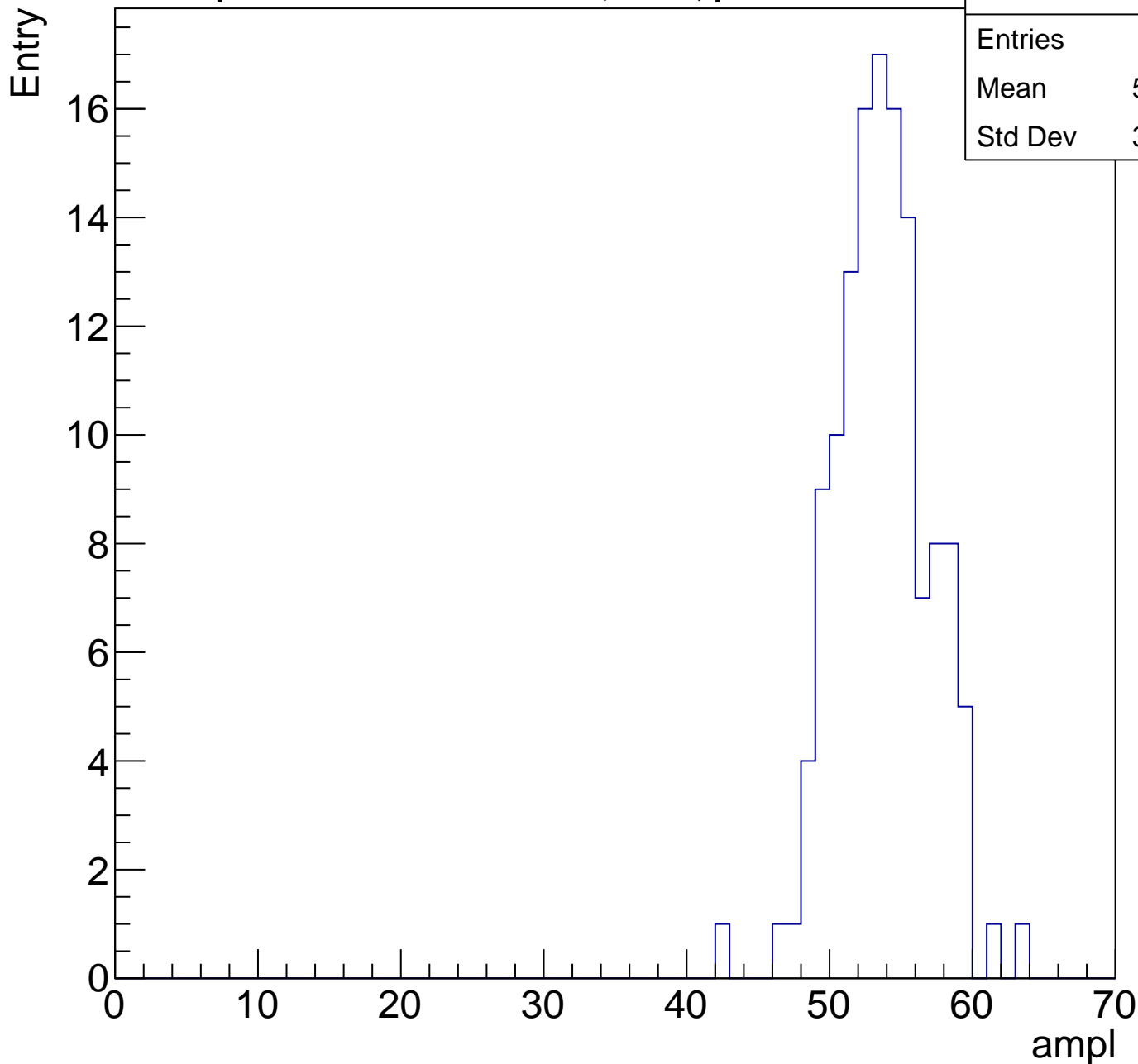
60

70

B1L001S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#2, port C2

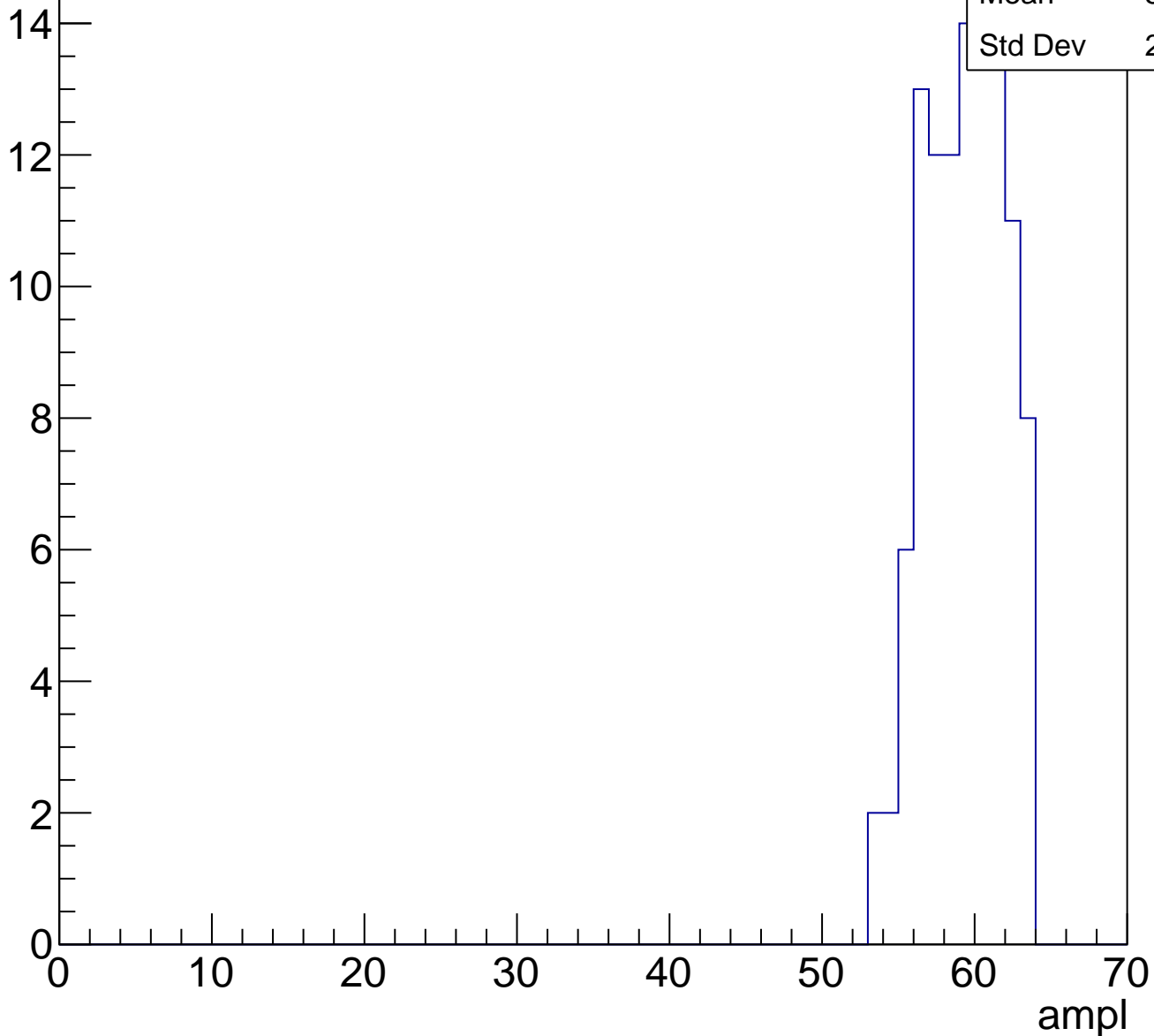
Entries	132
Mean	53.25
Std Dev	3.265



B1L001S, U14-ch28, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

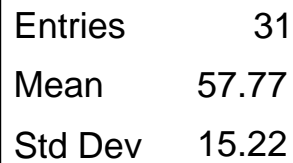
50

60

70

ampl

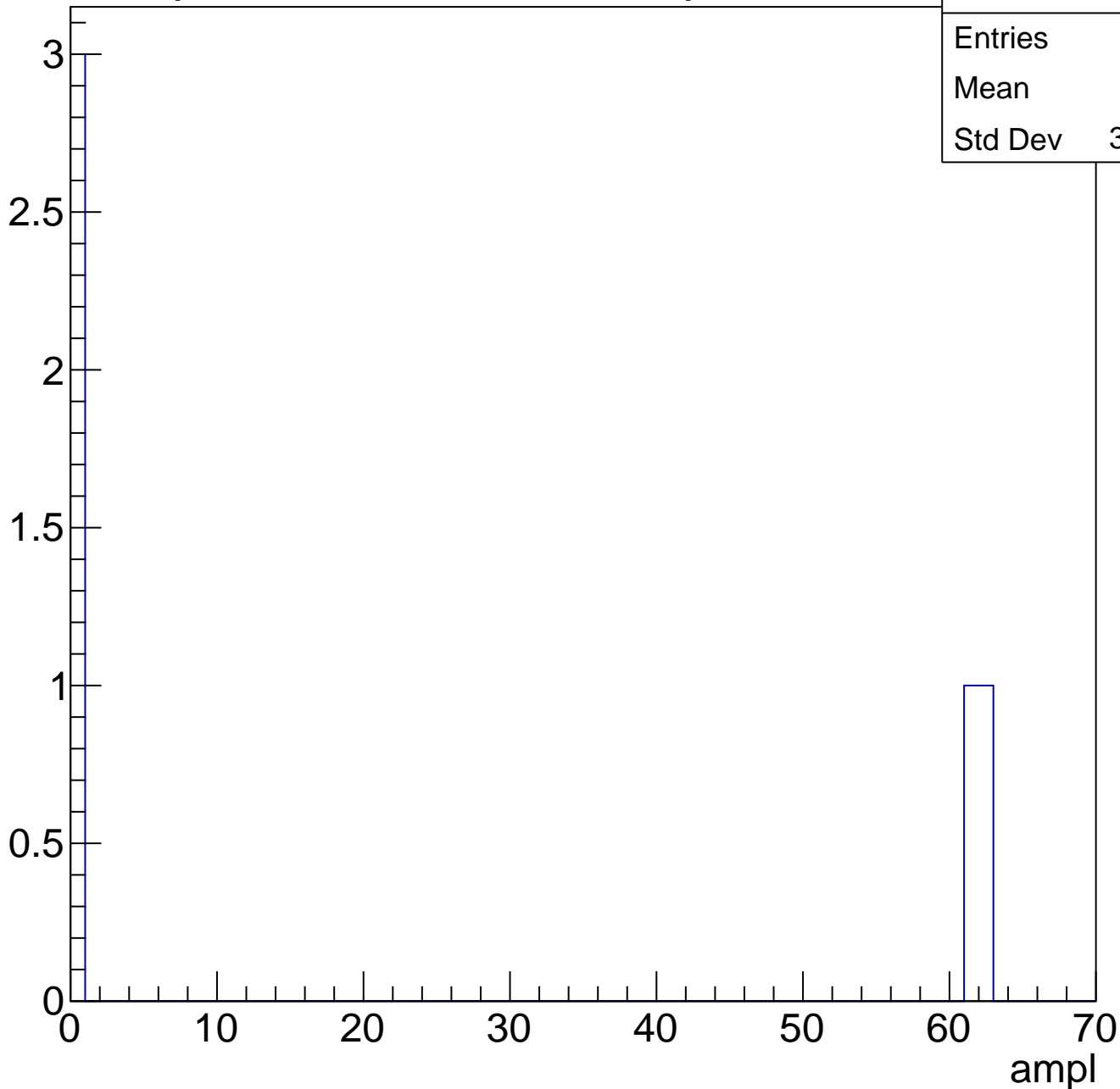
Entries	31
Mean	57.77
Std Dev	15.22



B1L001S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch29, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.52
Std Dev	3.646

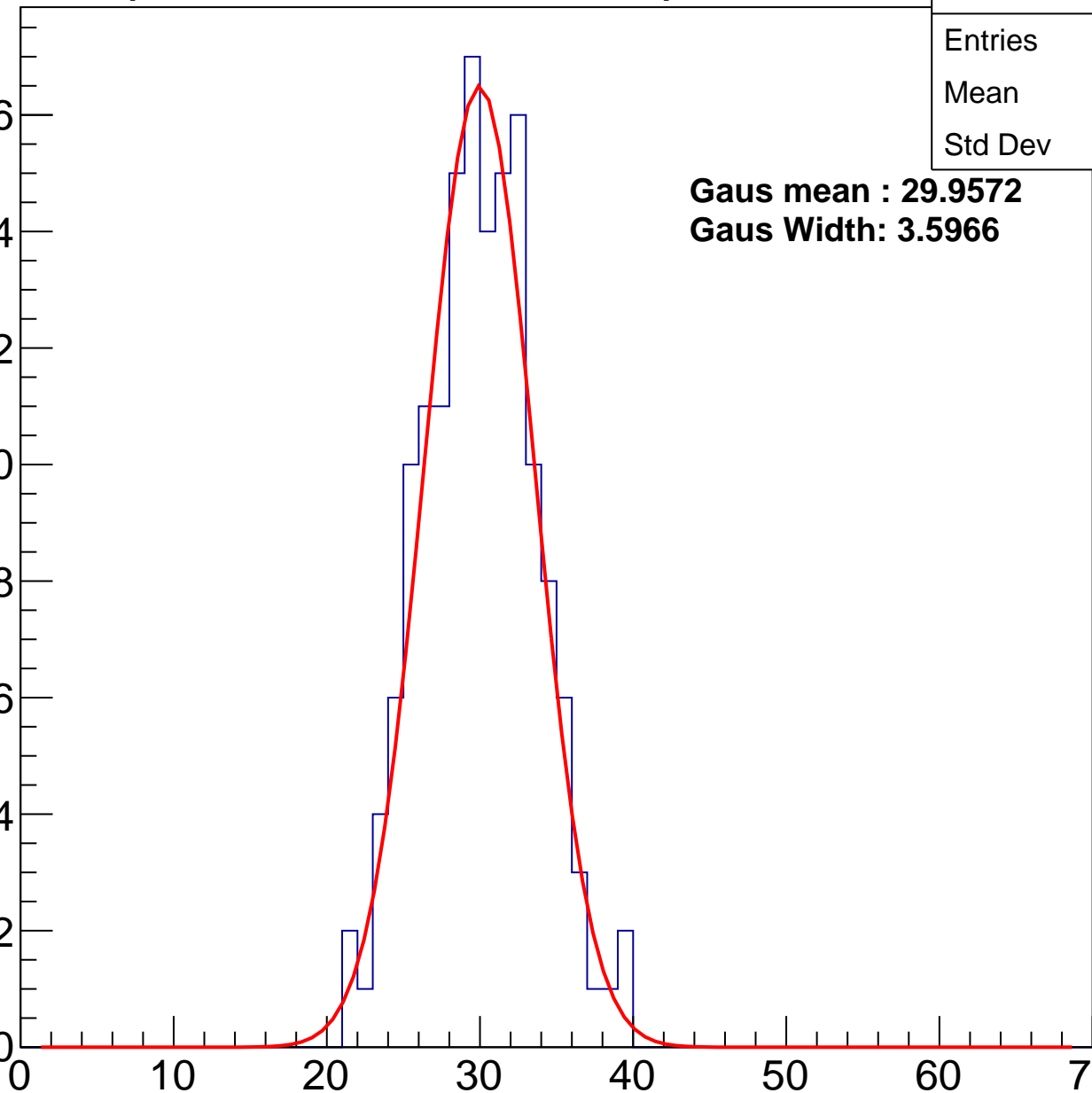
Gaus mean : 29.9572

Gaus Width: 3.5966

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch29, adc1

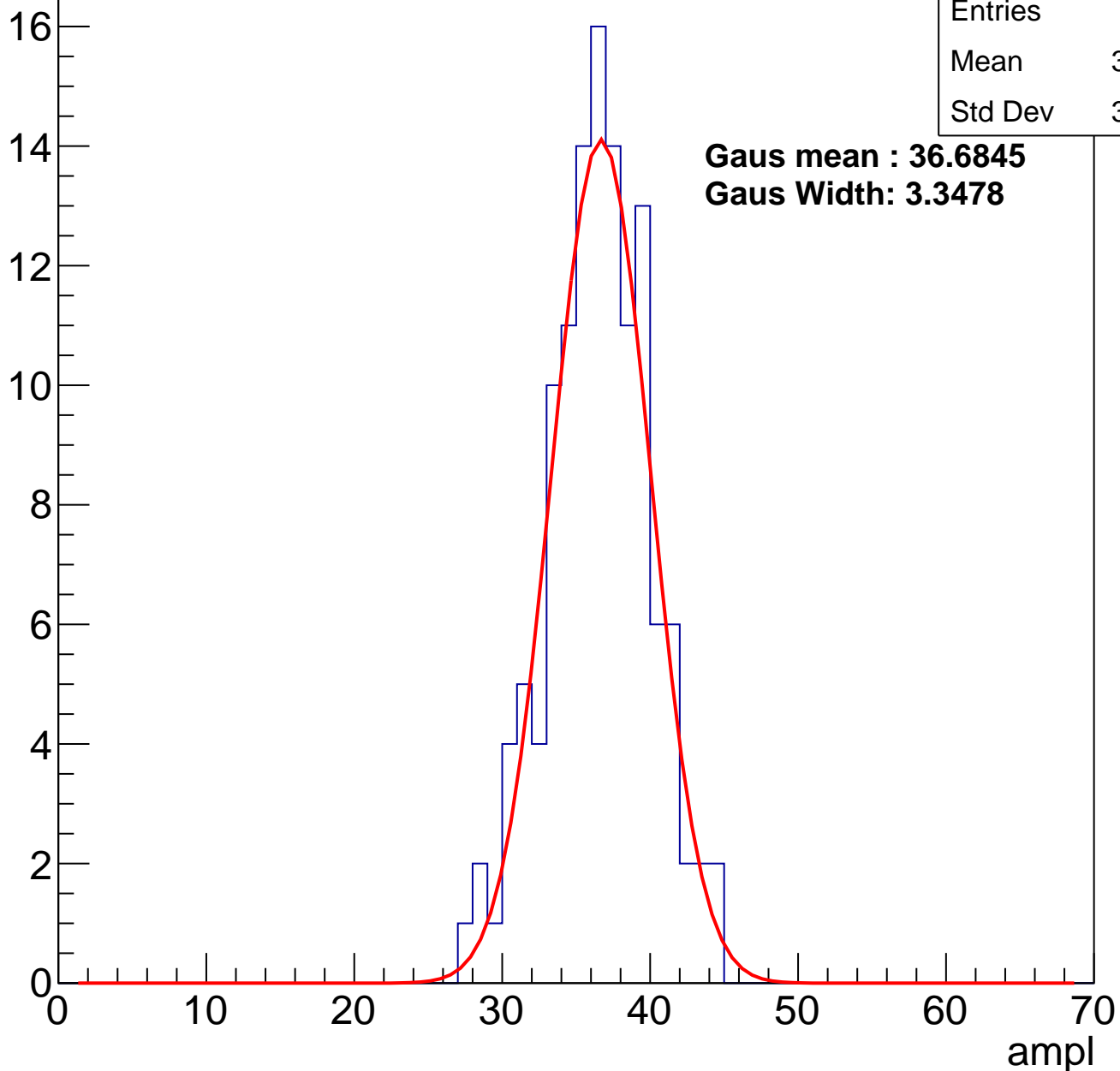
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.06
Std Dev	3.428

Gaus mean : 36.6845

Gaus Width: 3.3478

Entry



B1L001S, U14-ch29, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	92
Mean	41.87
Std Dev	3.04

Gaus mean : 42.5552

Gaus Width: 3.1990

0

2

4

6

8

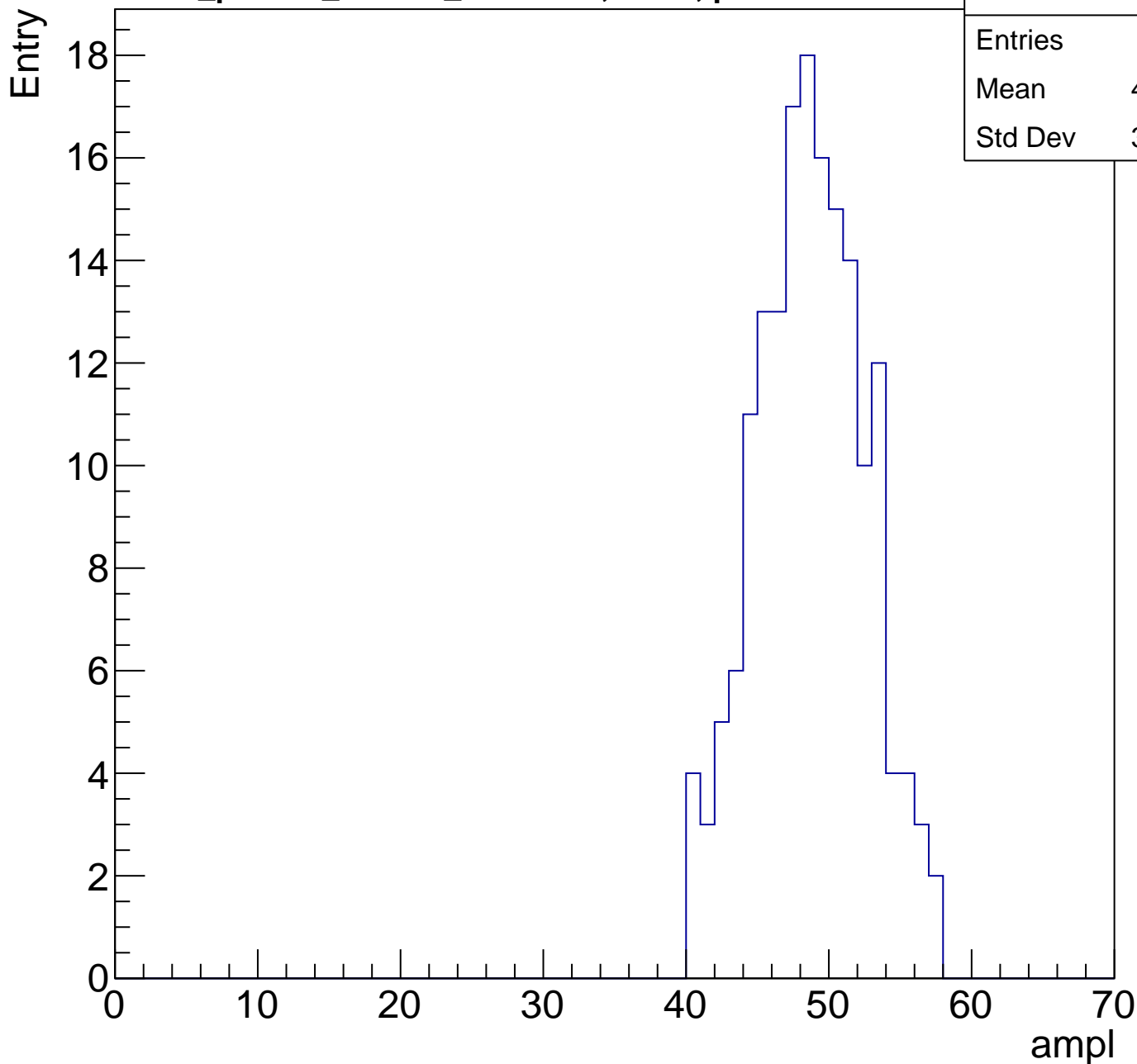
10

12

B1L001S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#2, port C2

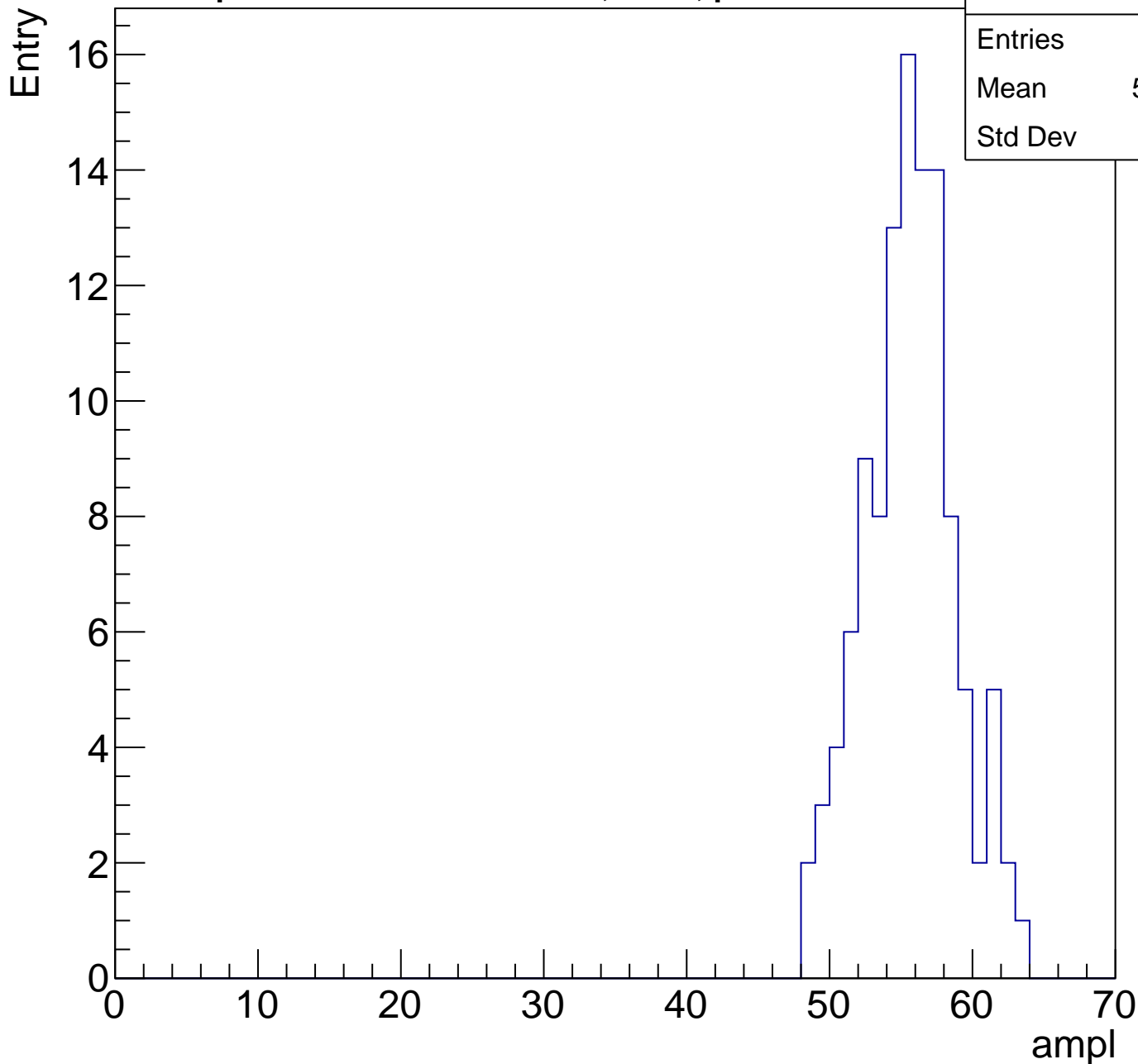
Entries	170
Mean	48.25
Std Dev	3.789



B1L001S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	55.14
Std Dev	3.19



B1L001S, U14-ch29, adc5

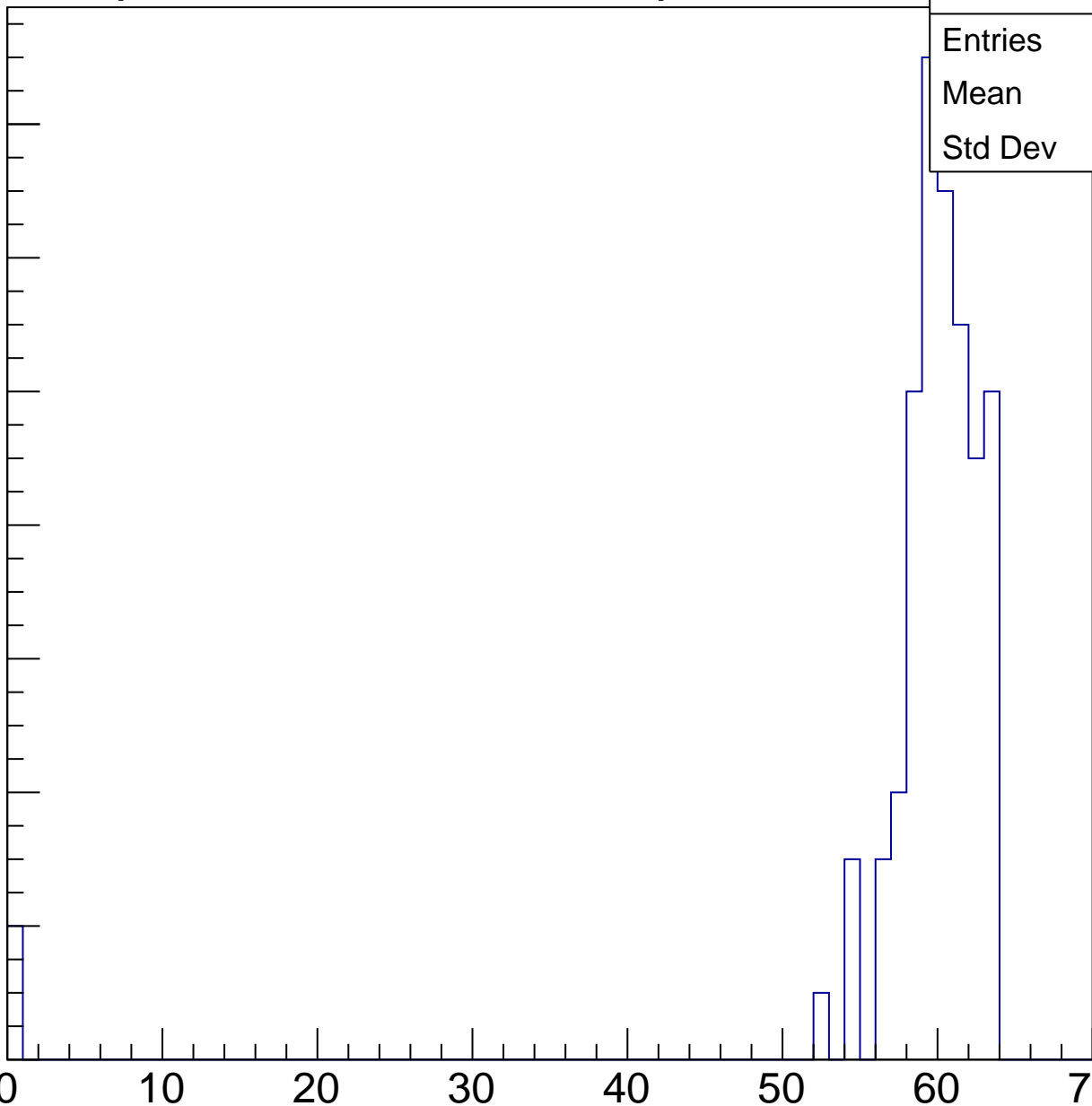
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	81
Mean	58.2
Std Dev	9.548

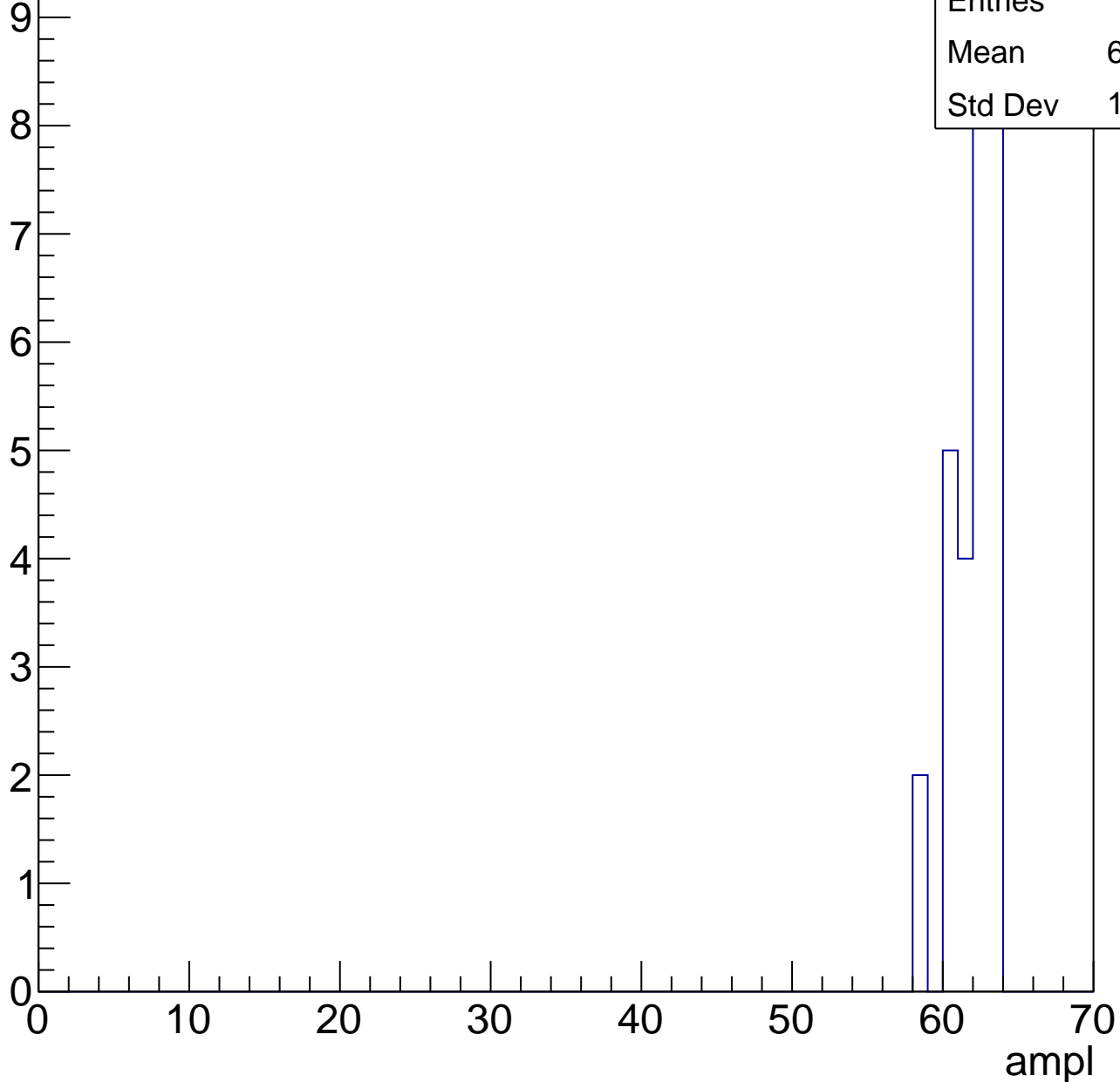
ampl



B1L001S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch29, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

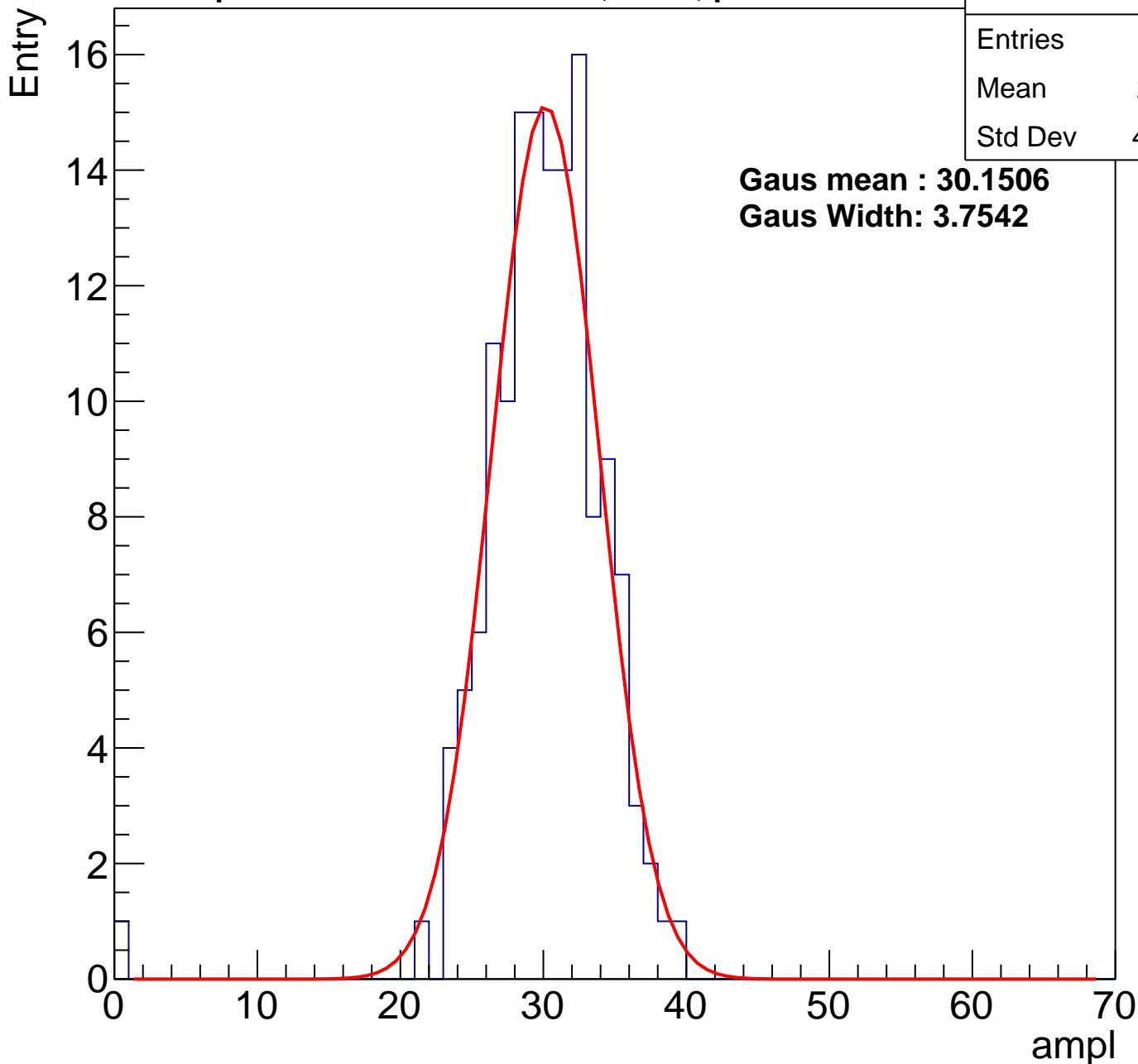
B1L001S, U14-ch30, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	29.61
Std Dev	4.302

Gaus mean : 30.1506

Gaus Width: 3.7542



B1L001S, U14-ch30, adc1

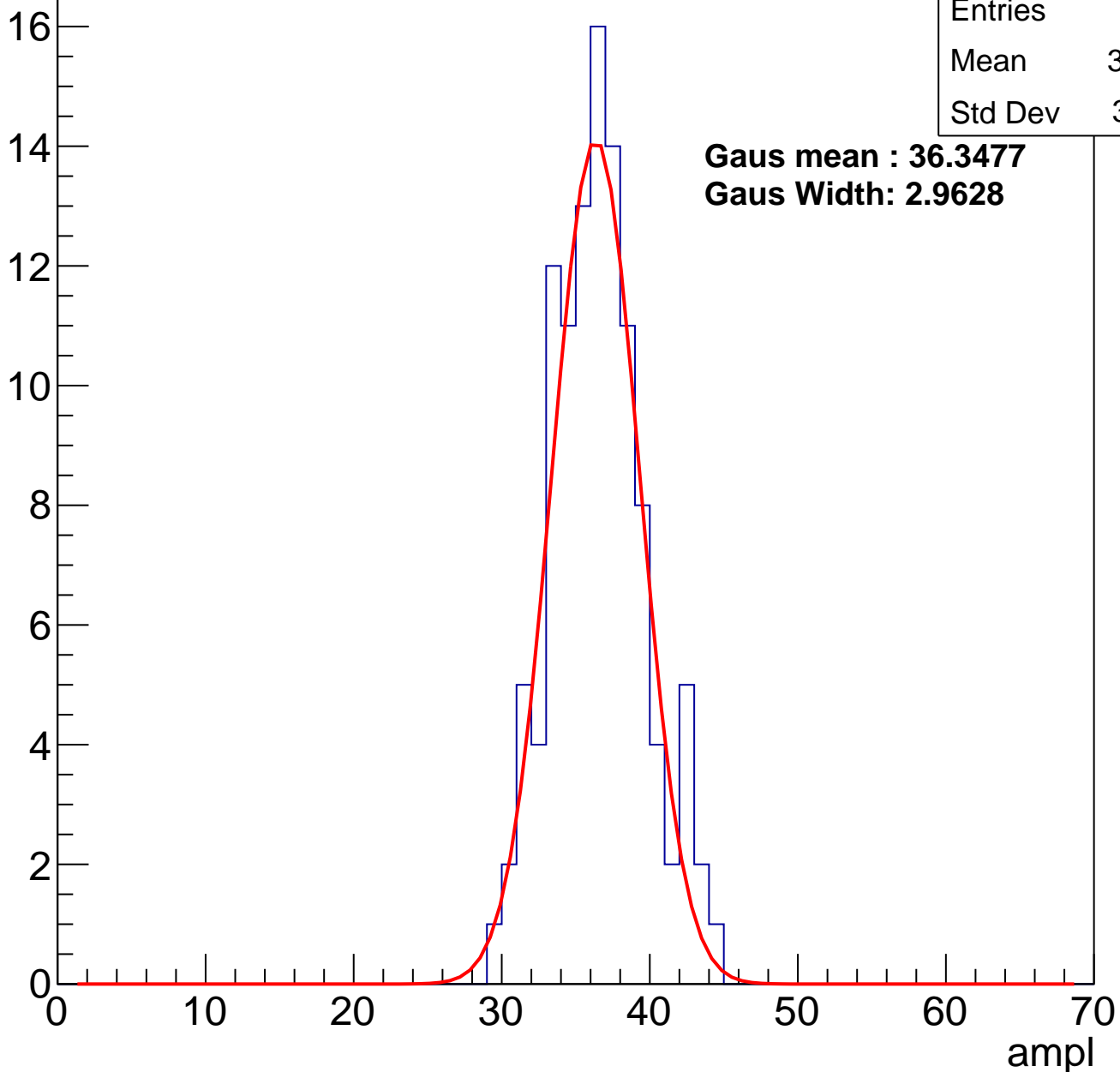
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.06
Std Dev	3.091

Gaus mean : 36.3477

Gaus Width: 2.9628

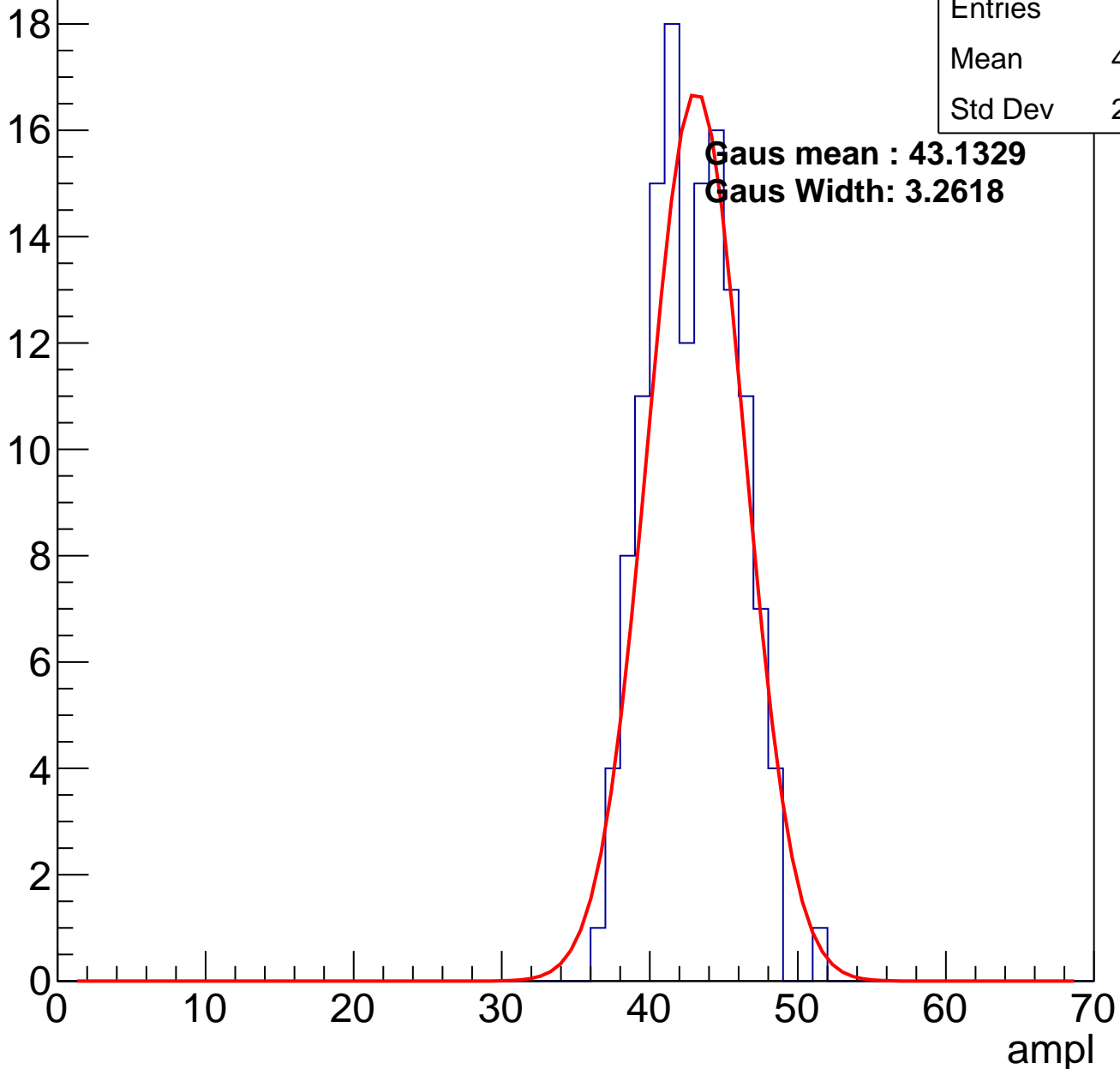
Entry



B1L001S, U14-ch30, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

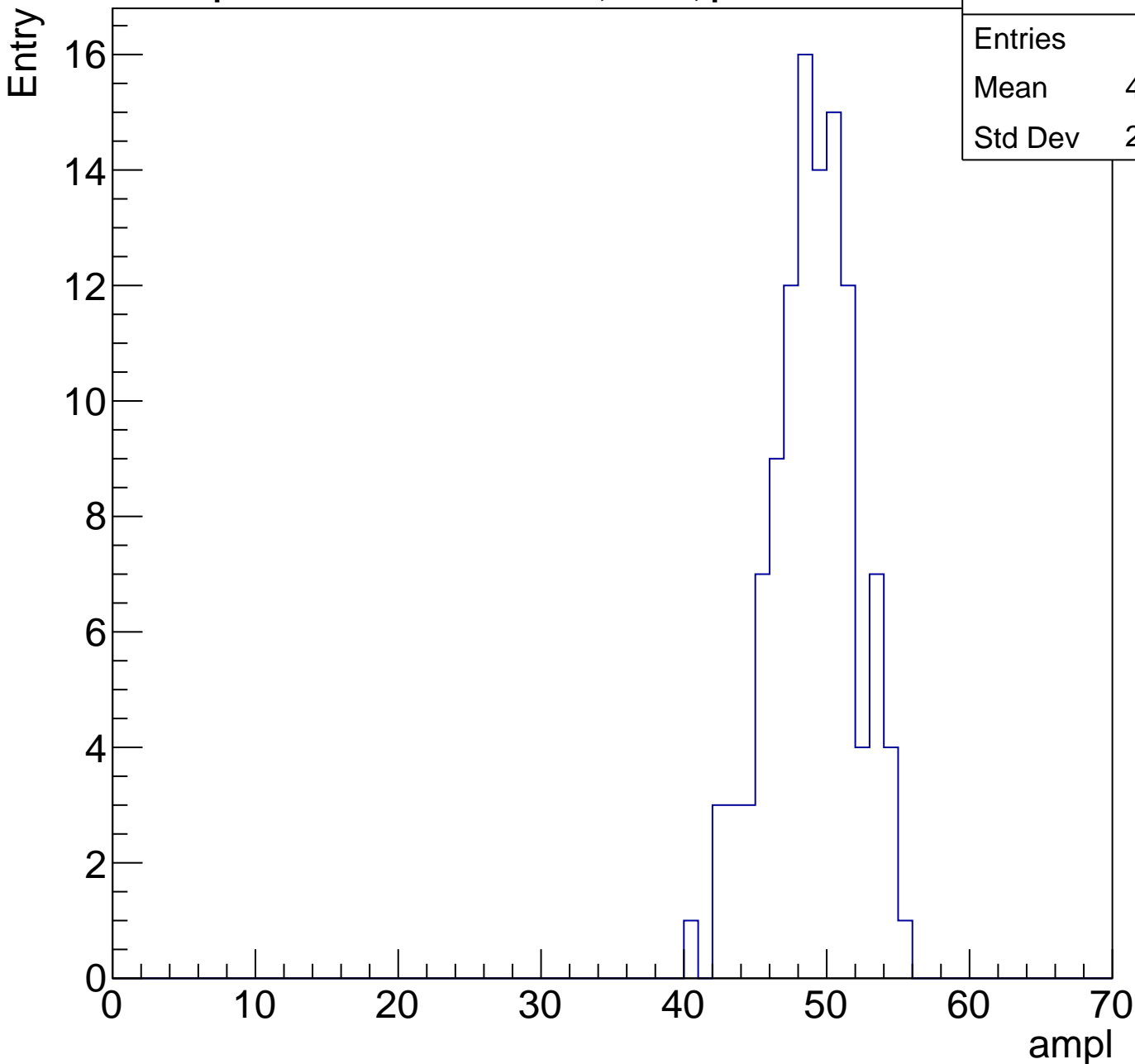


Entries	136
Mean	42.43
Std Dev	2.955

B1L001S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	48.52
Std Dev	2.986



B1L001S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	54.42
Std Dev	3.101

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch30, adc5

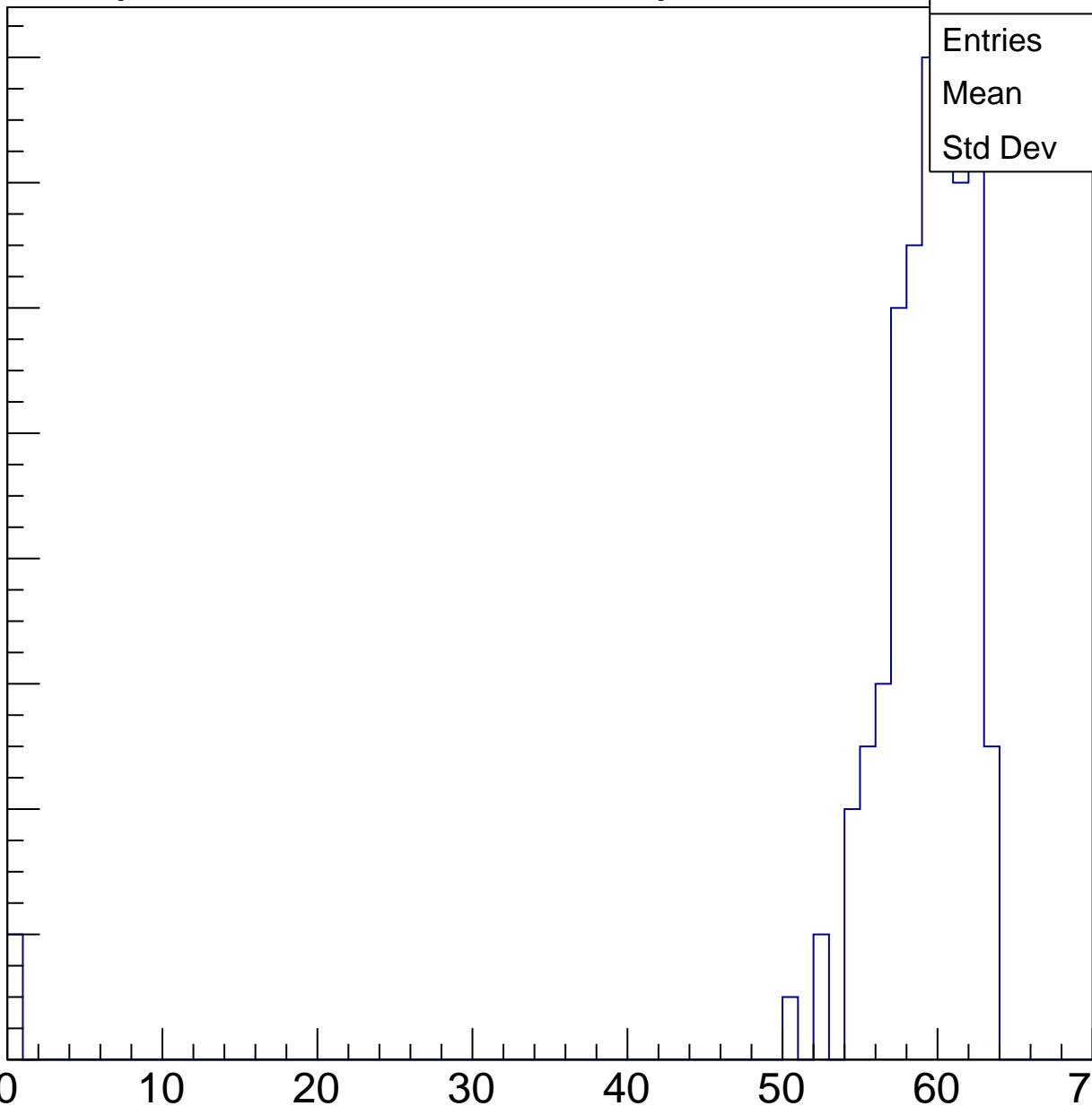
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	57.86
Std Dev	8.262

ampl



B1L001S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	21
Mean	62.43
Std Dev	0.7911

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

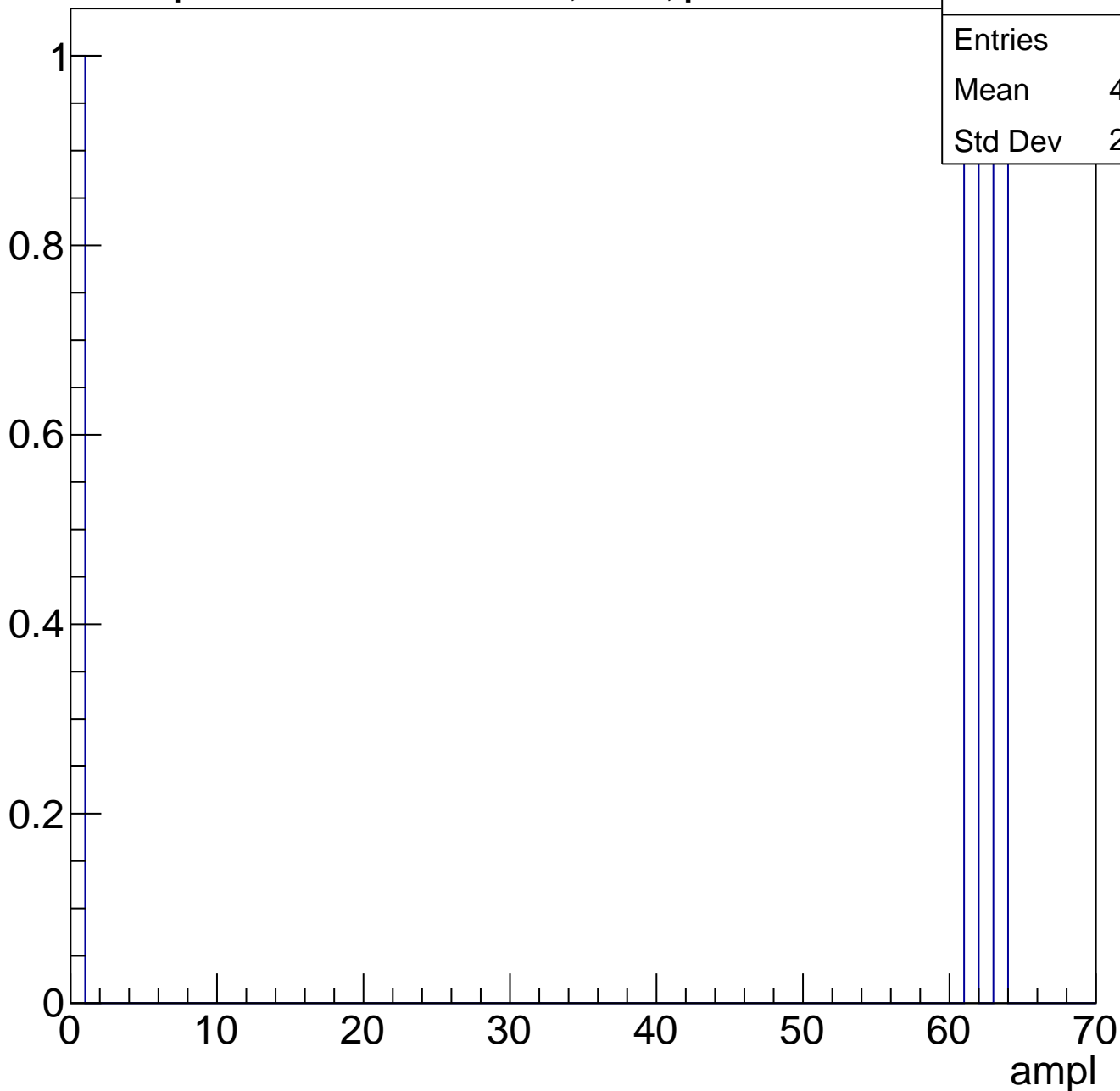
10

12

B1L001S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch31, adc0

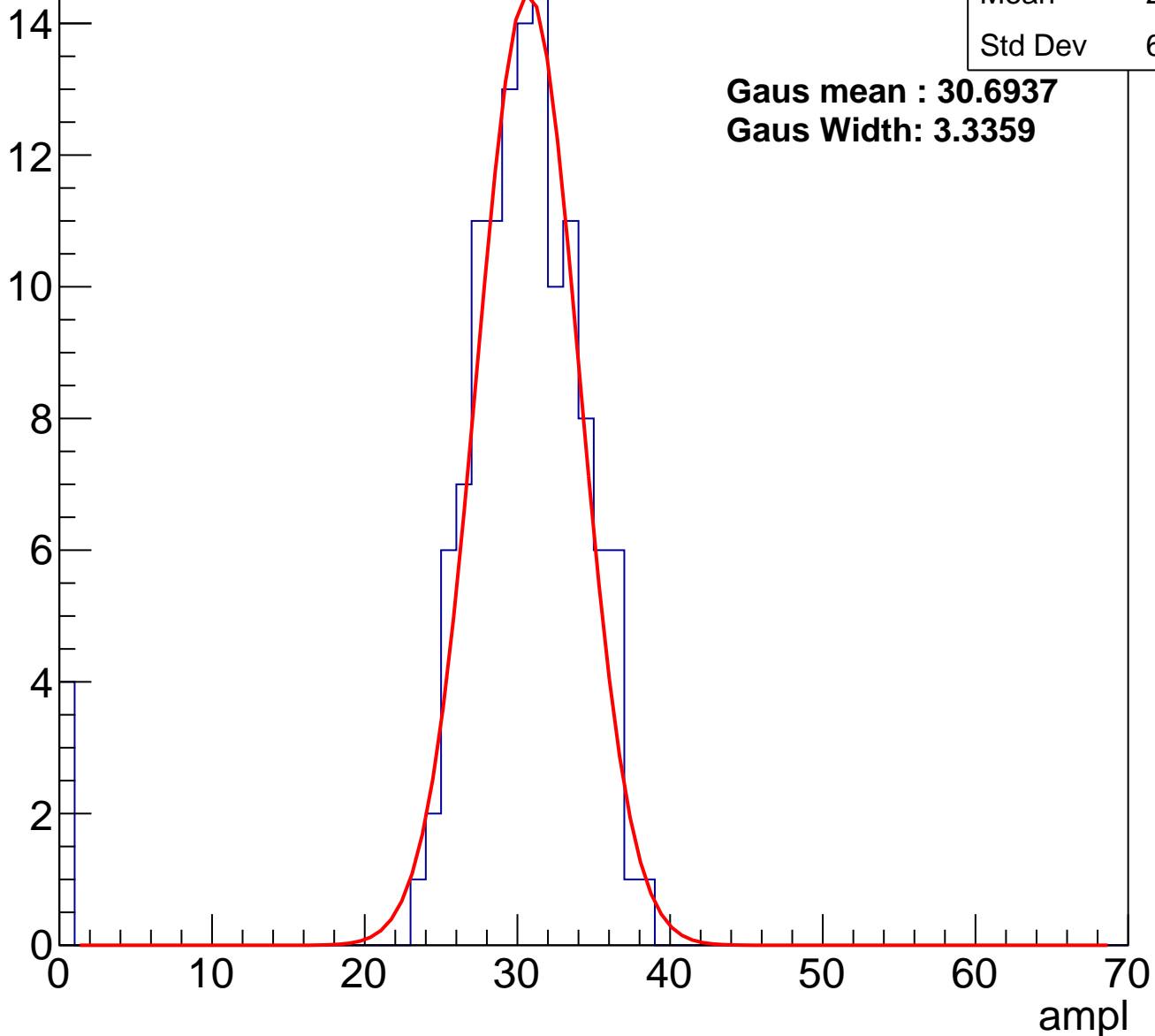
calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	29.34
Std Dev	6.176

Gaus mean : 30.6937

Gaus Width: 3.3359

Entry



B1L001S, U14-ch31, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	36.74
Std Dev	3.393

Gaus mean : 37.3231

Gaus Width: 3.7358

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

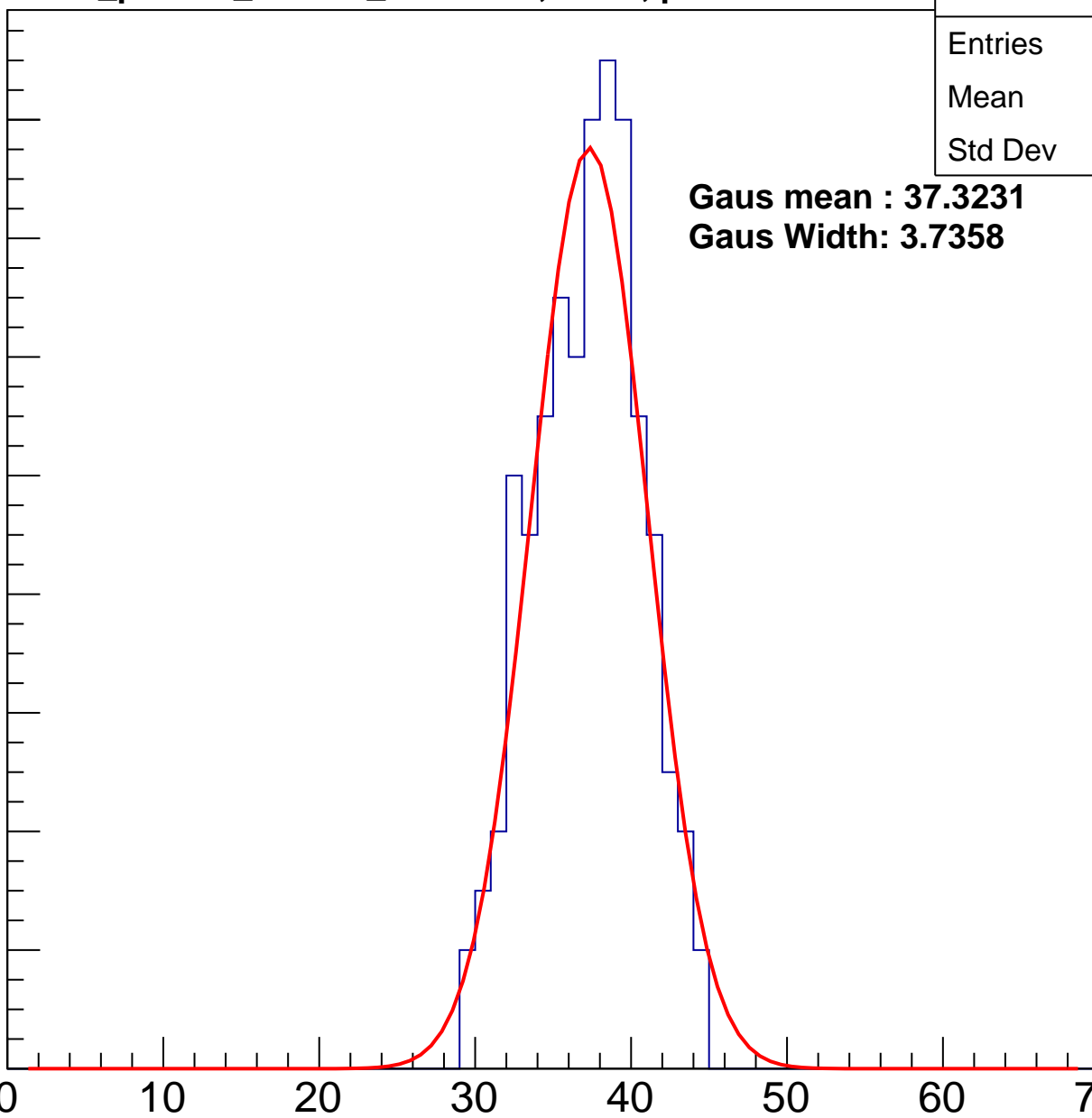
40

50

60

70

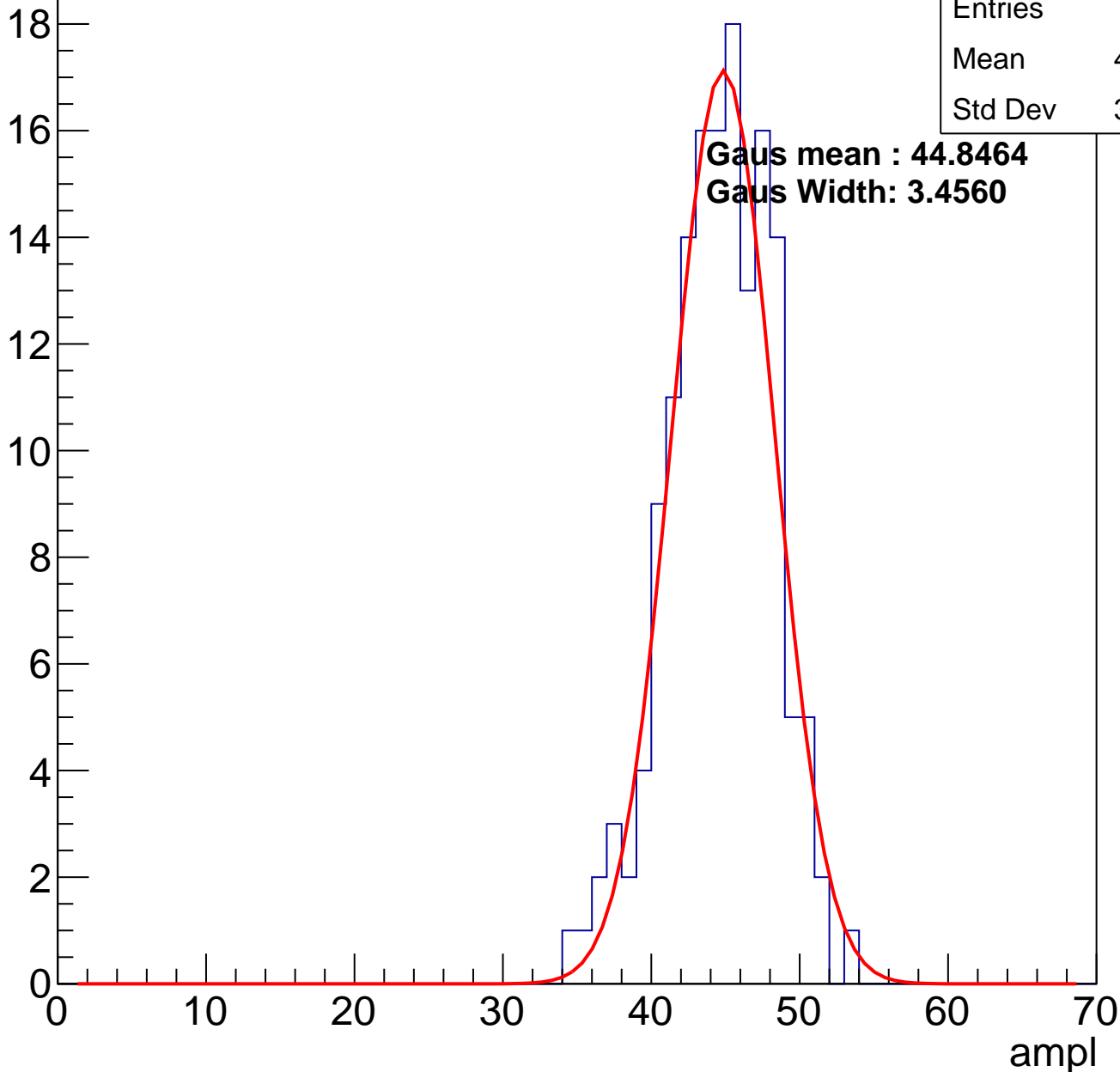
ampl



B1L001S, U14-ch31, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#2, port C2

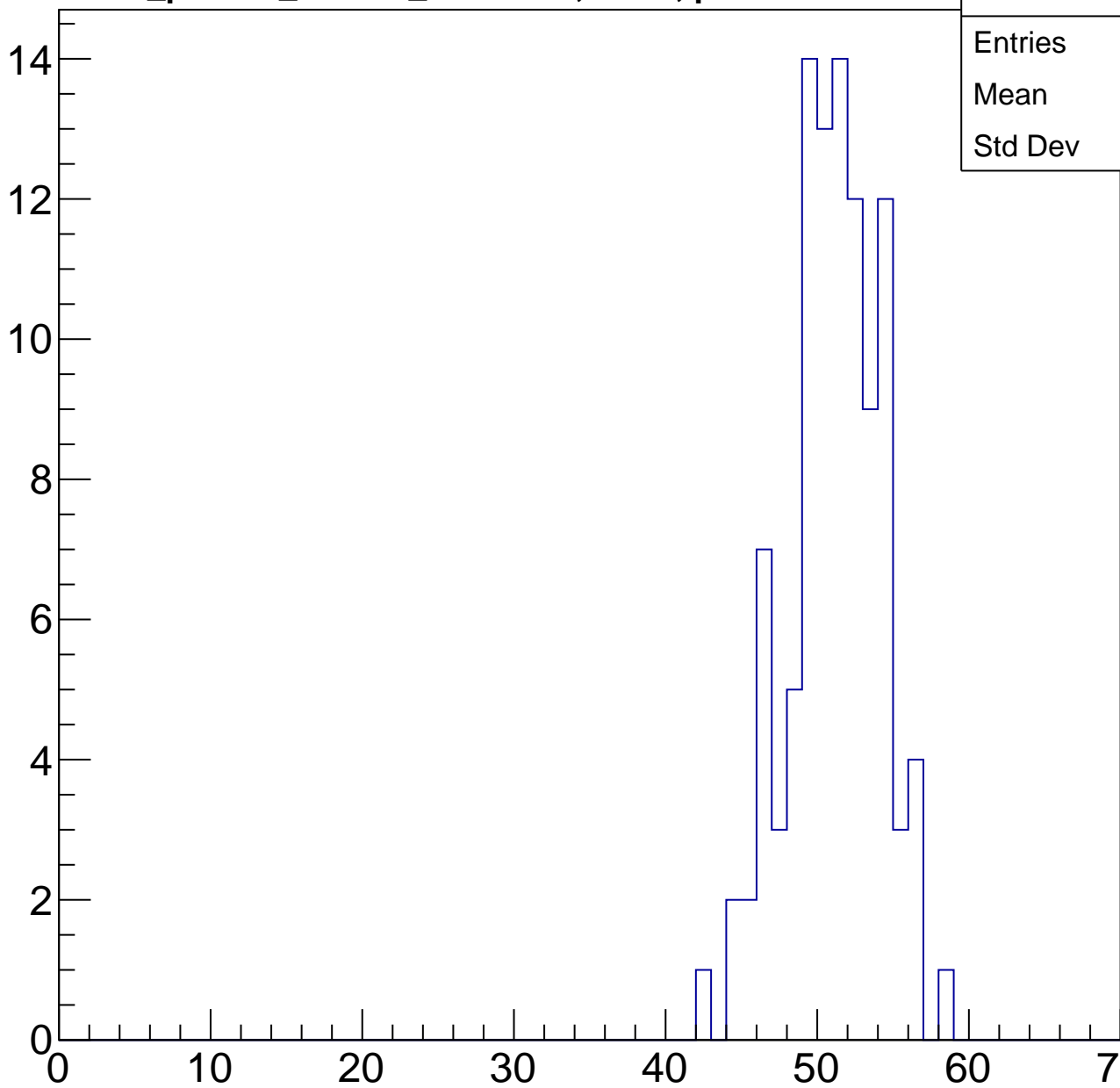
Entries	102
Mean	50.68
Std Dev	3.02

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch31, adc4

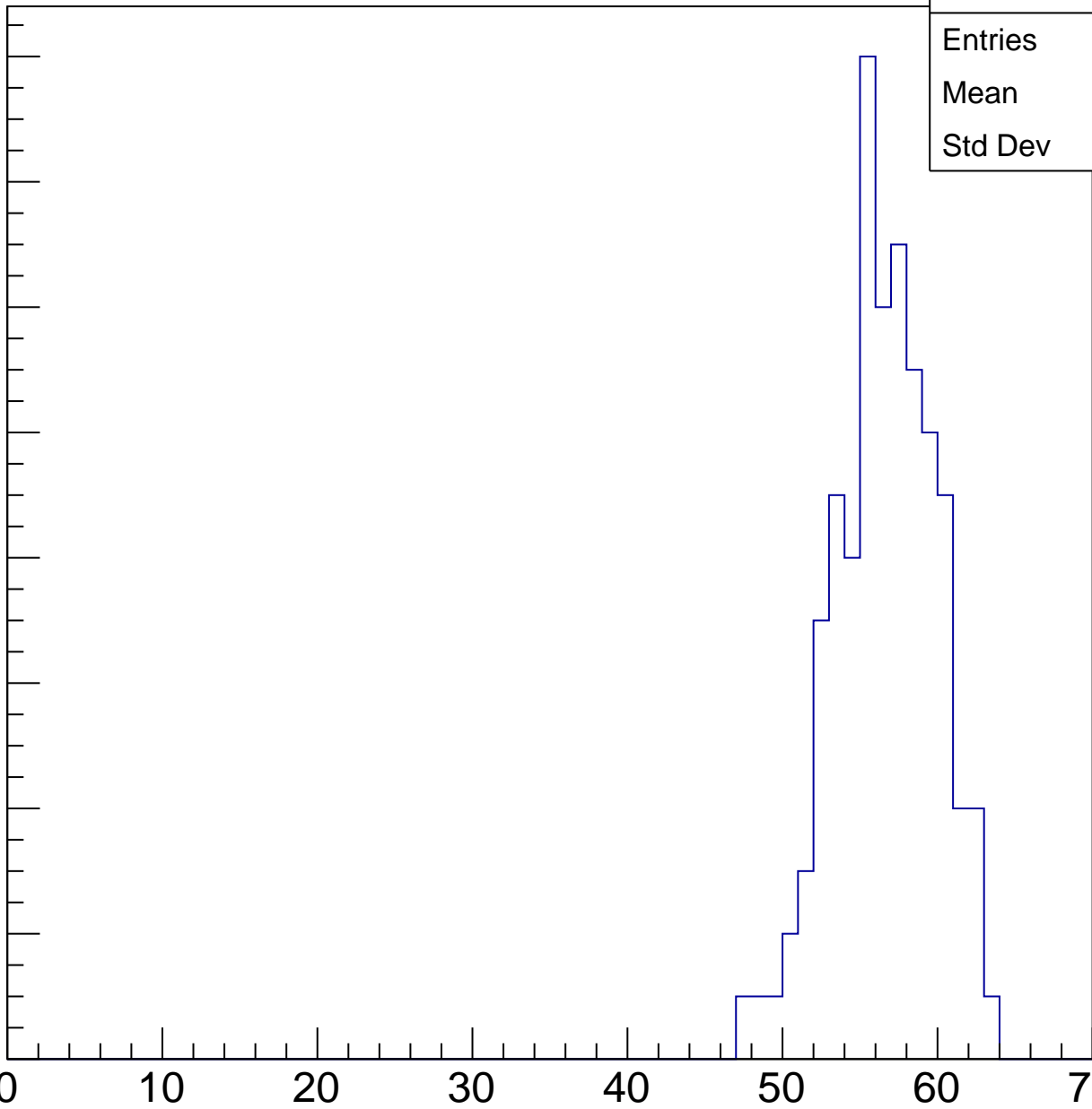
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	56.12
Std Dev	3.224

ampl



B1L001S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#2, port C2

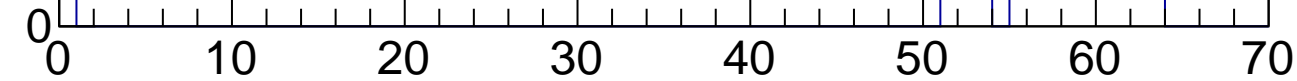
Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	58.63
Std Dev	9.206

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch31, adc7

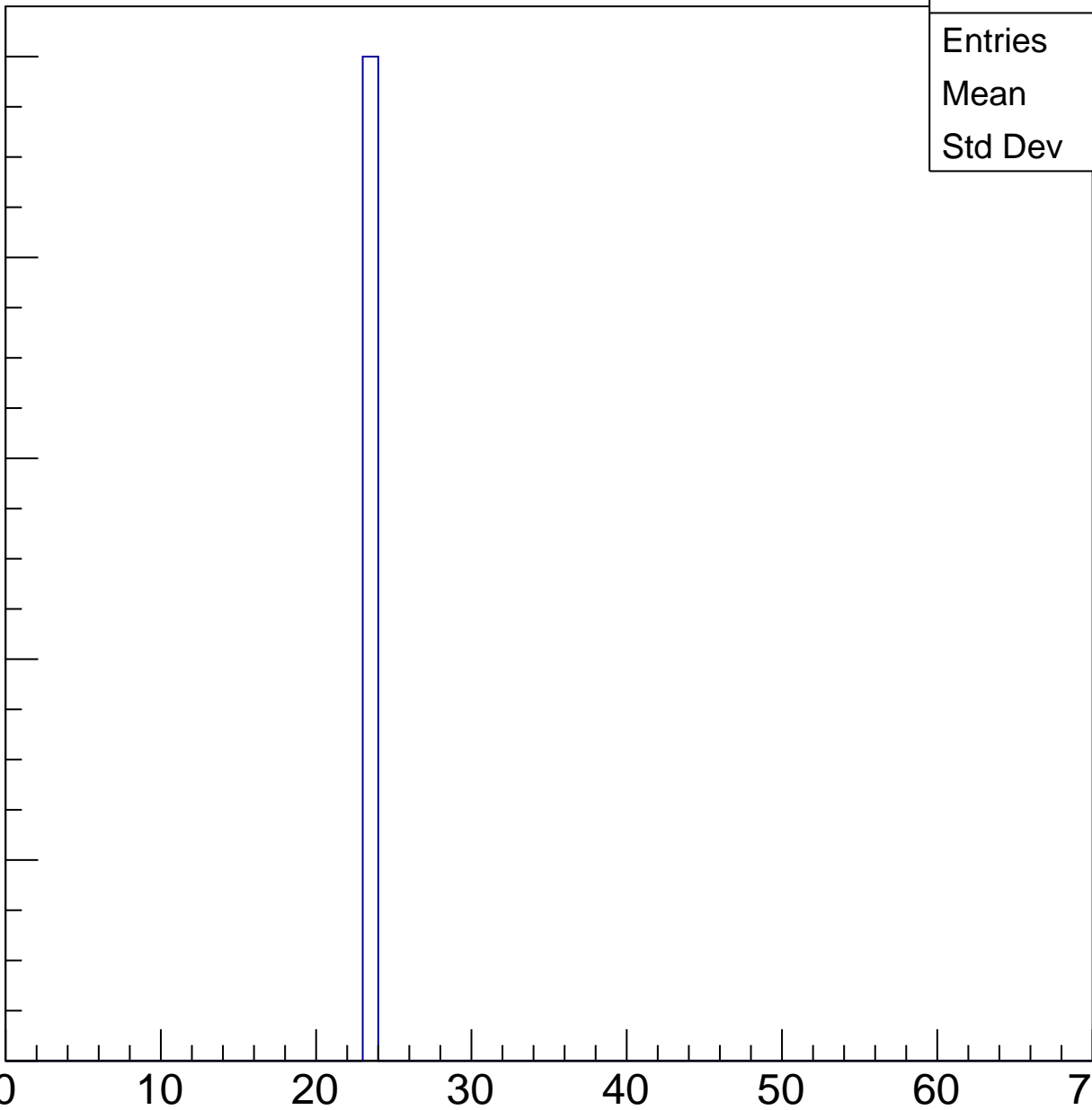
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L001S, U14-ch32, adc0

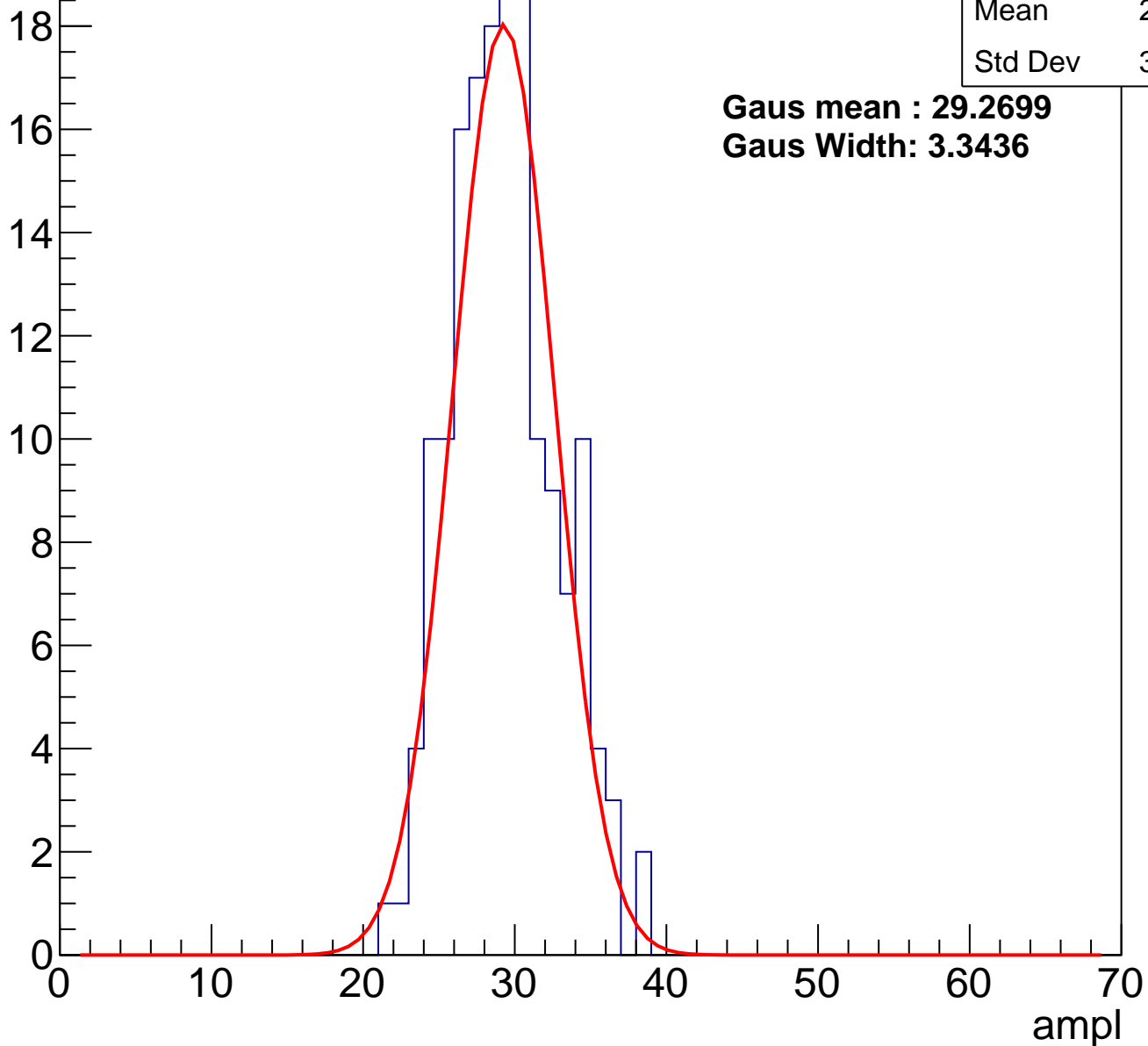
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	28.86
Std Dev	3.399

Gaus mean : 29.2699

Gaus Width: 3.3436

Entry



B1L001S, U14-ch32, adc1

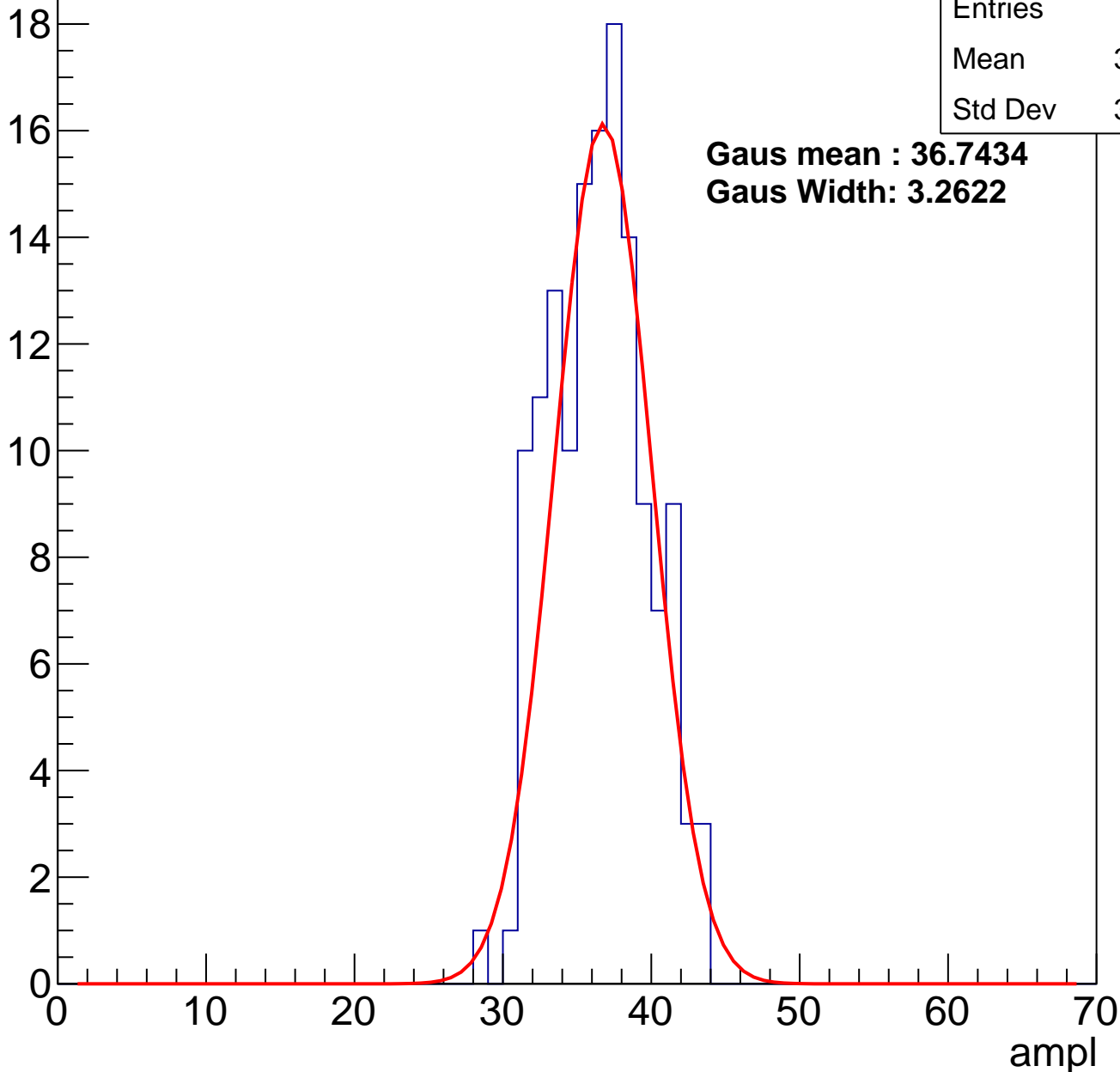
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	36.02
Std Dev	3.213

Gaus mean : 36.7434

Gaus Width: 3.2622

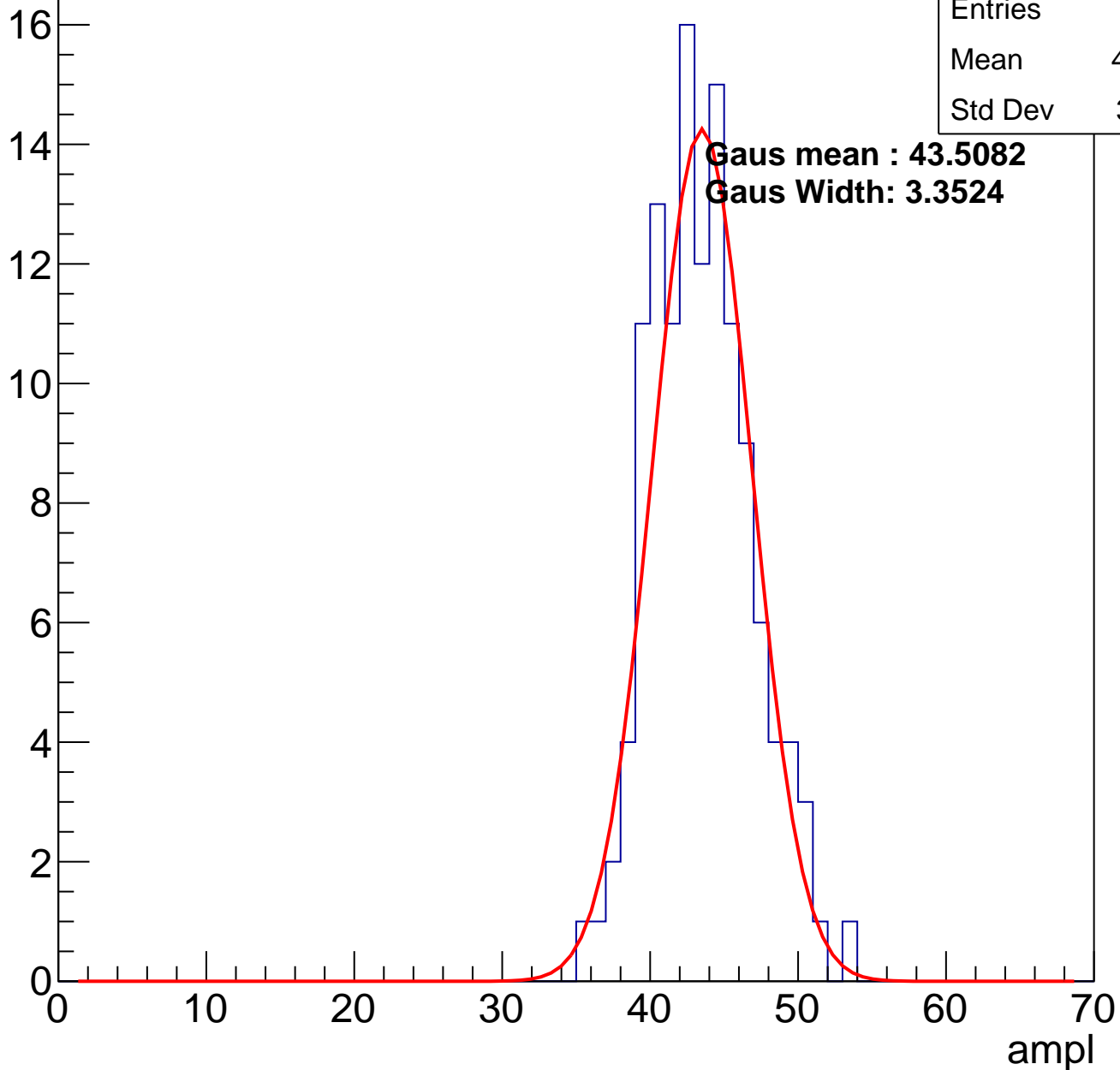
Entry



B1L001S, U14-ch32, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	48.98
Std Dev	3.205

Entry

14

12

10

8

6

4

2

0

0

10

20

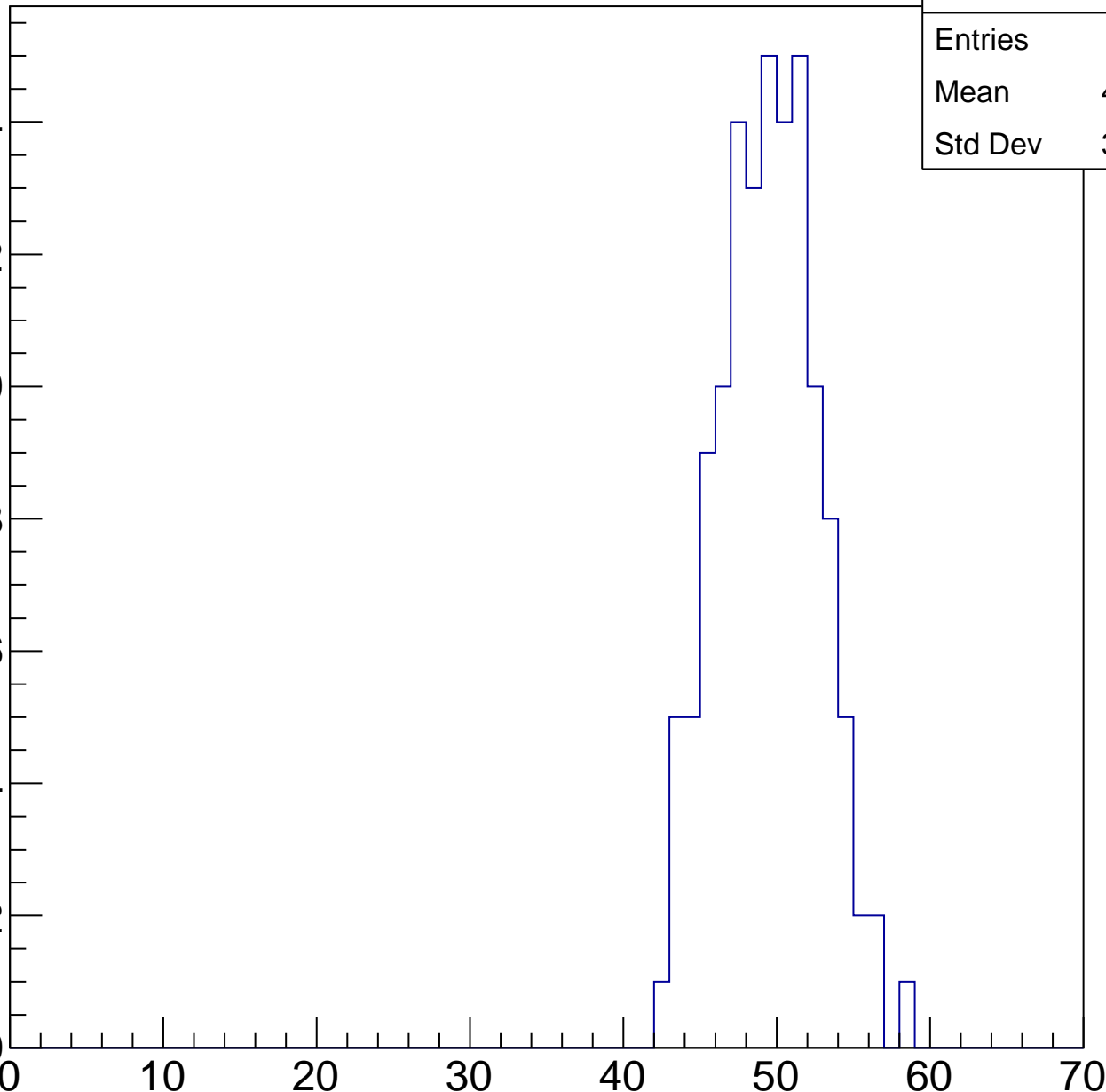
30

40

50

60

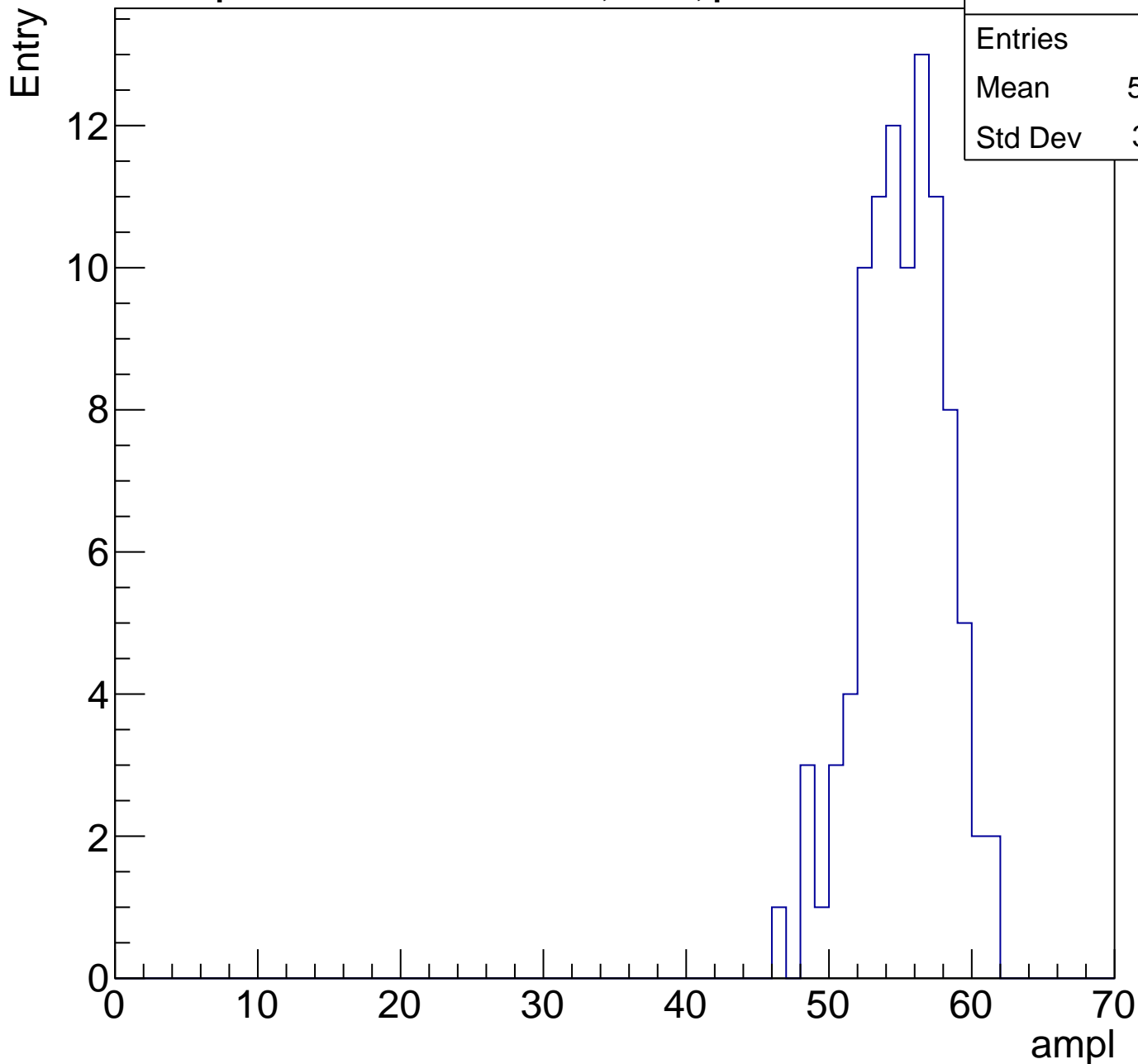
ampl



B1L001S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	54.69
Std Dev	3.011



B1L001S, U14-ch32, adc5

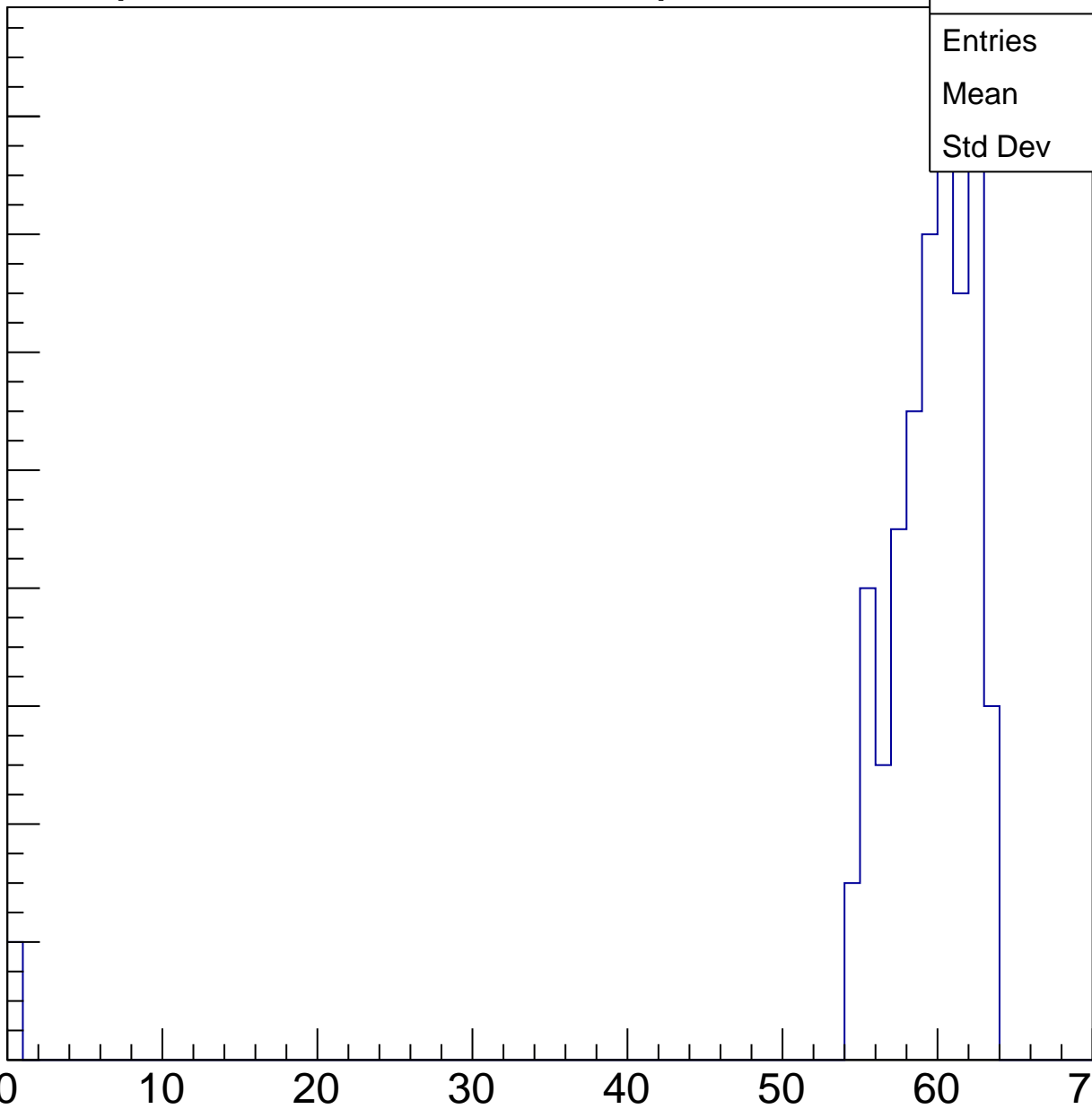
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	104
Mean	58.12
Std Dev	8.488

ampl



B1L001S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

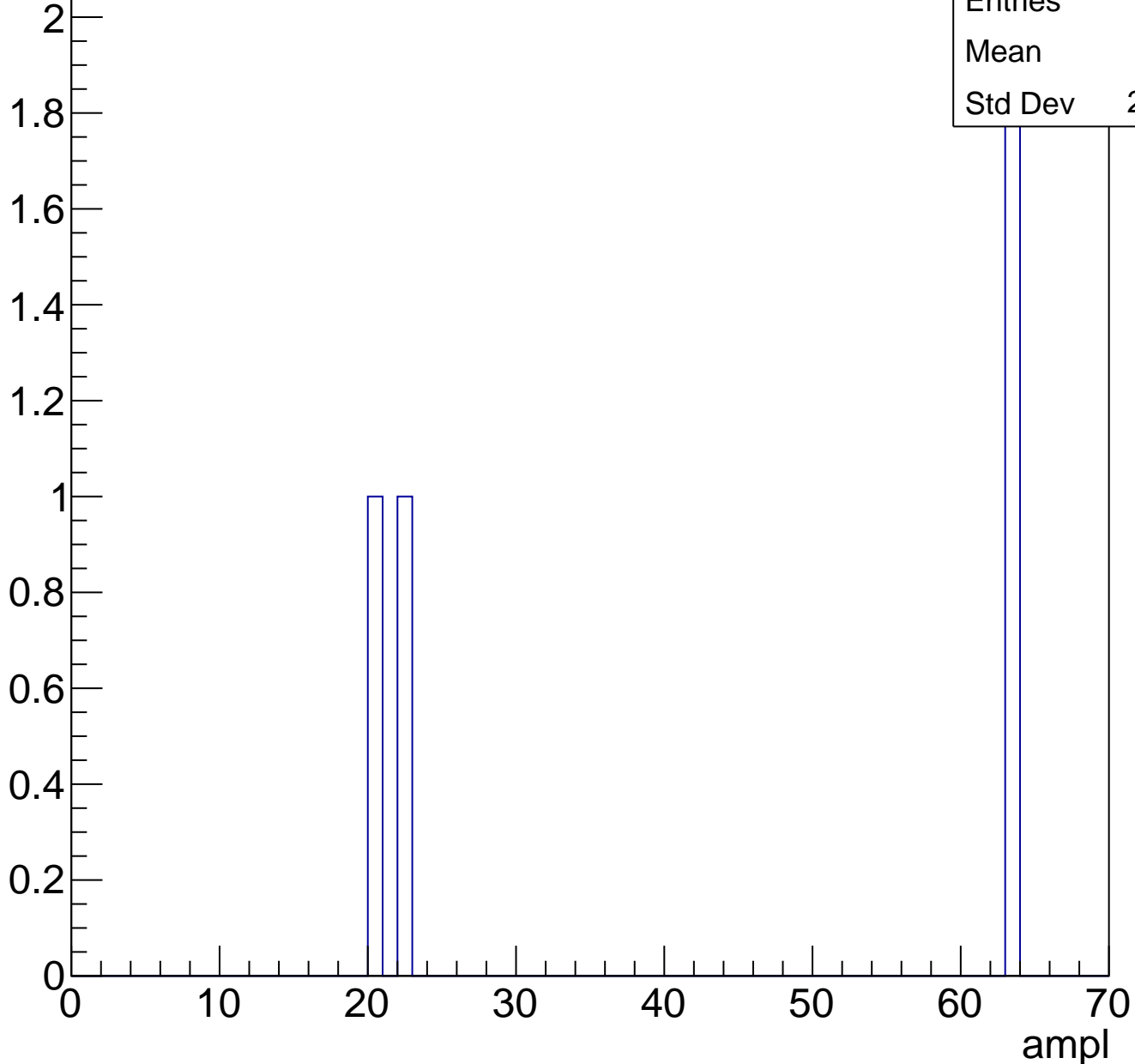
Entries	23
Mean	62
Std Dev	1.216

0

B1L001S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch33, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	31.64
Std Dev	3.096

Gaus mean : 32.0857

Gaus Width: 3.1570

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

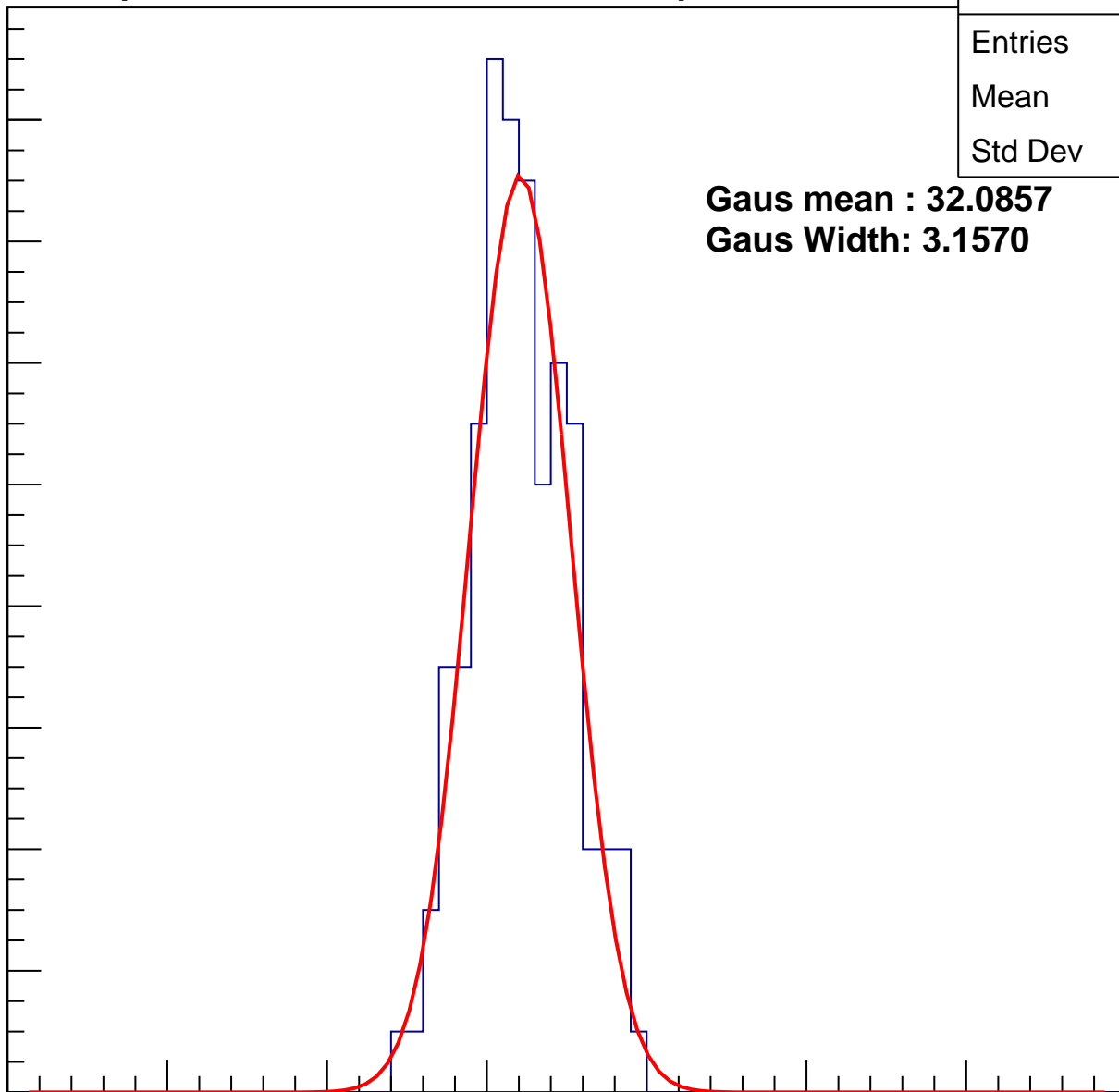
40

50

60

70

ampl



B1L001S, U14-ch33, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	37.4
Std Dev	3.143

Gaus mean : 38.0131

Gaus Width: 3.0199

Entry

14

12

10

8

6

4

2

0

0

10

20

30

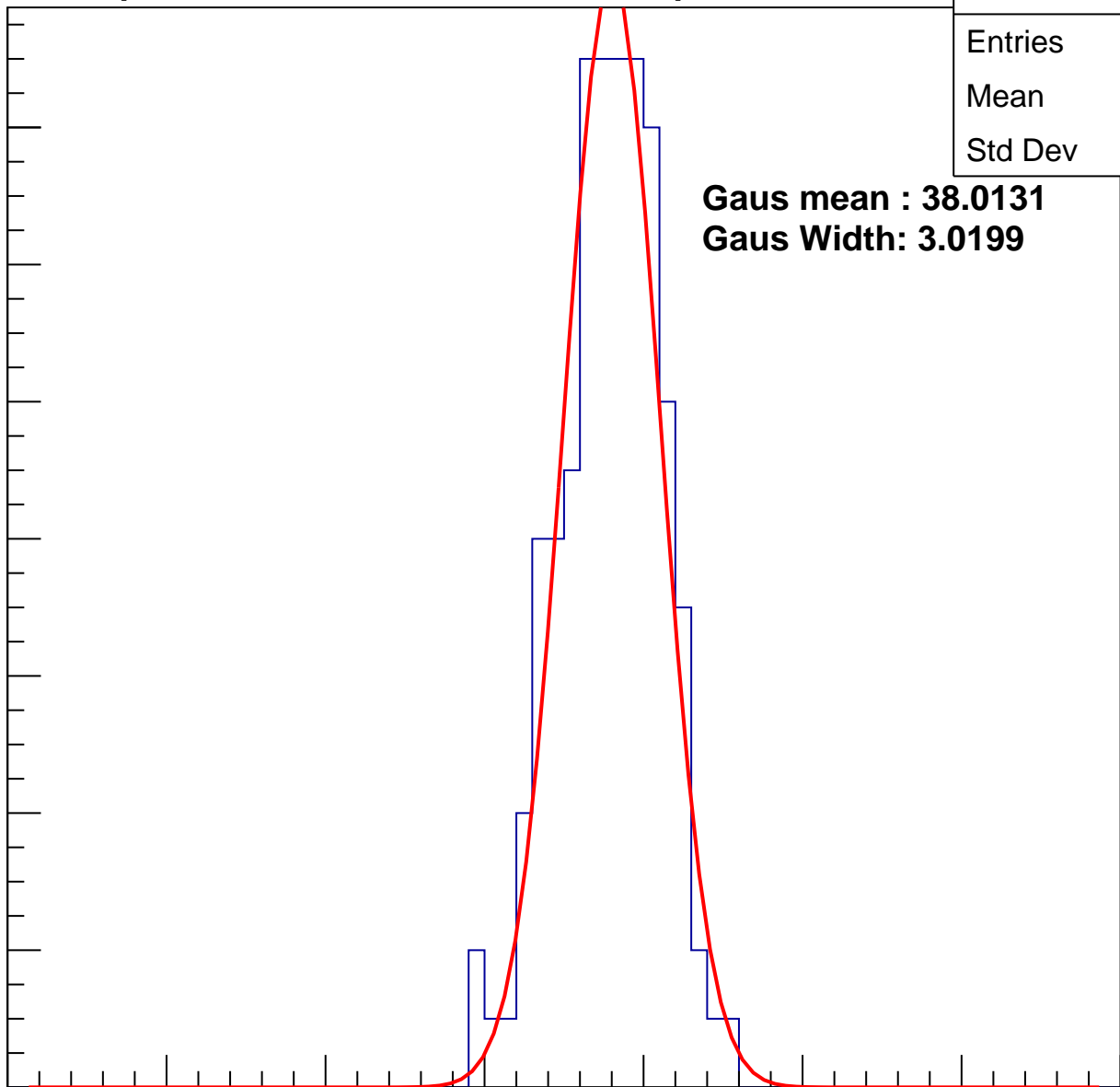
40

50

60

70

ampl



B1L001S, U14-ch33, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

123

Mean

44.28

Std Dev

3.051

Gaus mean : 44.8202

Gaus Width: 3.0789

0

10

20

30

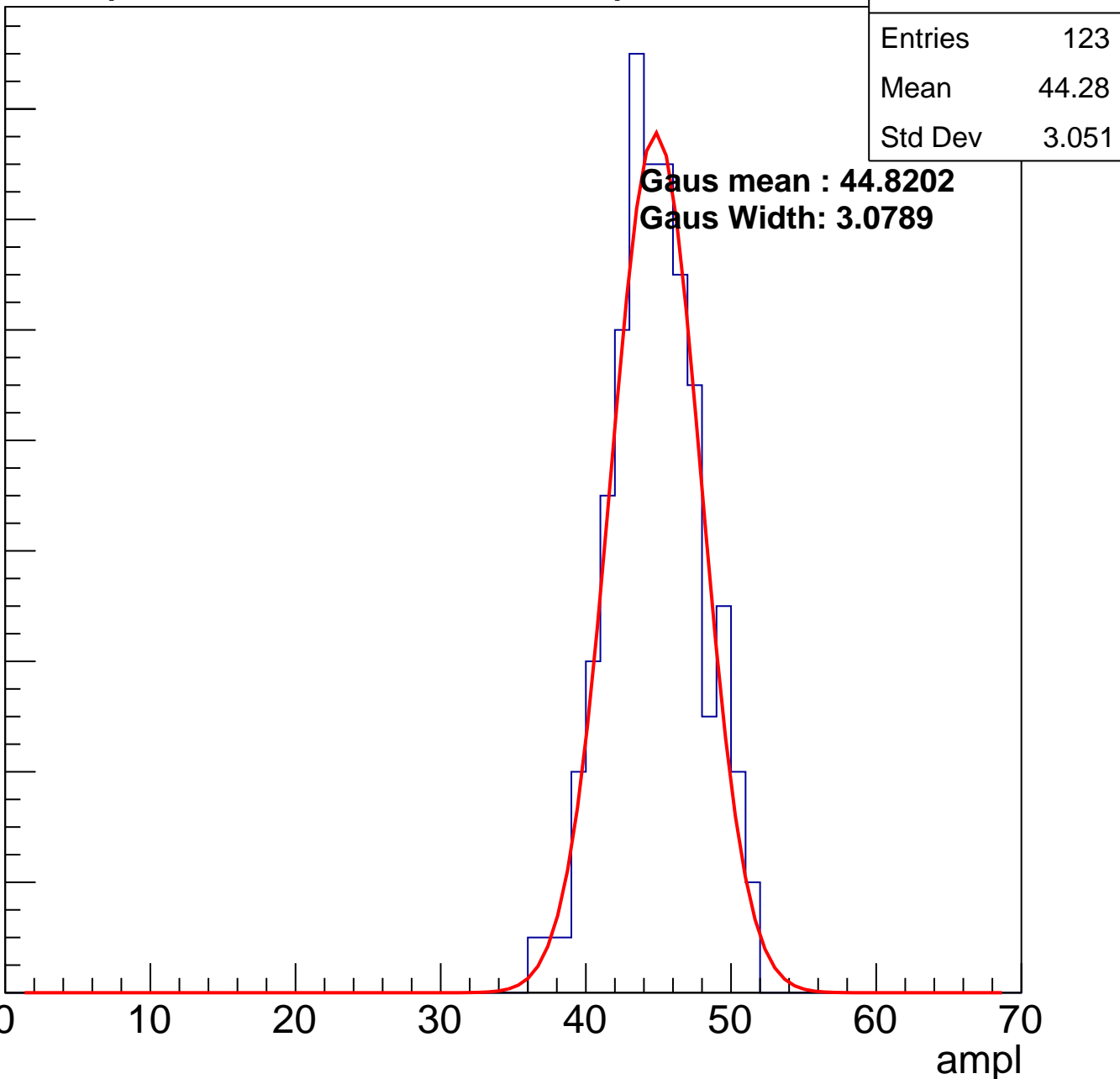
40

50

60

70

ampl



B1L001S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	50.14
Std Dev	3.481

Entry

14

12

10

8

6

4

2

0

0

10

20

30

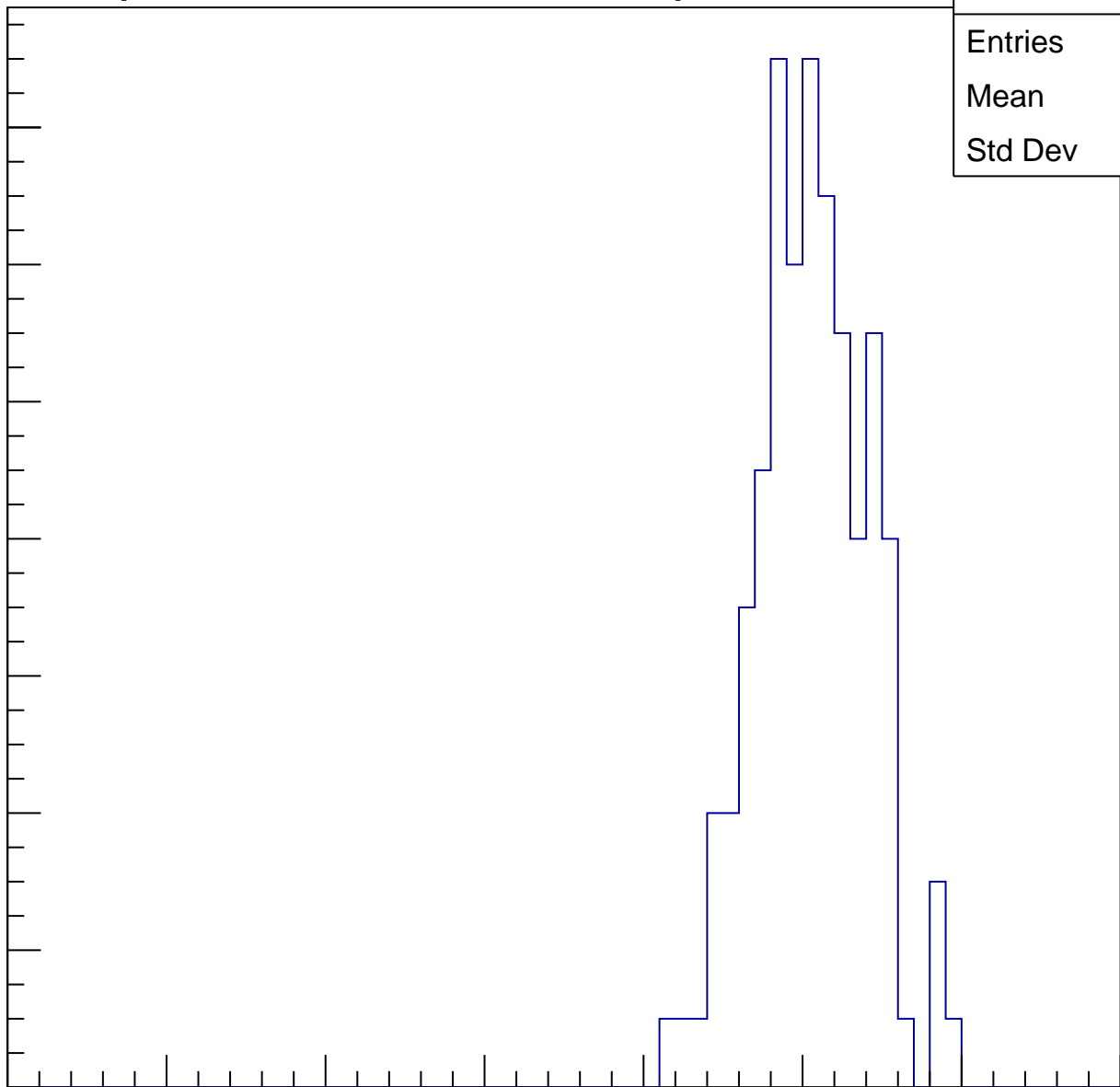
40

50

60

70

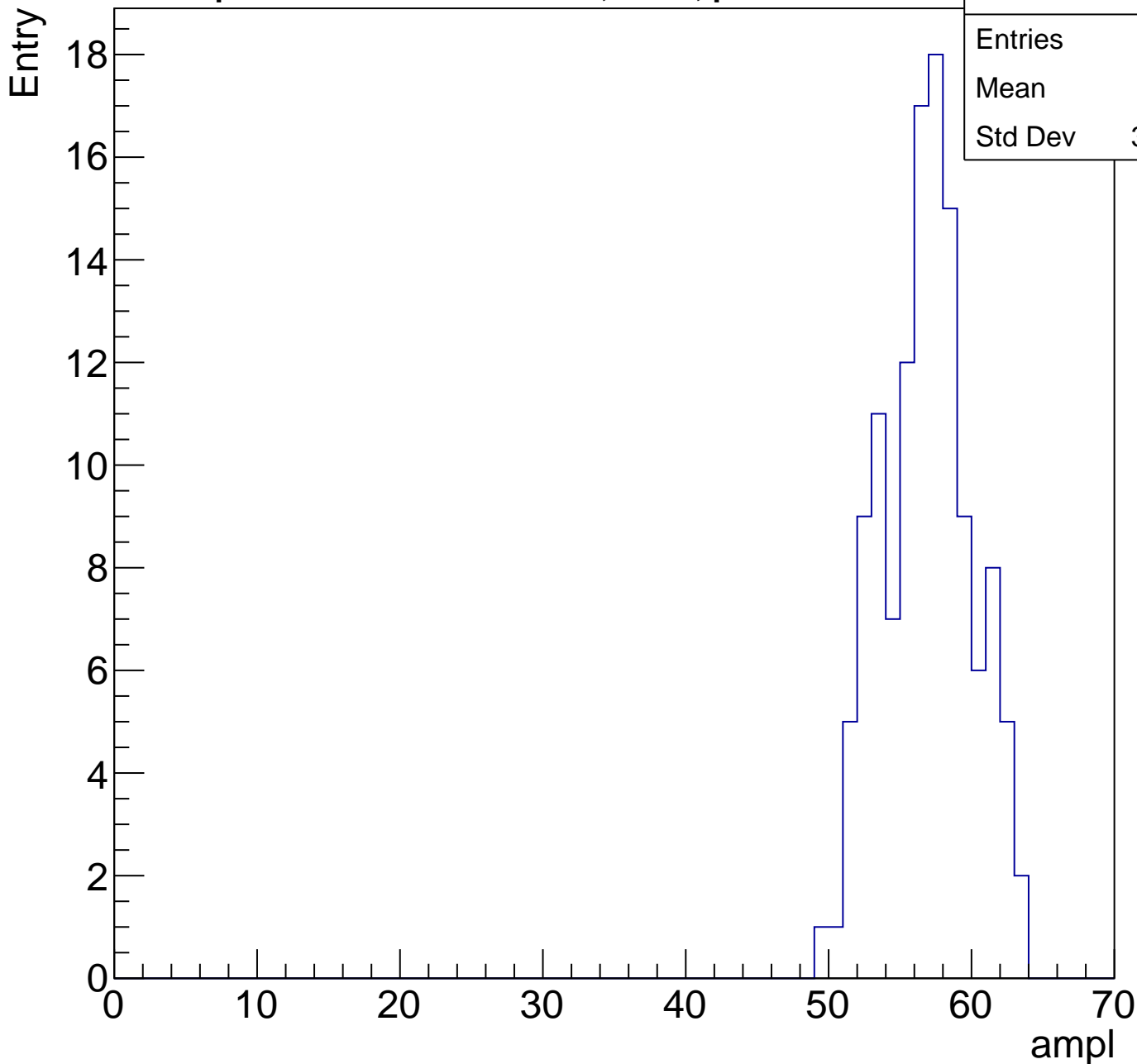
ampl



B1L001S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	56.4
Std Dev	3.094



B1L001S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

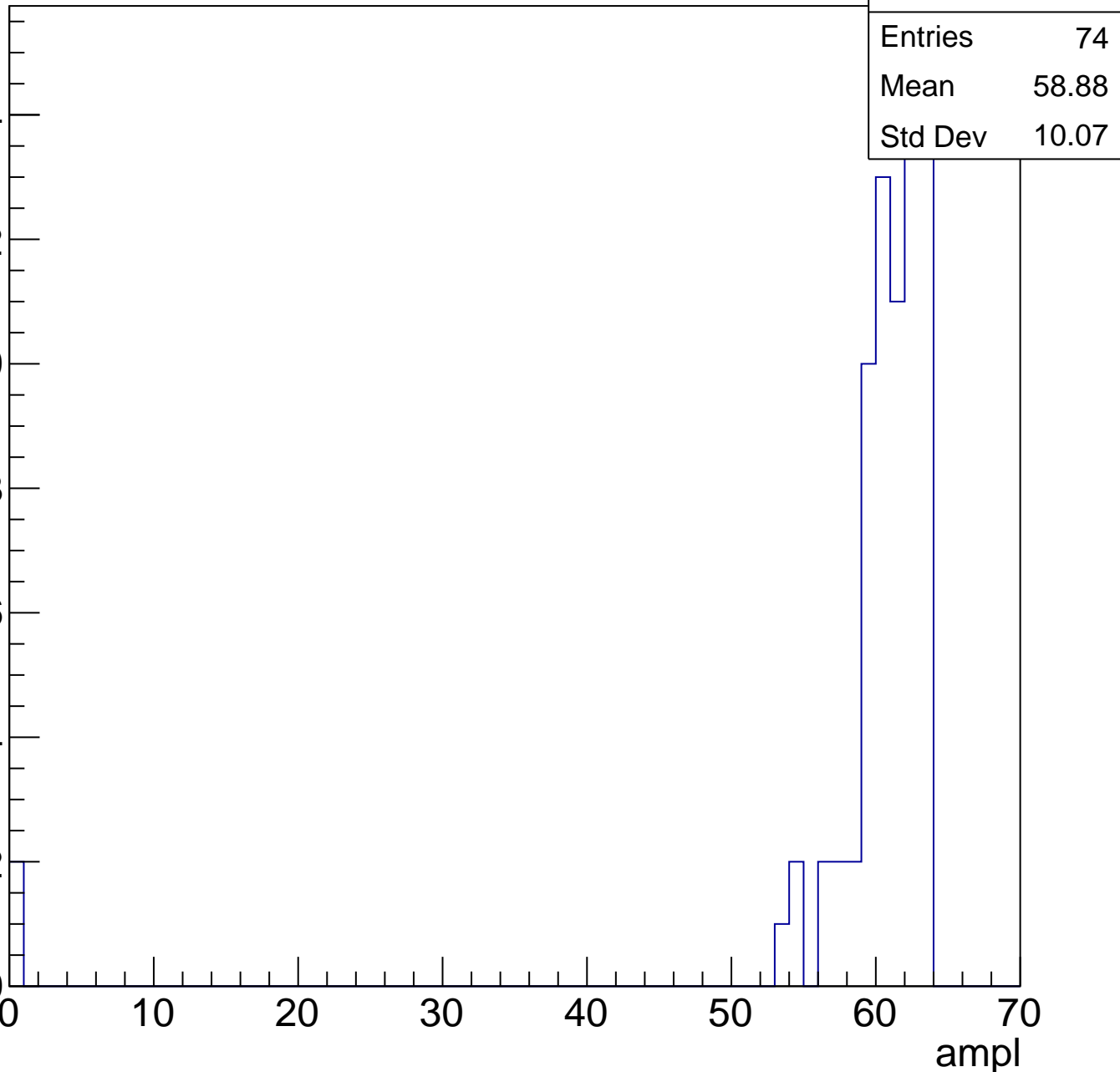
50

60

70

ampl

Entries	74
Mean	58.88
Std Dev	10.07



B1L001S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch34, adc0

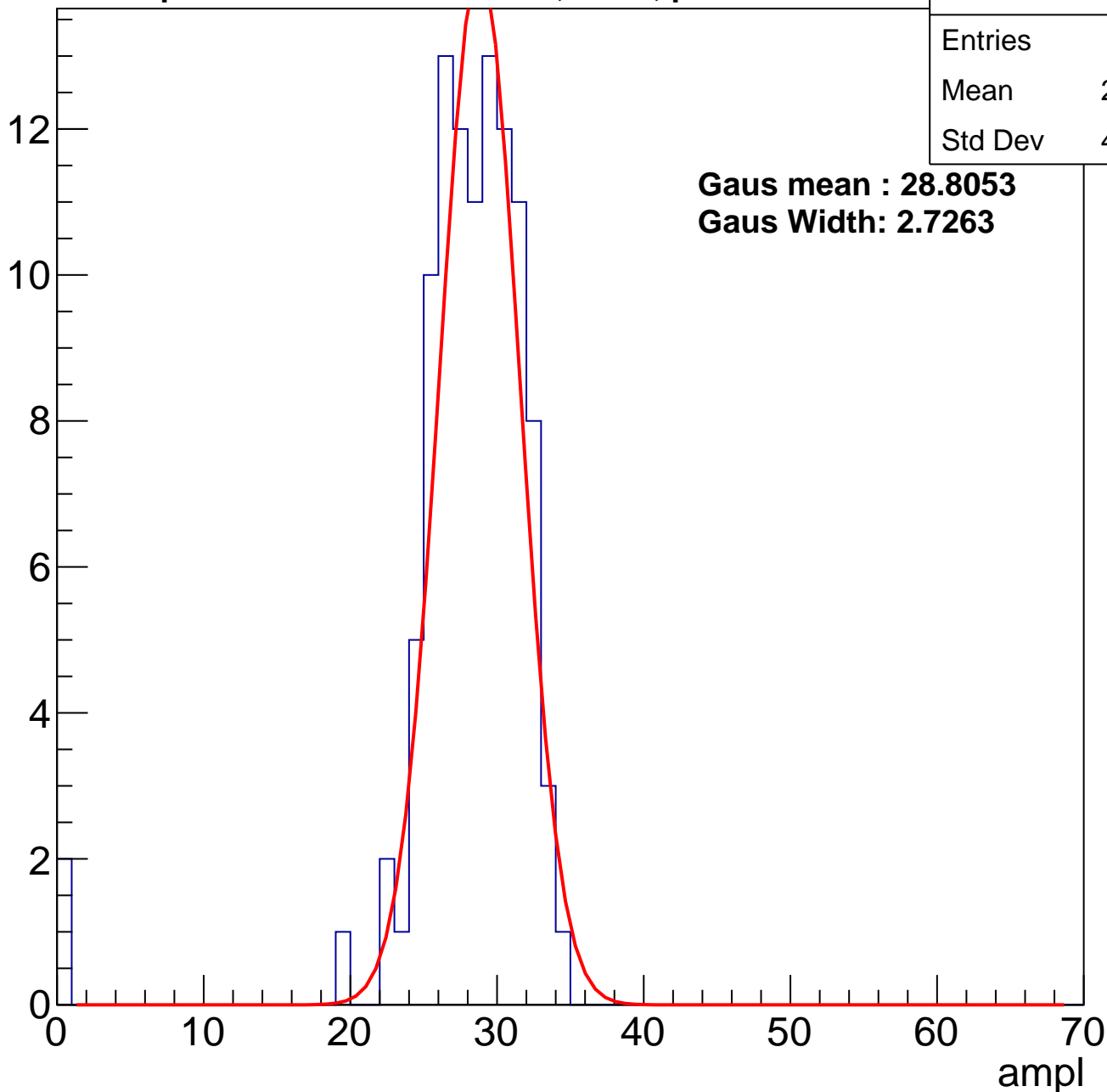
calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	27.55
Std Dev	4.739

Gaus mean : 28.8053

Gaus Width: 2.7263

Entry



B1L001S, U14-ch34, adc1

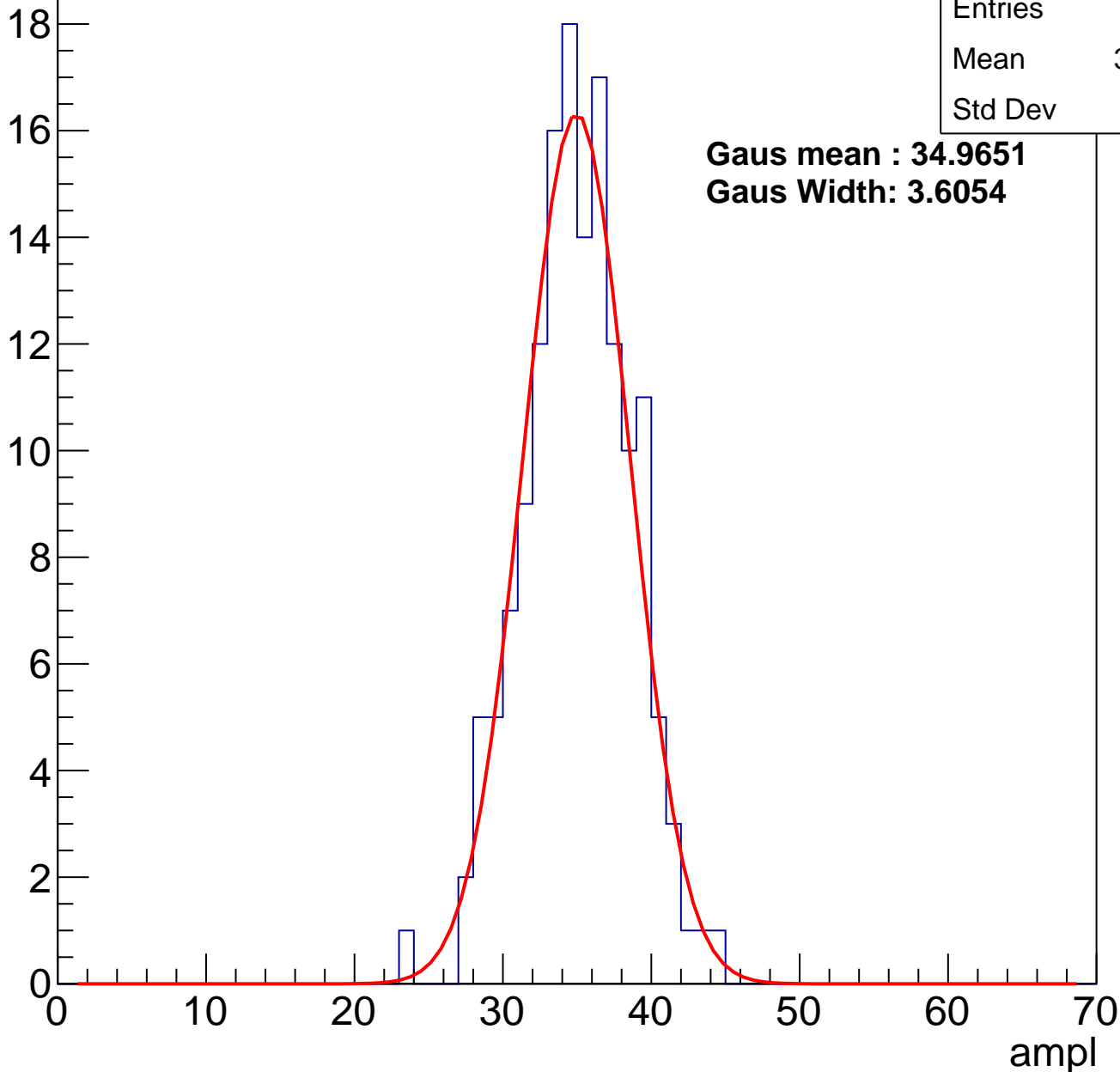
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	34.55
Std Dev	3.59

Gaus mean : 34.9651

Gaus Width: 3.6054

Entry



B1L001S, U14-ch34, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	41.94
Std Dev	3.566

Gaus mean : 42.6951

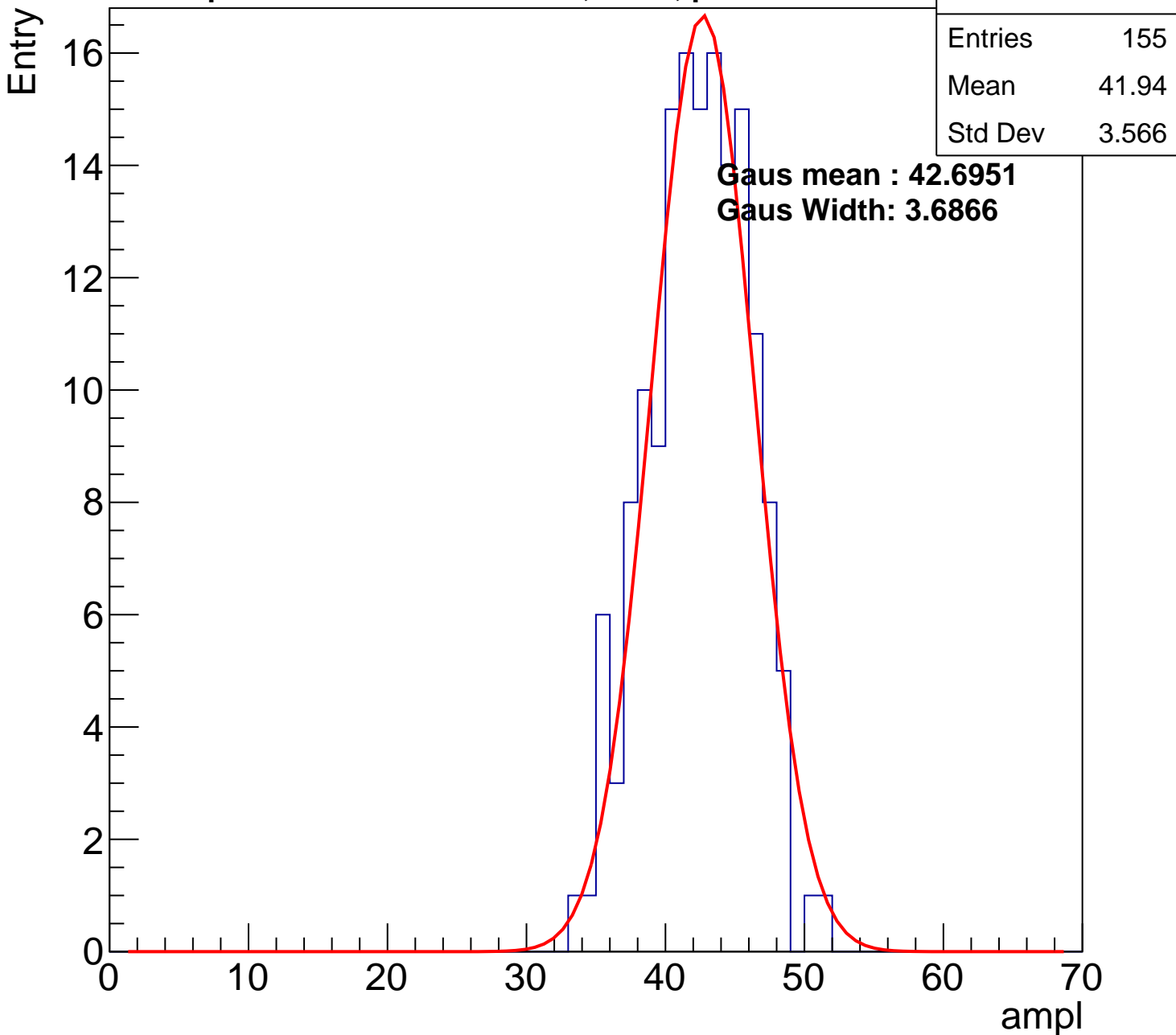
Gaus Width: 3.6866

Entry

16
14
12
10
8
6
4
2
0

ampl

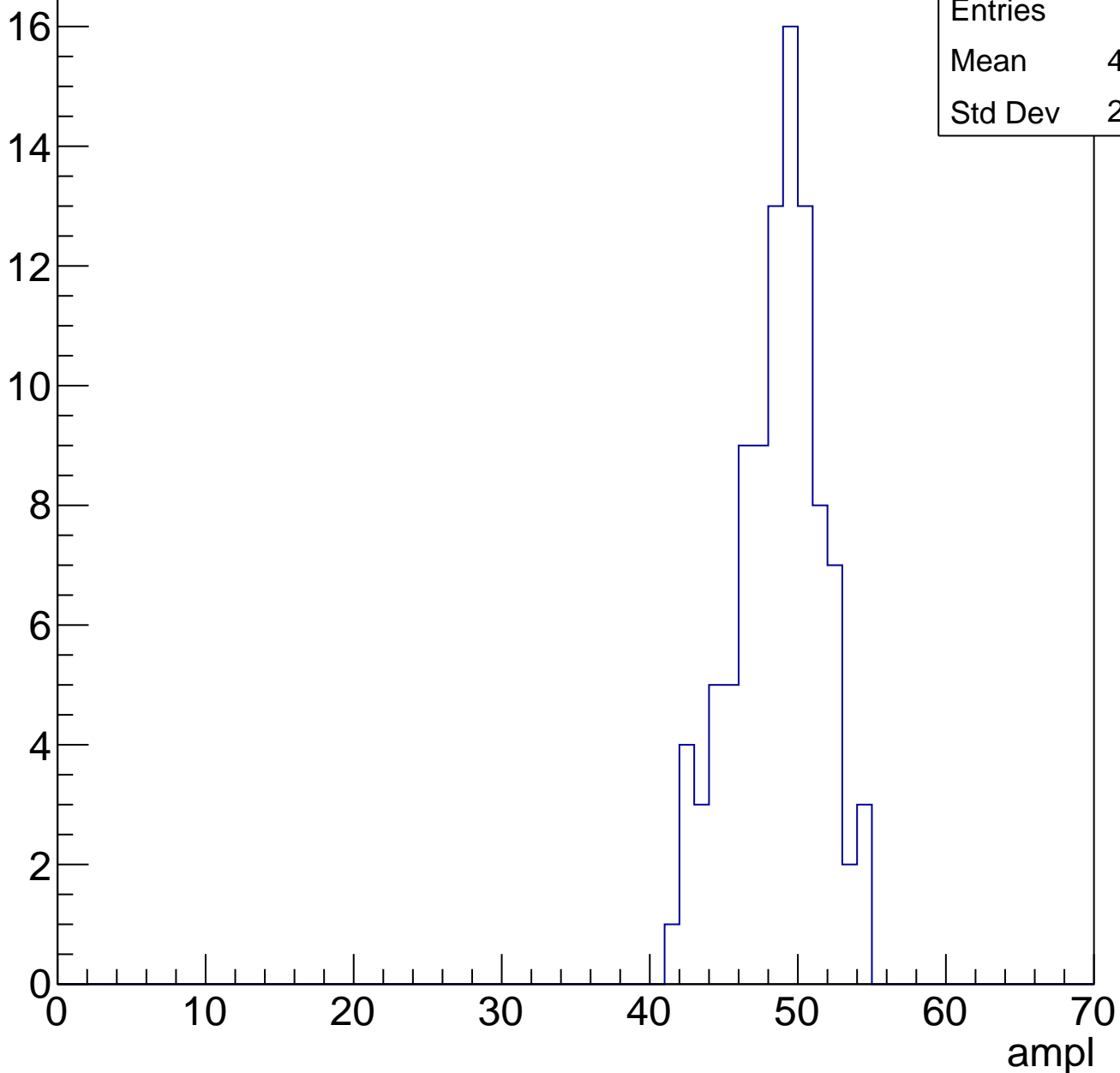
0 10 20 30 40 50 60 70



B1L001S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

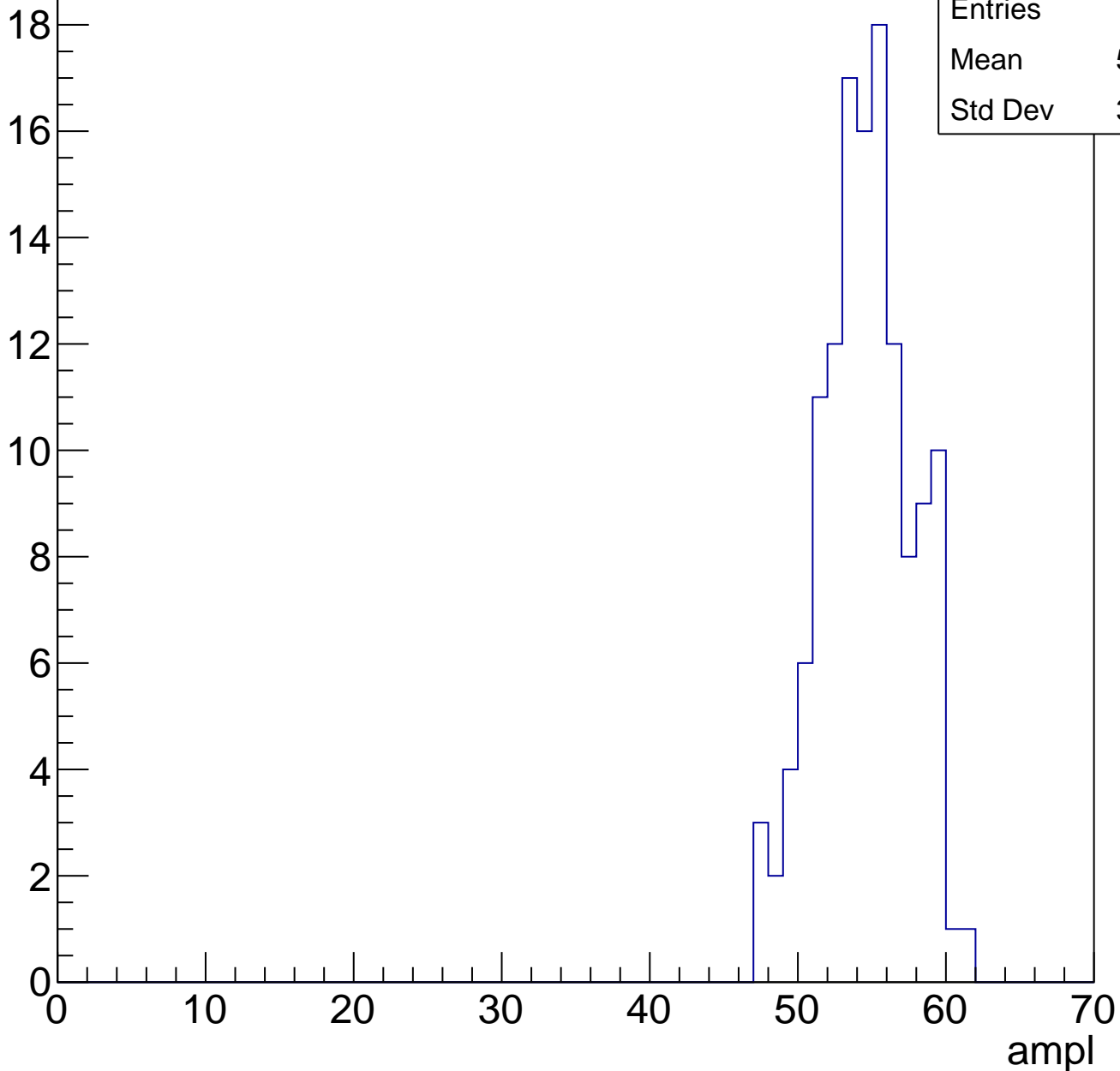


Entries	98
Mean	48.14
Std Dev	2.938

B1L001S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

81

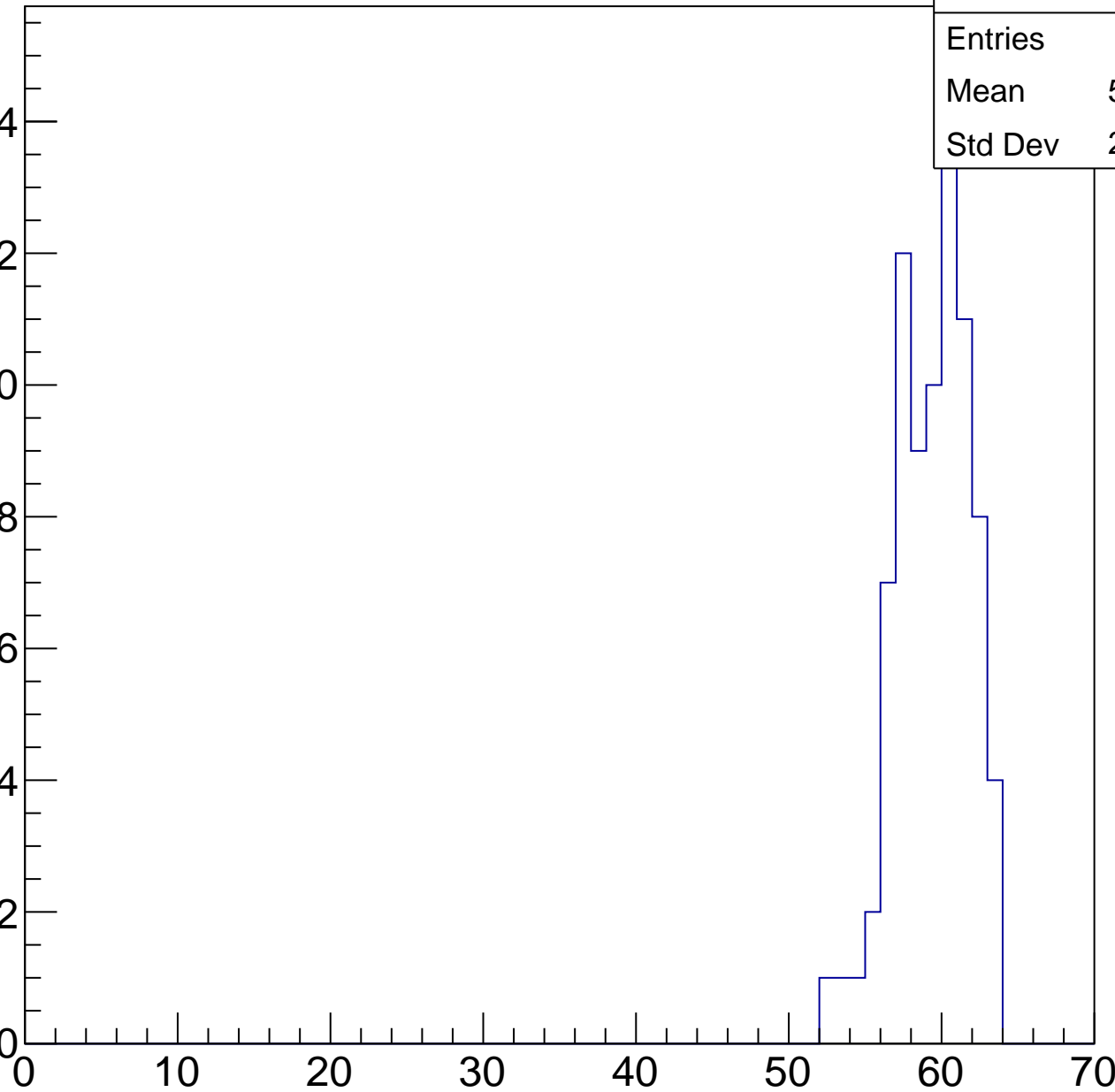
Mean

58.96

Std Dev

2.375

ampl



B1L001S, U14-ch34, adc6

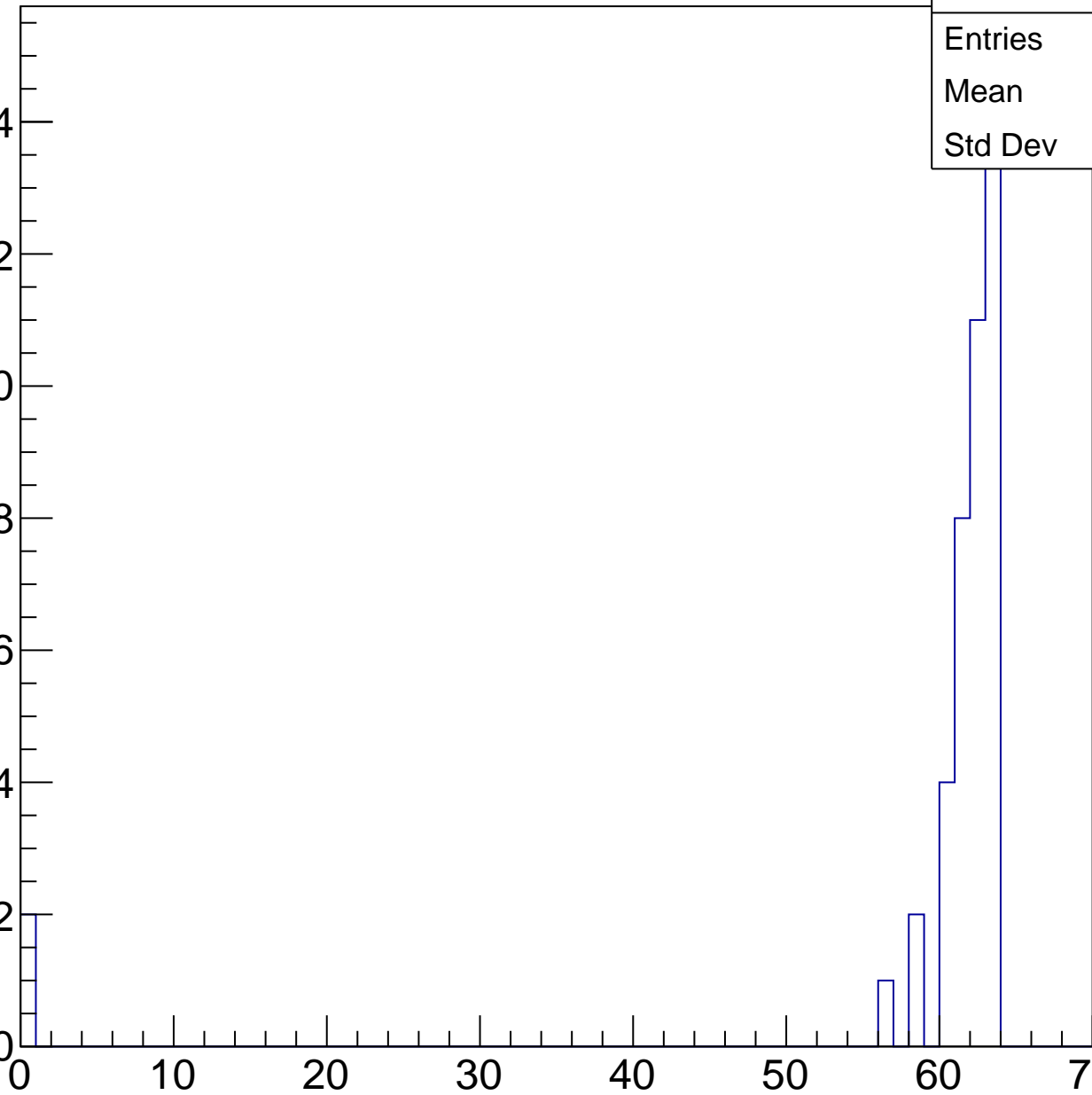
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	43
Mean	58.77
Std Dev	13.07

ampl



B1L001S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch35, adc0

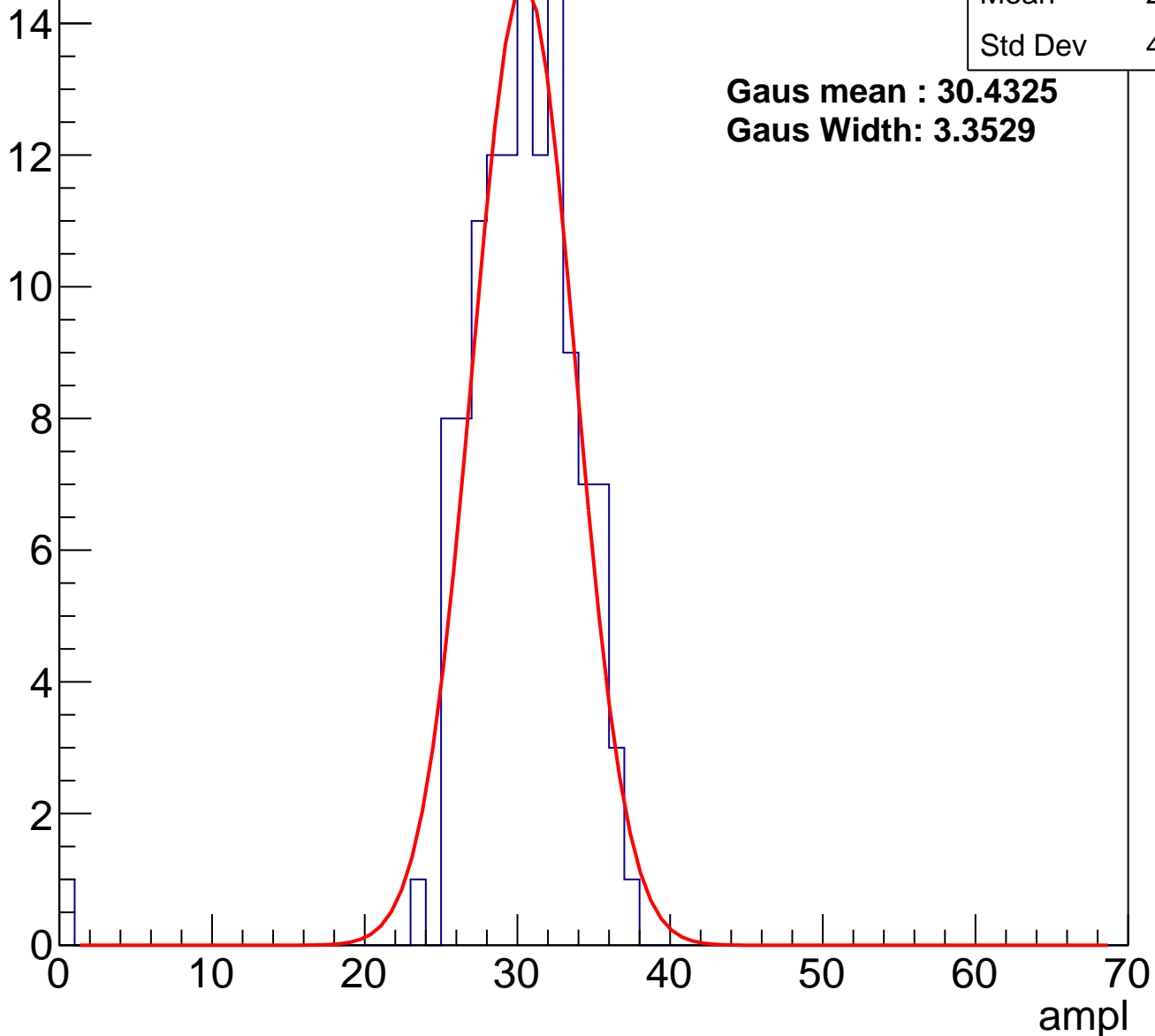
calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	29.83
Std Dev	4.079

Gaus mean : 30.4325

Gaus Width: 3.3529

Entry



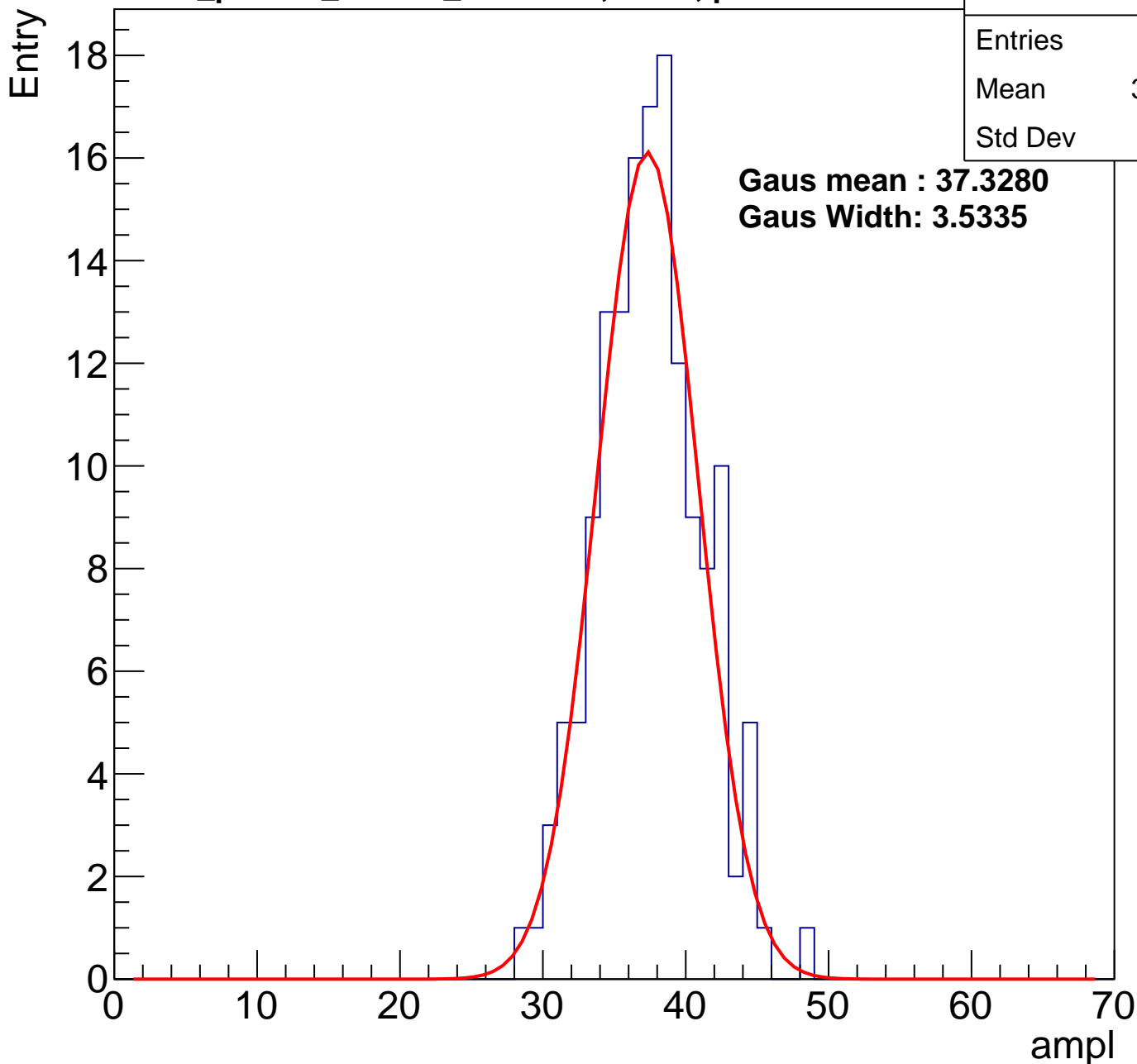
B1L001S, U14-ch35, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	37.05
Std Dev	3.62

Gaus mean : 37.3280

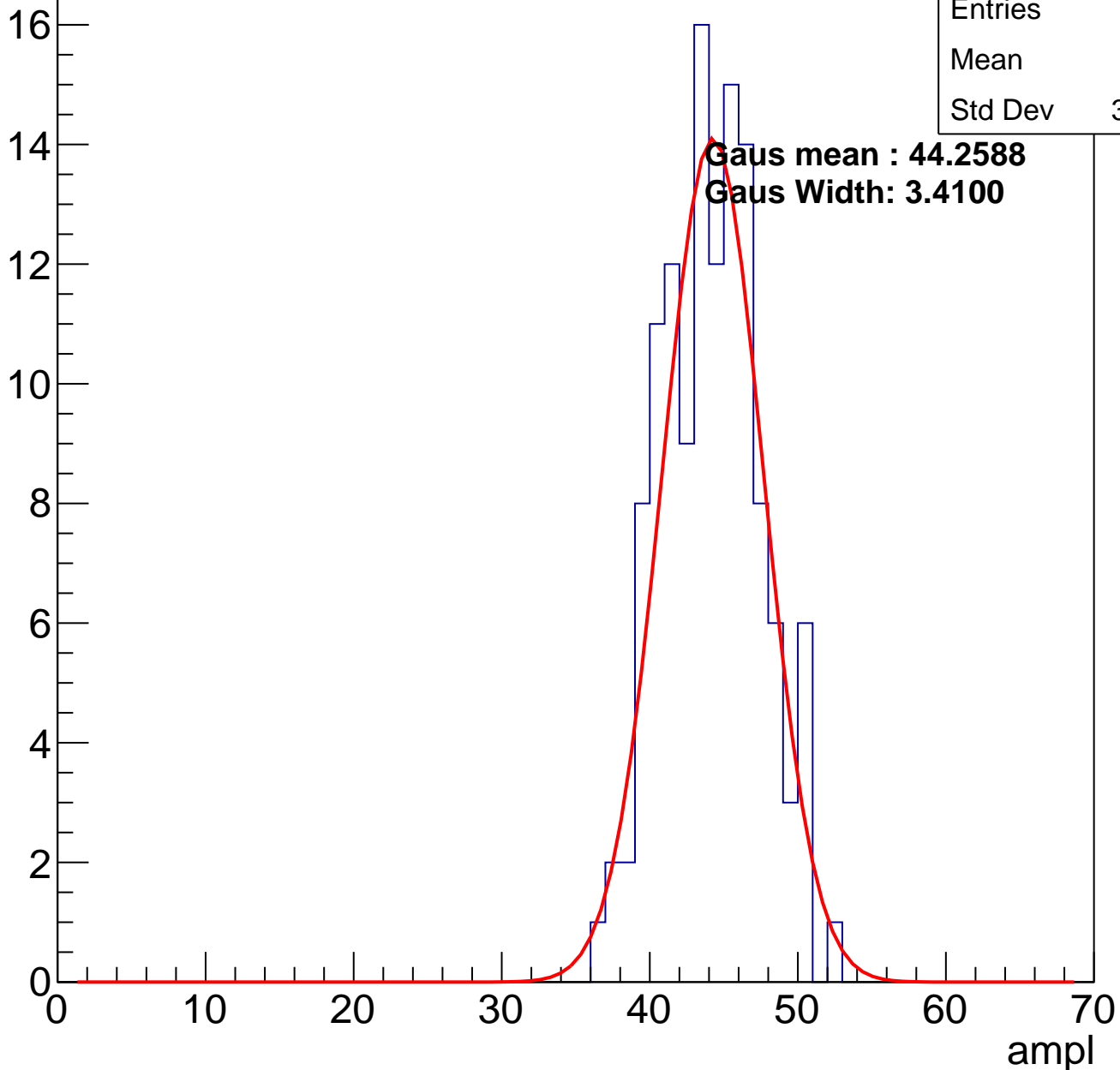
Gaus Width: 3.5335



B1L001S, U14-ch35, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

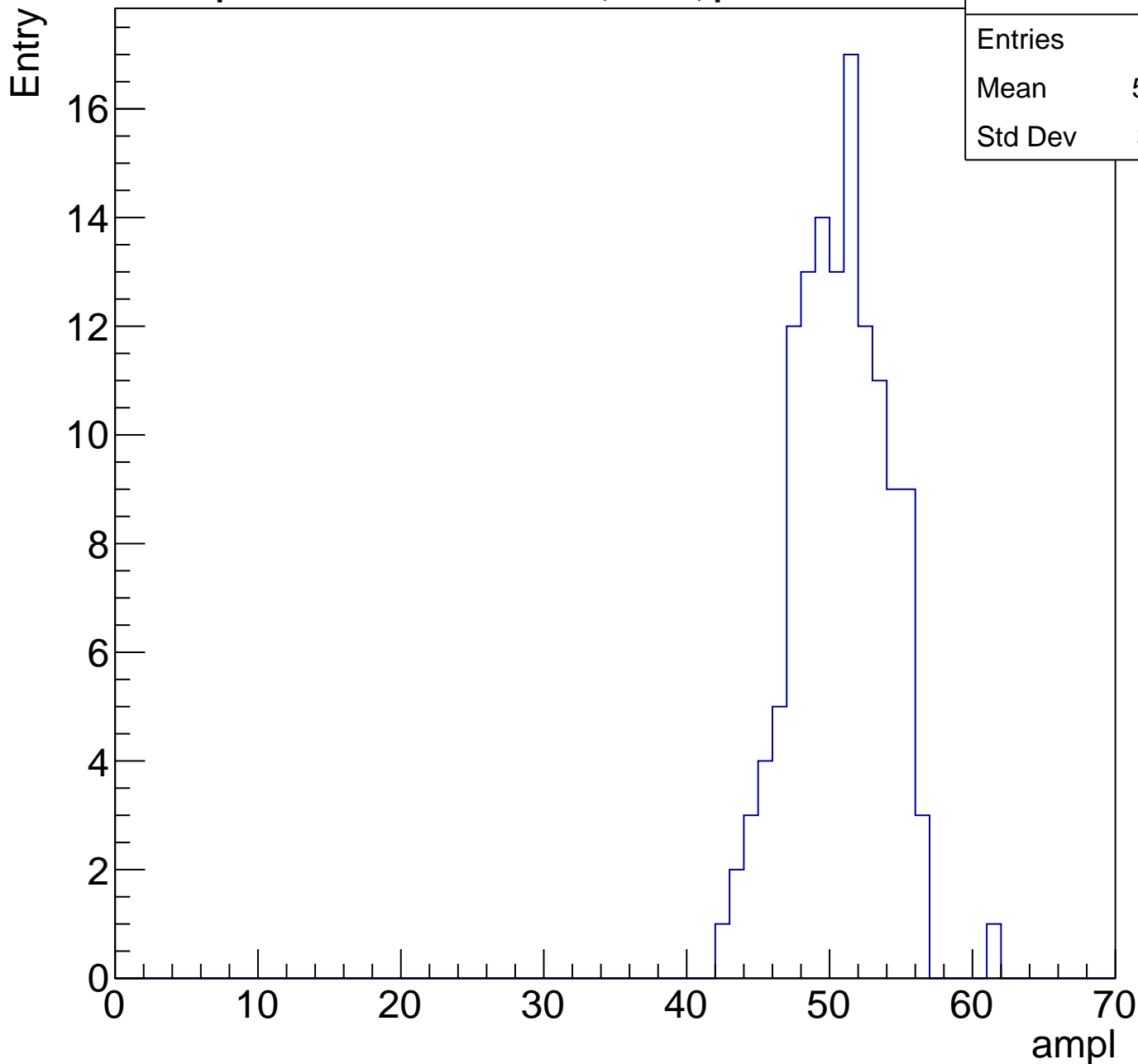


Entries	126
Mean	43.7
Std Dev	3.276

B1L001S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#2, port C2

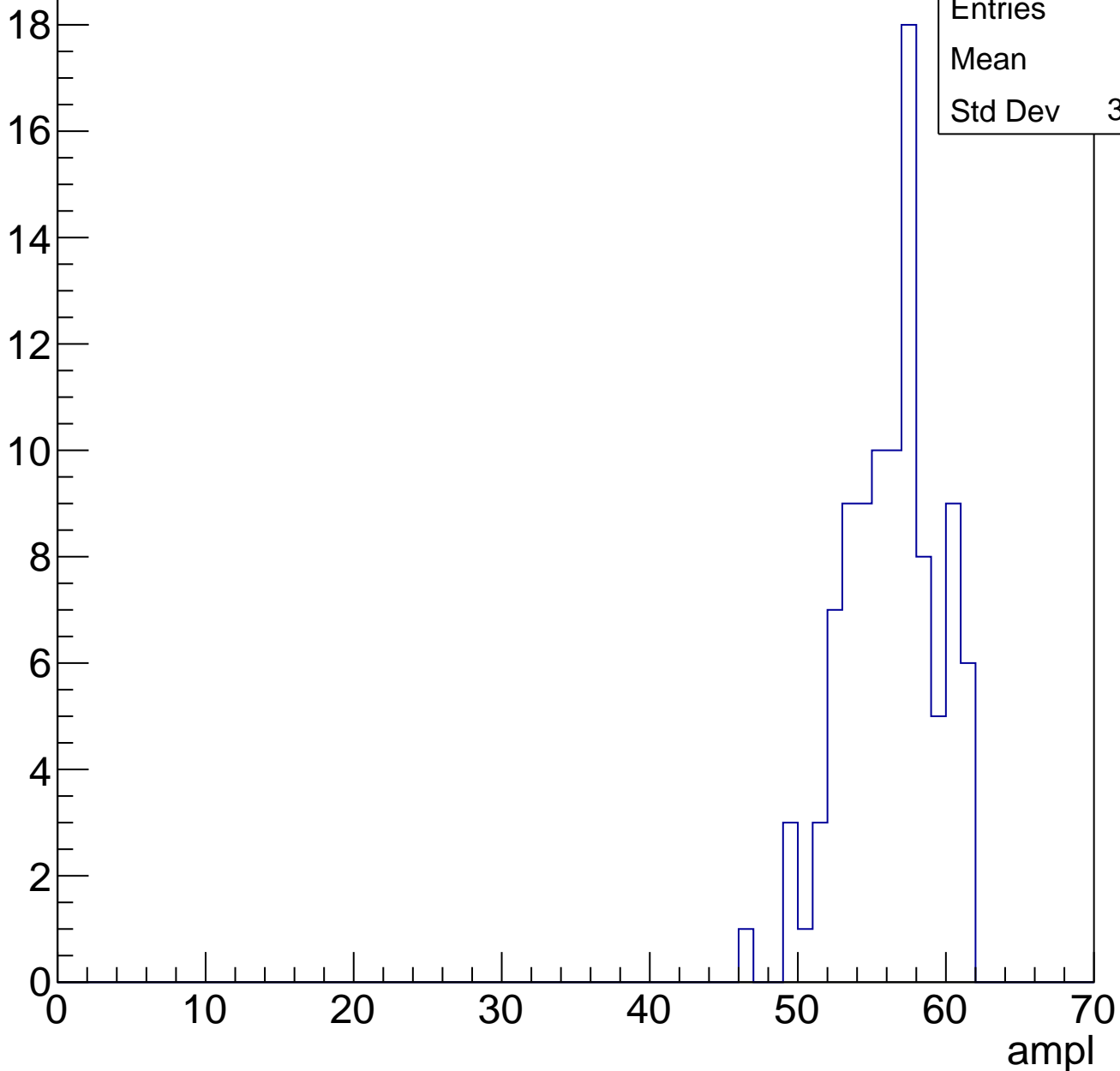
Entries	129
Mean	50.22
Std Dev	3.271



B1L001S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch35, adc5

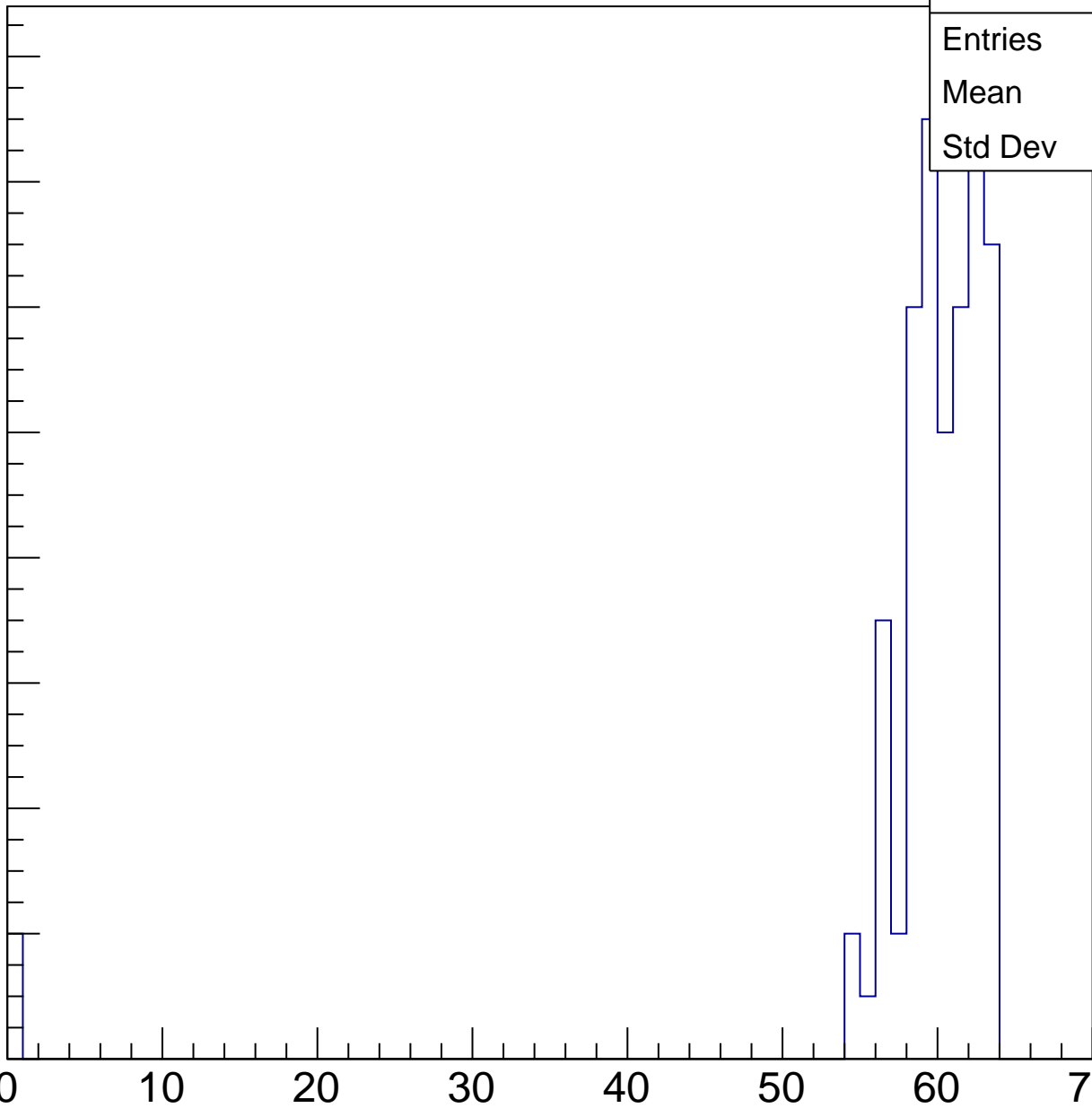
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	58.62
Std Dev	9.034

ampl

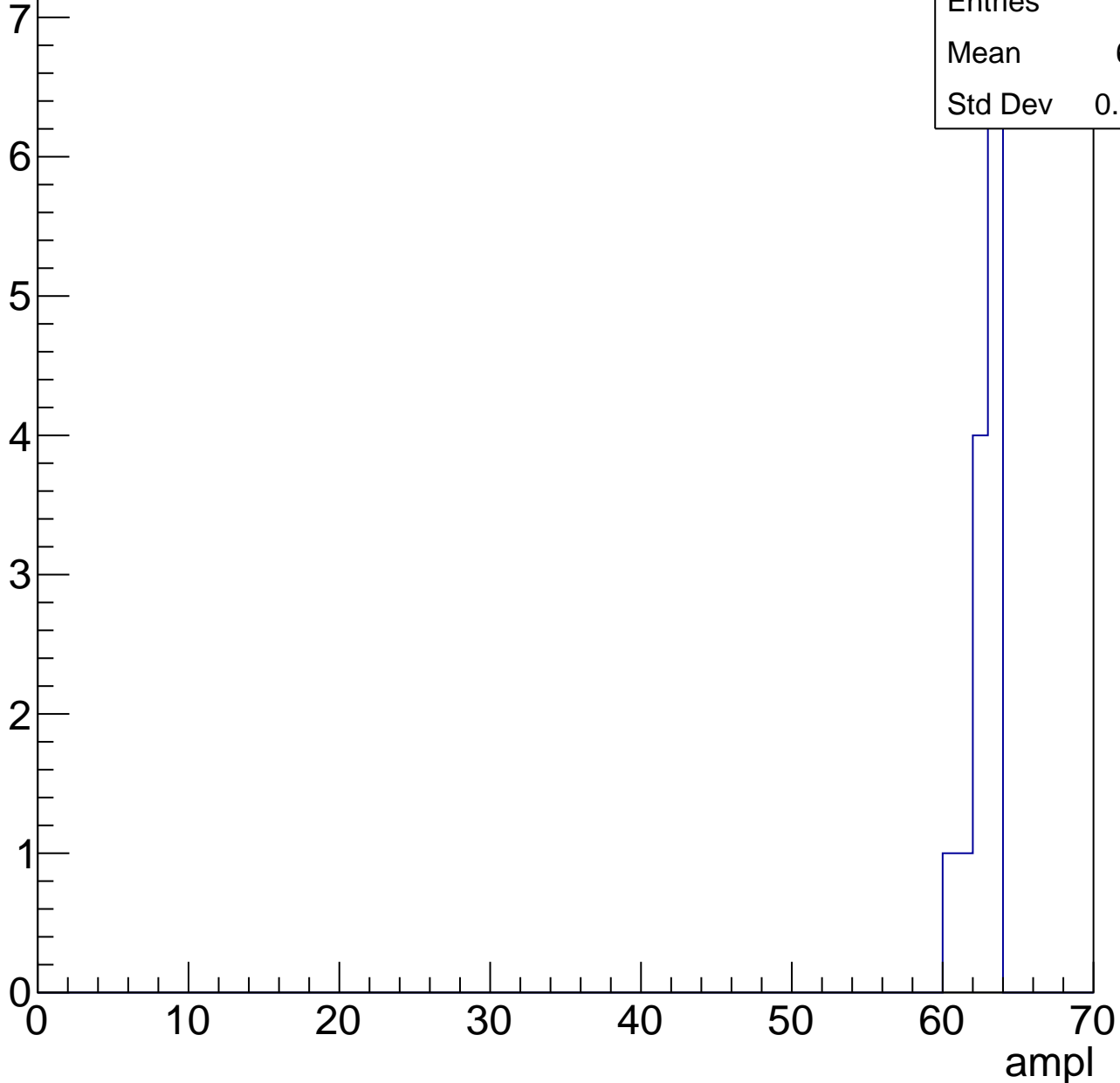


B1L001S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	13
Mean	62.31
Std Dev	0.9102



B1L001S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U14-ch36, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	28.84
Std Dev	2.931

Gaus mean : 29.2934

Gaus Width: 3.2909

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

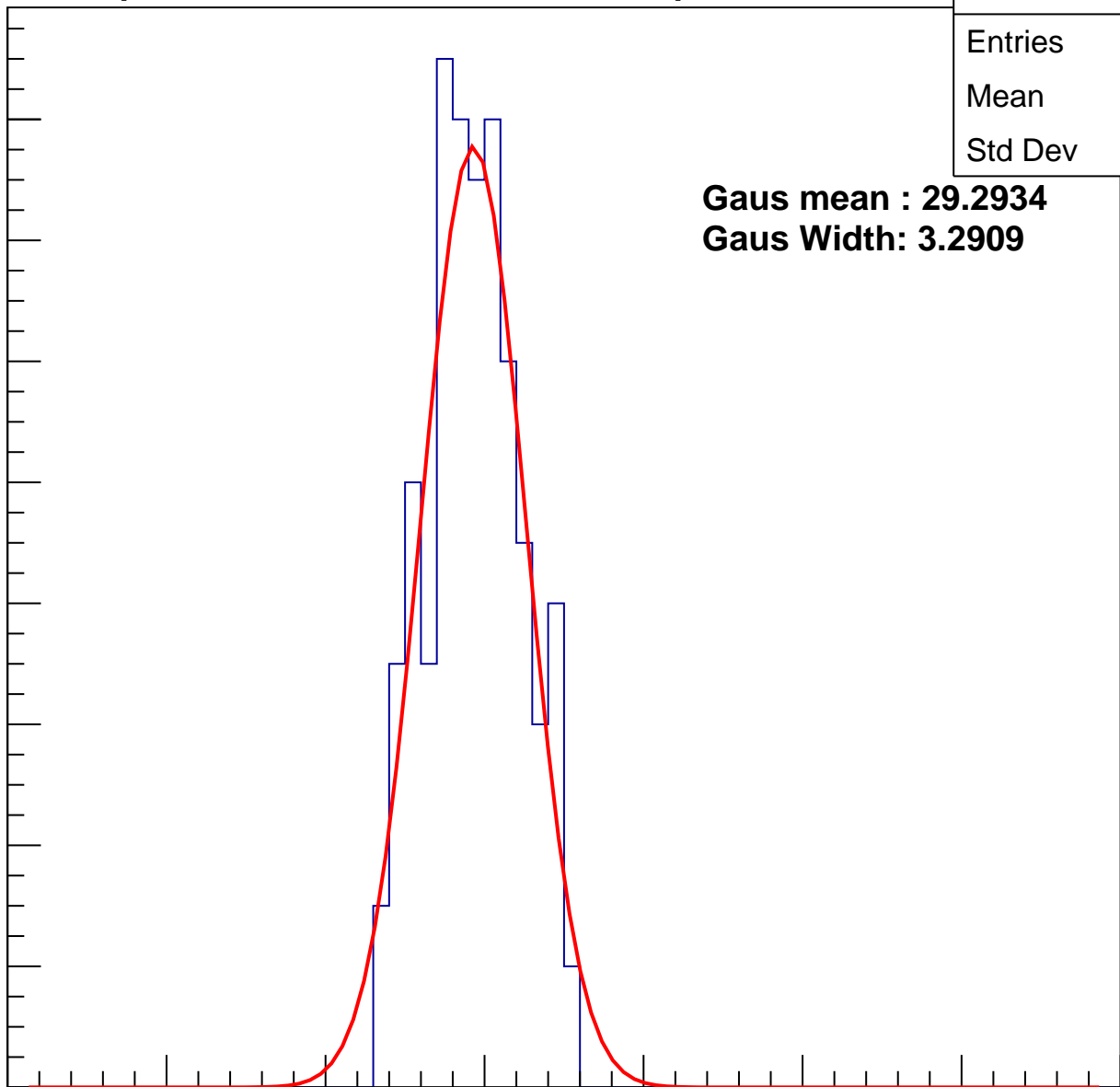
40

50

60

70

ampl



B1L001S, U14-ch36, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	34.86
Std Dev	3.24

Gaus mean : 35.4223

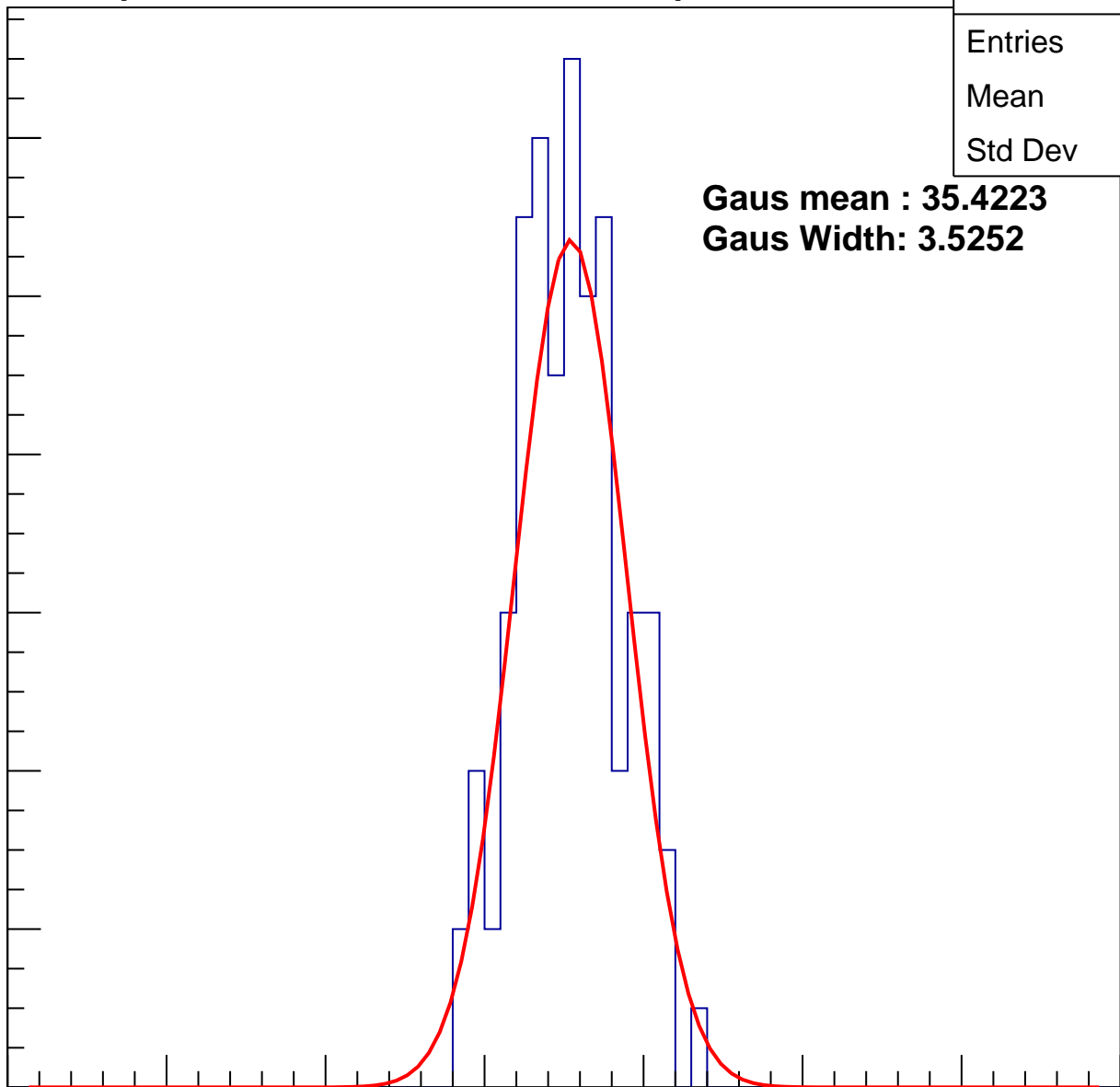
Gaus Width: 3.5252

Entry

12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch36, adc2

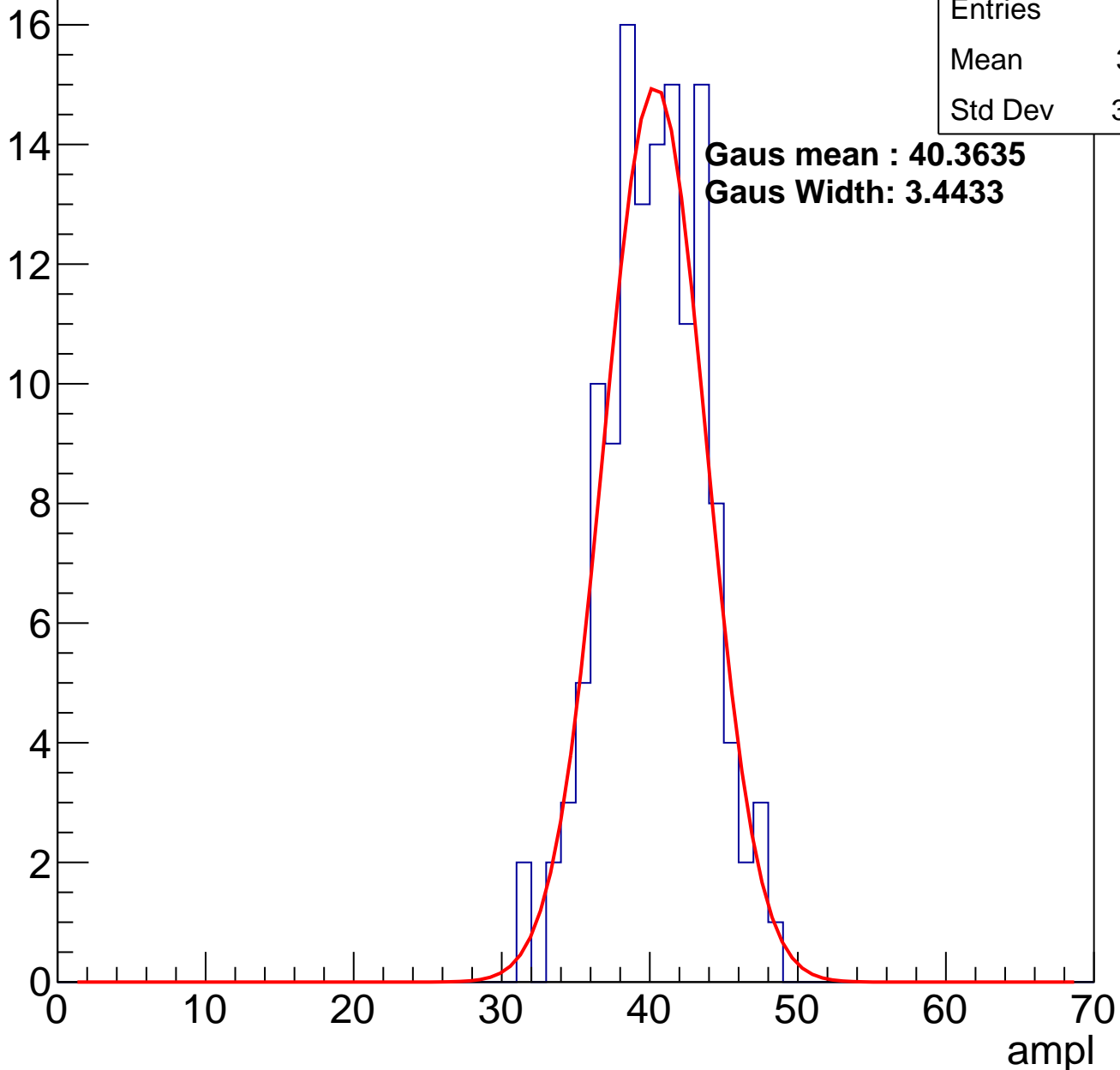
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	39.91
Std Dev	3.375

Gaus mean : 40.3635

Gaus Width: 3.4433

Entry



B1L001S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	47.31
Std Dev	3.343

Entry

16
14
12
10
8
6
4
2
0

0

10

20

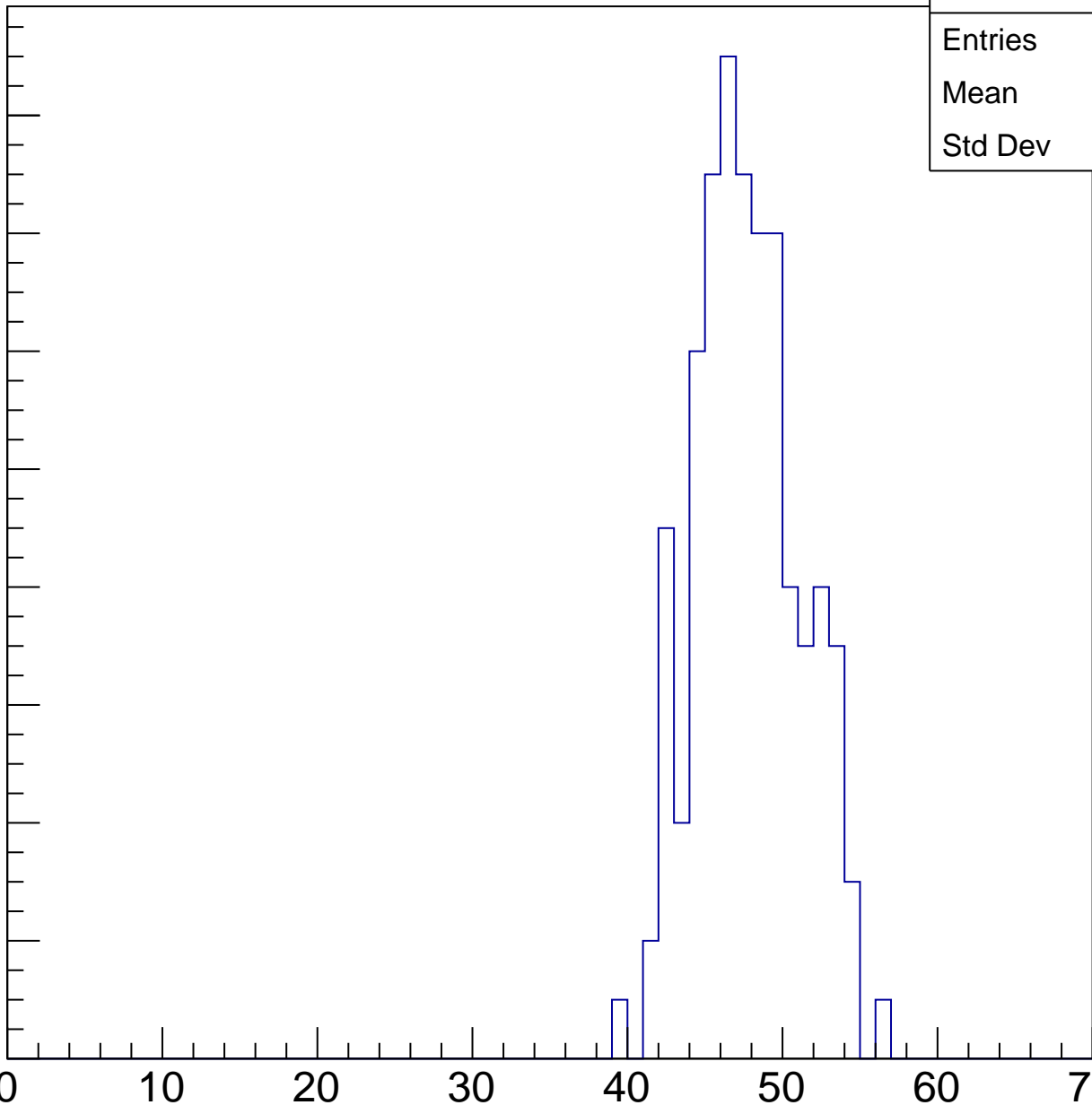
30

40

50

60

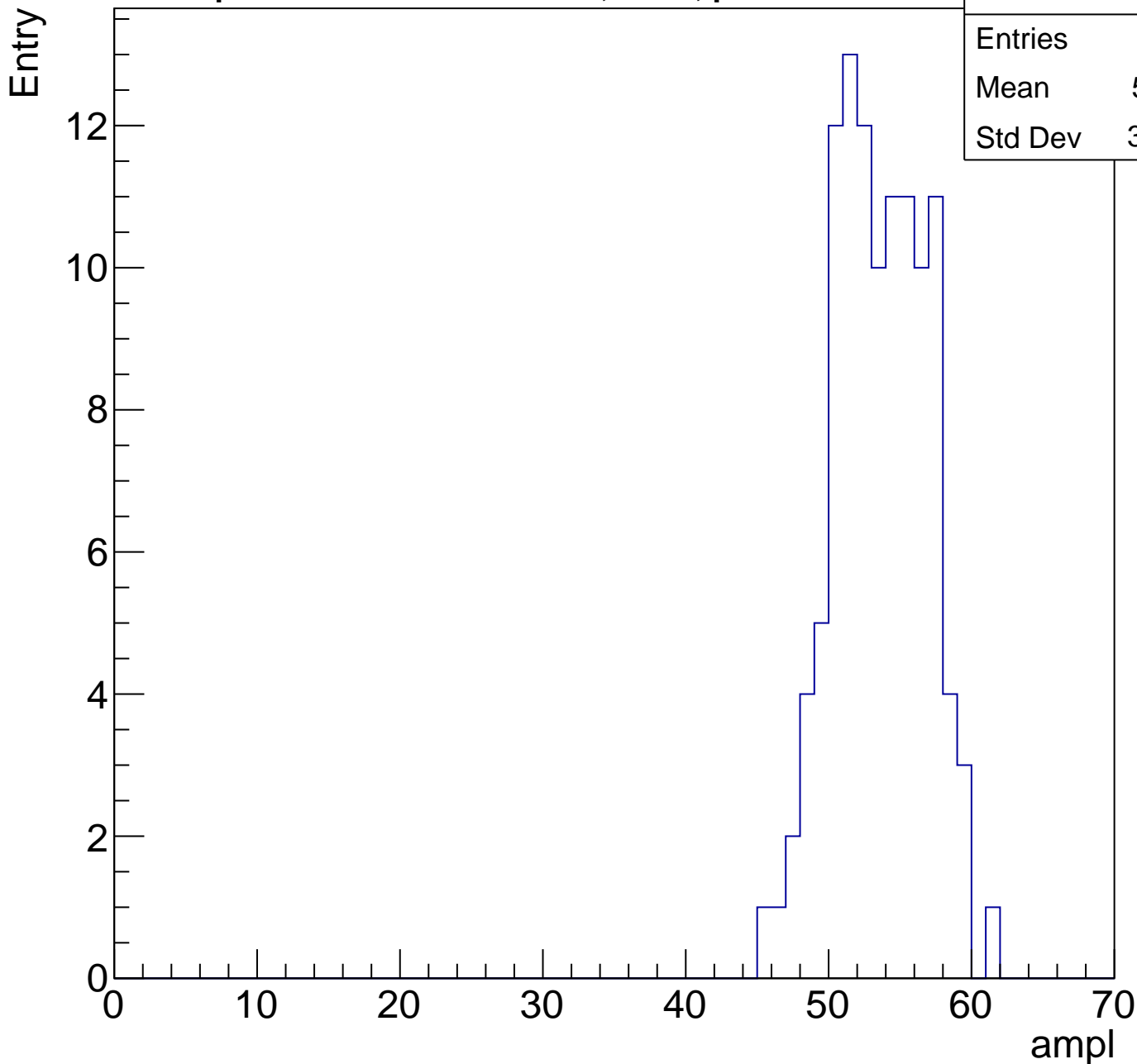
ampl



B1L001S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	53.11
Std Dev	3.189



B1L001S, U14-ch36, adc5

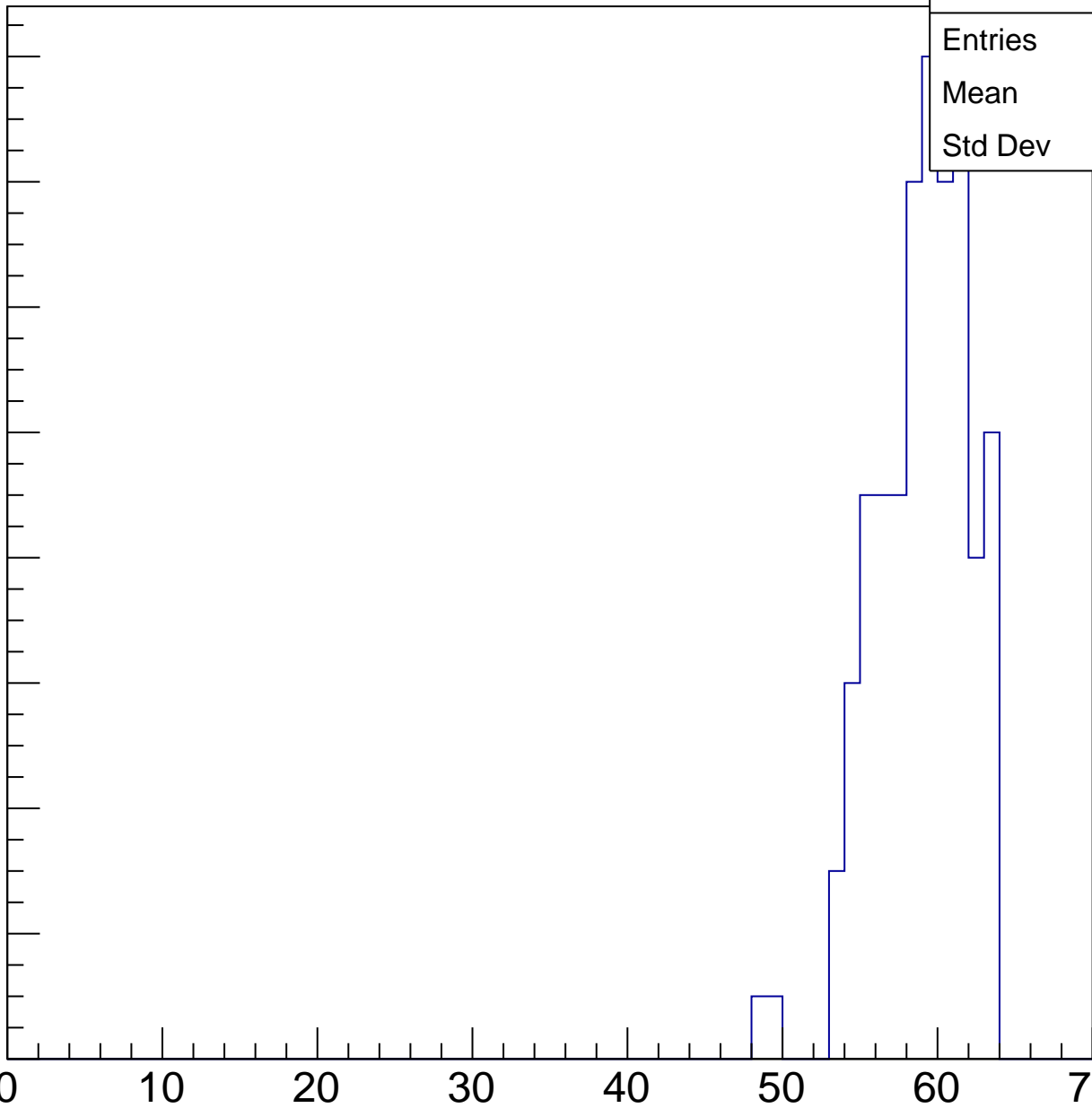
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	58.53
Std Dev	2.993

ampl



B1L001S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	35
Mean	58.03
Std Dev	14.36

B1L001S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#2, port C2

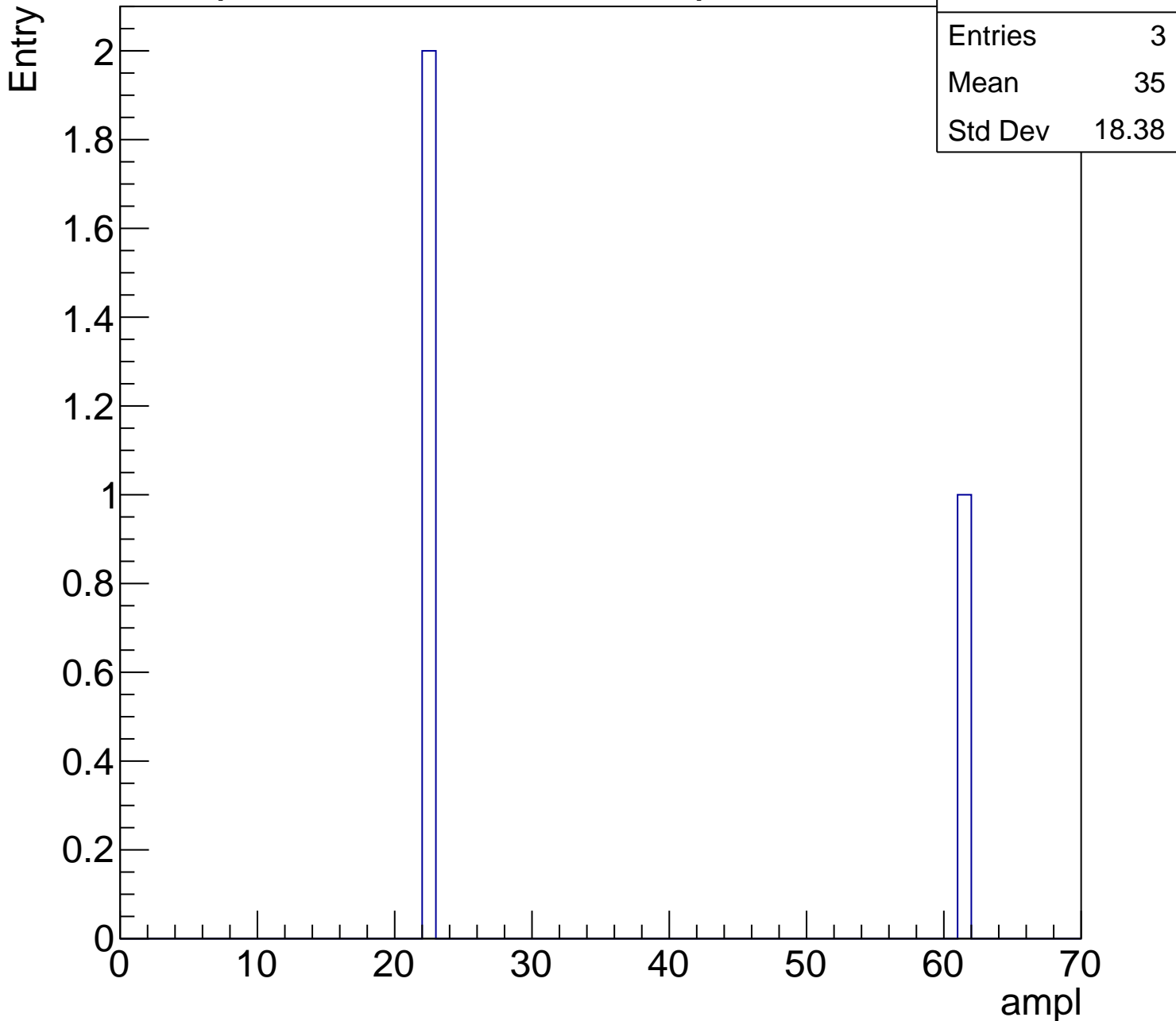
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	35
Std Dev	18.38

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch37, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	28.54
Std Dev	3.141

Gaus mean : 28.9526

Gaus Width: 2.9874

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

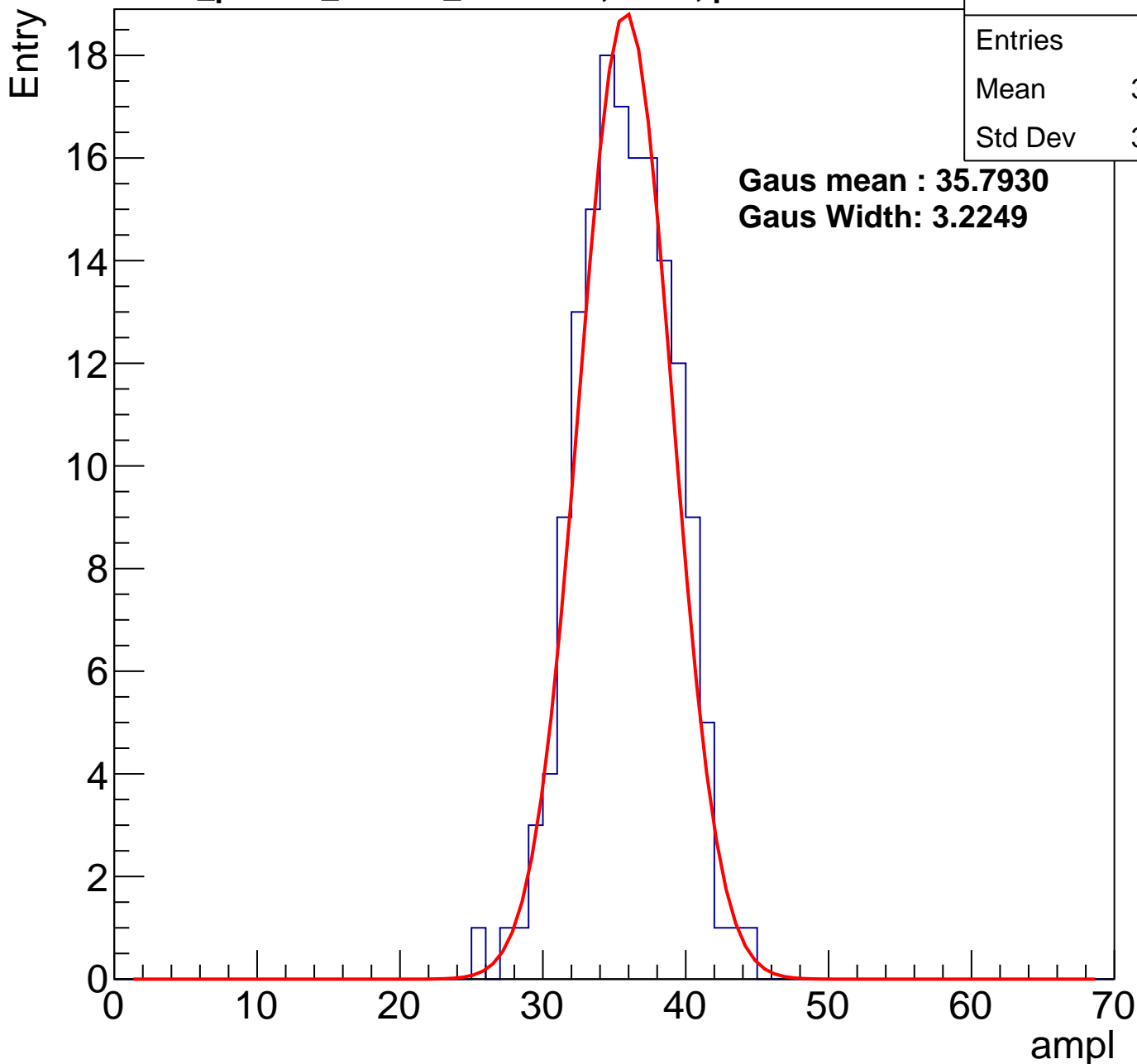
B1L001S, U14-ch37, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	35.32
Std Dev	3.329

Gaus mean : 35.7930

Gaus Width: 3.2249



B1L001S, U14-ch37, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	42.93
Std Dev	3.402

Gaus mean : 43.3555

Gaus Width: 3.5623

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

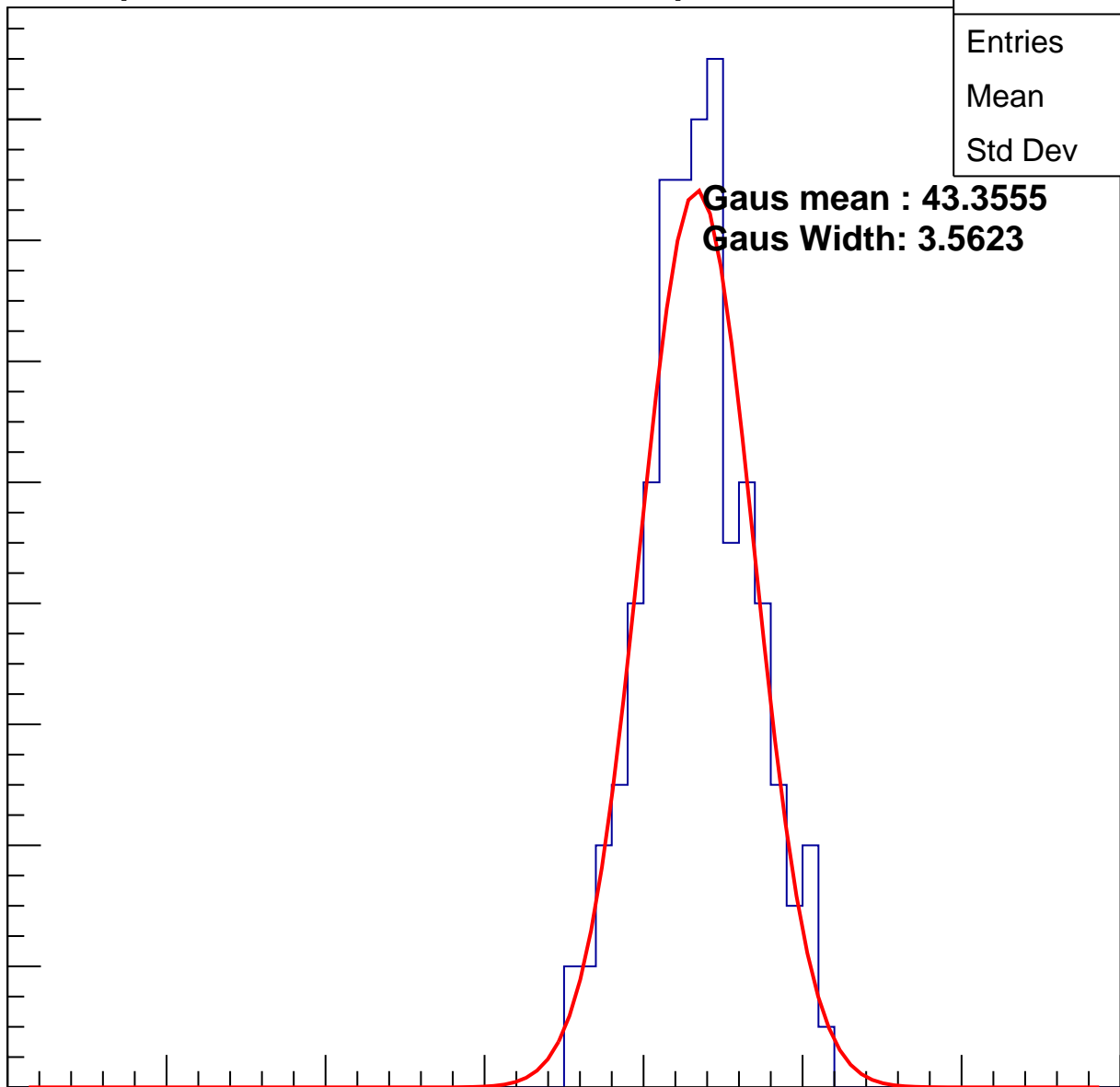
40

50

60

70

ampl

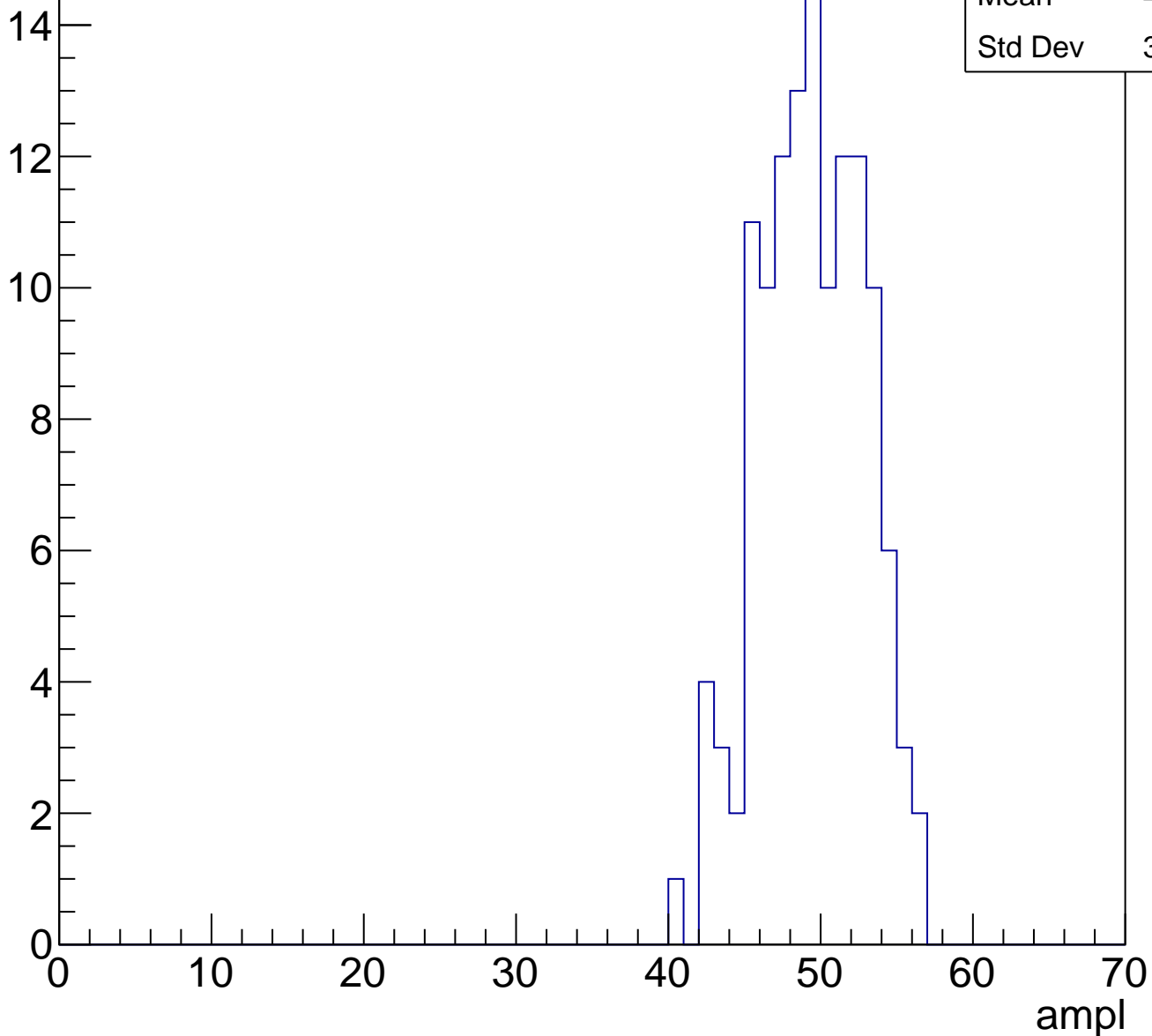


B1L001S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	48.97
Std Dev	3.385

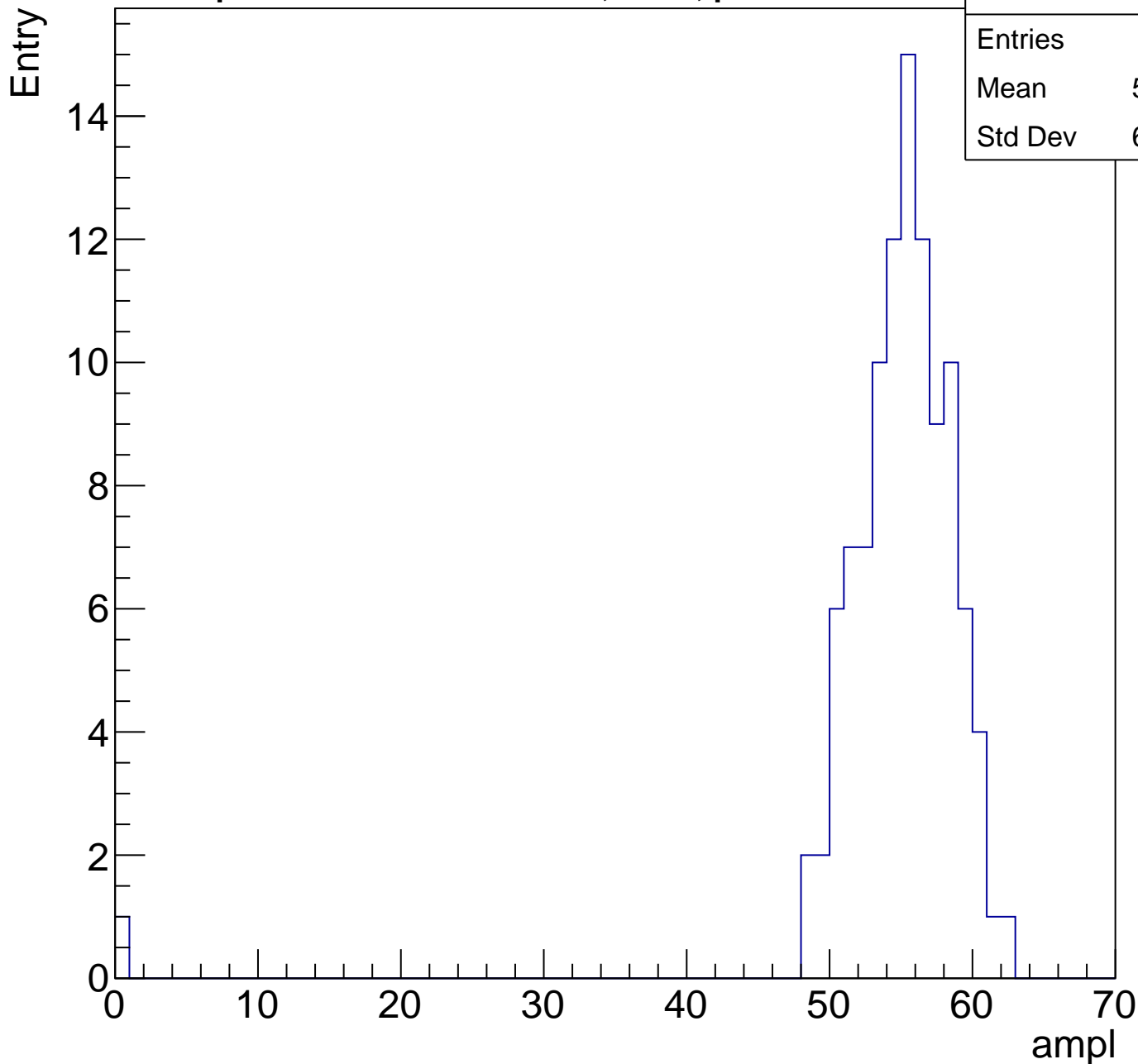
Entry



B1L001S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	54.29
Std Dev	6.124



B1L001S, U14-ch37, adc5

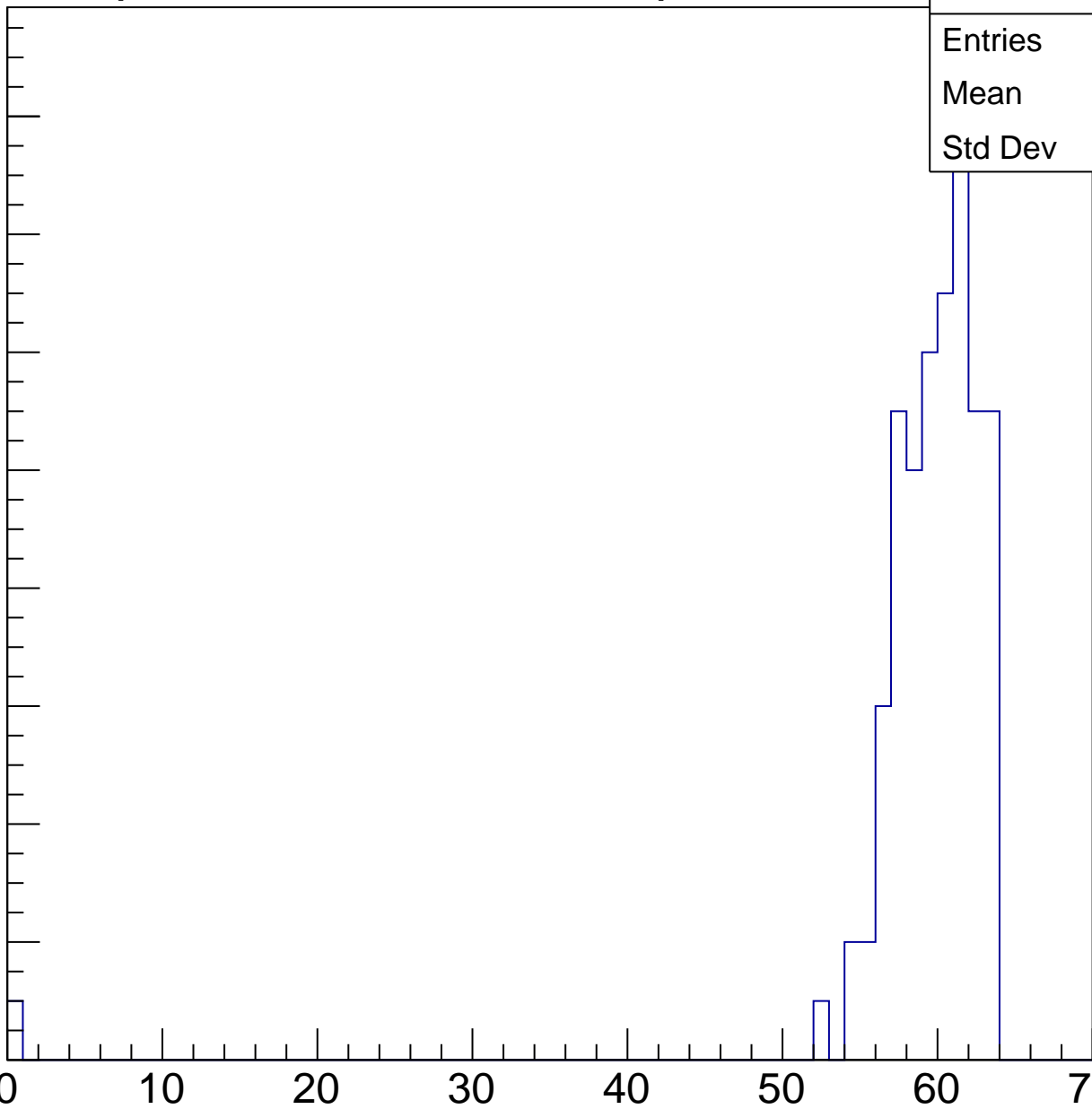
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	97
Mean	58.9
Std Dev	6.482

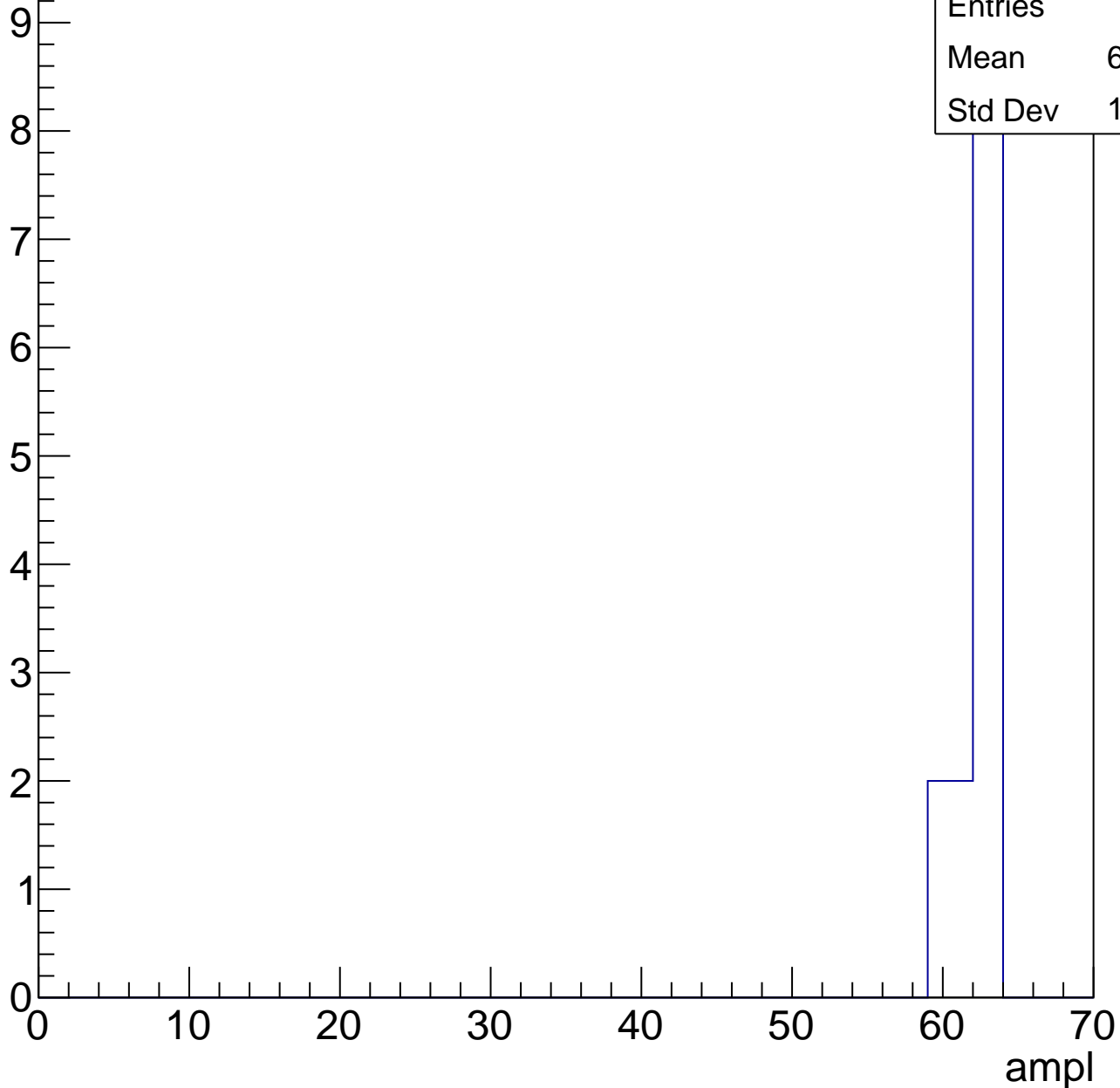
ampl



B1L001S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#2, port C2

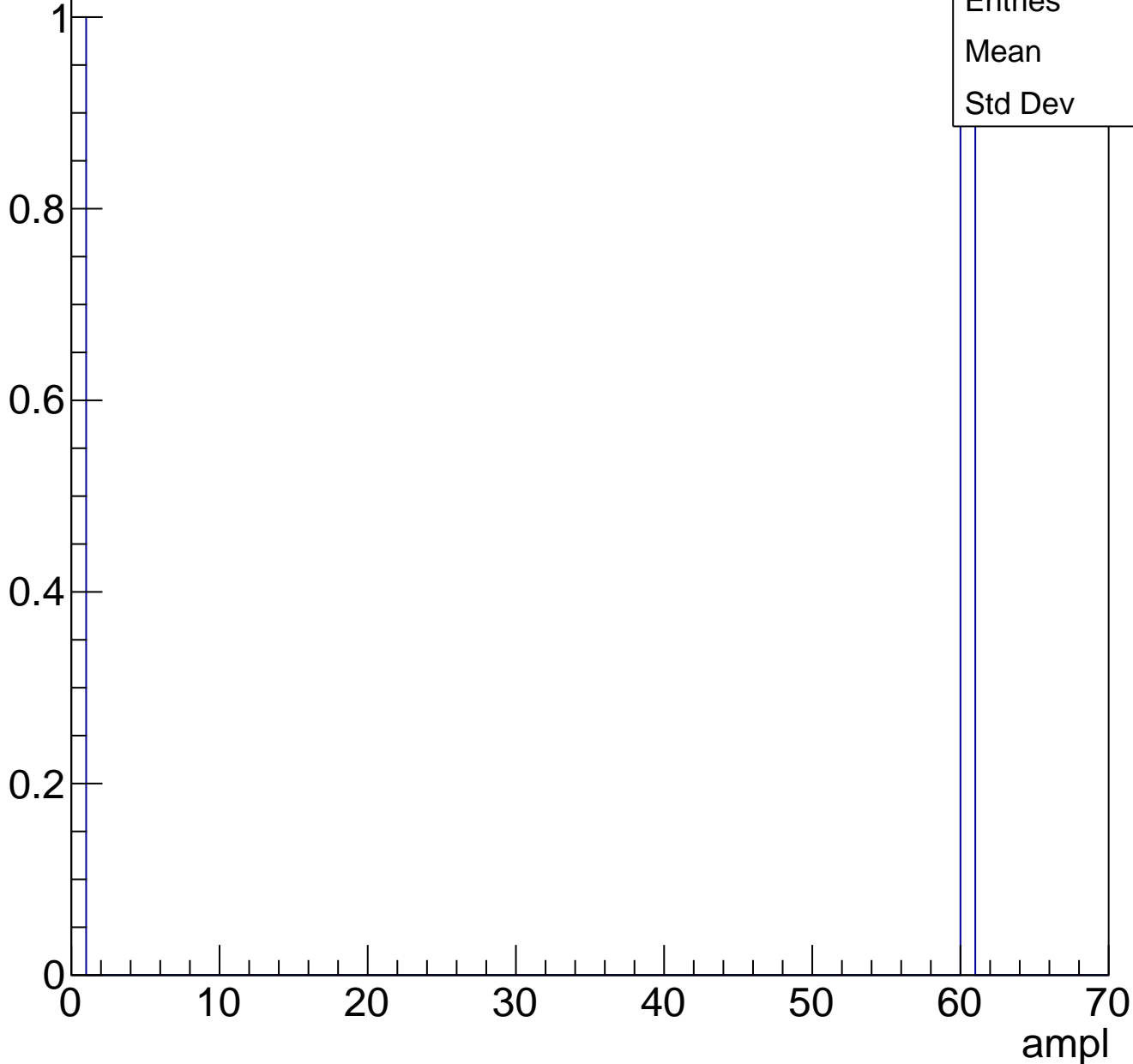
Entry



B1L001S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch38, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.37
Std Dev	3.171

Gaus mean : 30.8607

Gaus Width: 3.1631

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

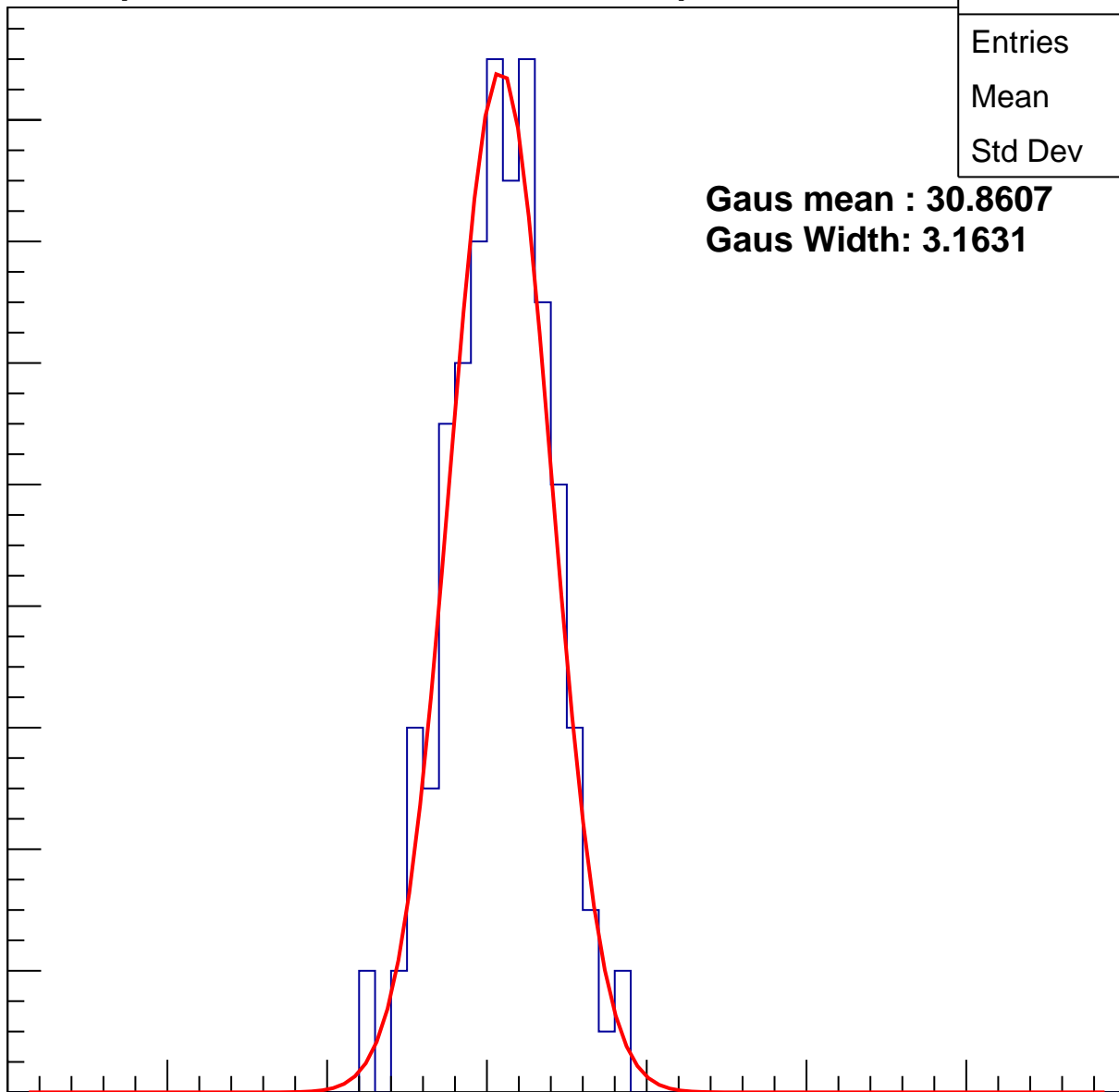
40

50

60

70

ampl



B1L001S, U14-ch38, adc1

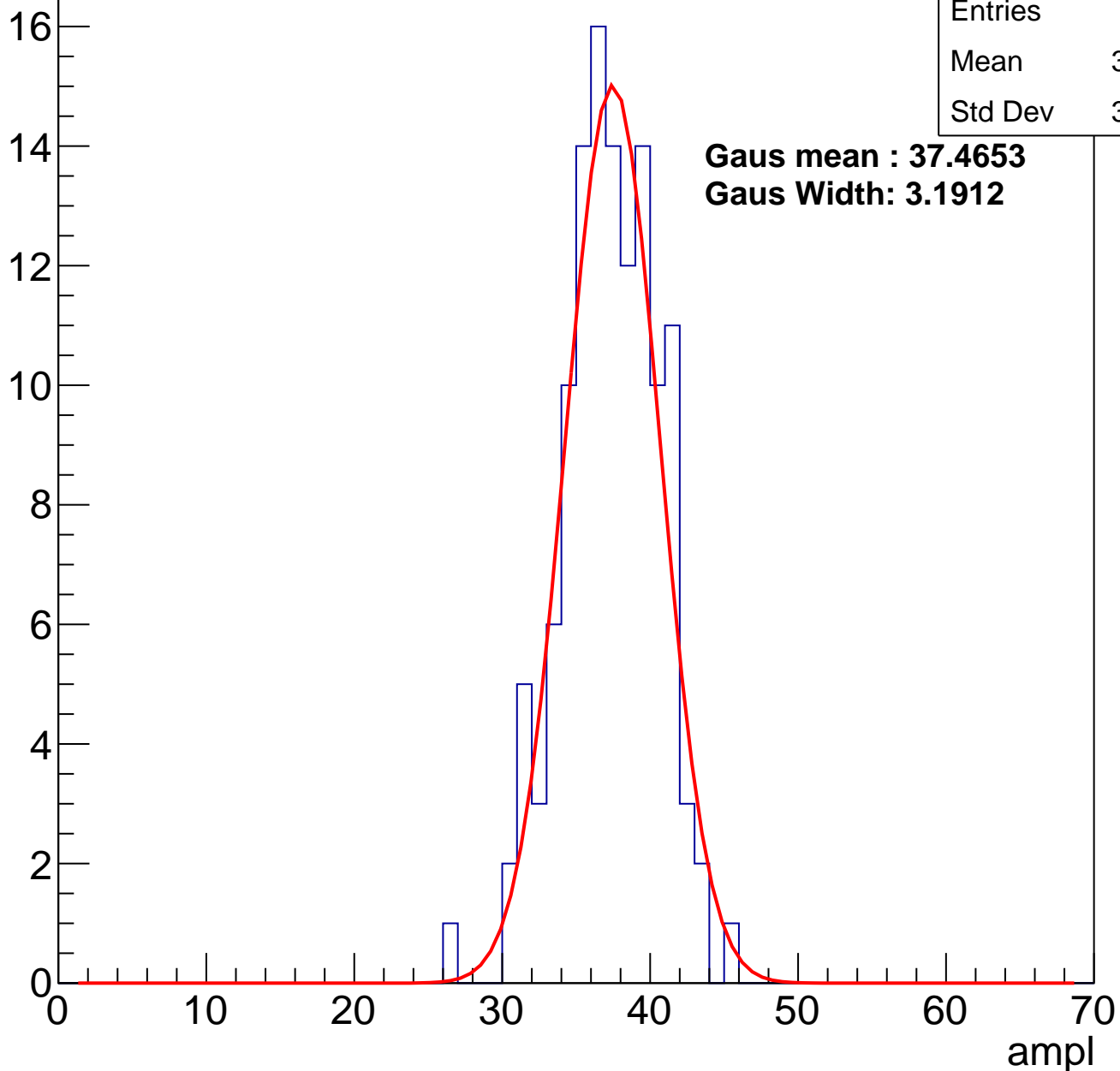
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	36.85
Std Dev	3.205

Gaus mean : 37.4653

Gaus Width: 3.1912

Entry



B1L001S, U14-ch38, adc2

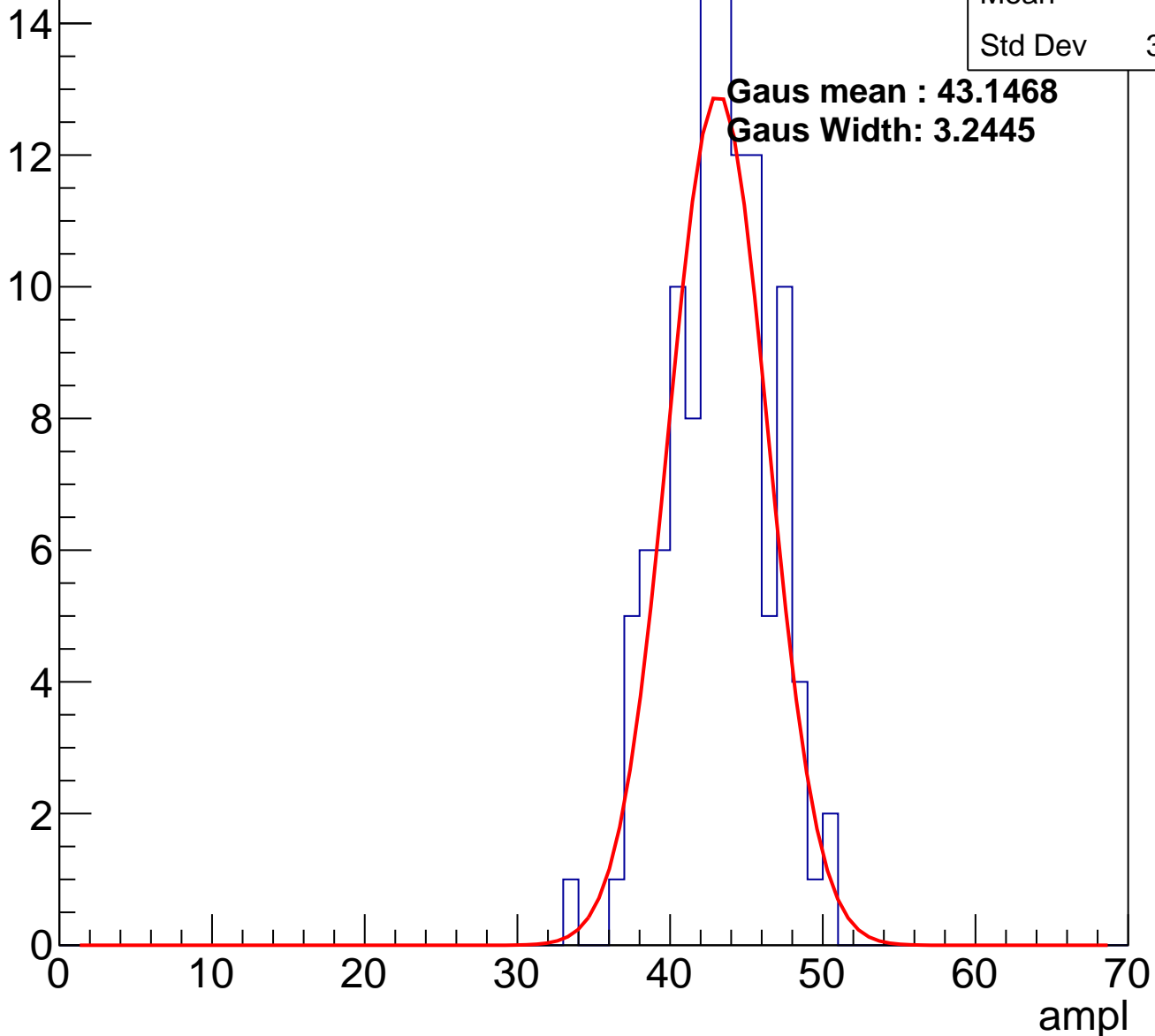
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	42.73
Std Dev	3.262

Gaus mean : 43.1468

Gaus Width: 3.2445

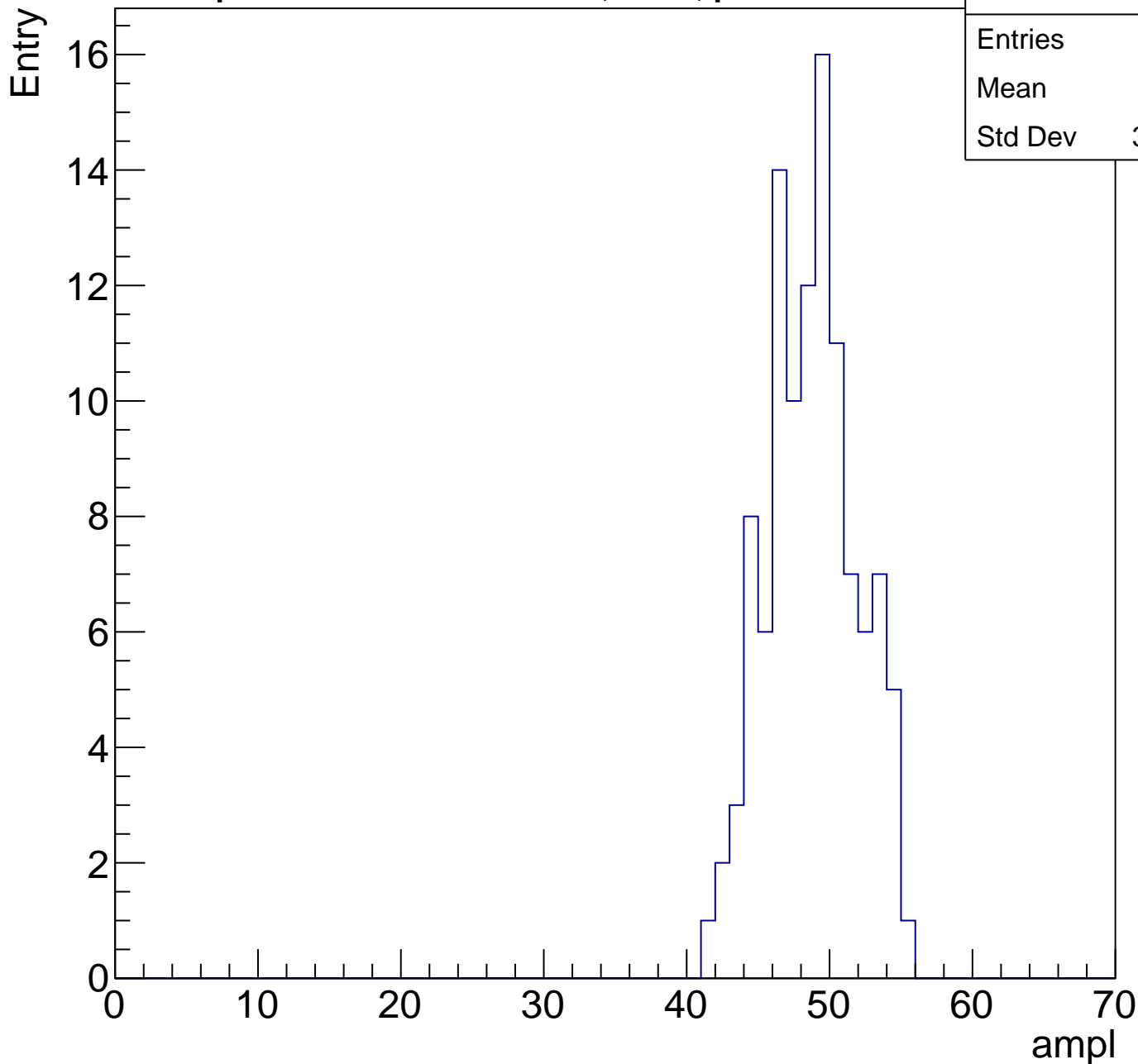
Entry



B1L001S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#2, port C2

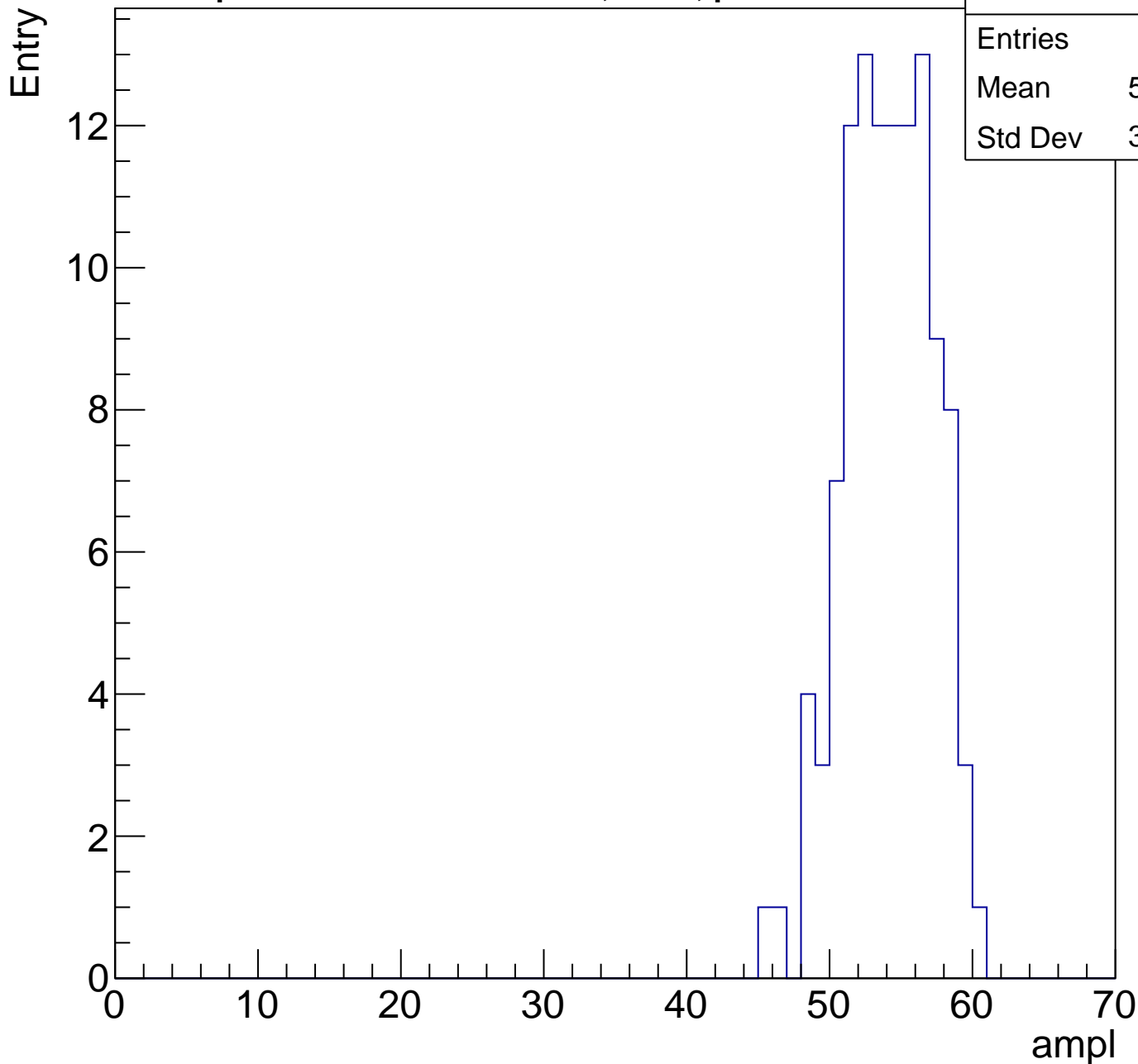
Entries	109
Mean	48.3
Std Dev	3.114



B1L001S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	53.64
Std Dev	3.028



B1L001S, U14-ch38, adc5

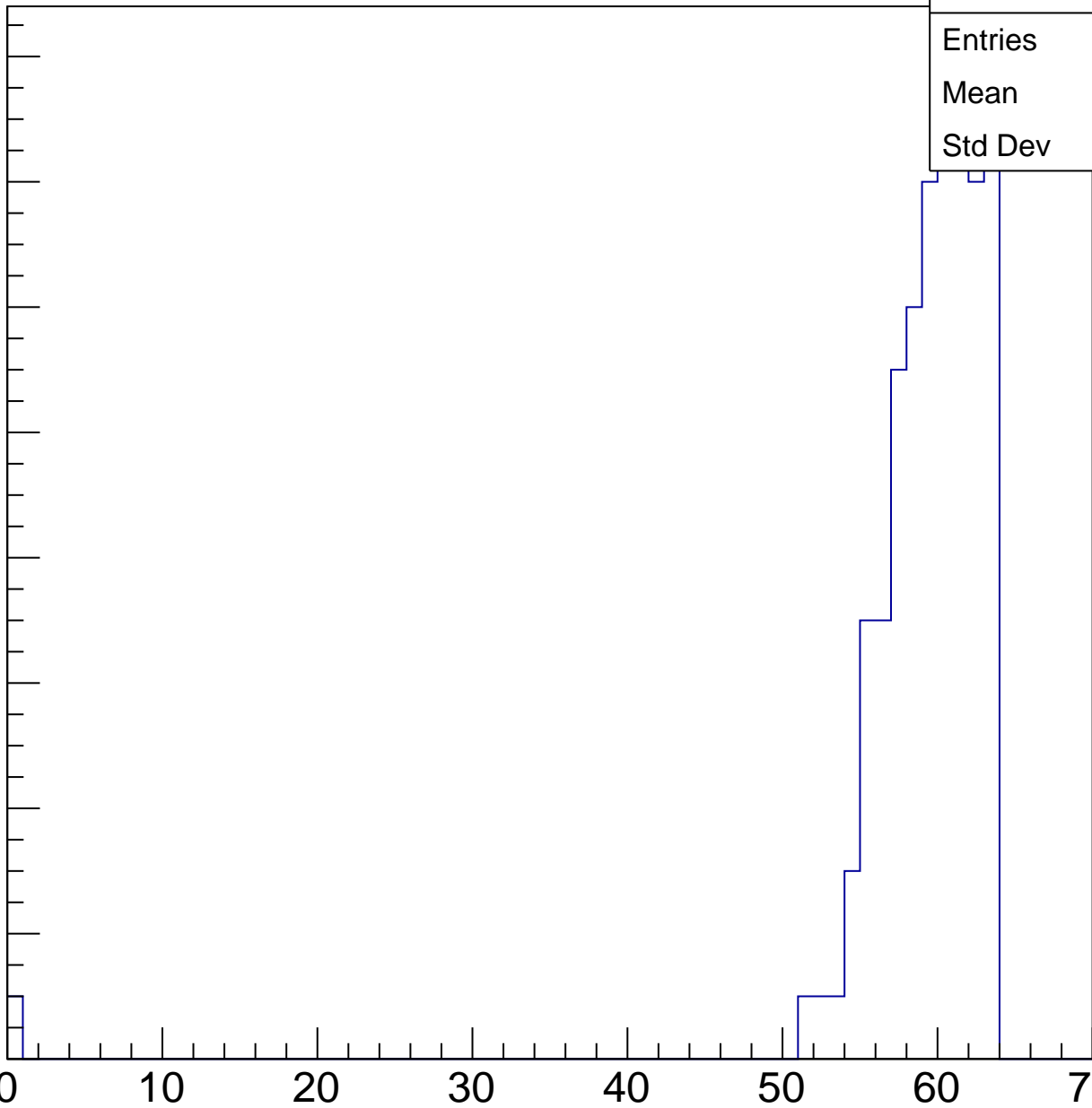
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	119
Mean	58.76
Std Dev	6.064

ampl

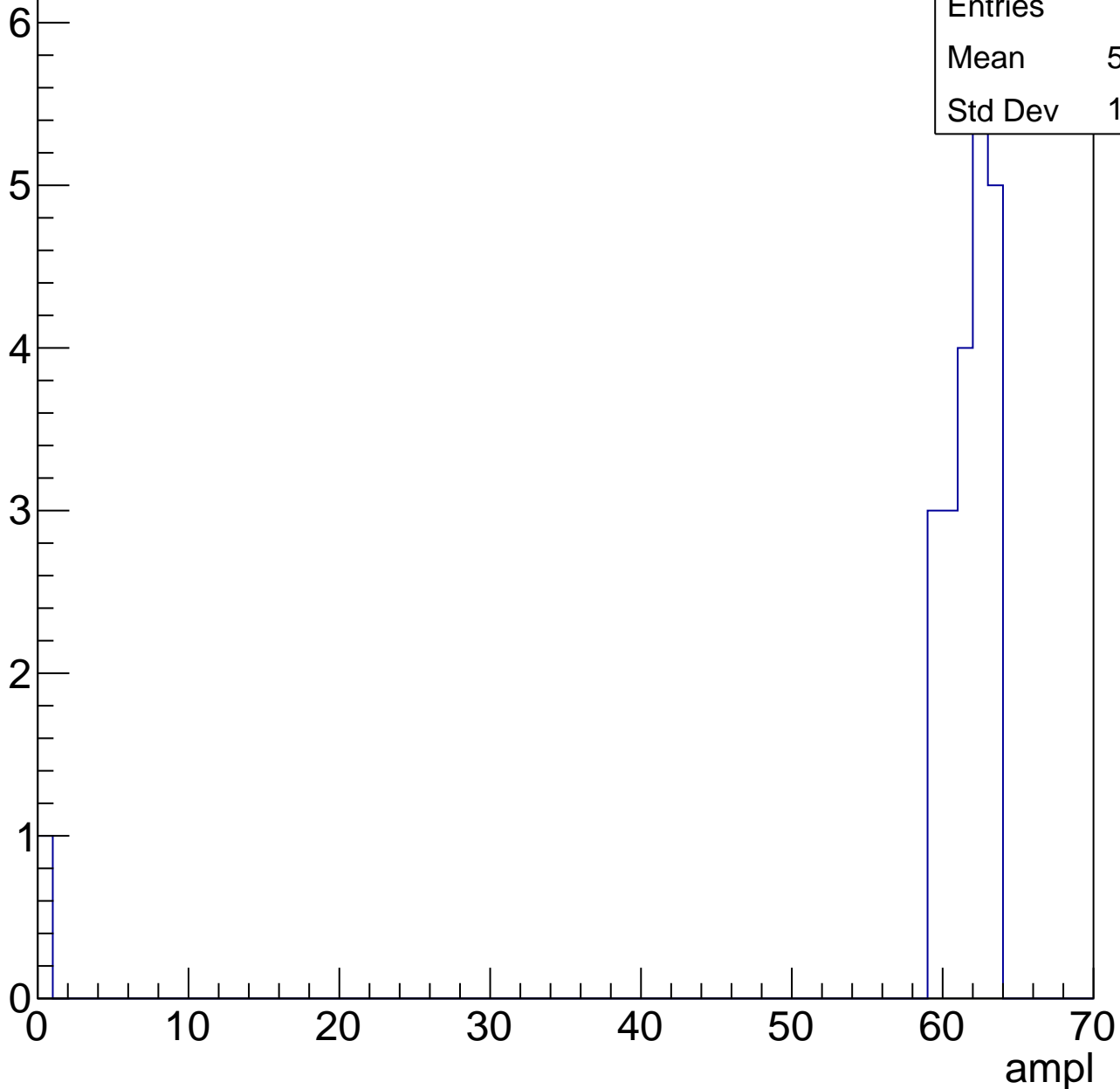


B1L001S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	58.55
Std Dev	12.84



B1L001S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch39, adc0

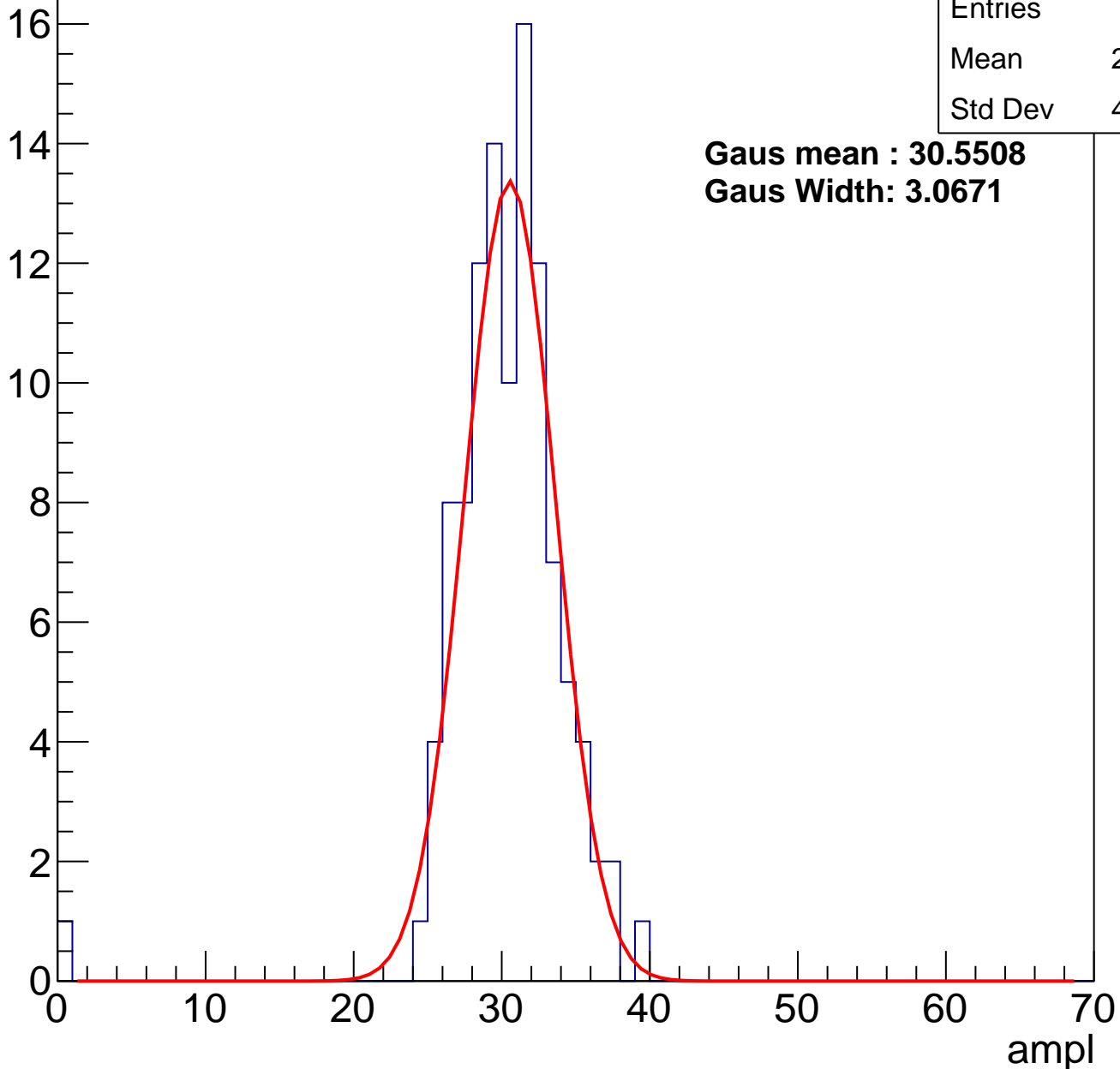
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	29.87
Std Dev	4.163

Gaus mean : 30.5508

Gaus Width: 3.0671

Entry



B1L001S, U14-ch39, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.46
Std Dev	3.293

Gaus mean : 36.9418

Gaus Width: 3.5712

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

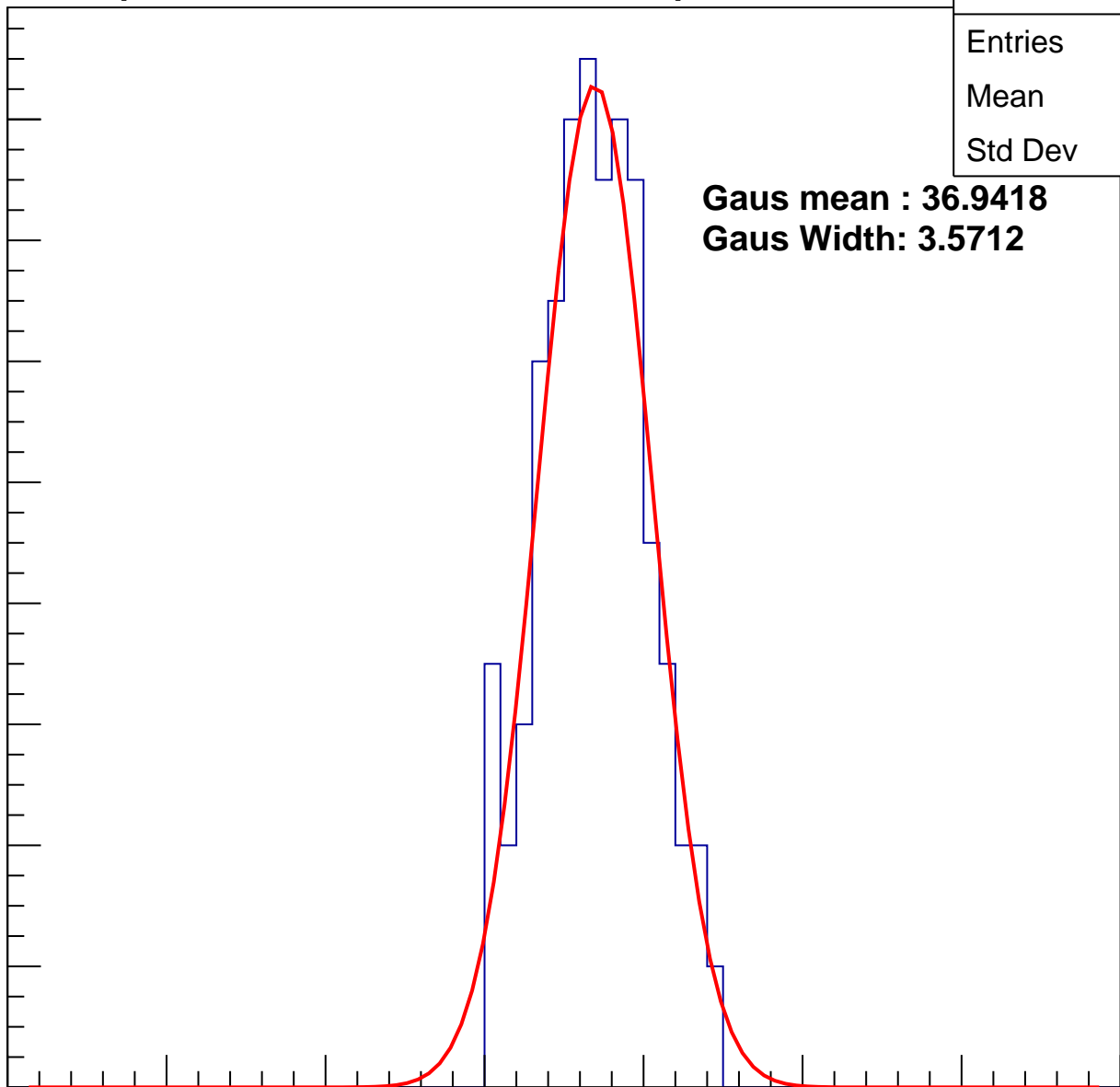
40

50

60

70

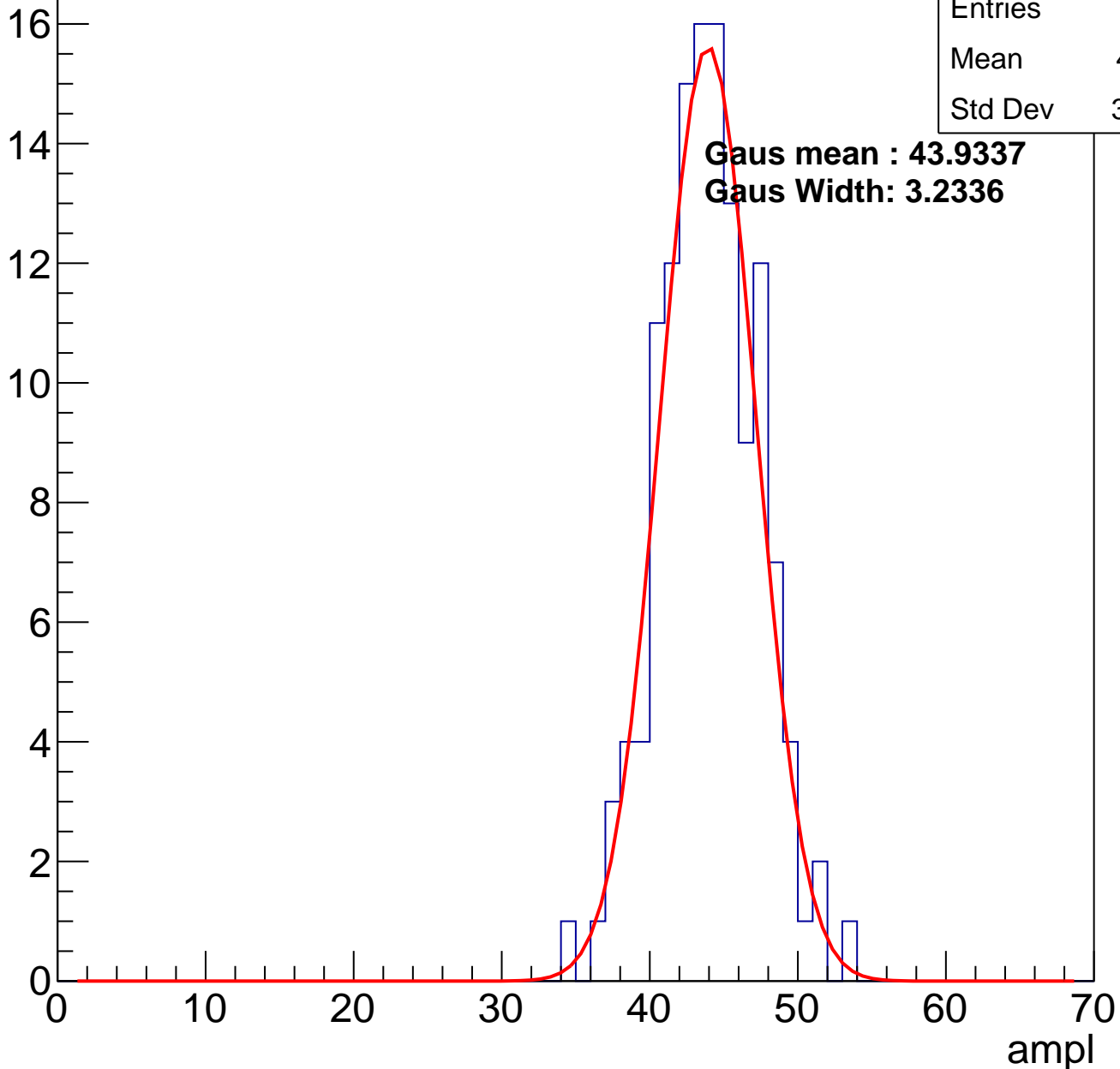
ampl



B1L001S, U14-ch39, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	50.06
Std Dev	3.337

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

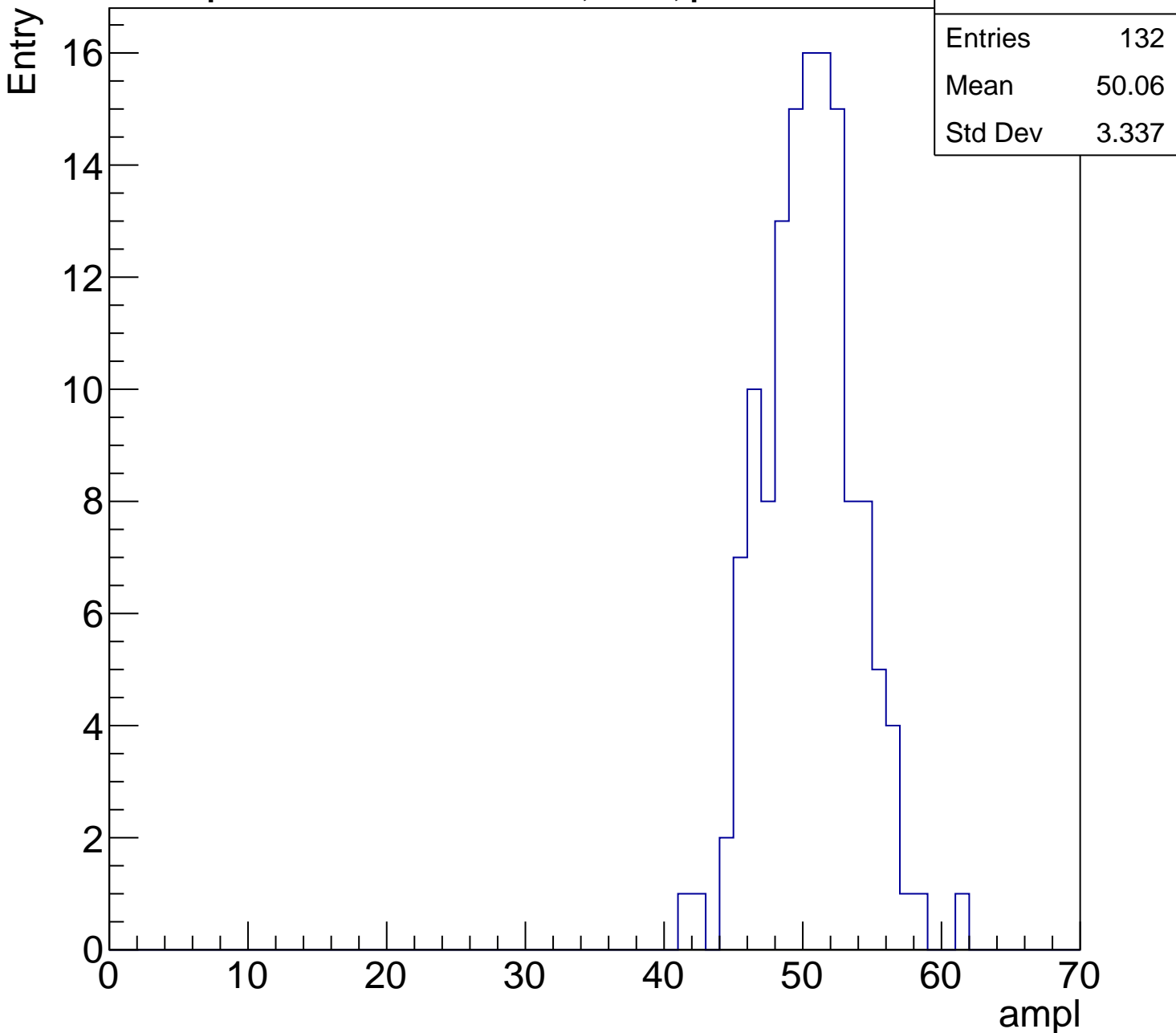
40

50

60

70

ampl



B1L001S, U14-ch39, adc4

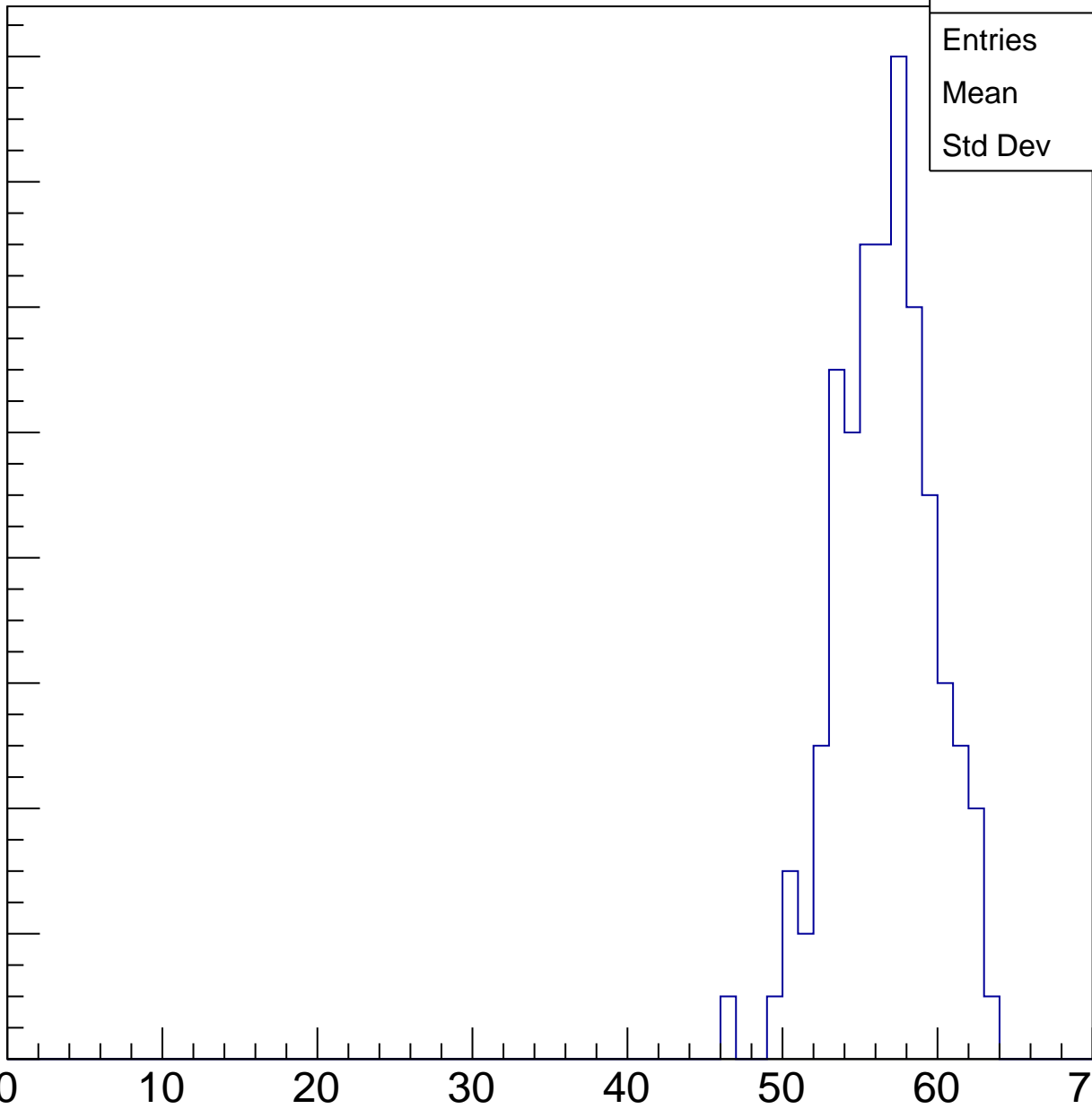
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	56.14
Std Dev	3.128

ampl



B1L001S, U14-ch39, adc5

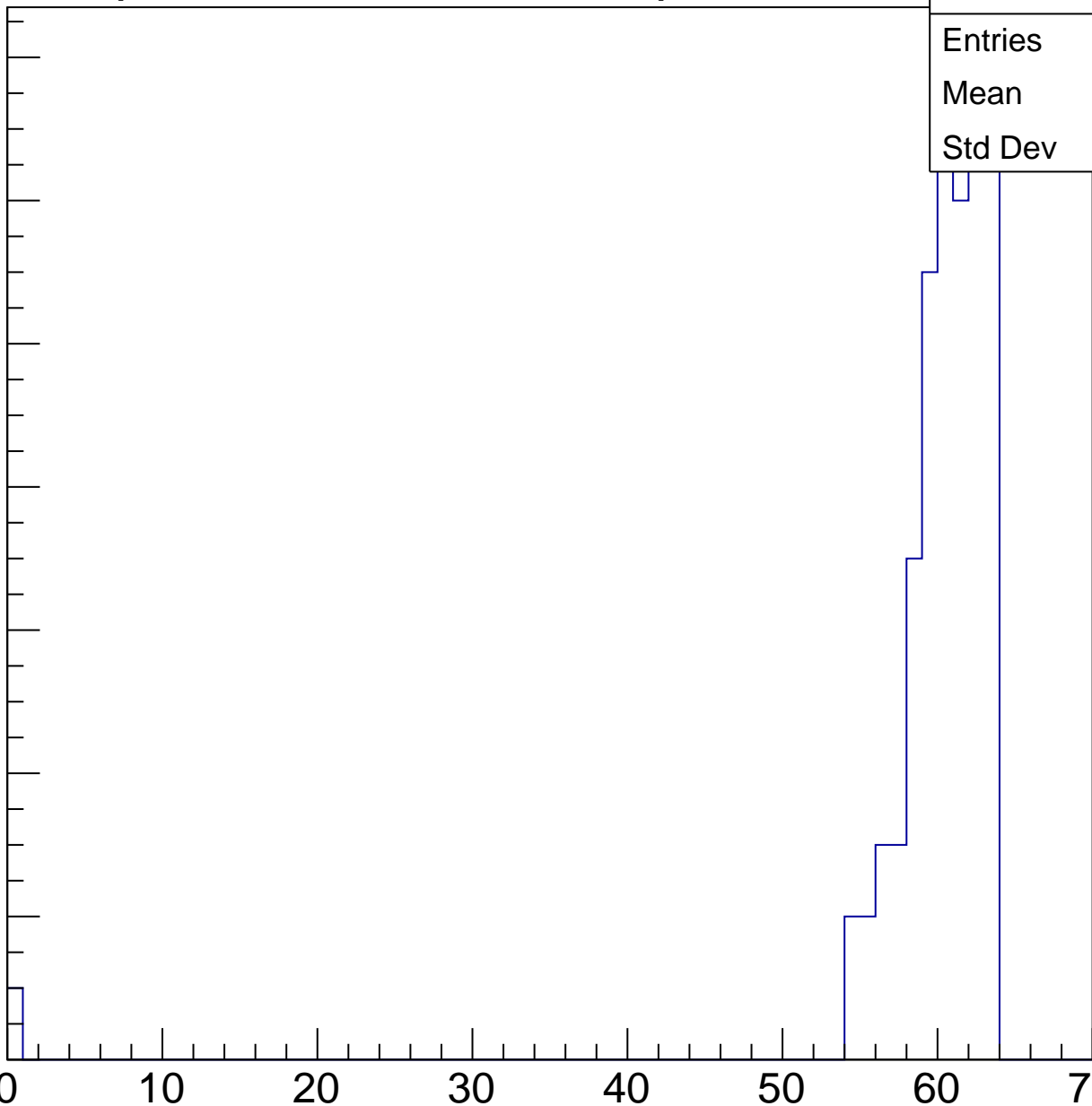
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	82
Mean	59.41
Std Dev	6.982

ampl

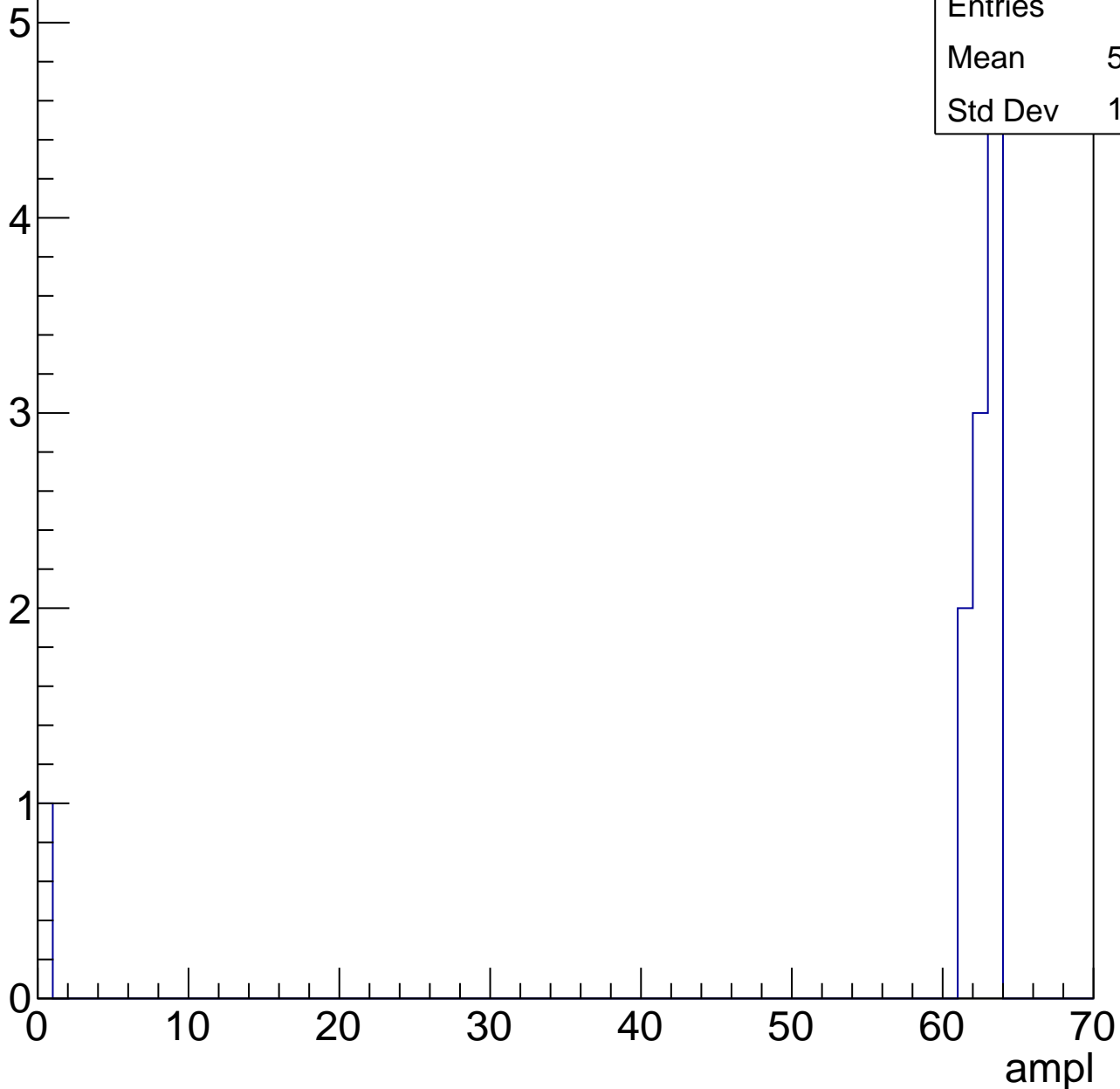


B1L001S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.64
Std Dev	17.93



B1L001S, U14-ch39, adc7

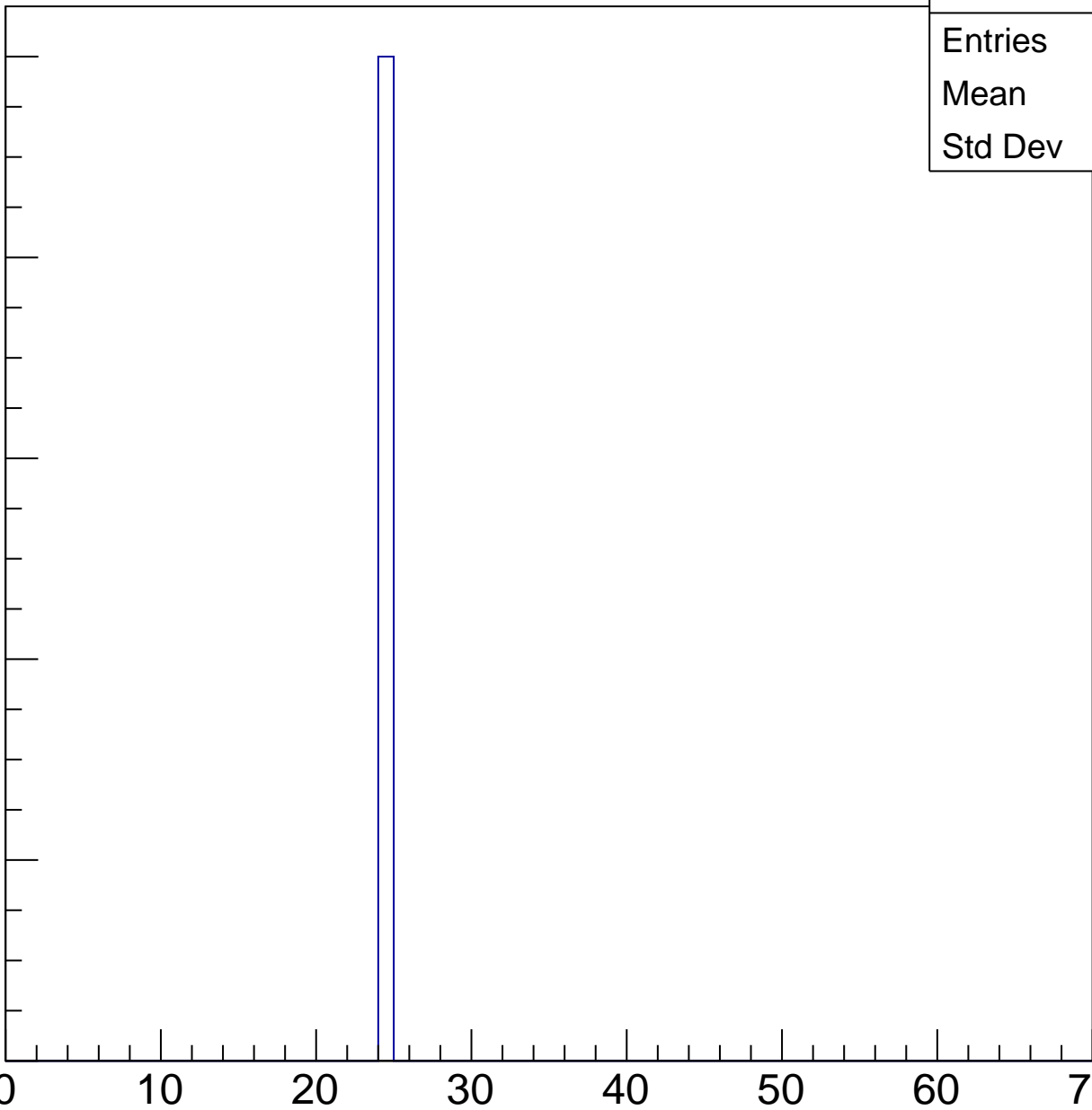
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl



B1L001S, U14-ch40, adc0

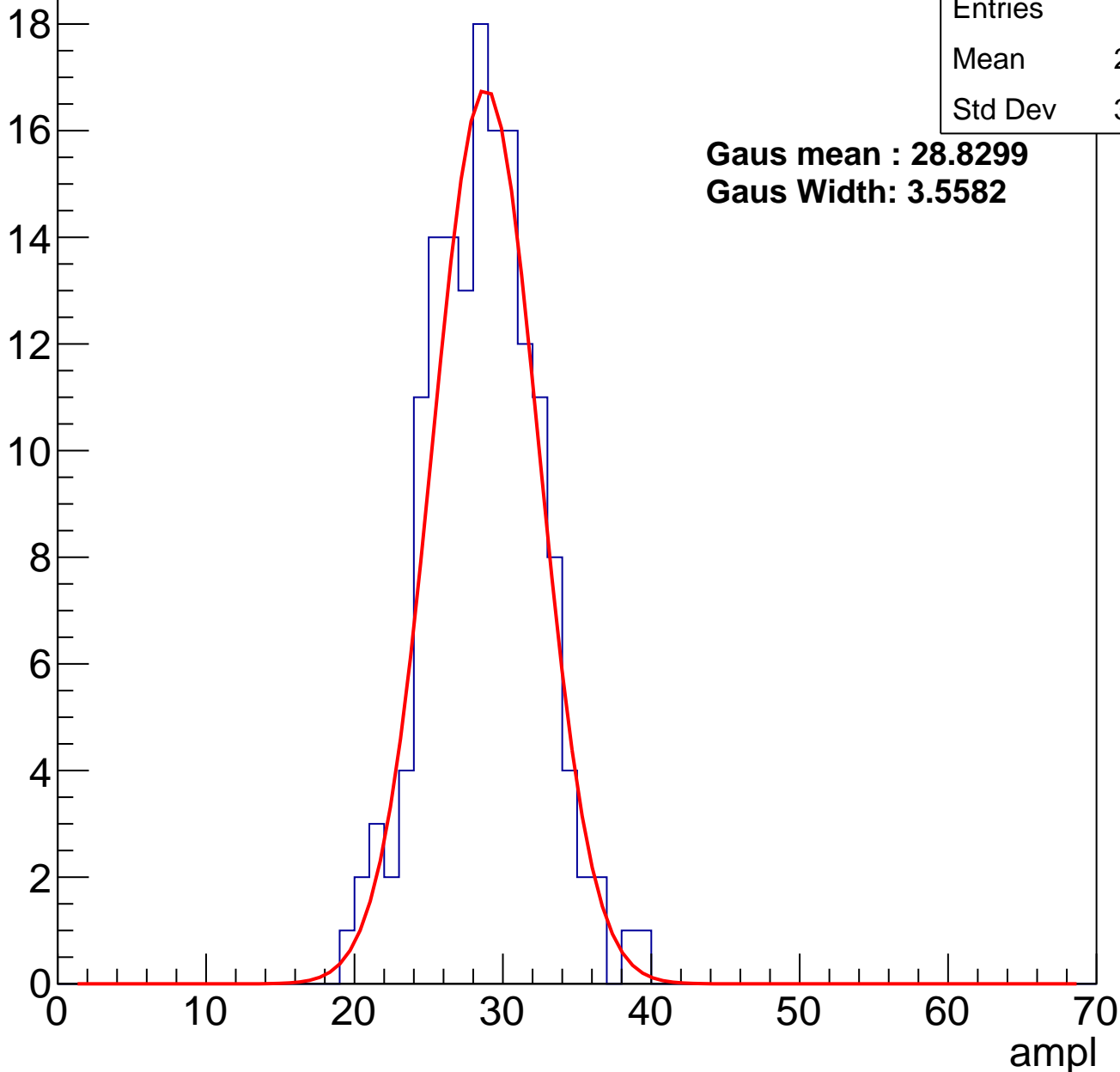
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	28.25
Std Dev	3.632

Gaus mean : 28.8299

Gaus Width: 3.5582

Entry

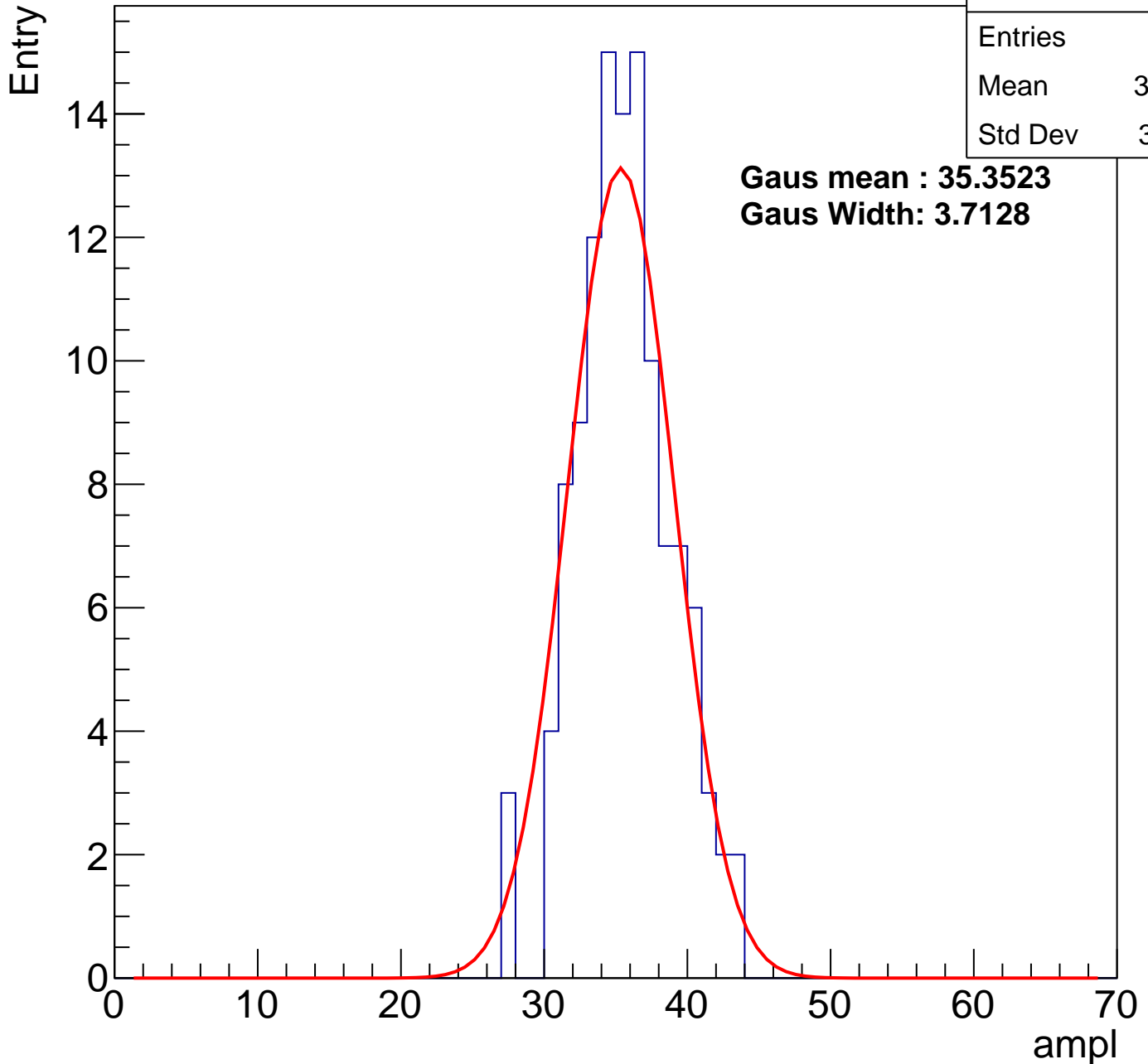


B1L001S, U14-ch40, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	35.17
Std Dev	3.301

Gaus mean : 35.3523
Gaus Width: 3.7128



B1L001S, U14-ch40, adc2

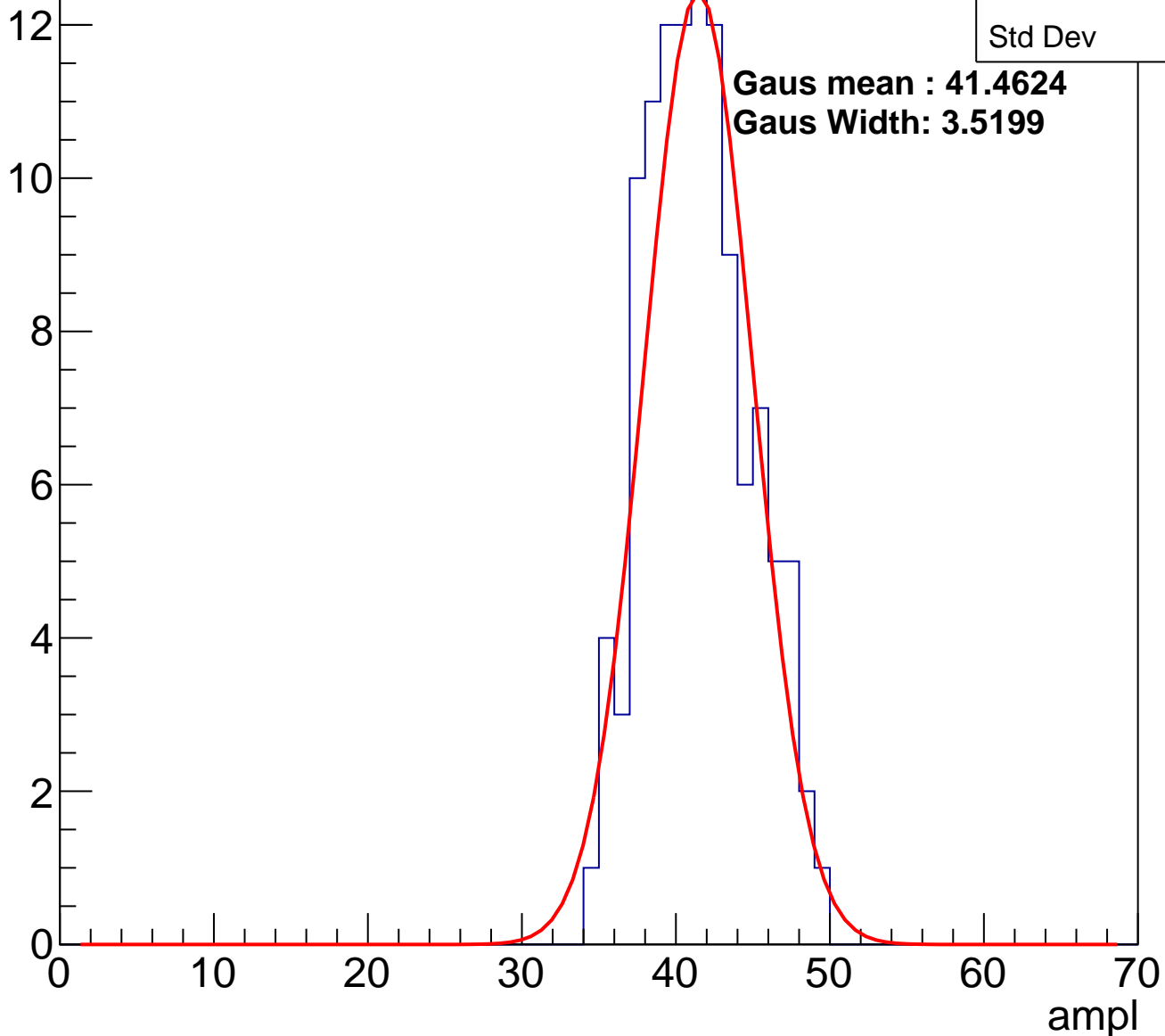
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	40.98
Std Dev	3.35

Gaus mean : 41.4624

Gaus Width: 3.5199

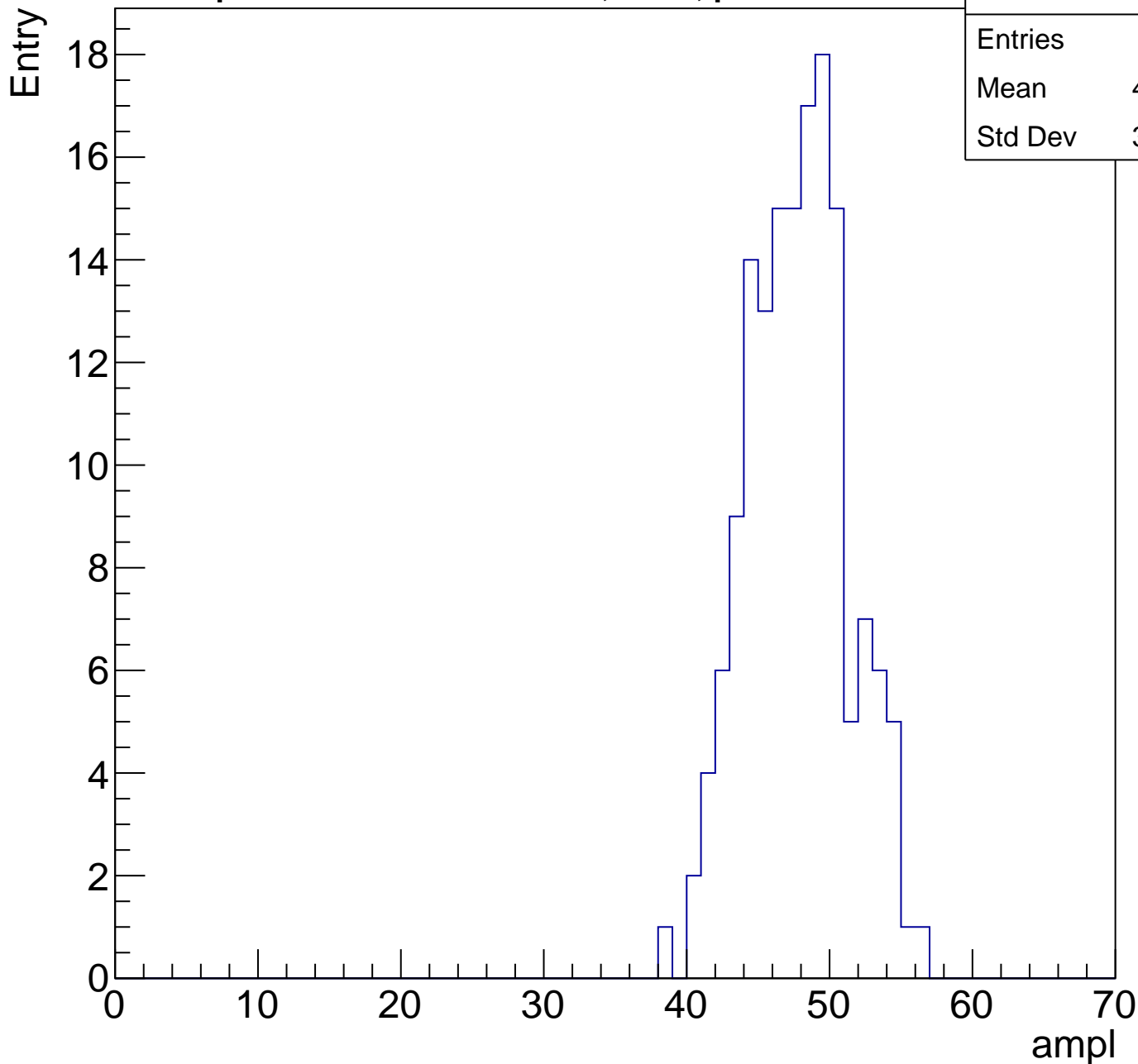
Entry



B1L001S, U14-ch40, adc3

calib_packv5_042523_0143.root, FC#2, port C2

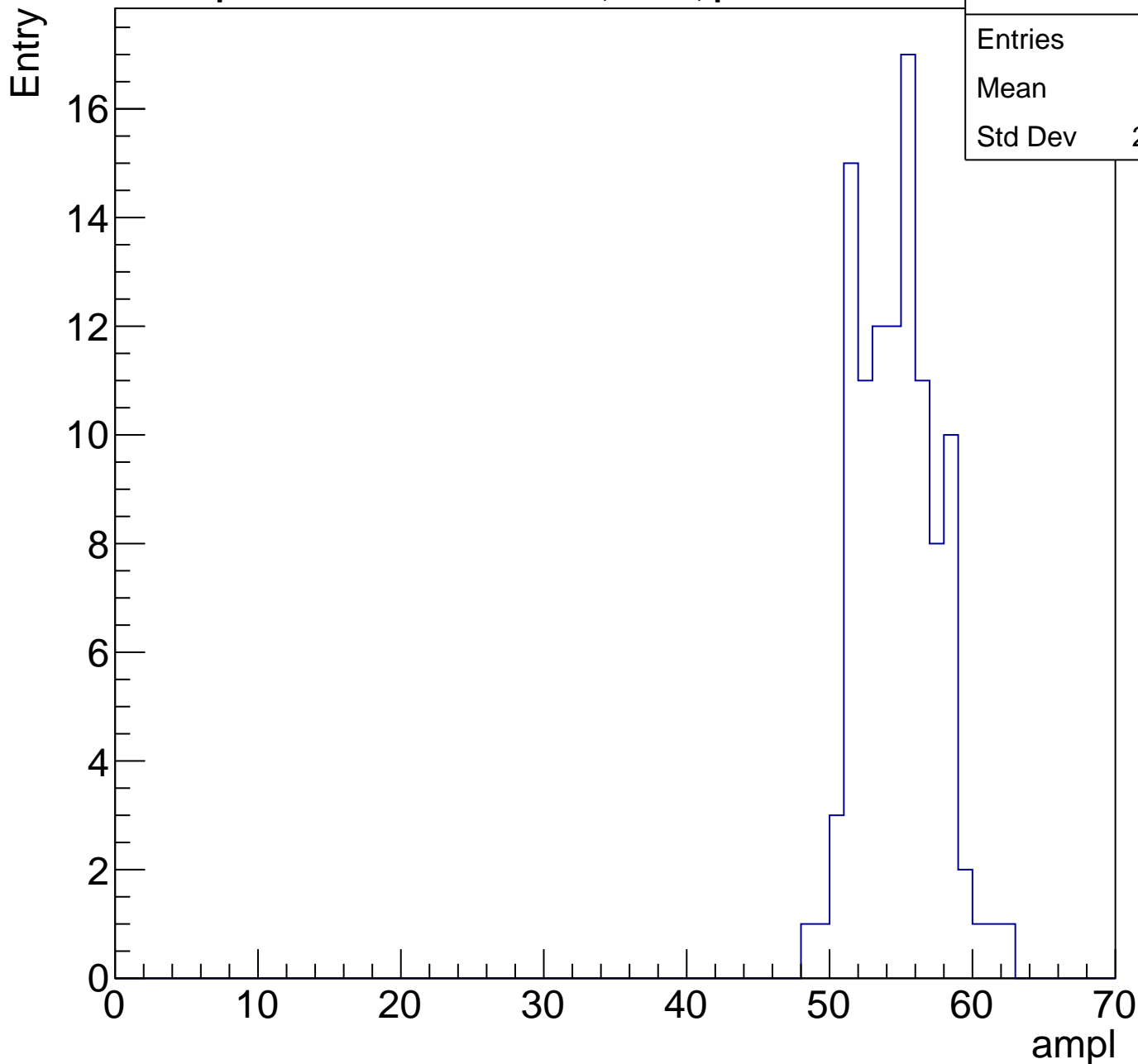
Entries	154
Mean	47.29
Std Dev	3.496



B1L001S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	54.3
Std Dev	2.717



B1L001S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	58.36
Std Dev	6.476

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

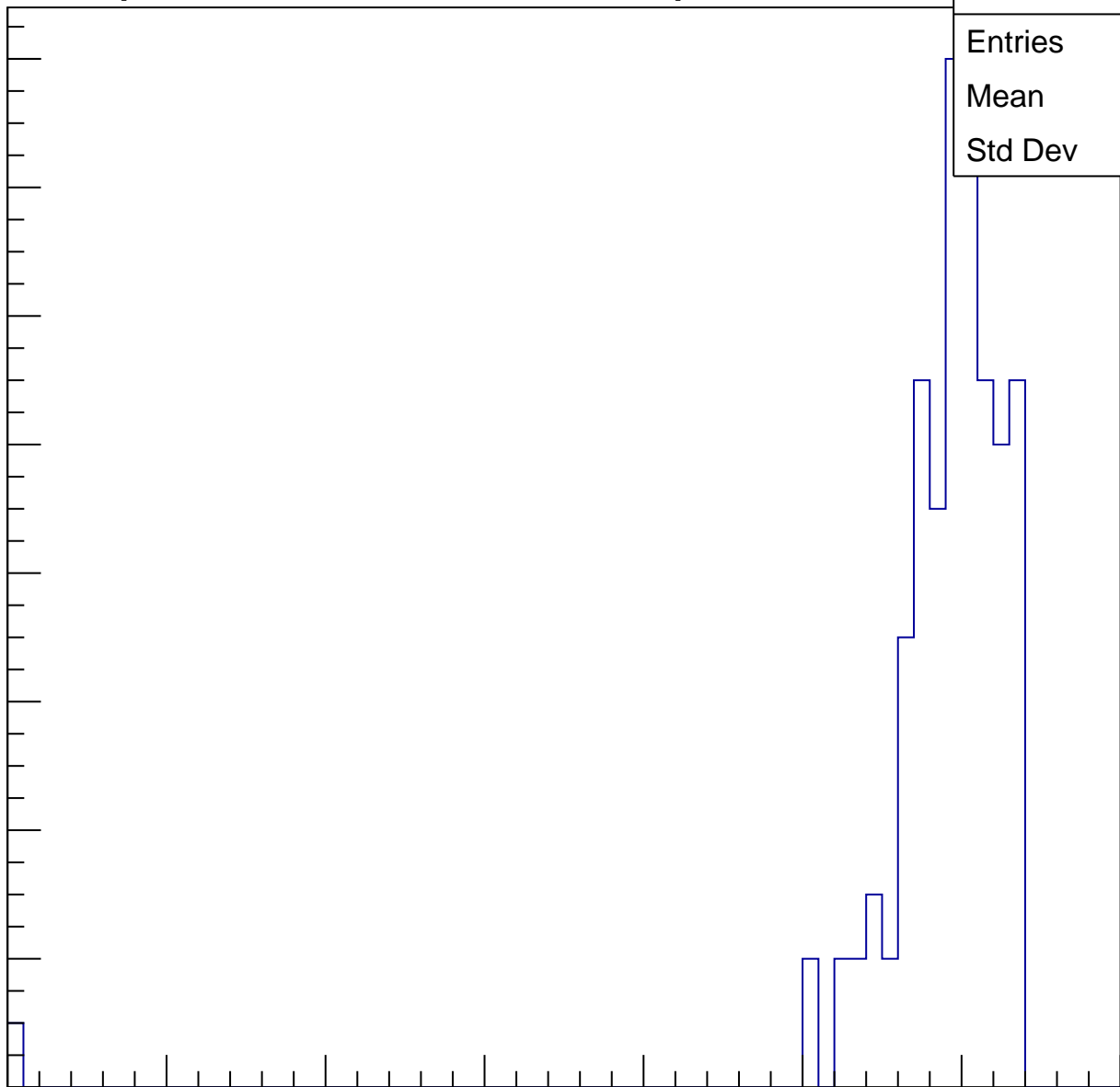
40

50

60

70

ampl

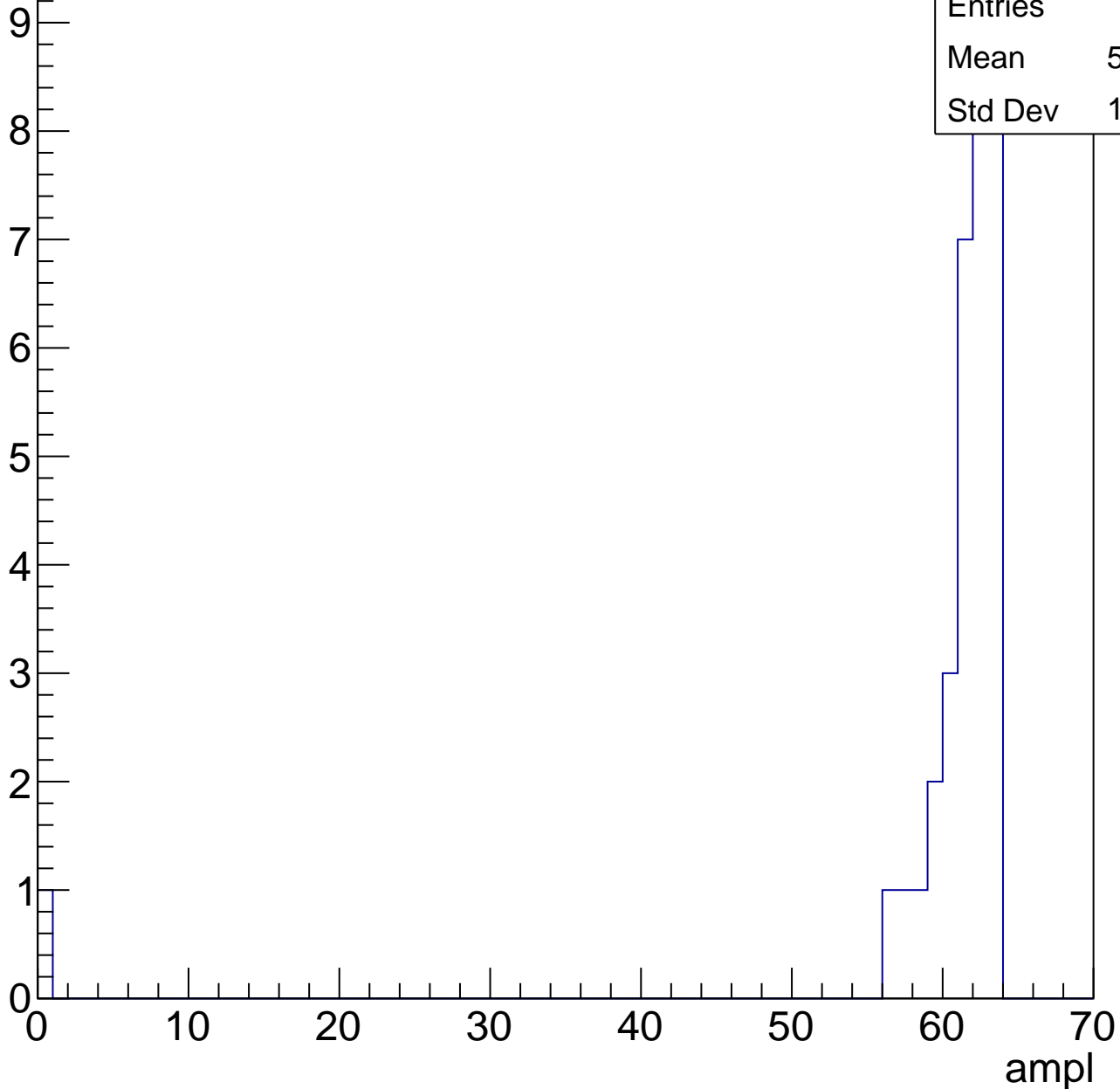


B1L001S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

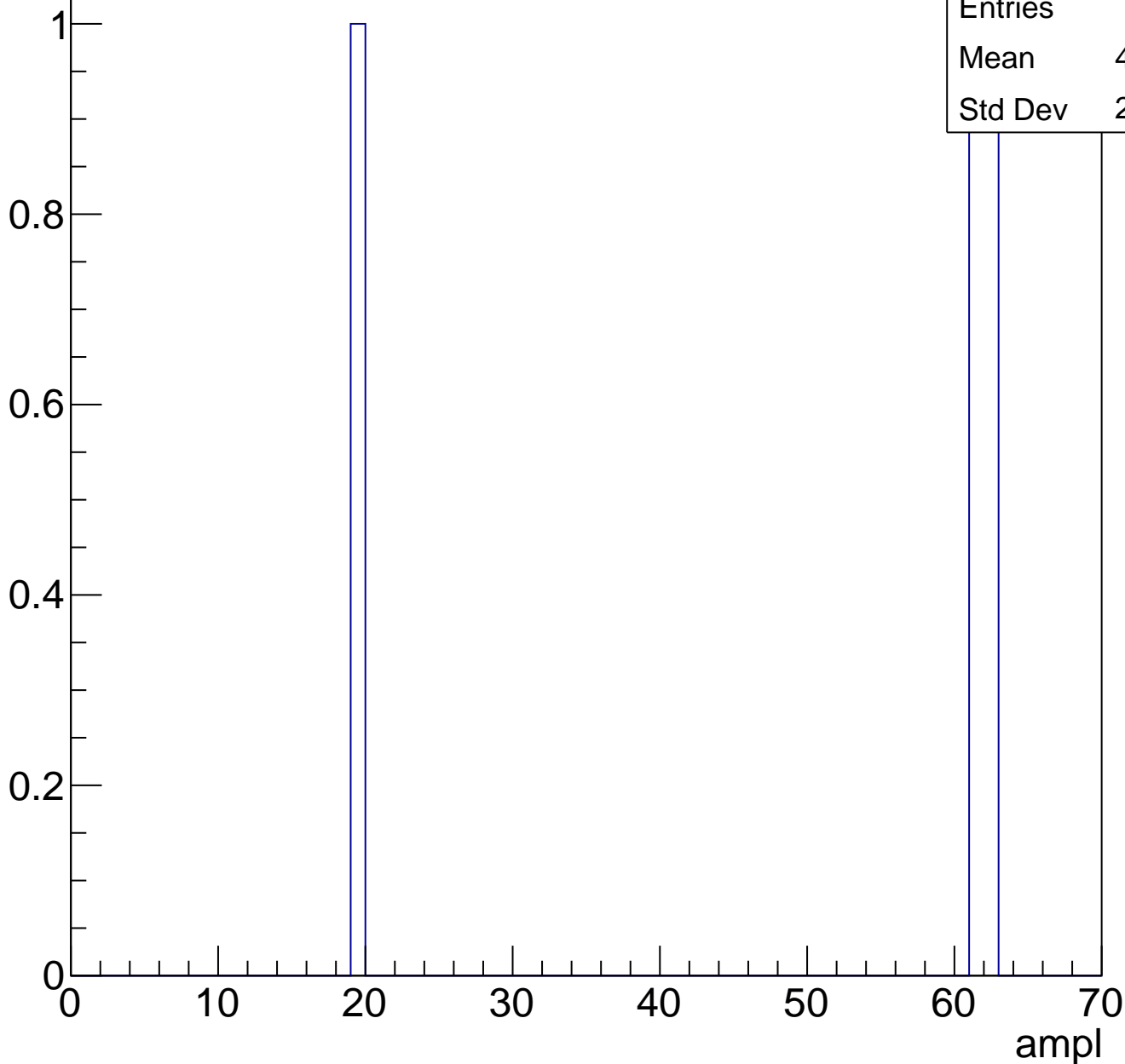
Entries	33
Mean	59.36
Std Dev	10.64



B1L001S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch41, adc0

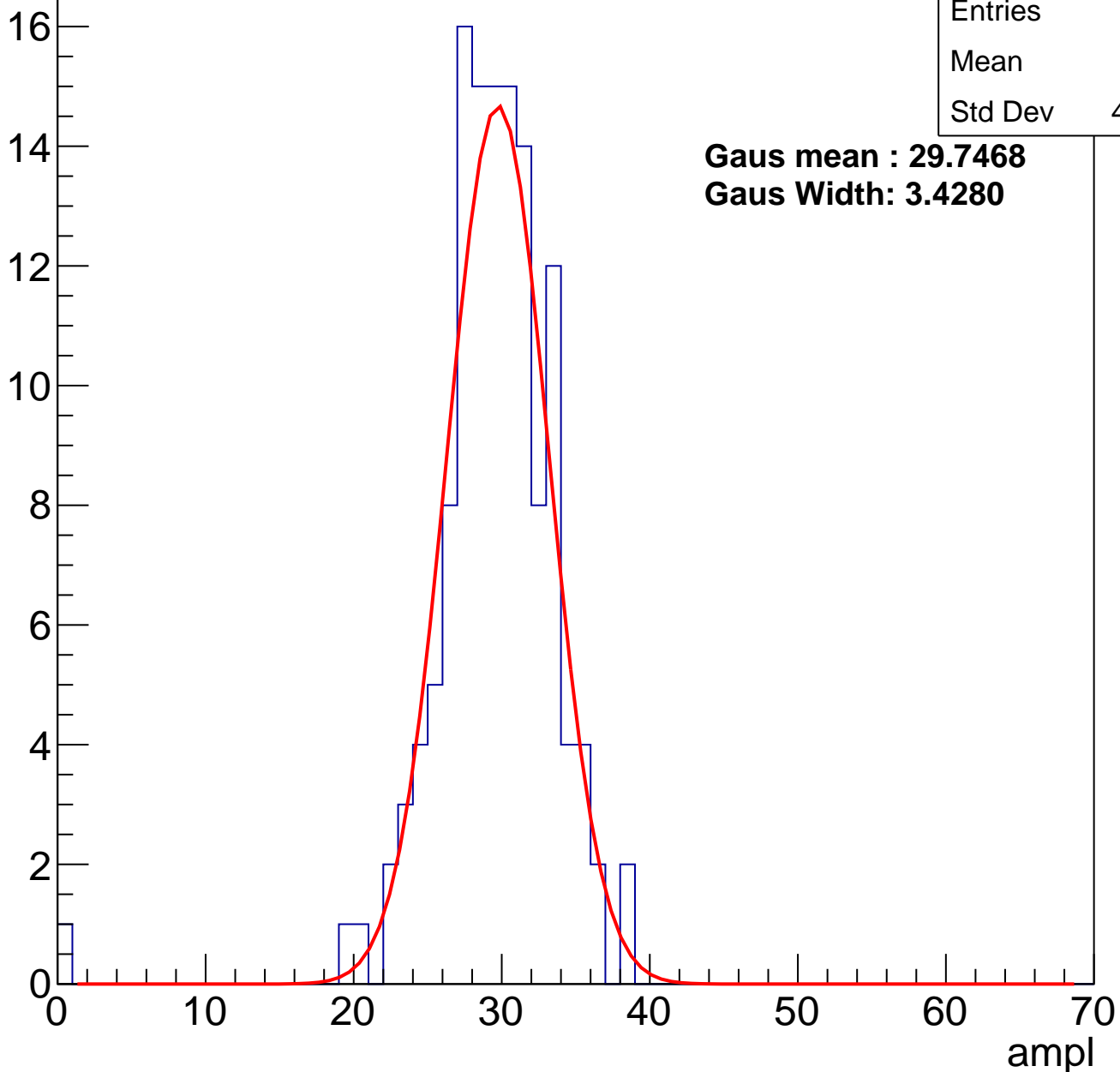
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	29
Std Dev	4.262

Gaus mean : 29.7468

Gaus Width: 3.4280

Entry



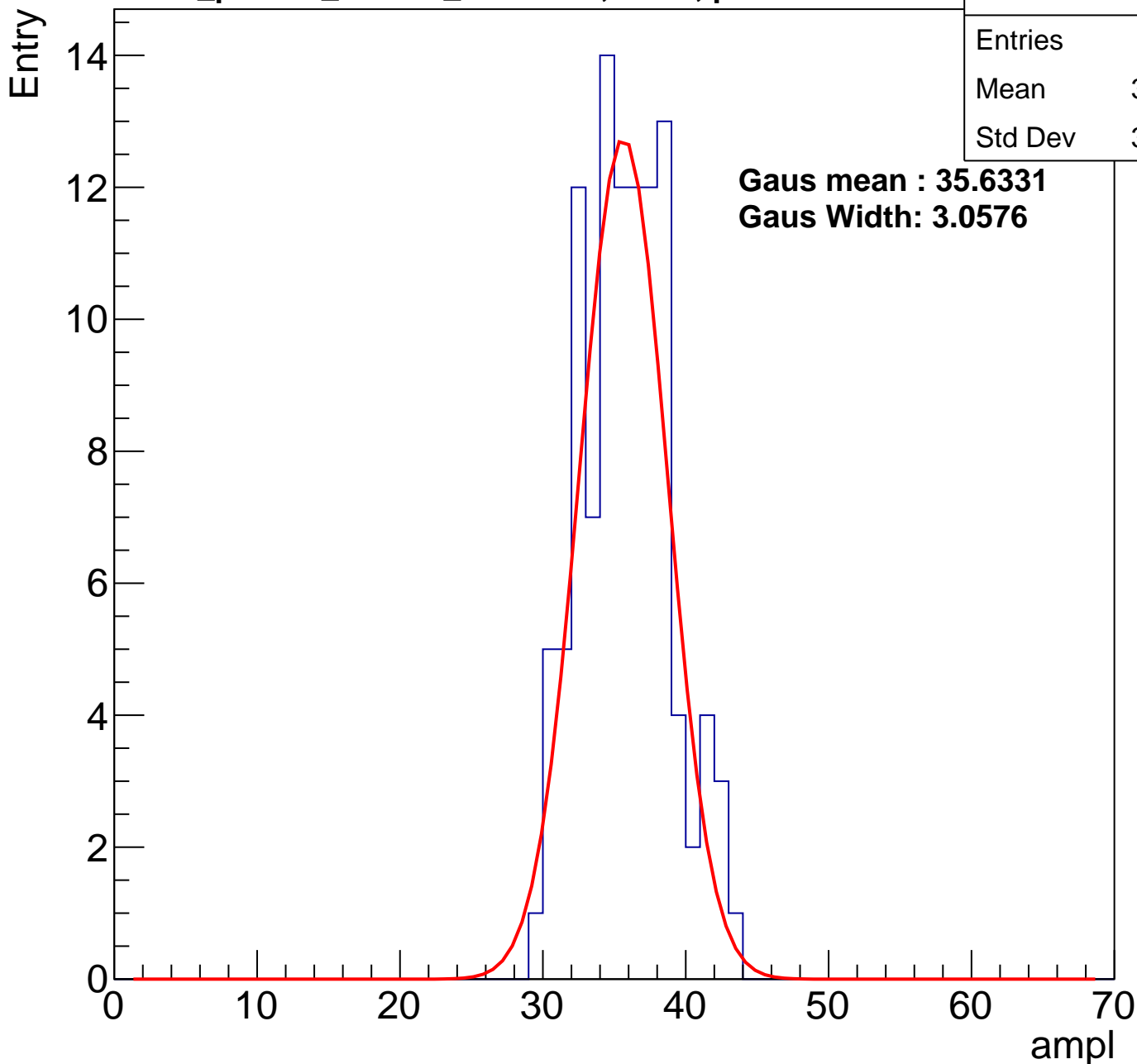
B1L001S, U14-ch41, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	35.36
Std Dev	3.092

Gaus mean : 35.6331

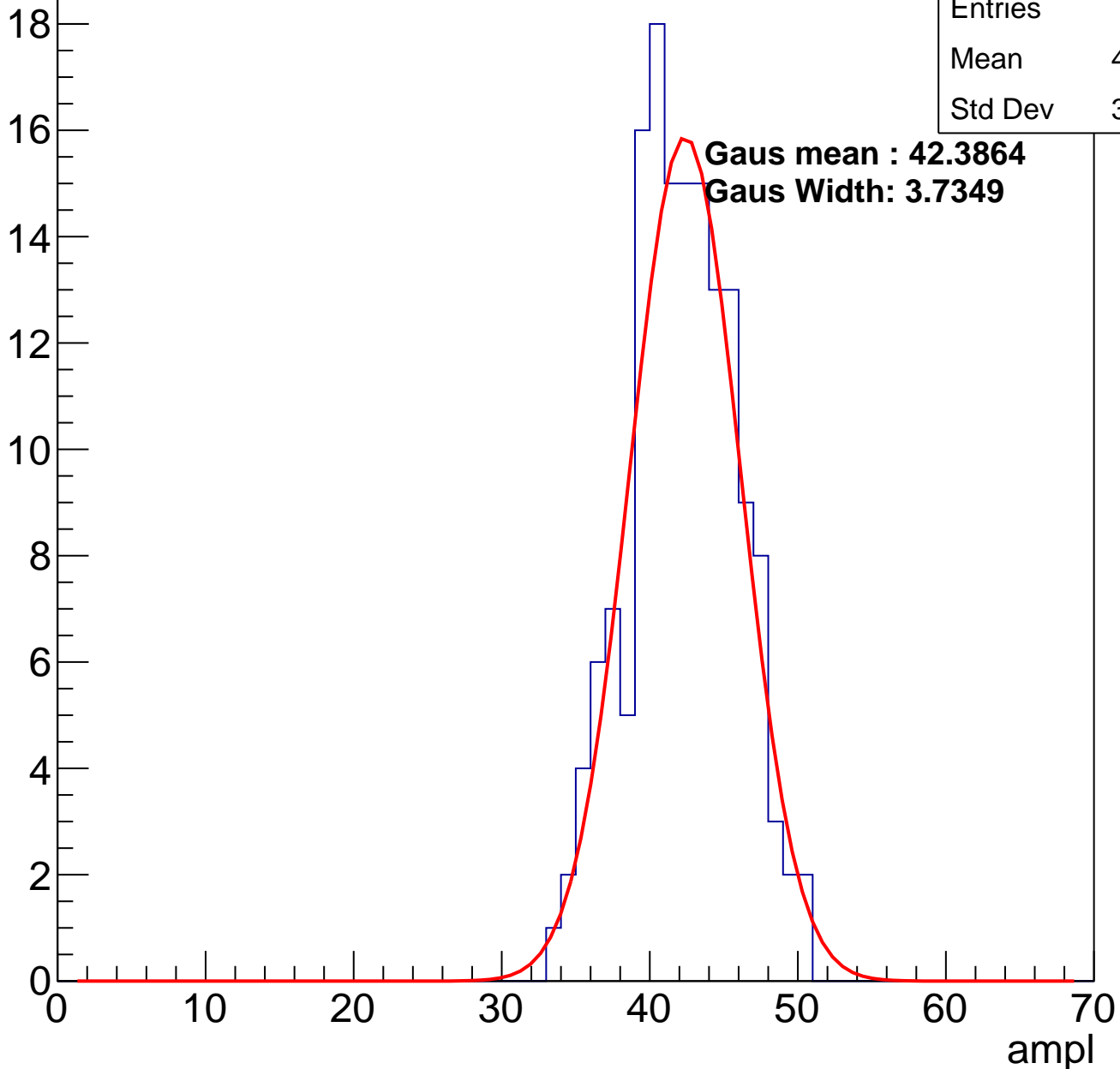
Gaus Width: 3.0576



B1L001S, U14-ch41, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch41, adc3

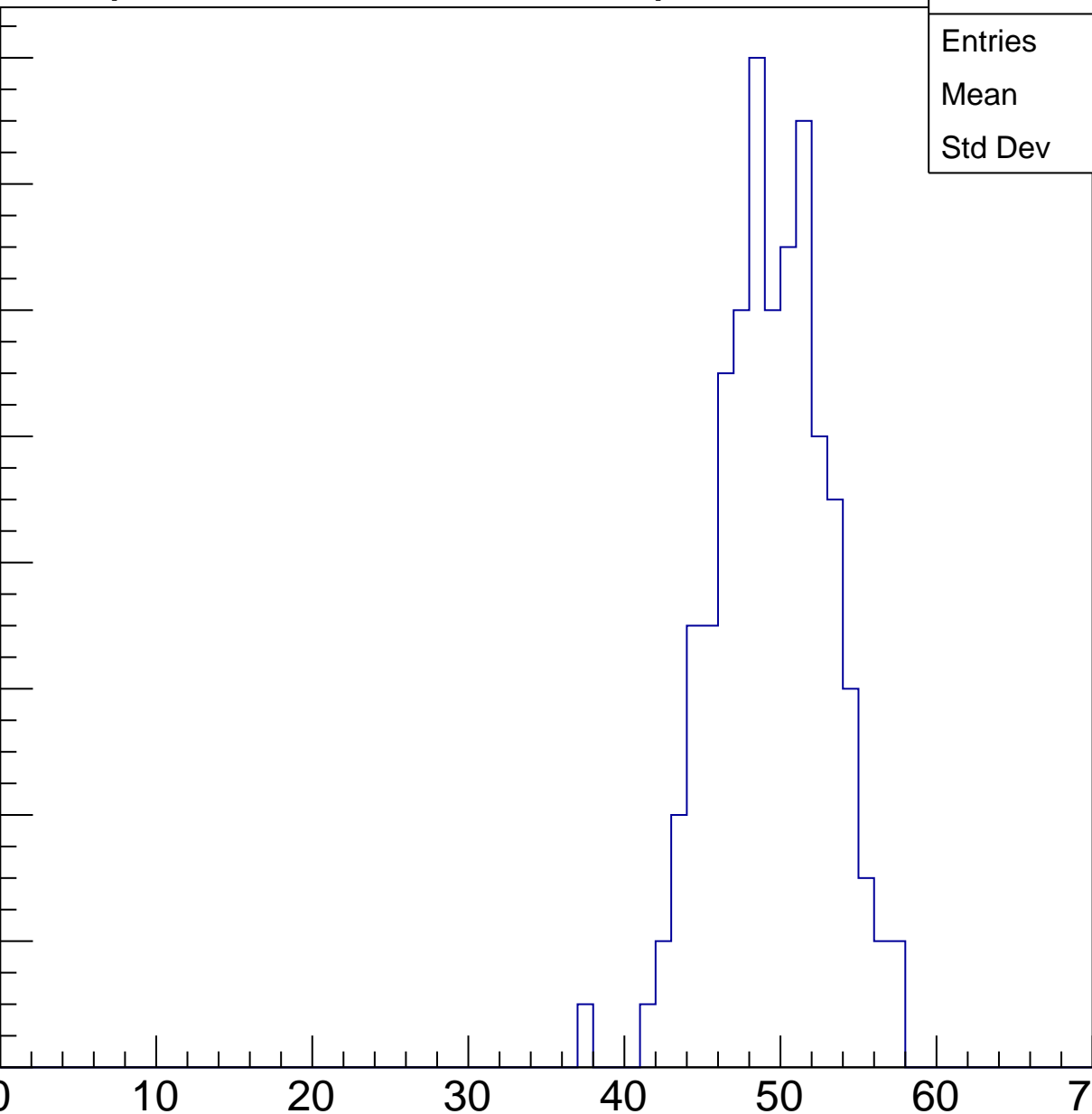
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	133
Mean	48.95
Std Dev	3.565

ampl

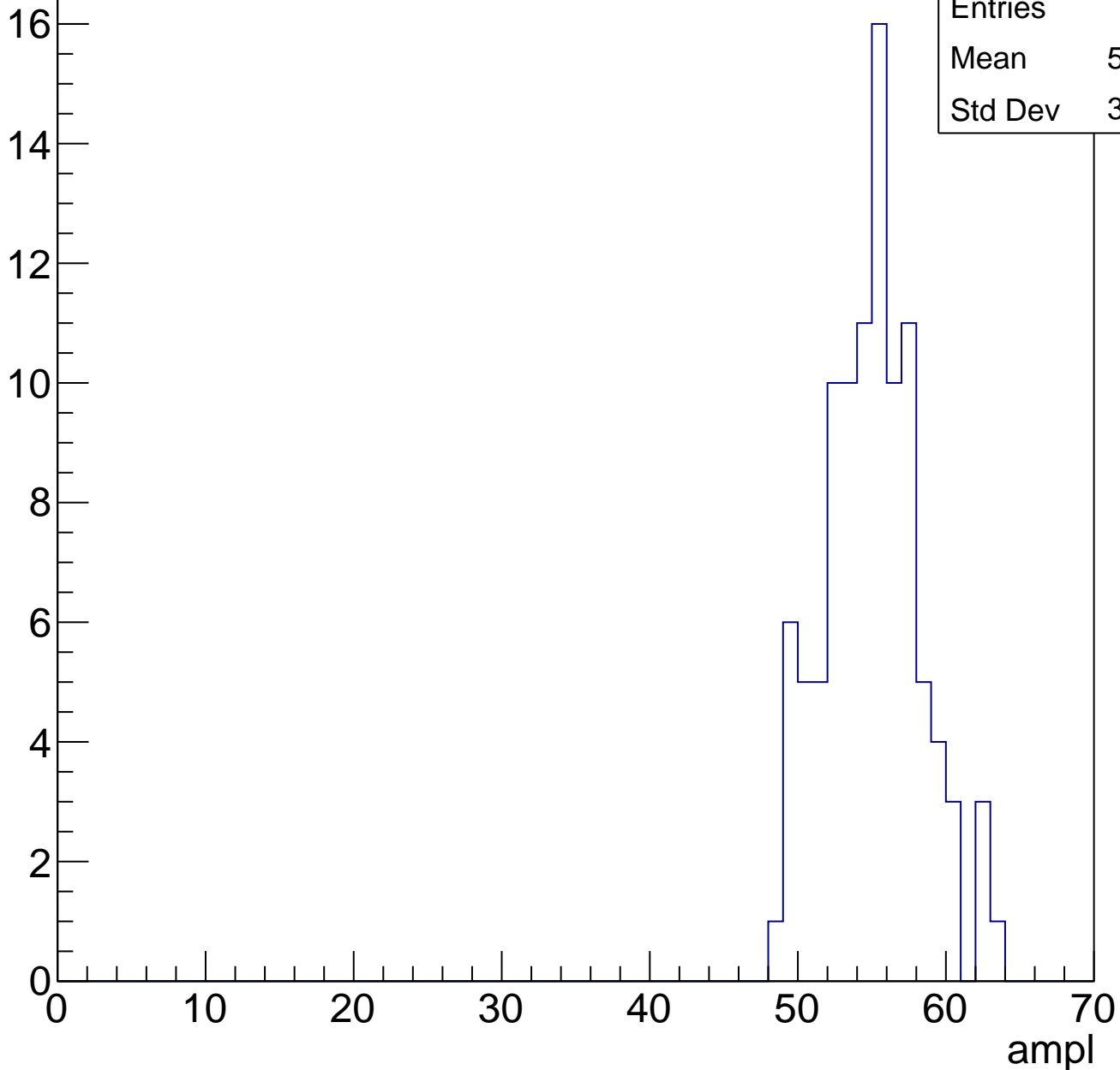


B1L001S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	54.58
Std Dev	3.213

Entry



B1L001S, U14-ch41, adc5

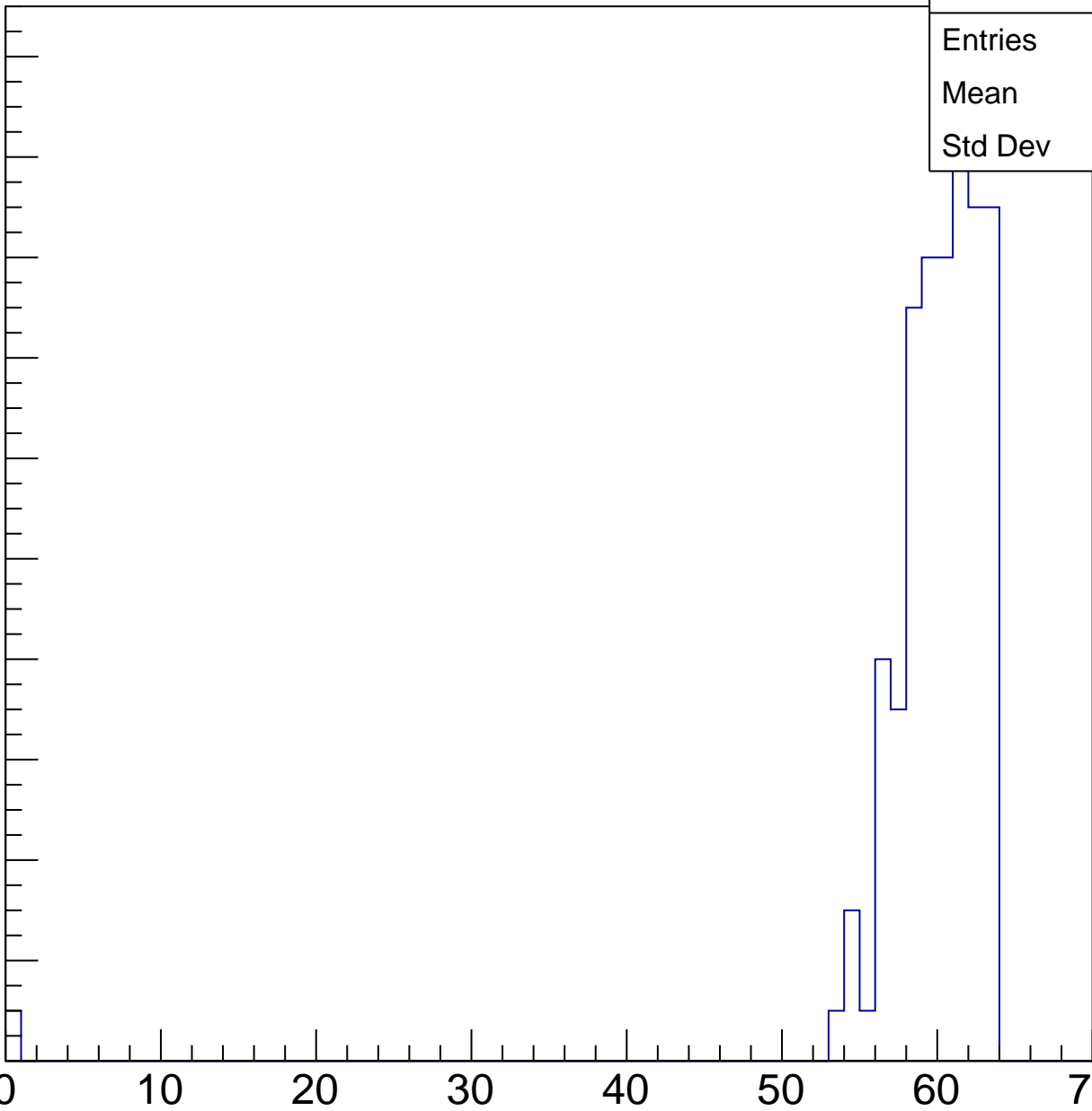
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	122
Mean	59.31
Std Dev	5.89

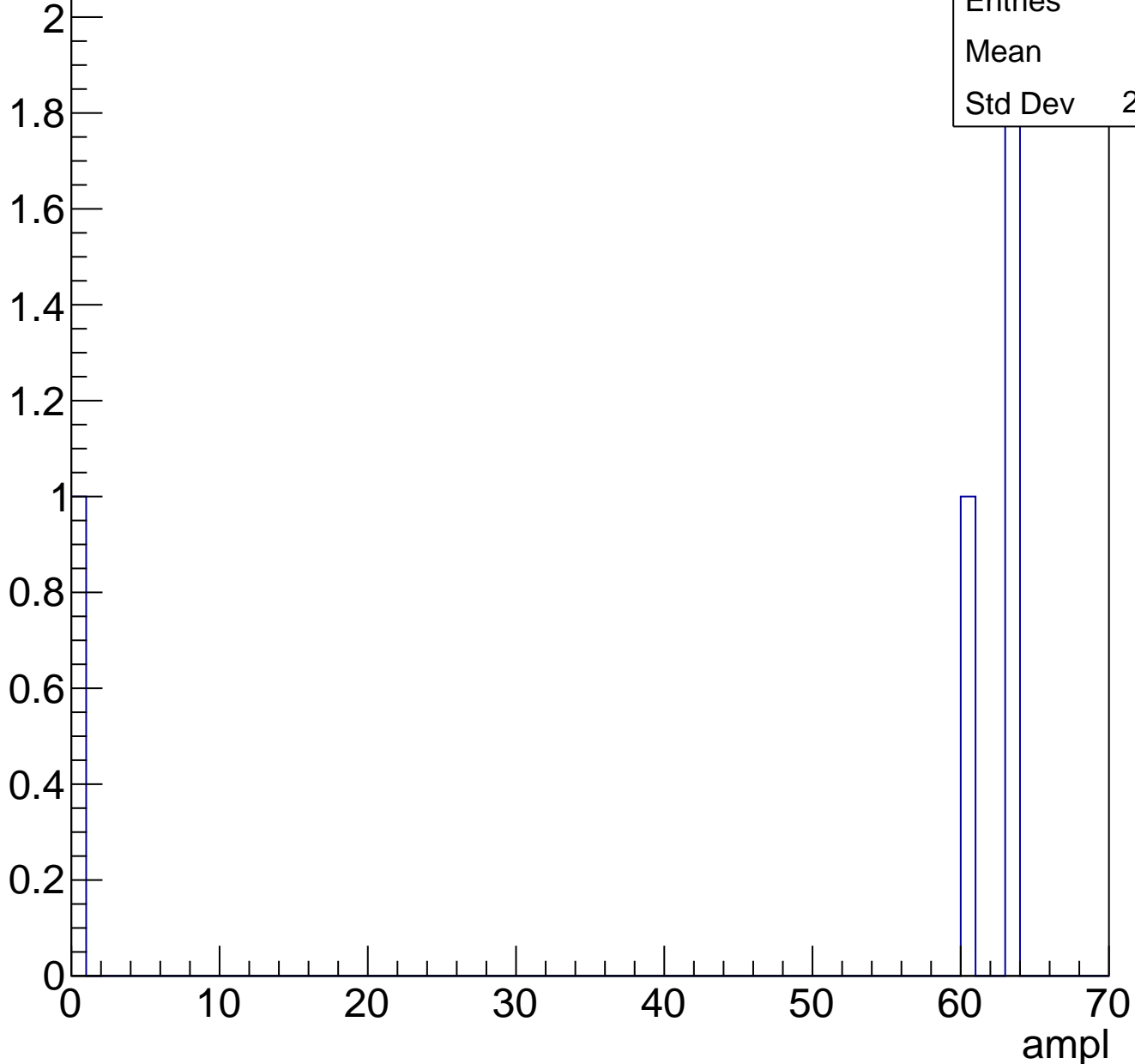
ampl



B1L001S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



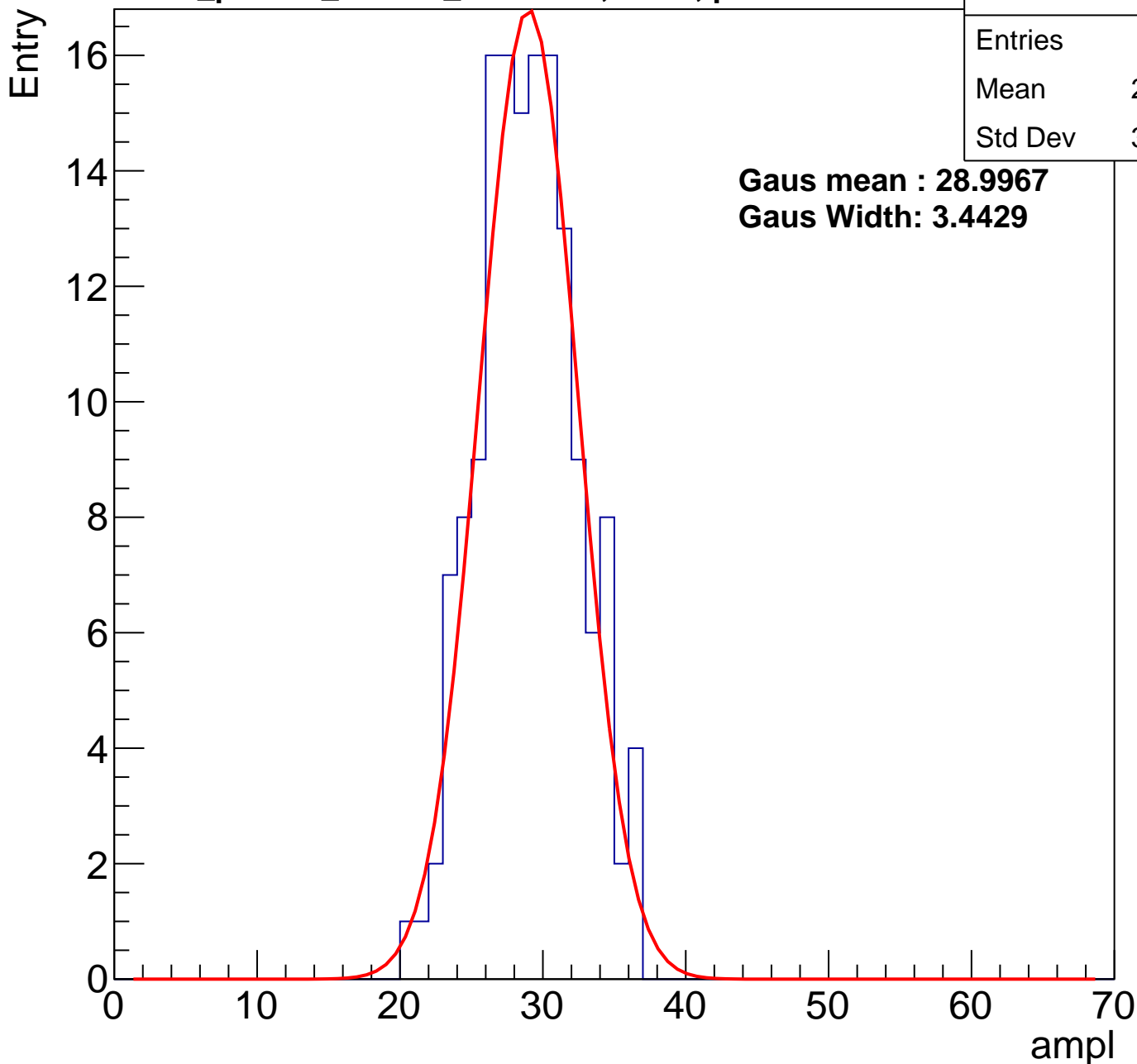
B1L001S, U14-ch42, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	28.52
Std Dev	3.418

Gaus mean : 28.9967

Gaus Width: 3.4429



B1L001S, U14-ch42, adc1

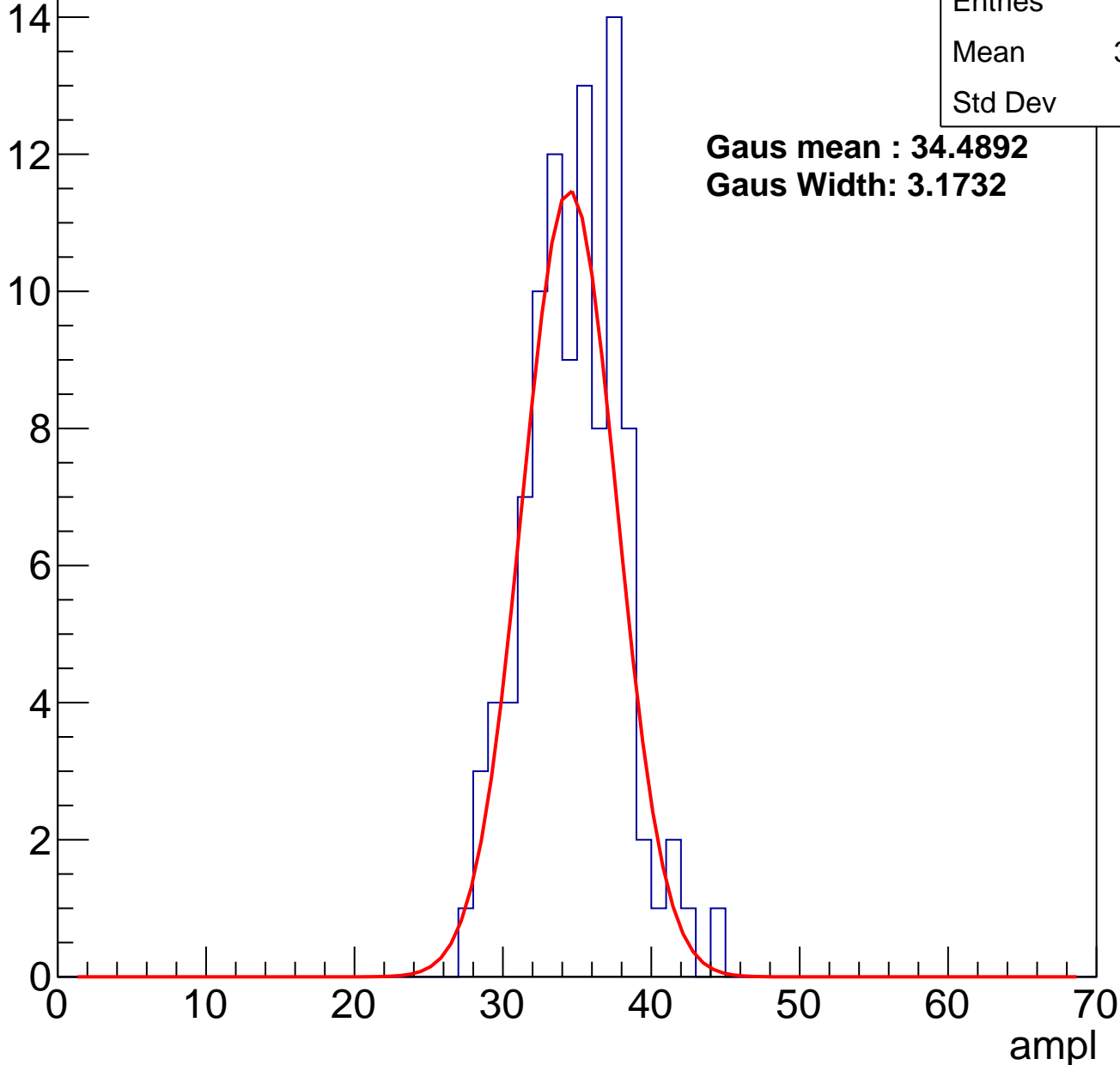
calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	34.37
Std Dev	3.27

Gaus mean : 34.4892

Gaus Width: 3.1732

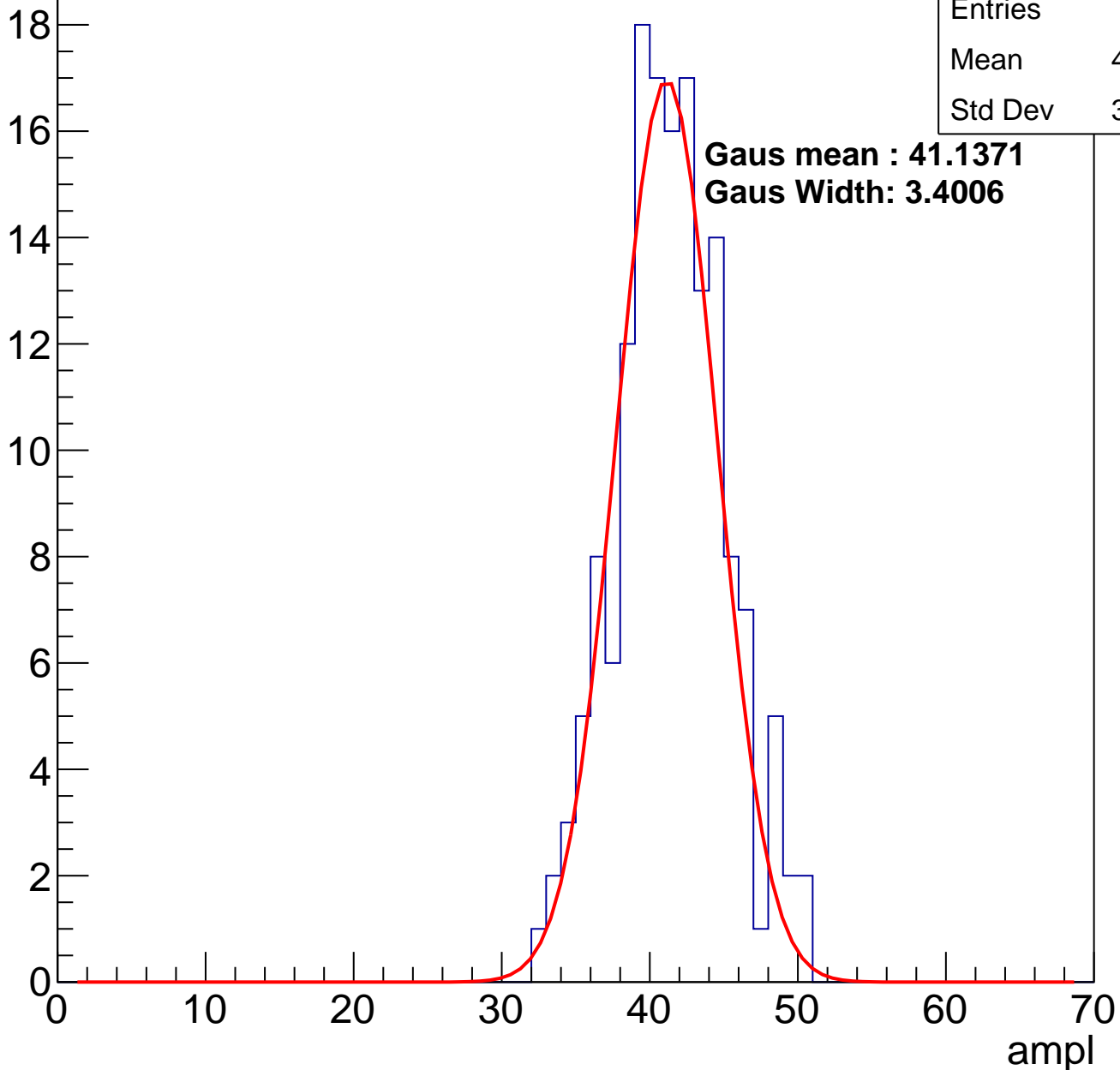
Entry



B1L001S, U14-ch42, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch42, adc3

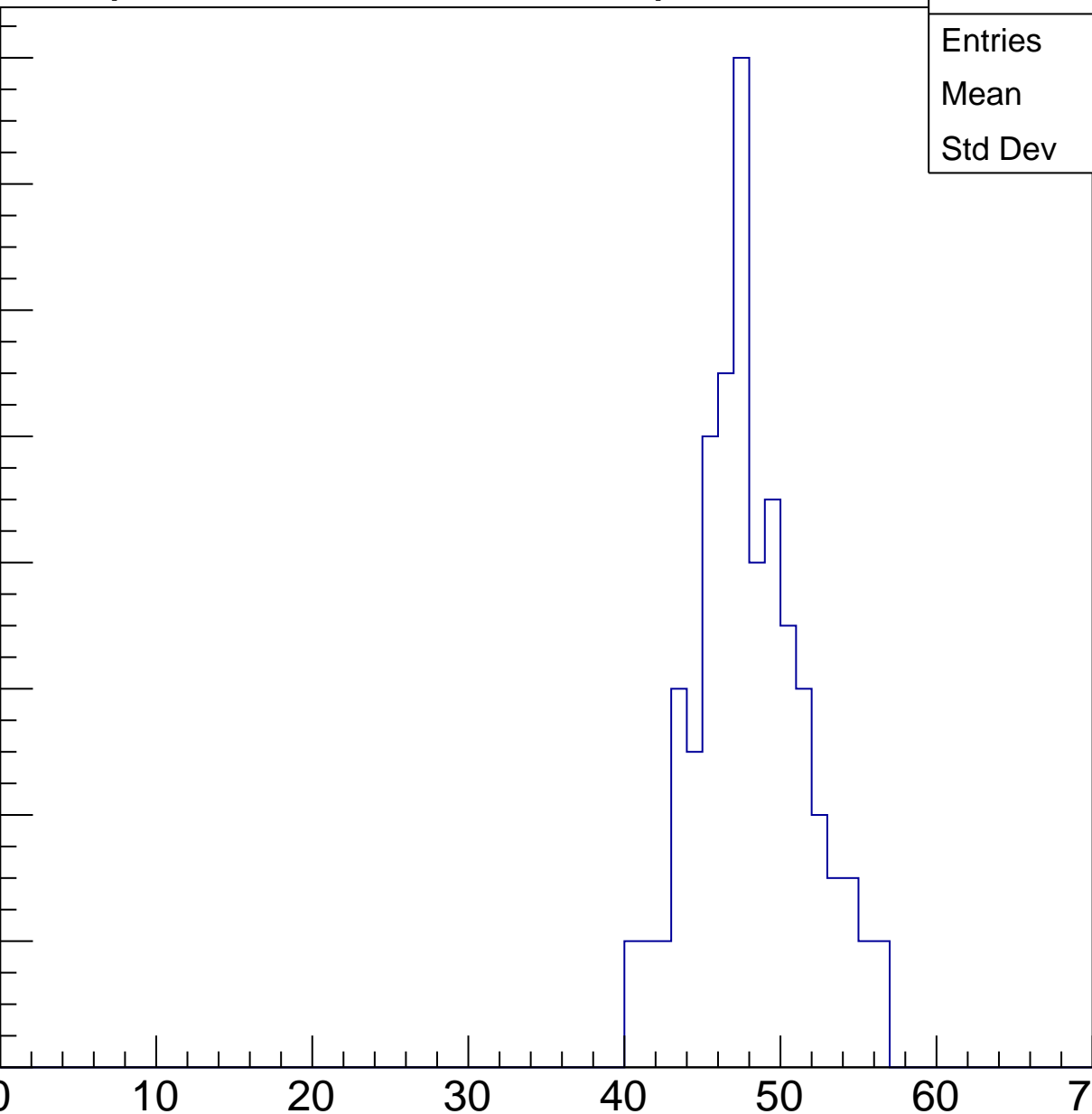
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	98
Mean	47.59
Std Dev	3.537

ampl



B1L001S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#2, port C2

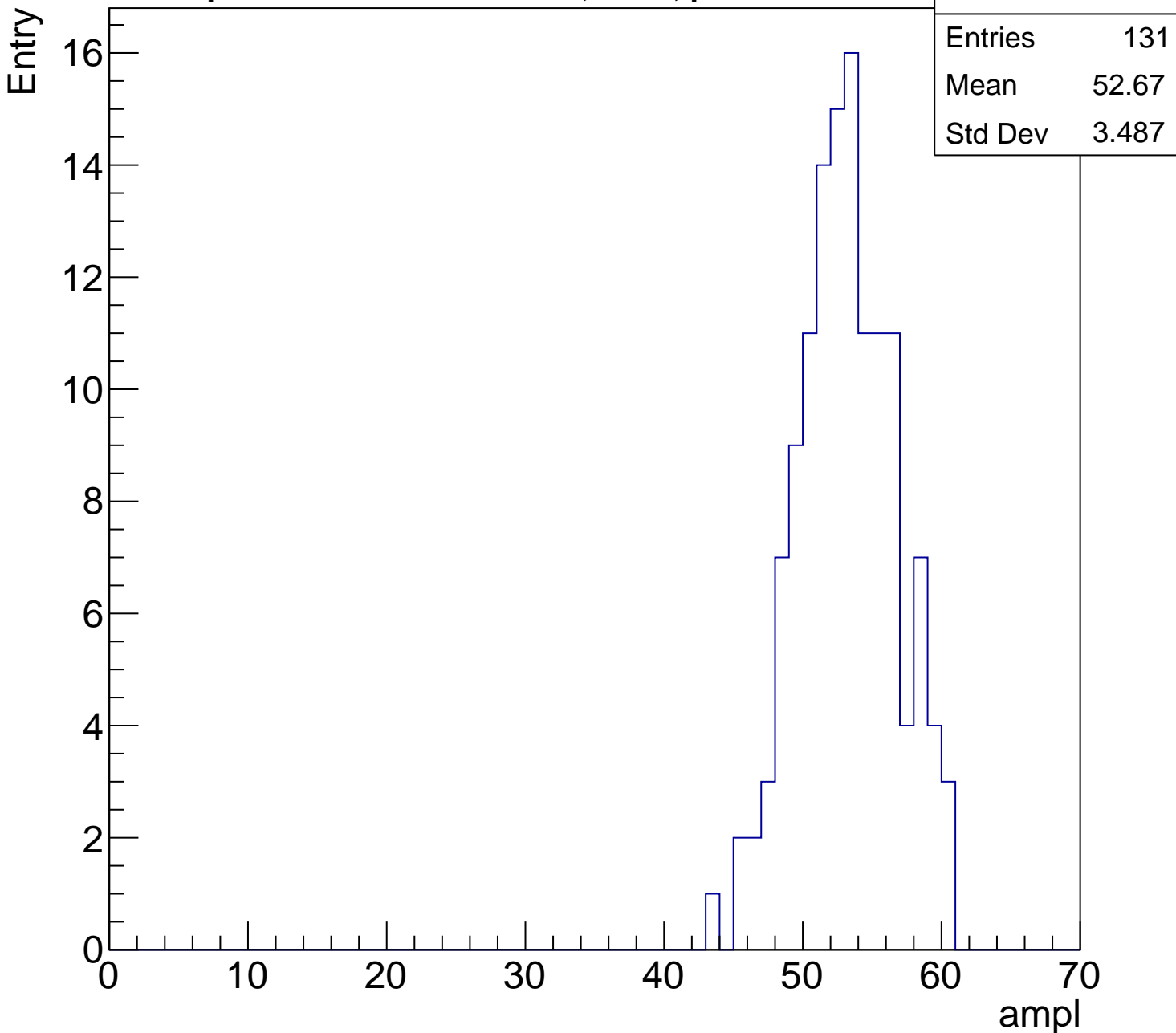
Entries	131
Mean	52.67
Std Dev	3.487

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch42, adc5

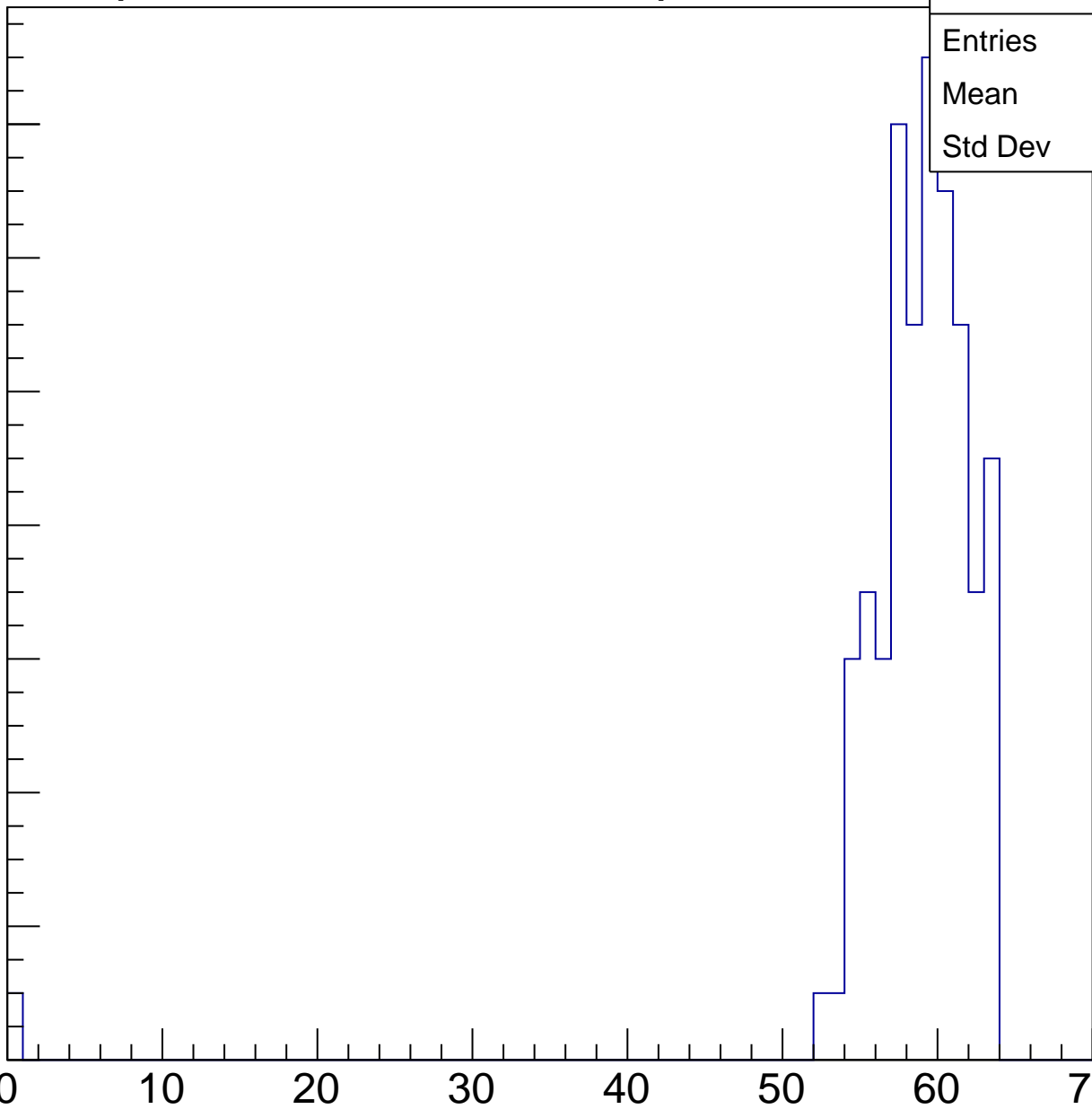
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	102
Mean	58.07
Std Dev	6.359

ampl



B1L001S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0 10 20 30 40 50 60 70

ampl

Entries	44
Mean	59.84
Std Dev	9.296

B1L001S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch43, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	28.91
Std Dev	3.831

Gaus mean : 29.5214

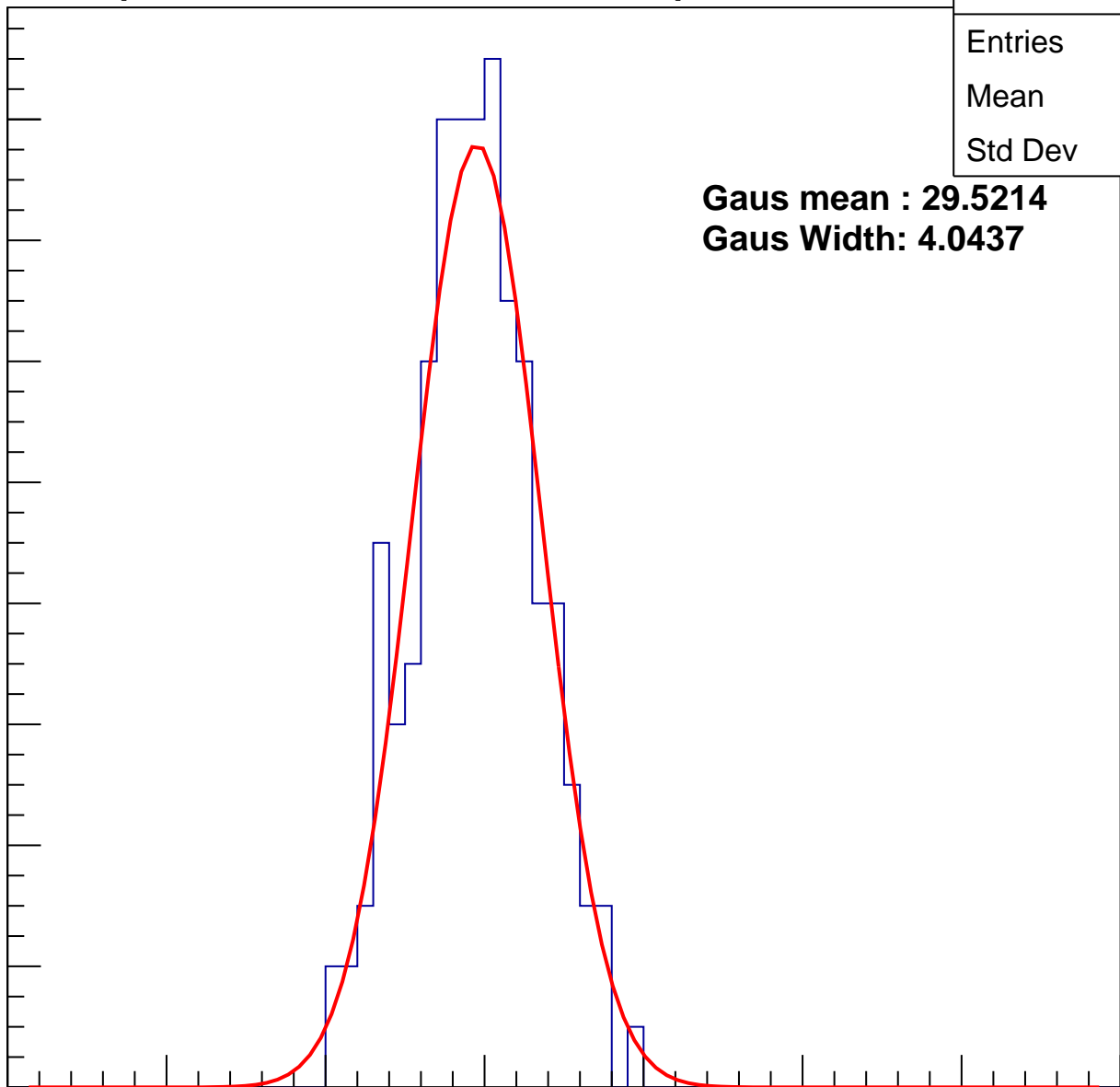
Gaus Width: 4.0437

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch43, adc1

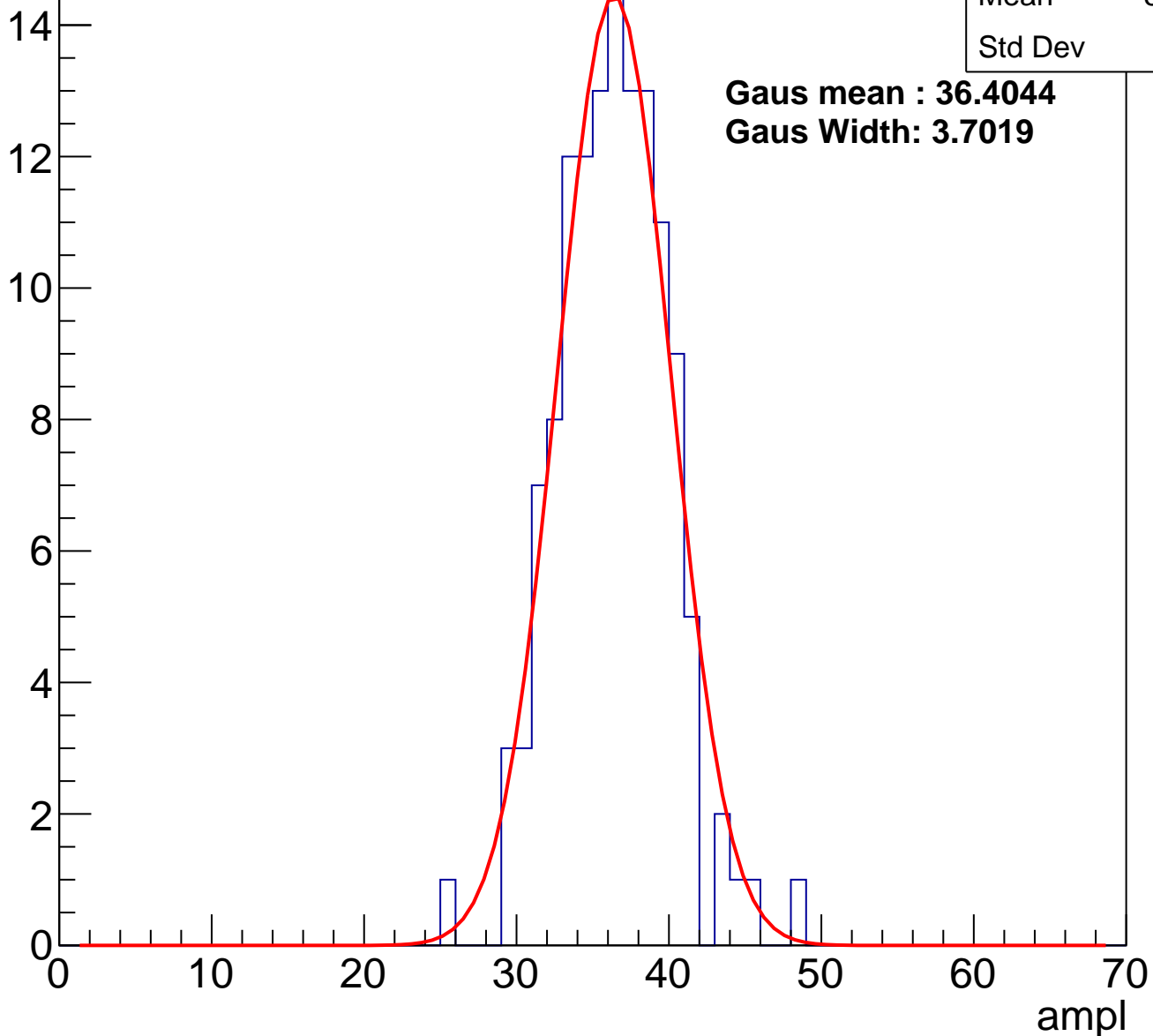
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	35.89
Std Dev	3.58

Gaus mean : 36.4044

Gaus Width: 3.7019

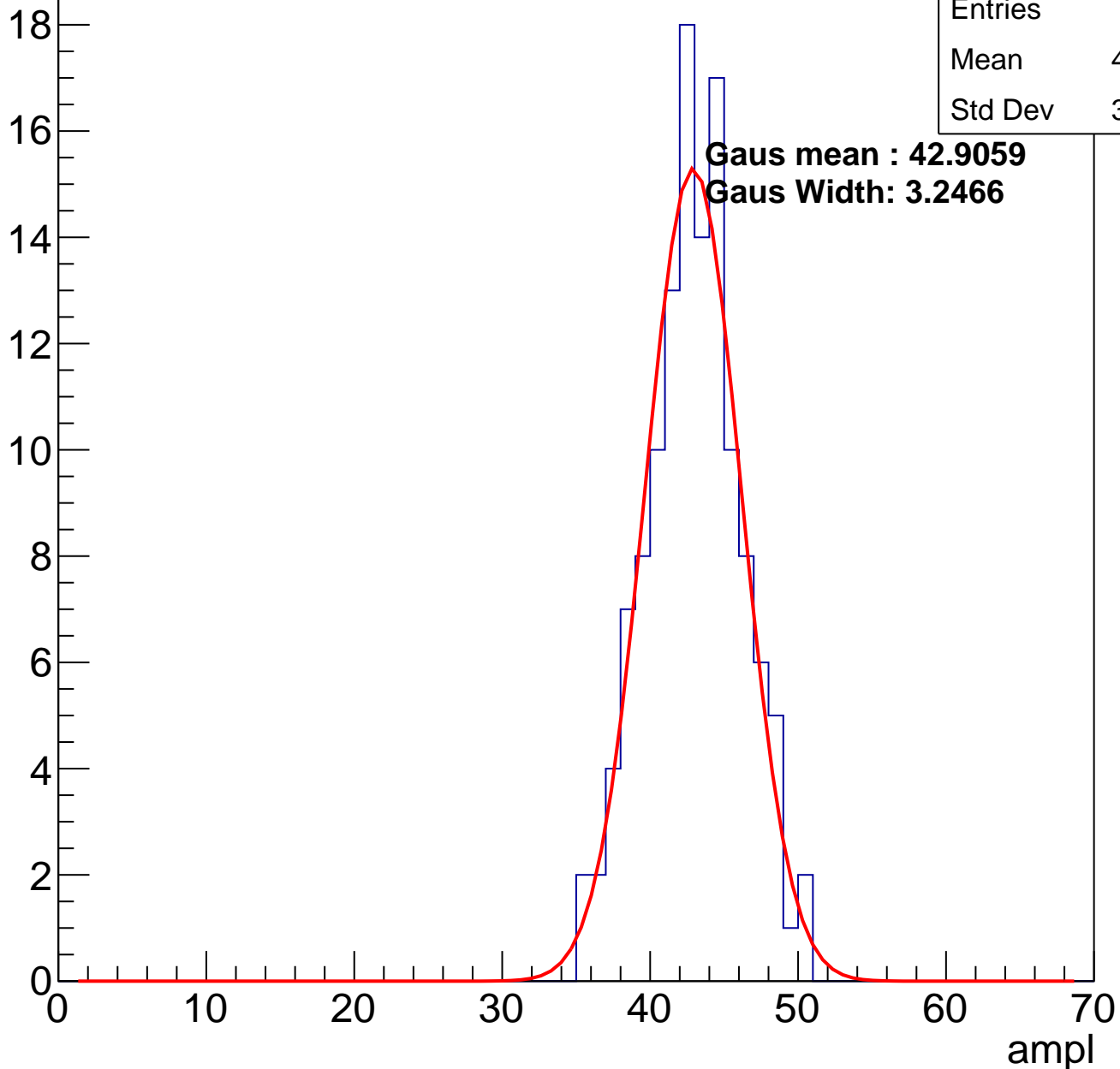
Entry



B1L001S, U14-ch43, adc2

calib_packv5_042523_0143.root, FC#2, port C2

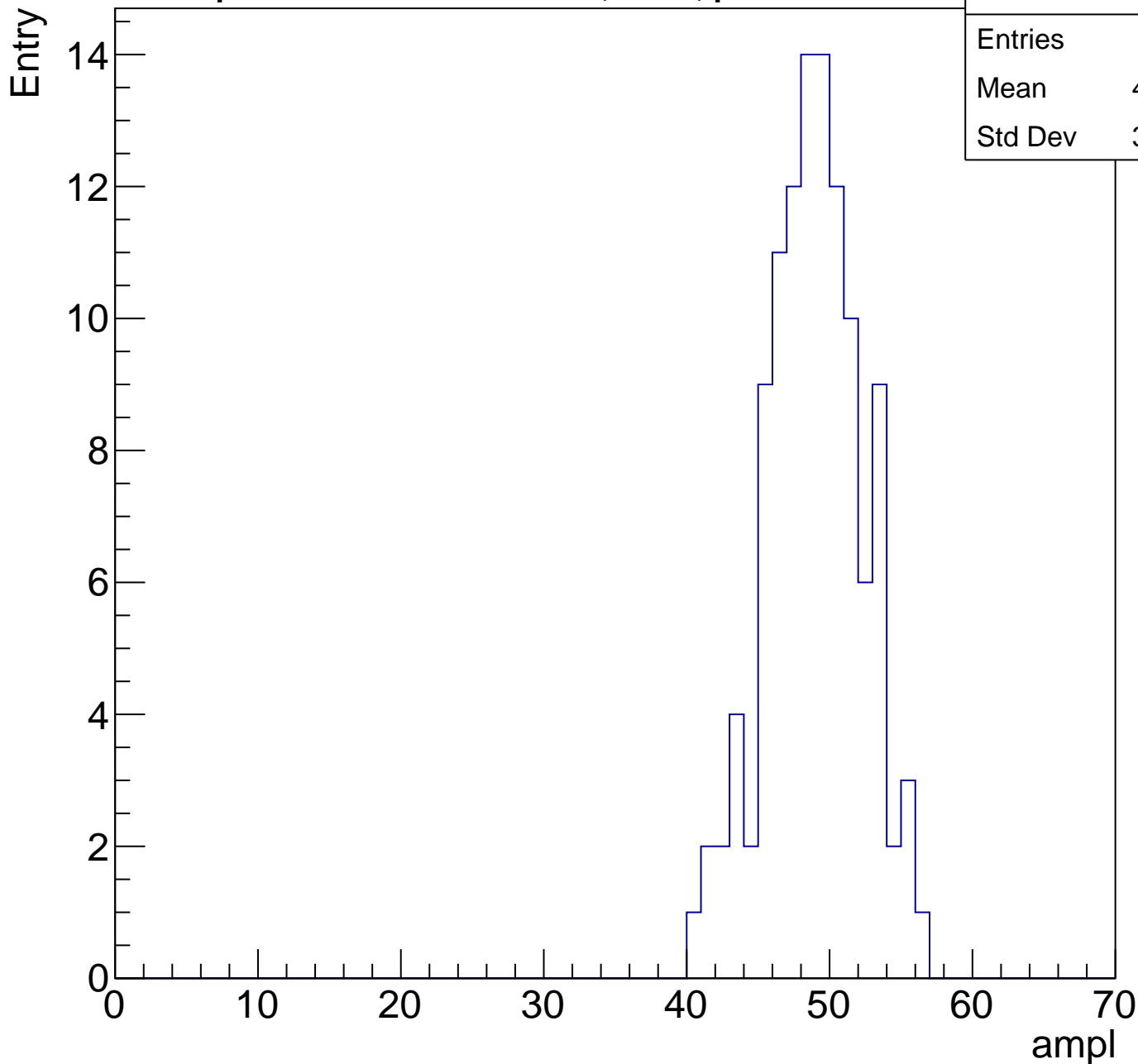
Entry



B1L001S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	48.48
Std Dev	3.288

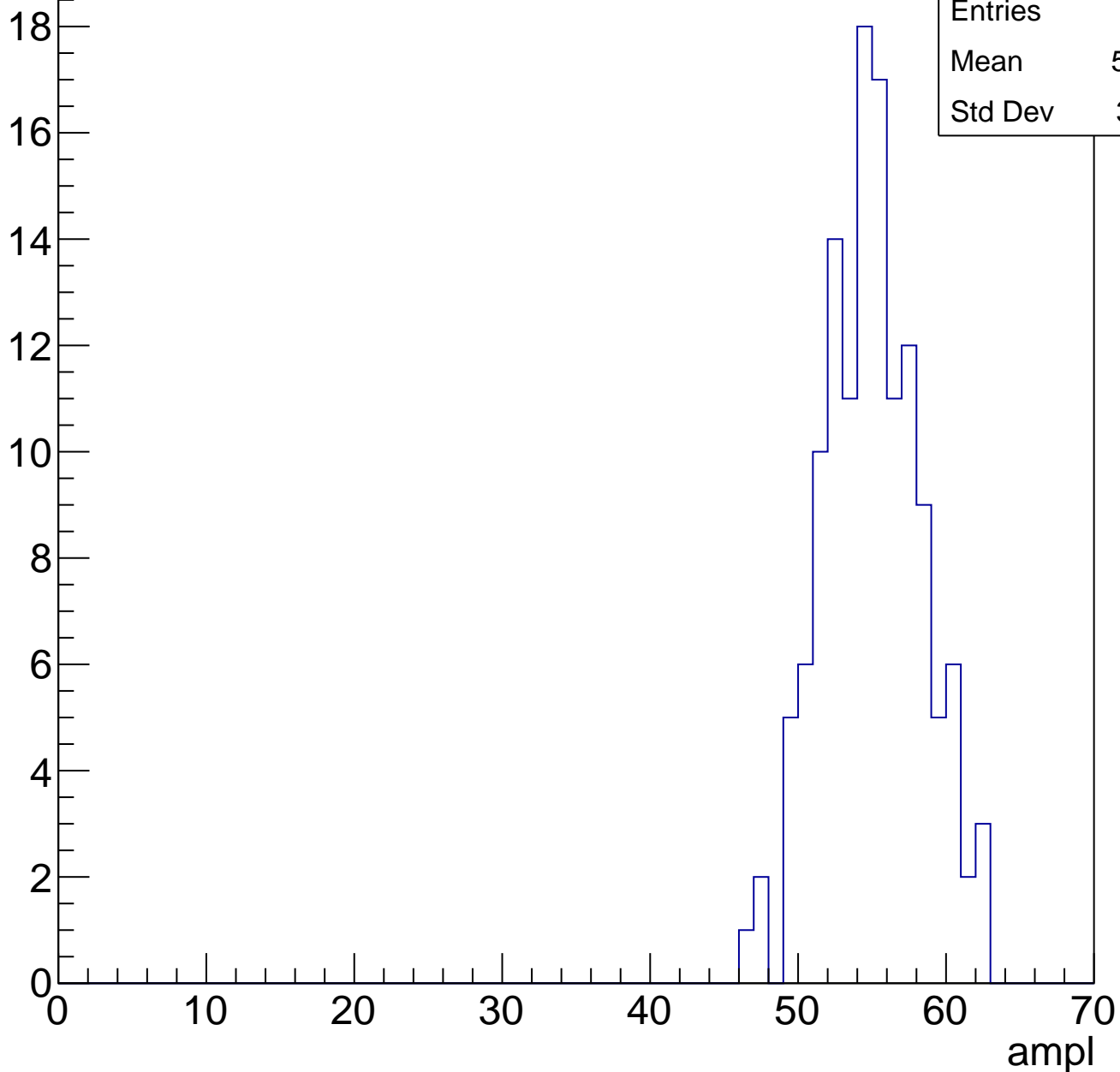


B1L001S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	54.53
Std Dev	3.311

Entry



B1L001S, U14-ch43, adc5

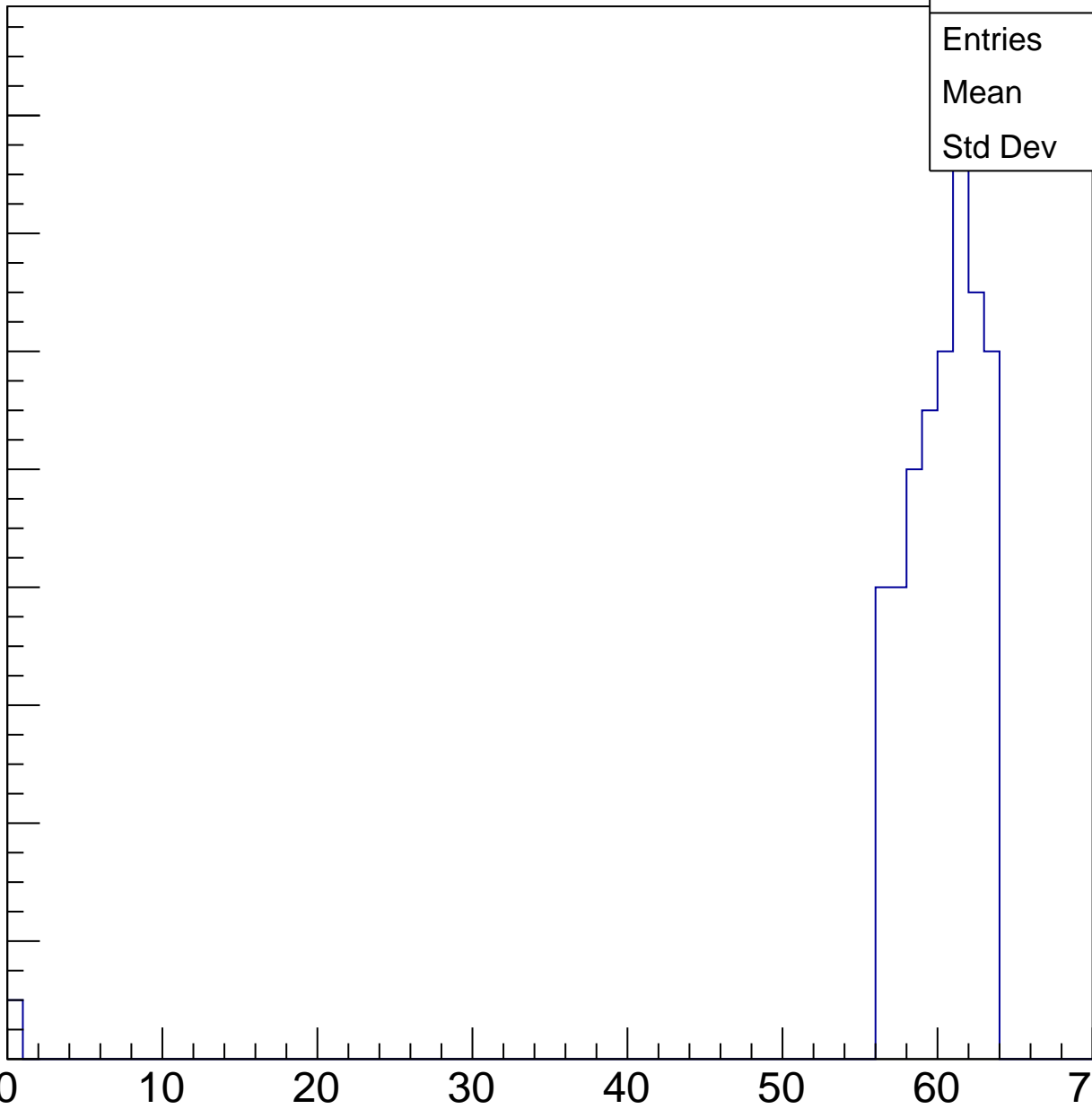
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.26
Std Dev	6.575

ampl

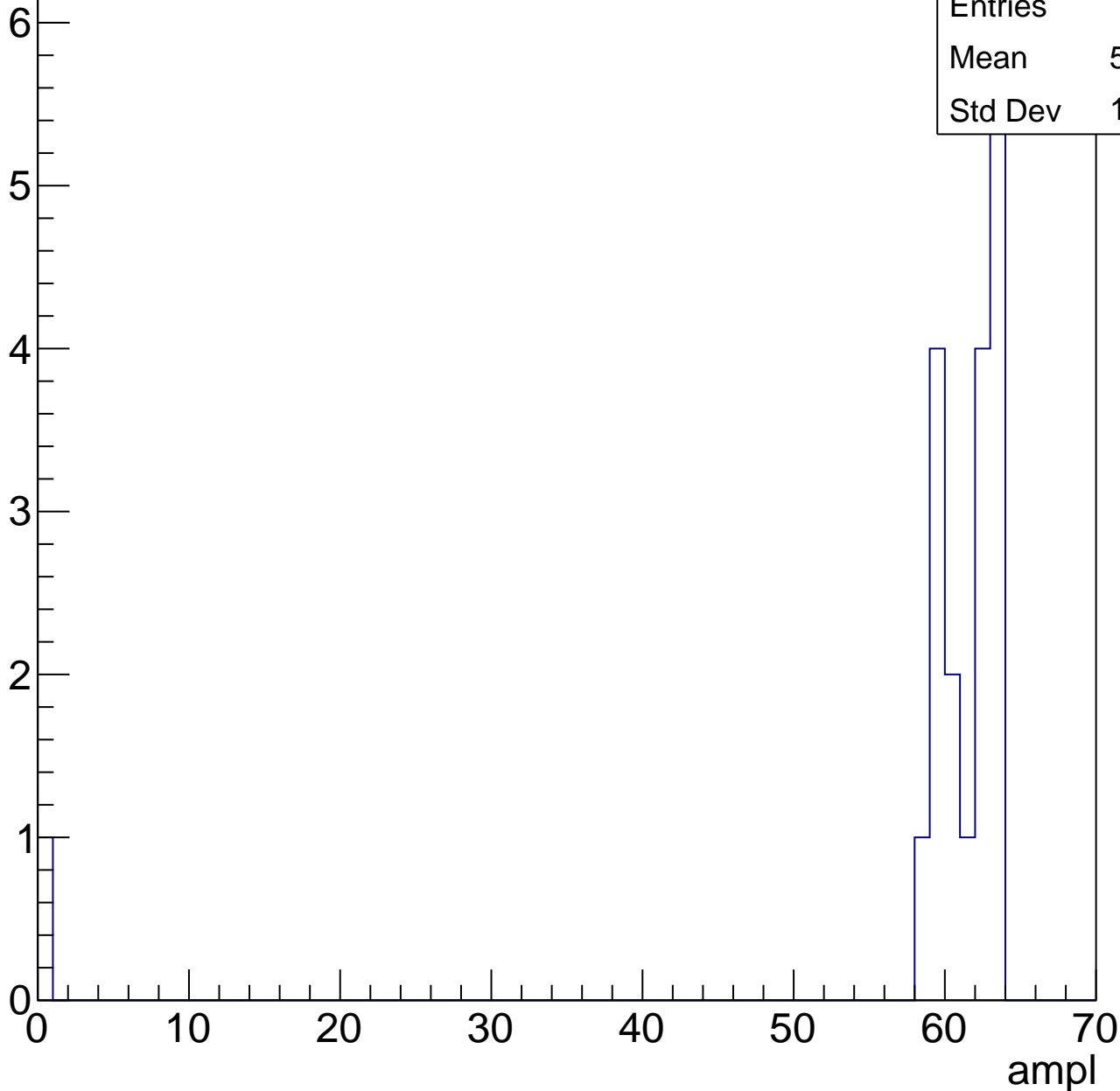


B1L001S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

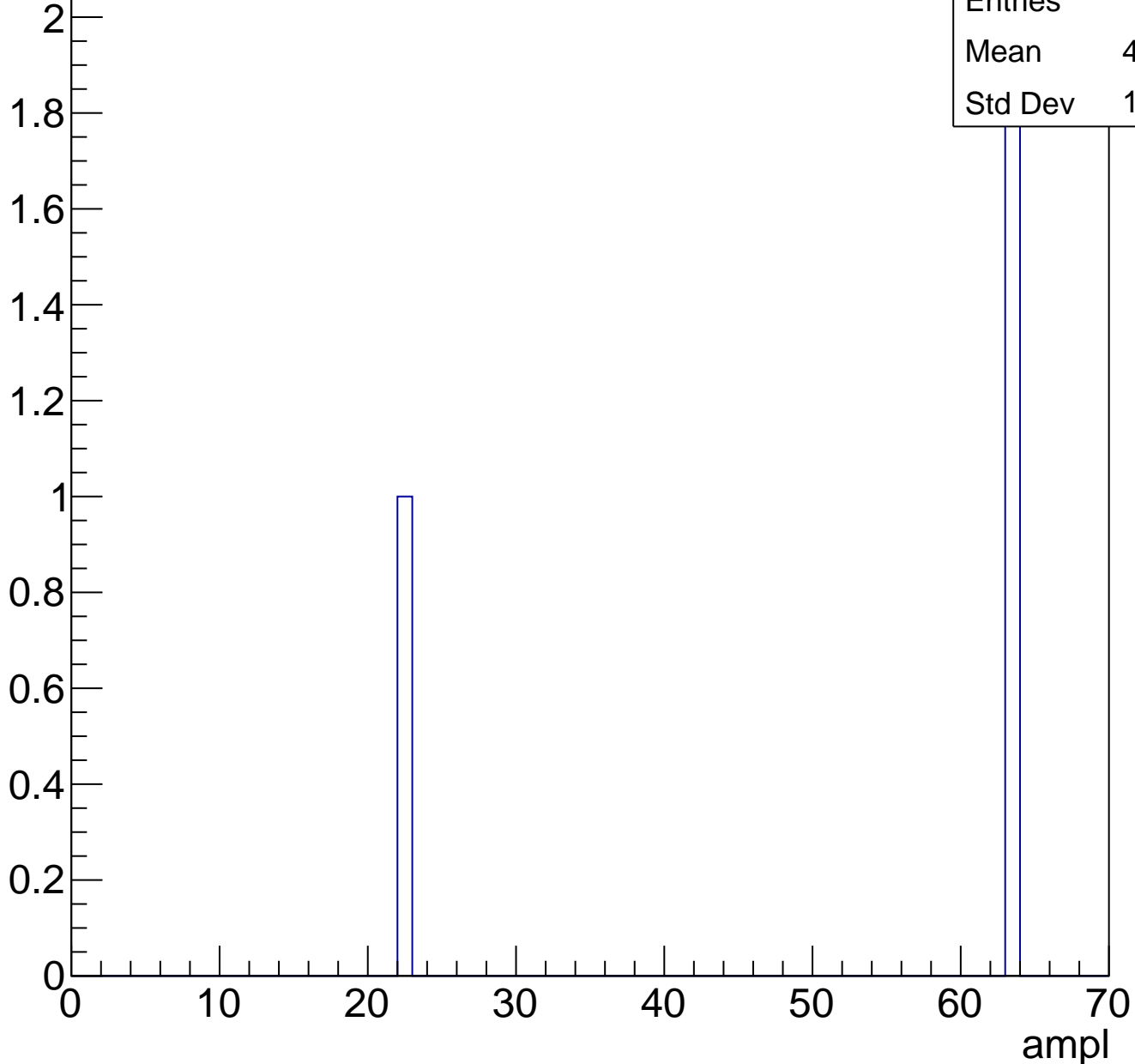
Entries	19
Mean	57.95
Std Dev	13.76



B1L001S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	49.33
Std Dev	19.33

B1L001S, U14-ch44, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	30.16
Std Dev	3.489

Gaus mean : 30.6204

Gaus Width: 3.6181

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

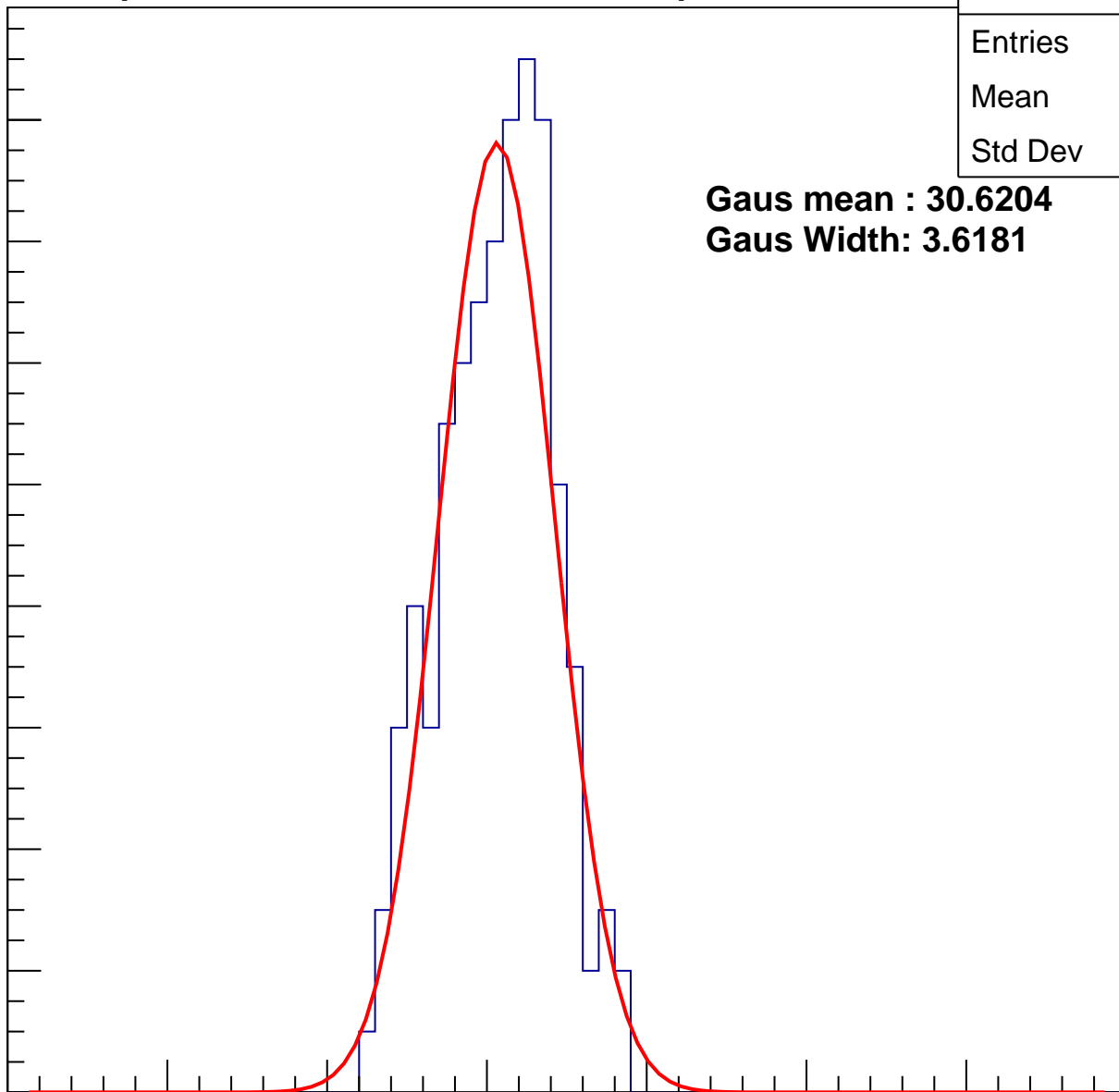
40

50

60

70

ampl



B1L001S, U14-ch44, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	36.82
Std Dev	3.513

Gaus mean : 37.6432

Gaus Width: 3.4460

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

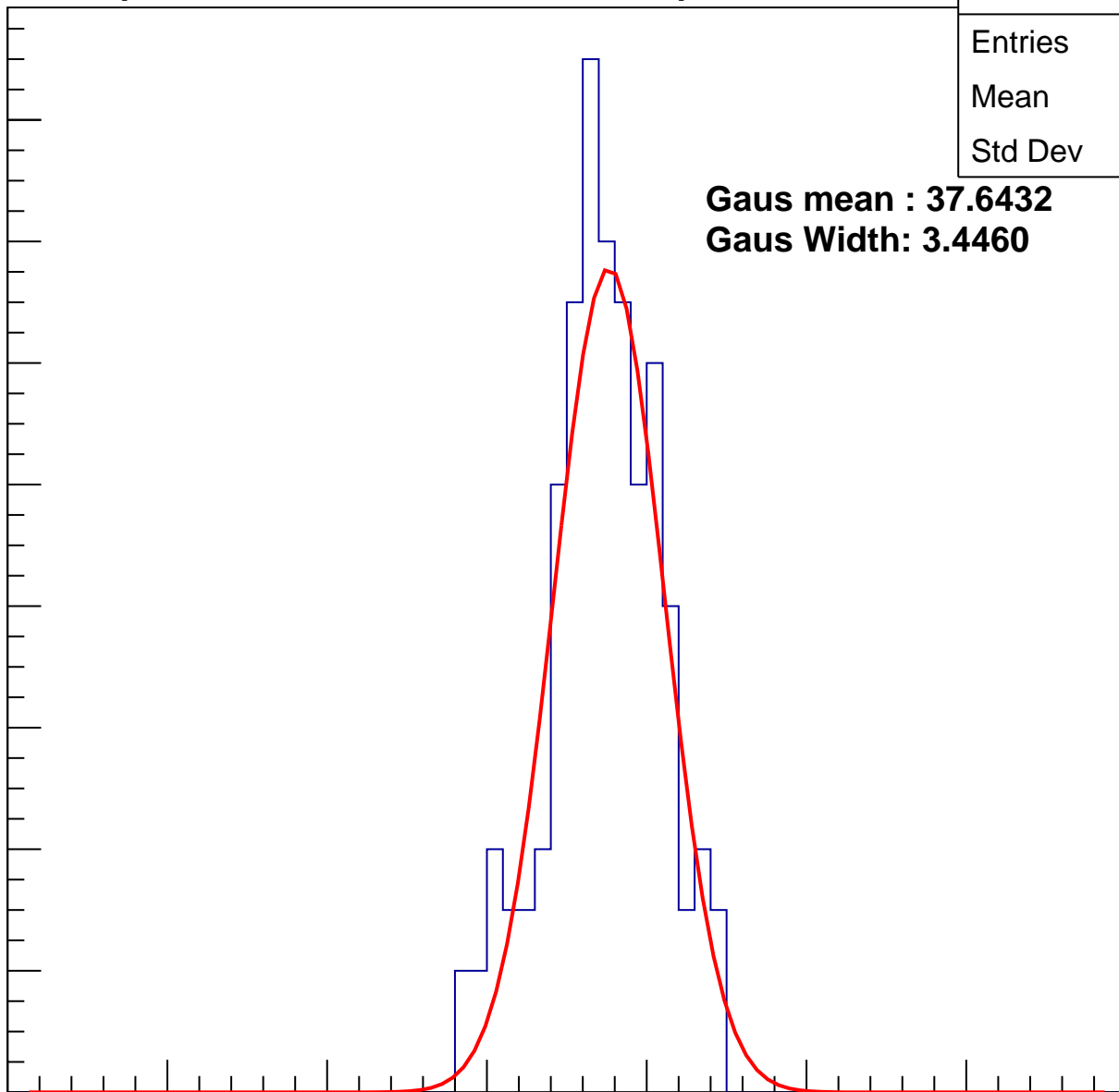
40

50

60

70

ampl



B1L001S, U14-ch44, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

Entries

135

Mean

43.47

Std Dev

3.32

Gaus mean : 43.8402

Gaus Width: 3.3963

0

10

20

30

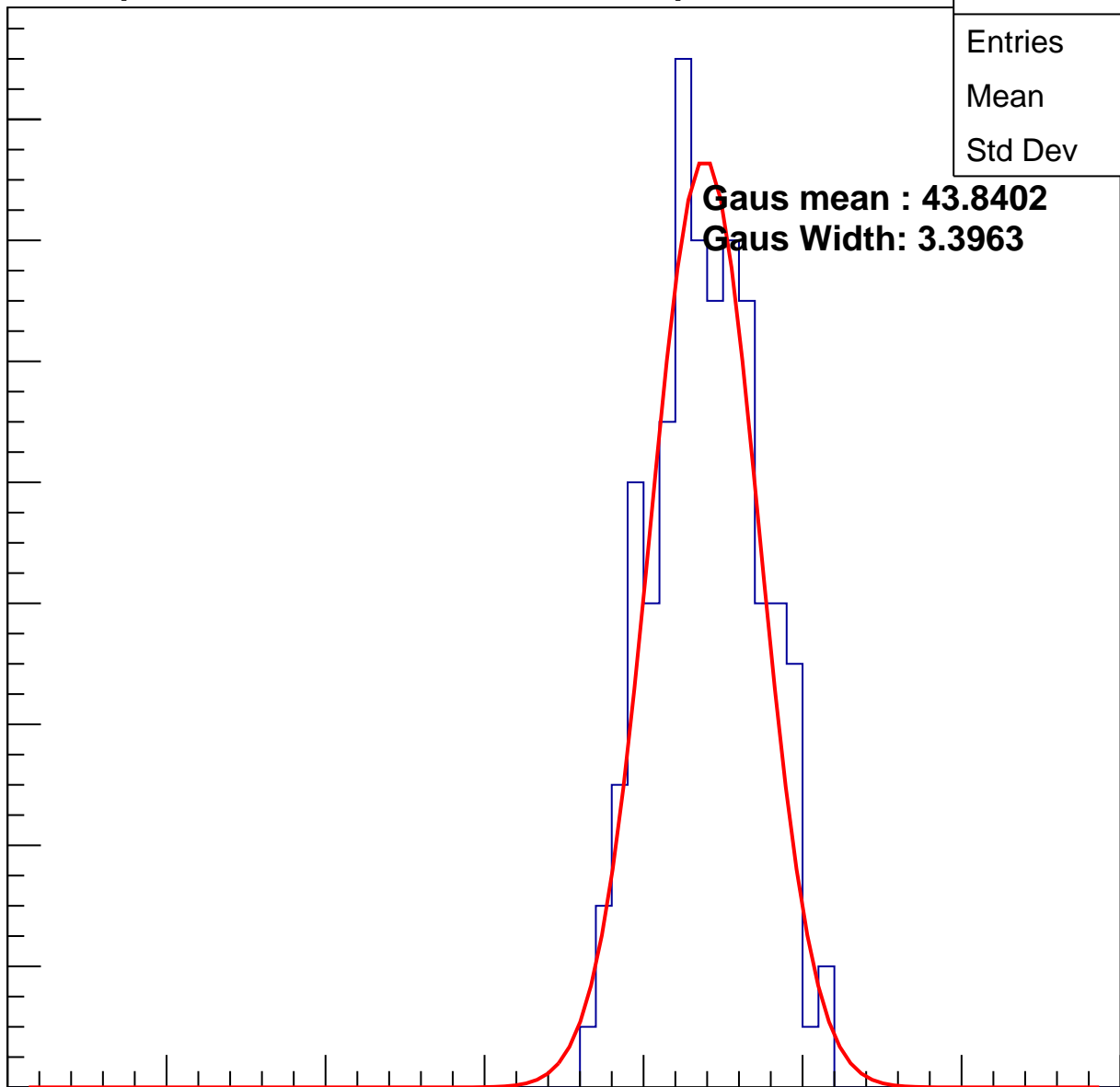
40

50

60

70

ampl

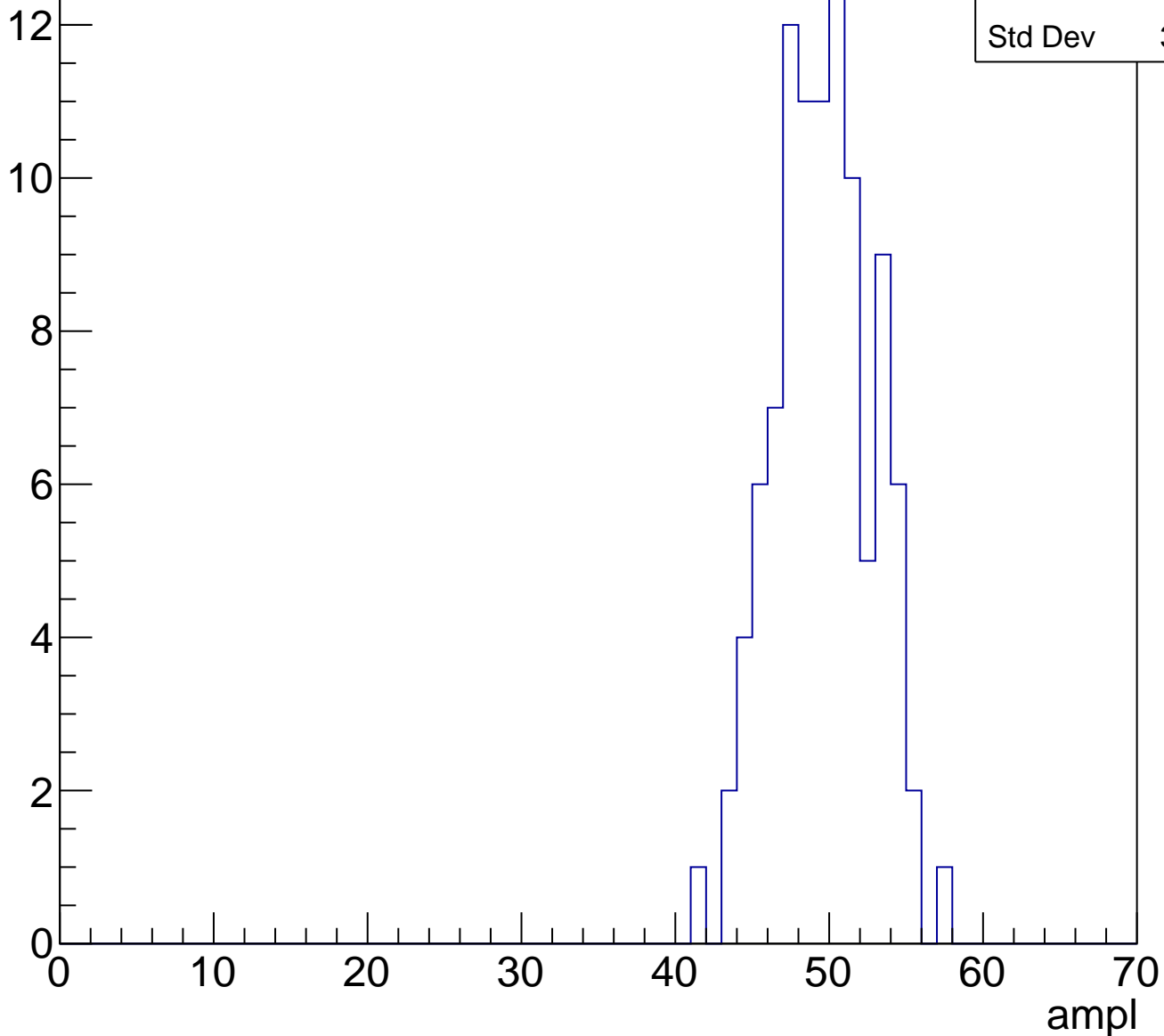


B1L001S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	49.14
Std Dev	3.131

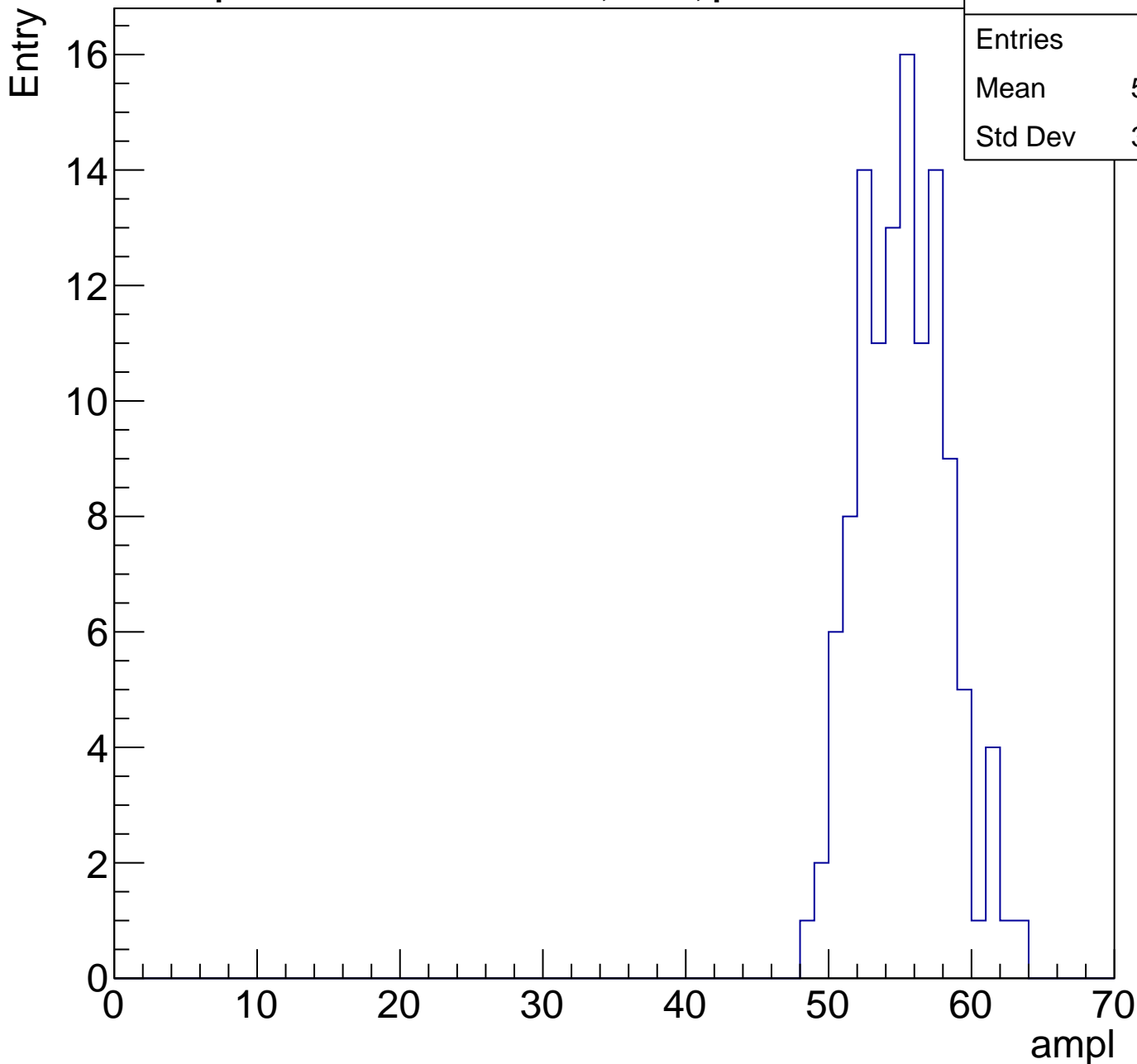
Entry



B1L001S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	54.76
Std Dev	3.046



B1L001S, U14-ch44, adc5

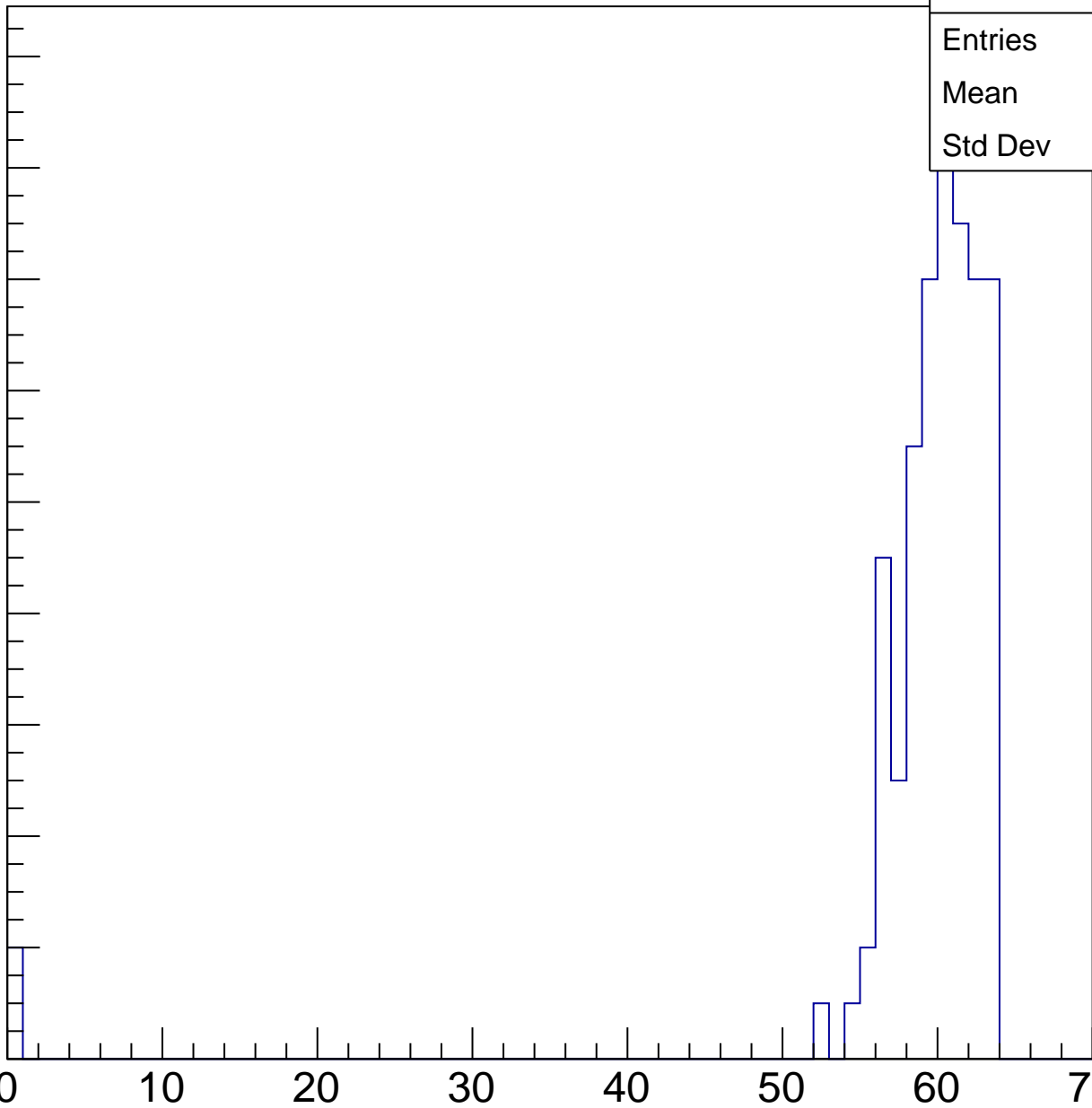
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	106
Mean	58.62
Std Dev	8.464

ampl

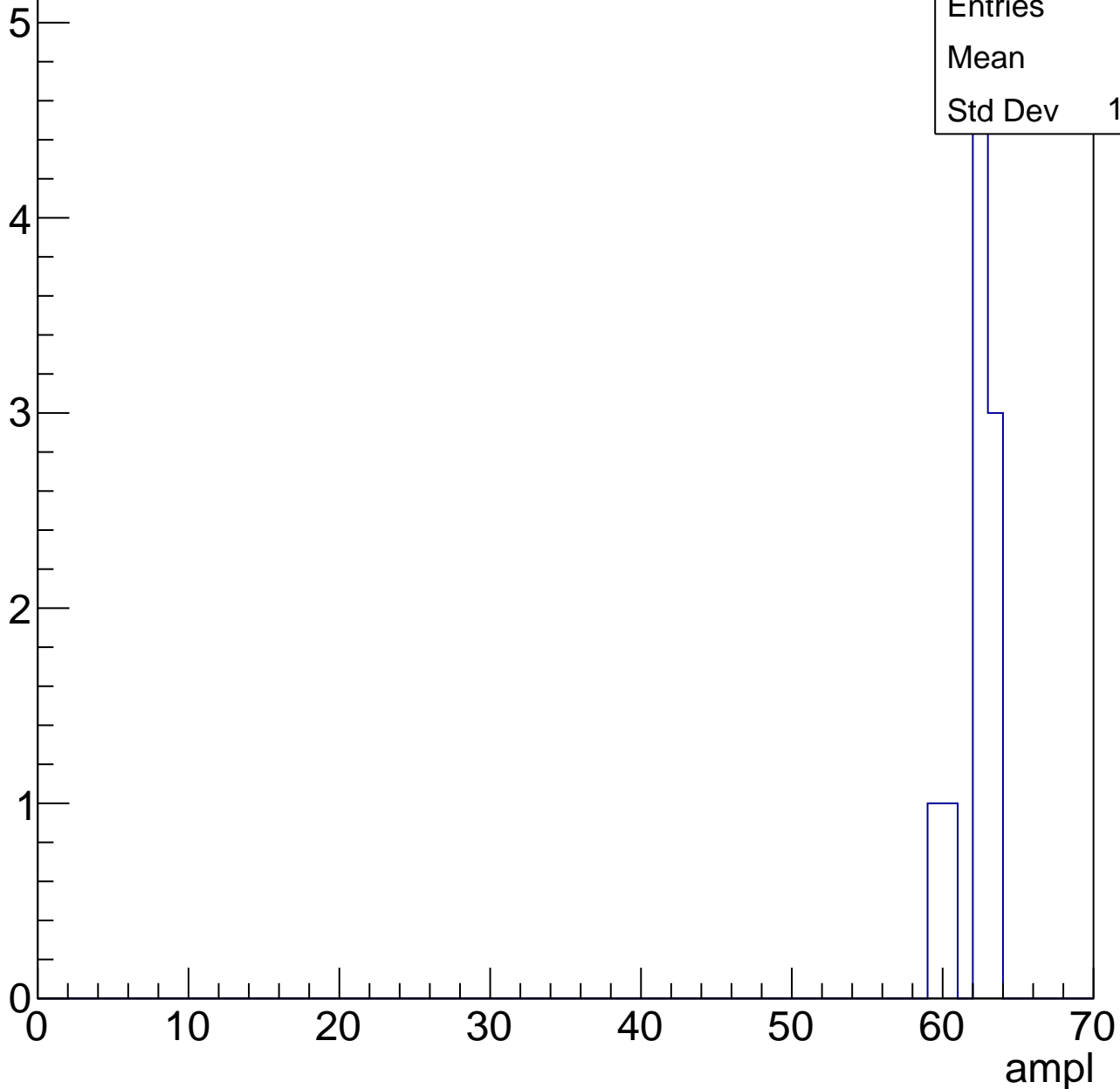


B1L001S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

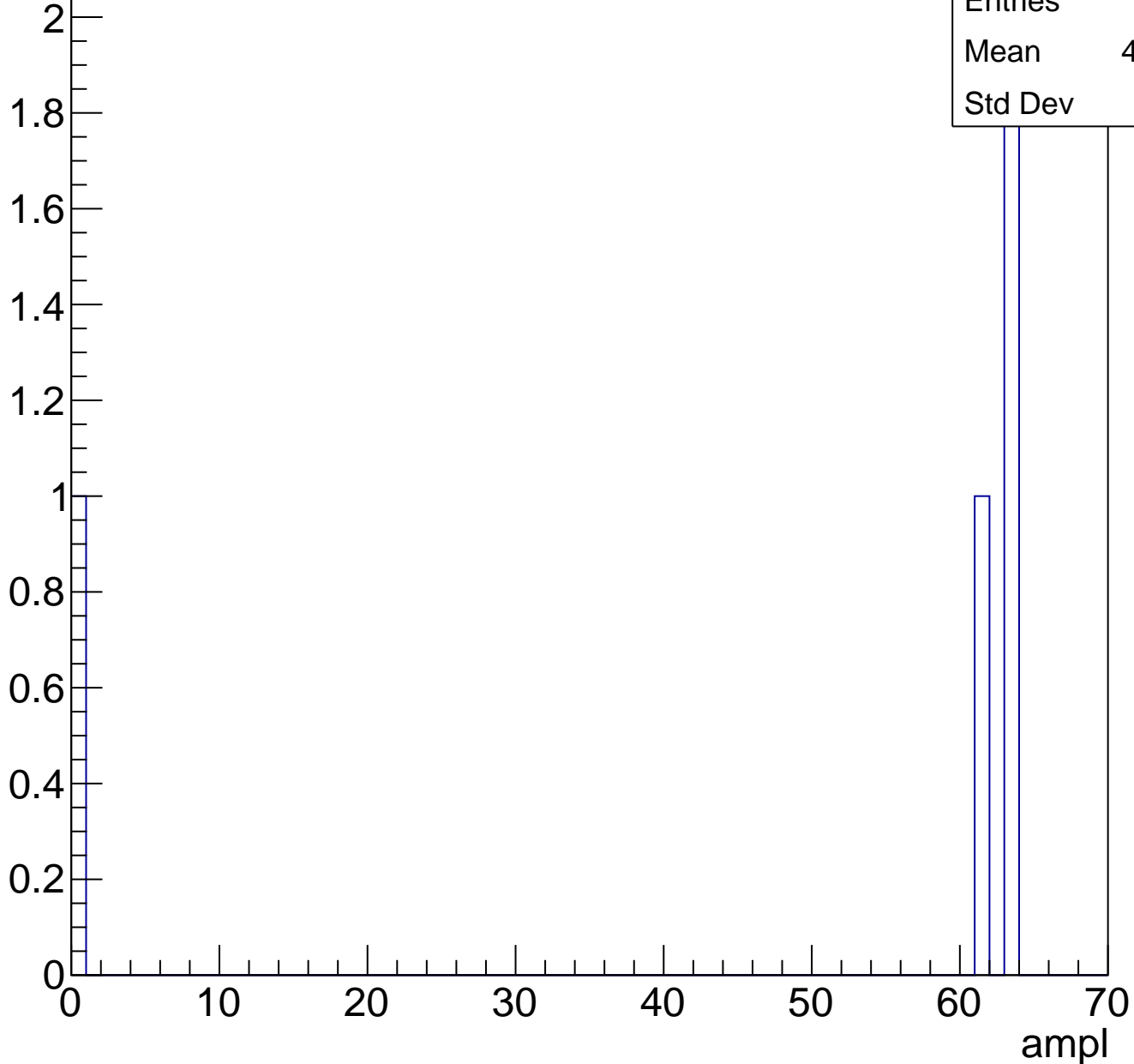
Entries	10
Mean	61.8
Std Dev	1.249



B1L001S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch45, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	27.84
Std Dev	4.746

Gaus mean : 28.8787

Gaus Width: 2.8456

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

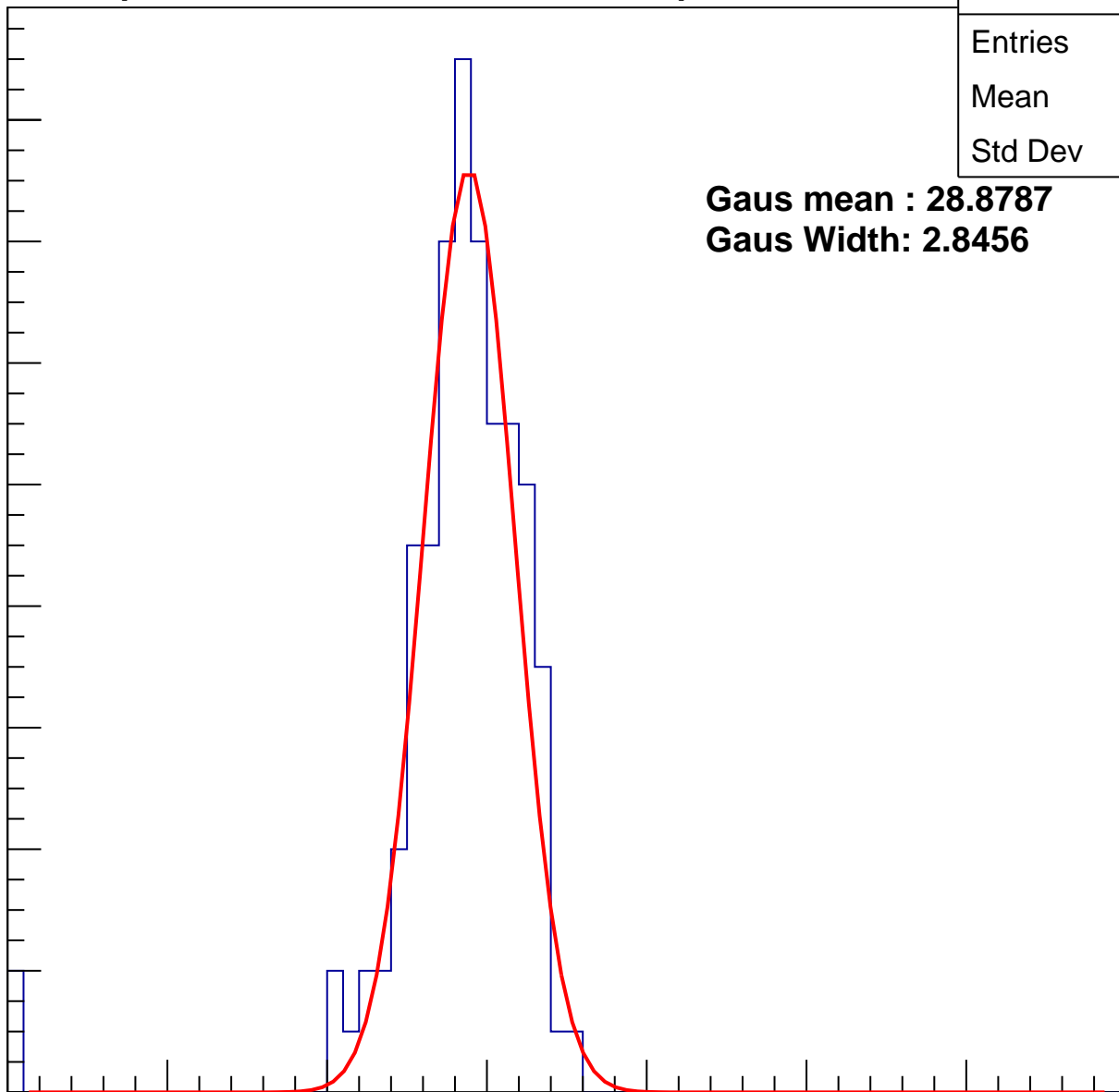
40

50

60

70

ampl



B1L001S, U14-ch45, adc1

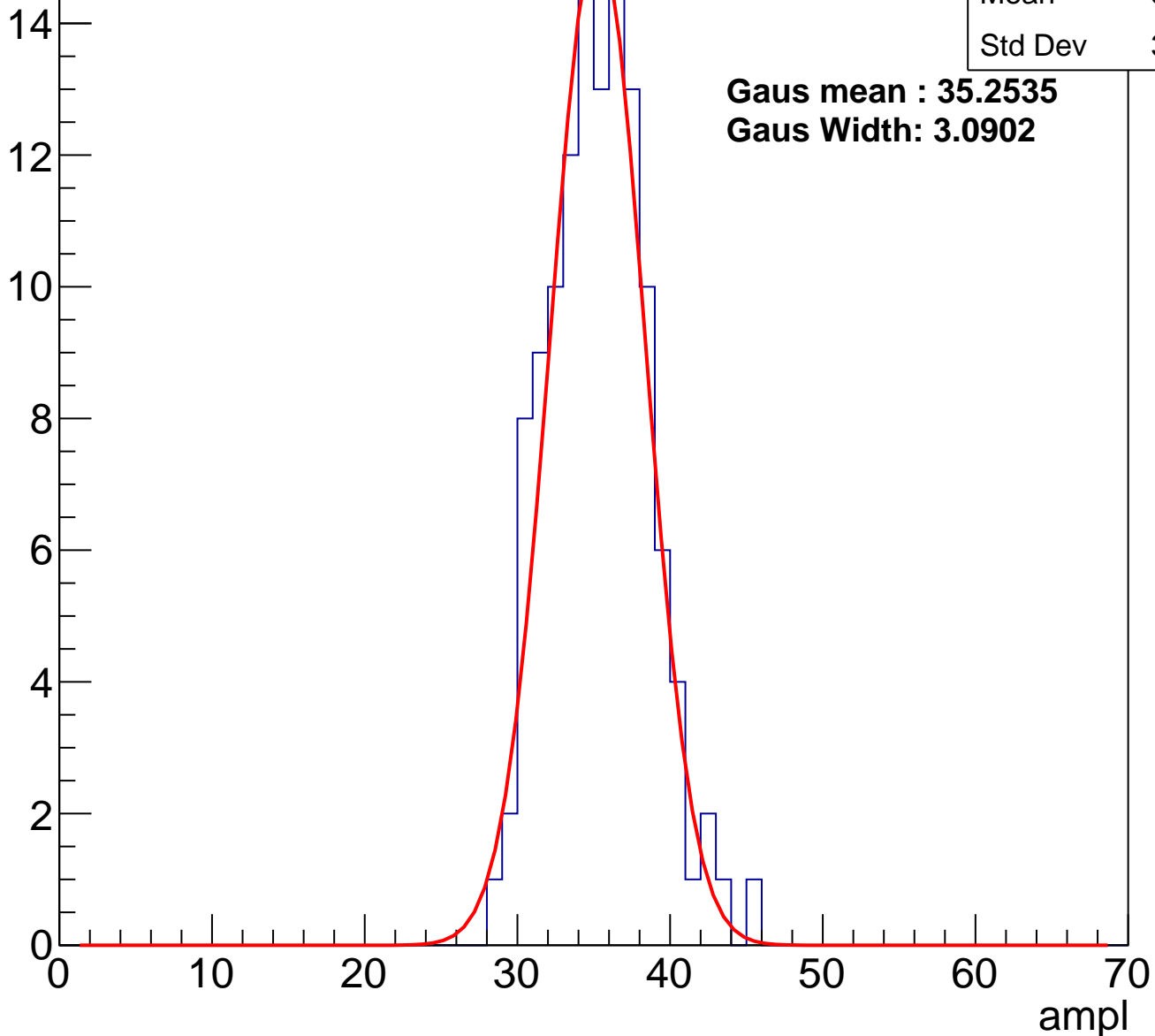
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	34.91
Std Dev	3.211

Gaus mean : 35.2535

Gaus Width: 3.0902

Entry



B1L001S, U14-ch45, adc2

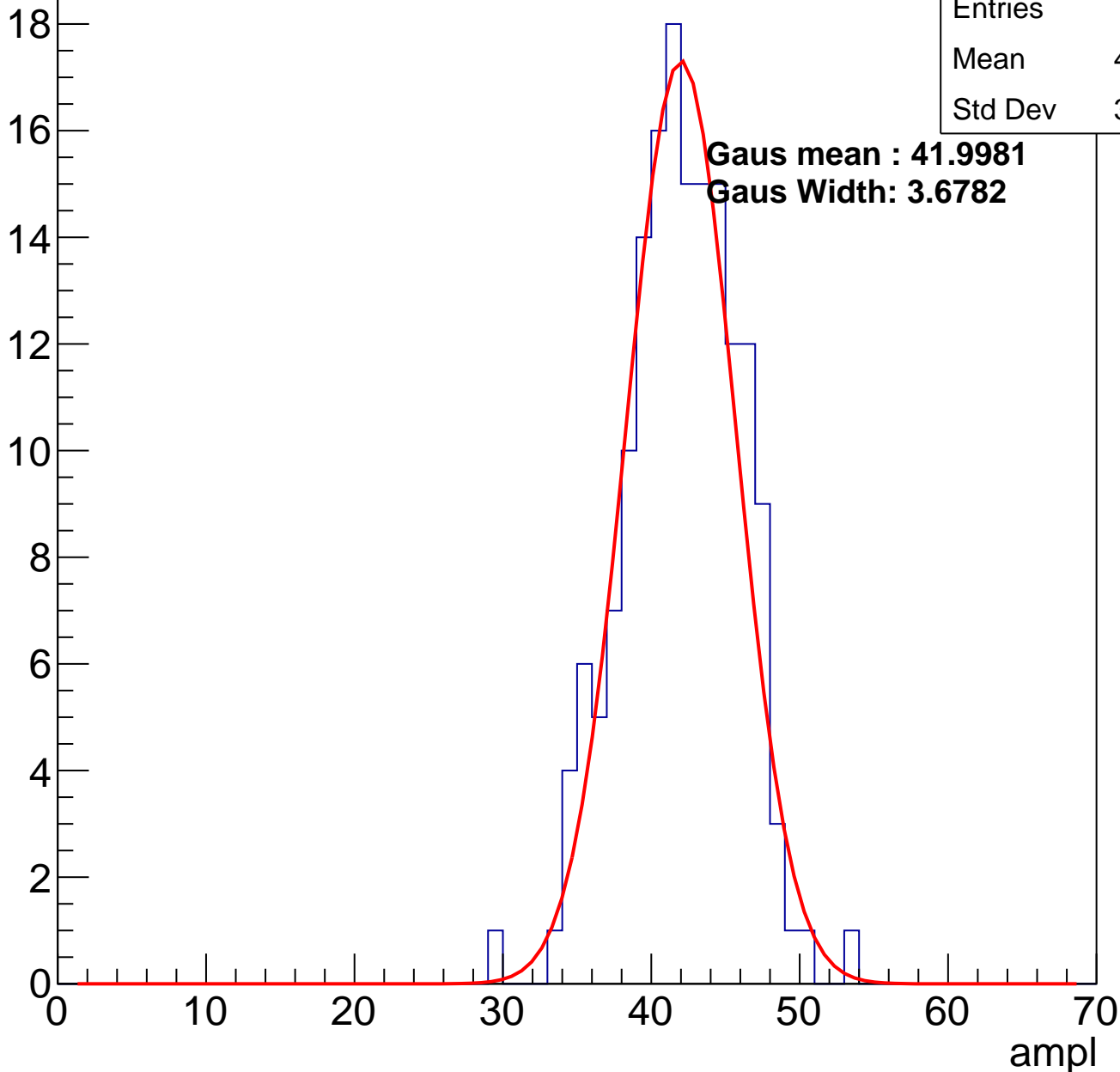
calib_packv5_042523_0143.root, FC#2, port C2

Entries	166
Mean	41.55
Std Dev	3.827

Gaus mean : 41.9981

Gaus Width: 3.6782

Entry

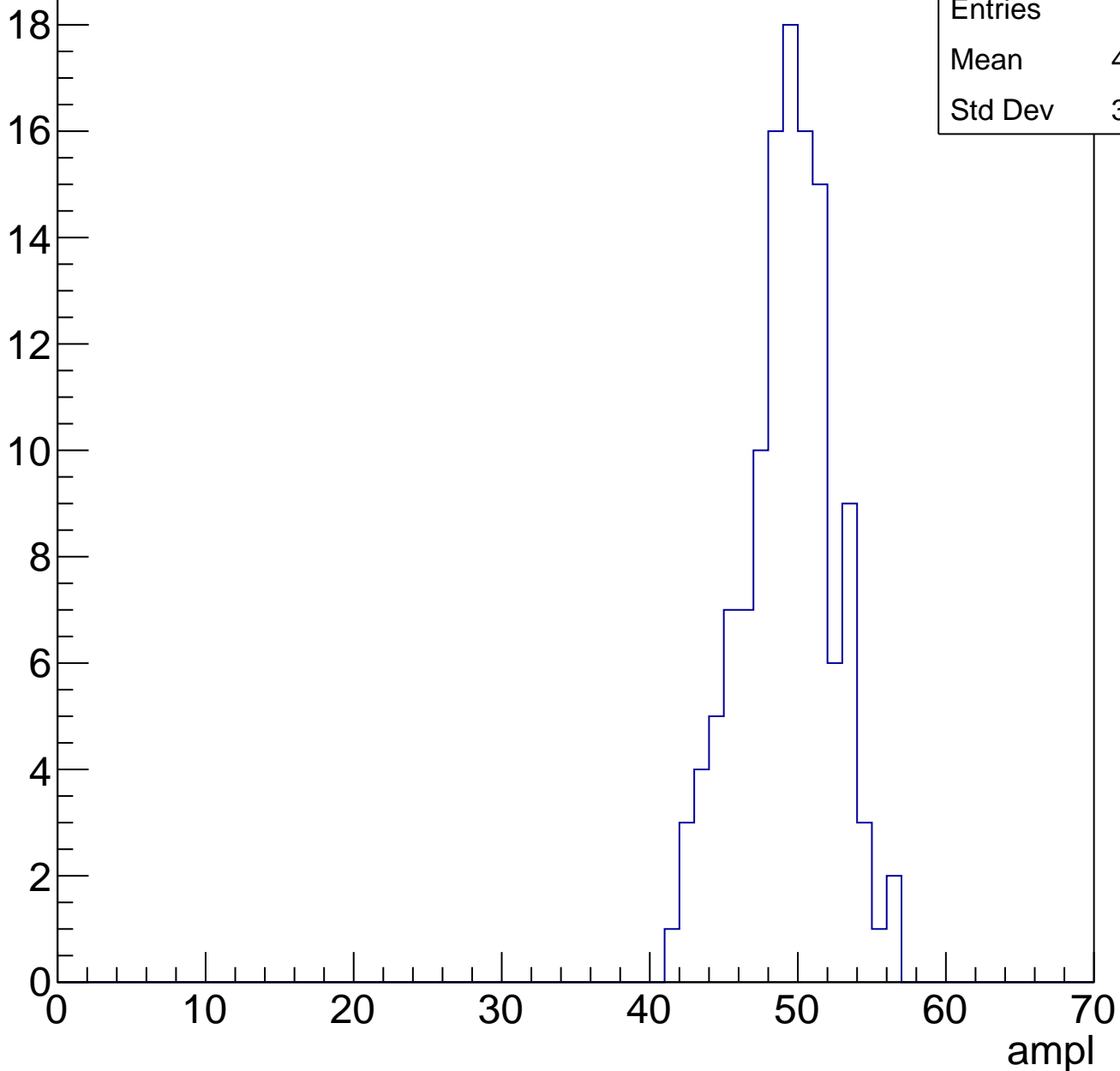


B1L001S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	48.77
Std Dev	3.107

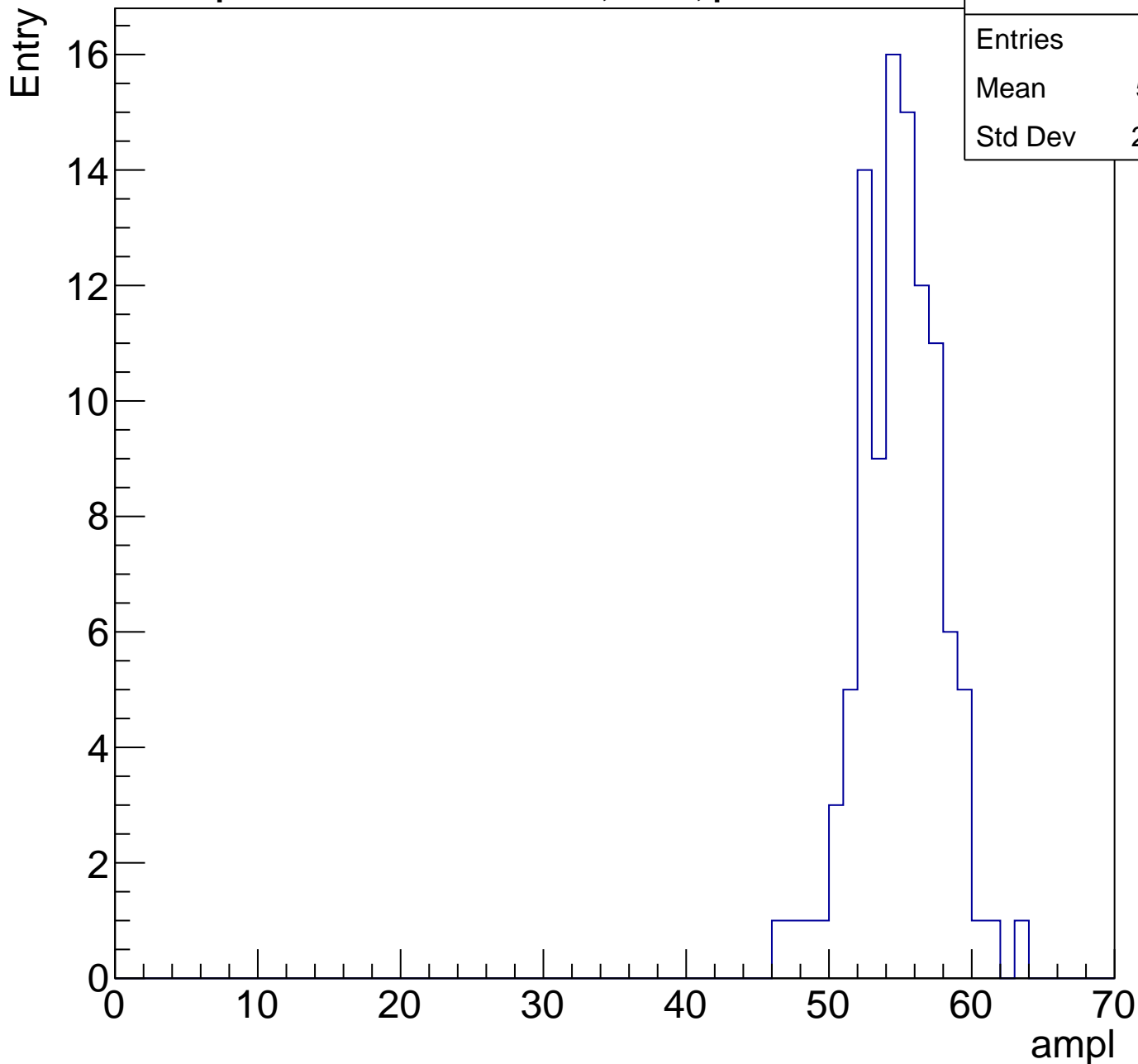
Entry



B1L001S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	54.51
Std Dev	2.879



B1L001S, U14-ch45, adc5

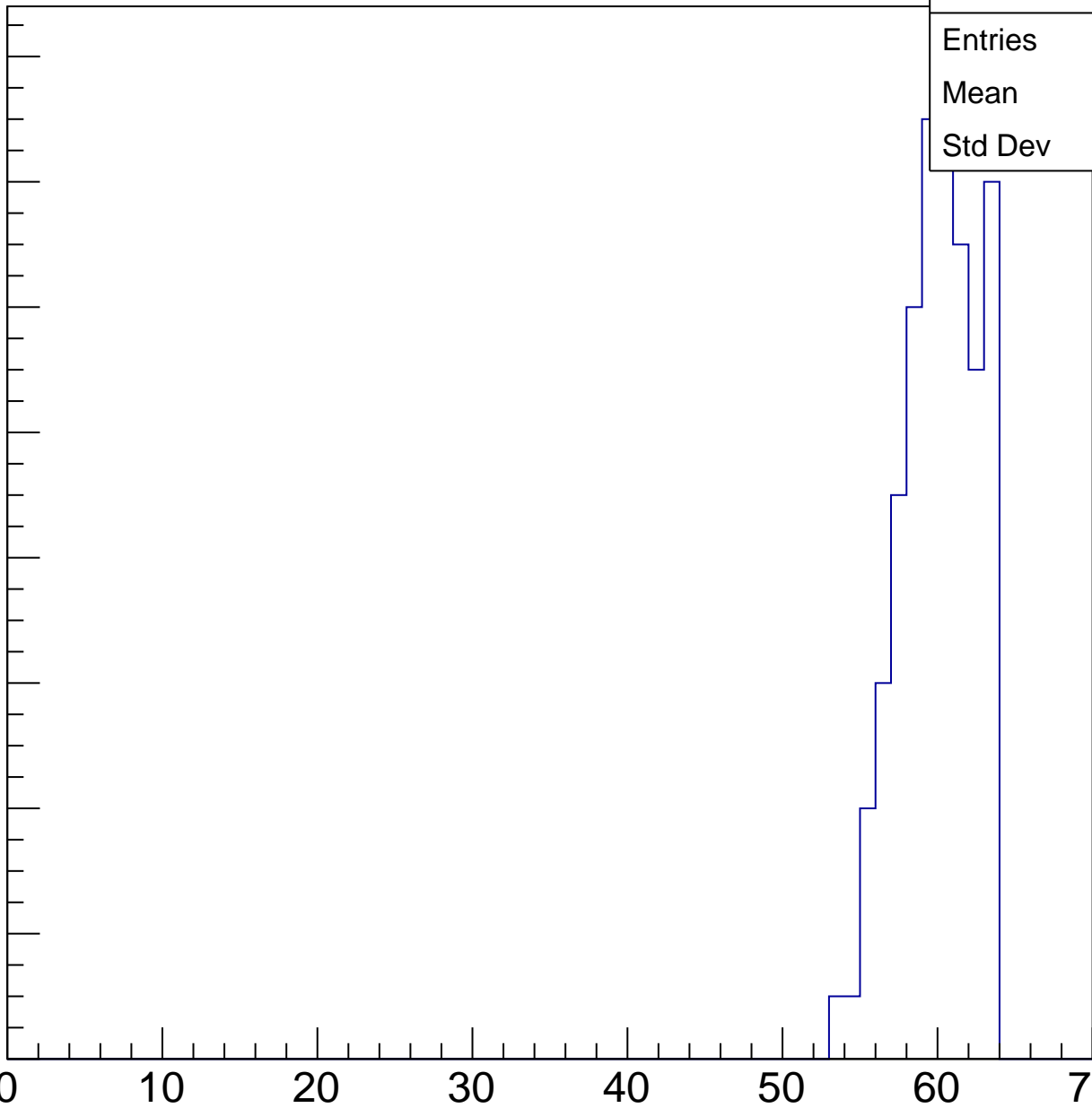
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	102
Mean	59.55
Std Dev	2.399

ampl

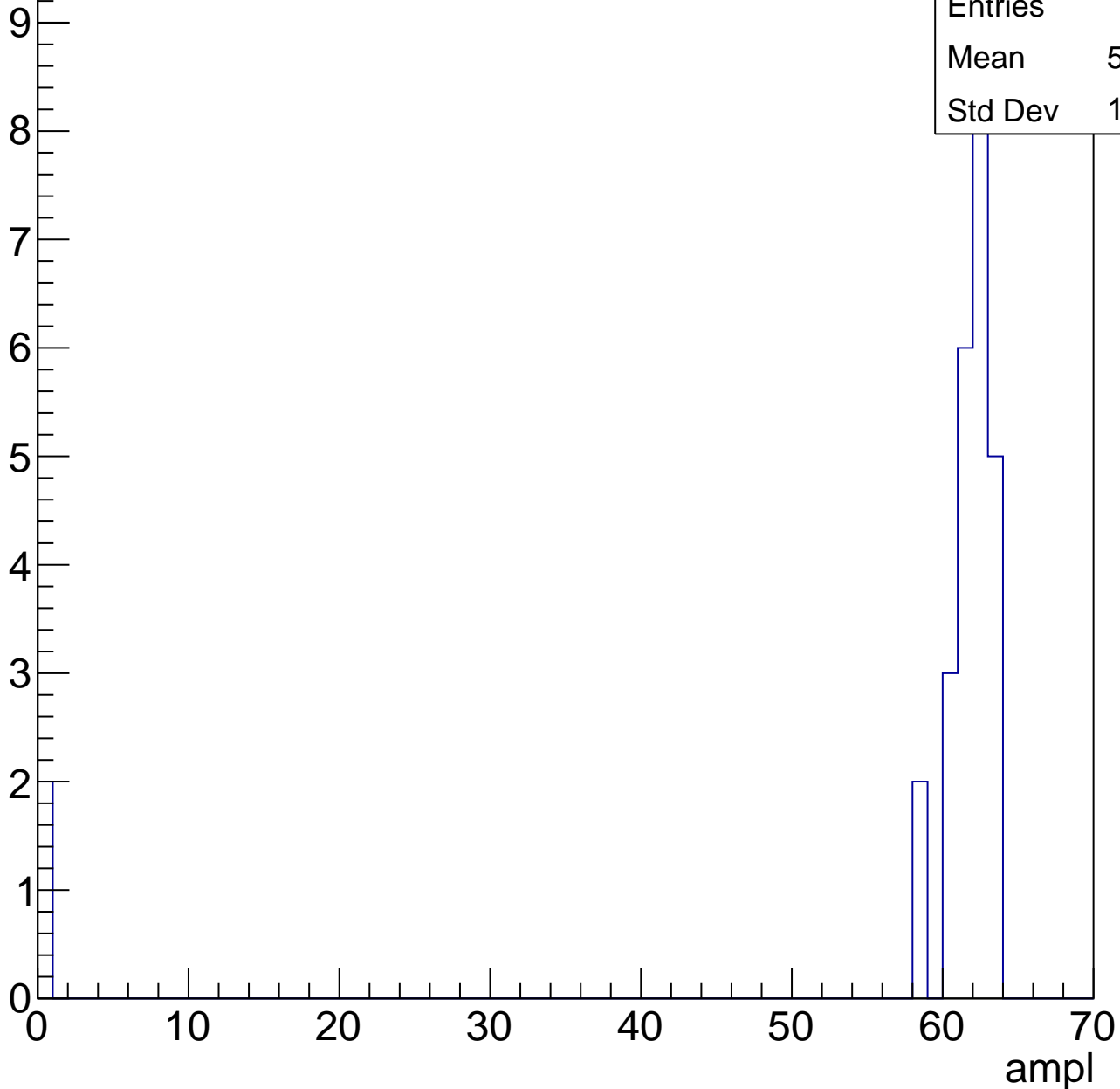


B1L001S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	27
Mean	56.85
Std Dev	16.13



B1L001S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L001S, U14-ch46, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	29.07
Std Dev	4.488

Gaus mean : 29.6590

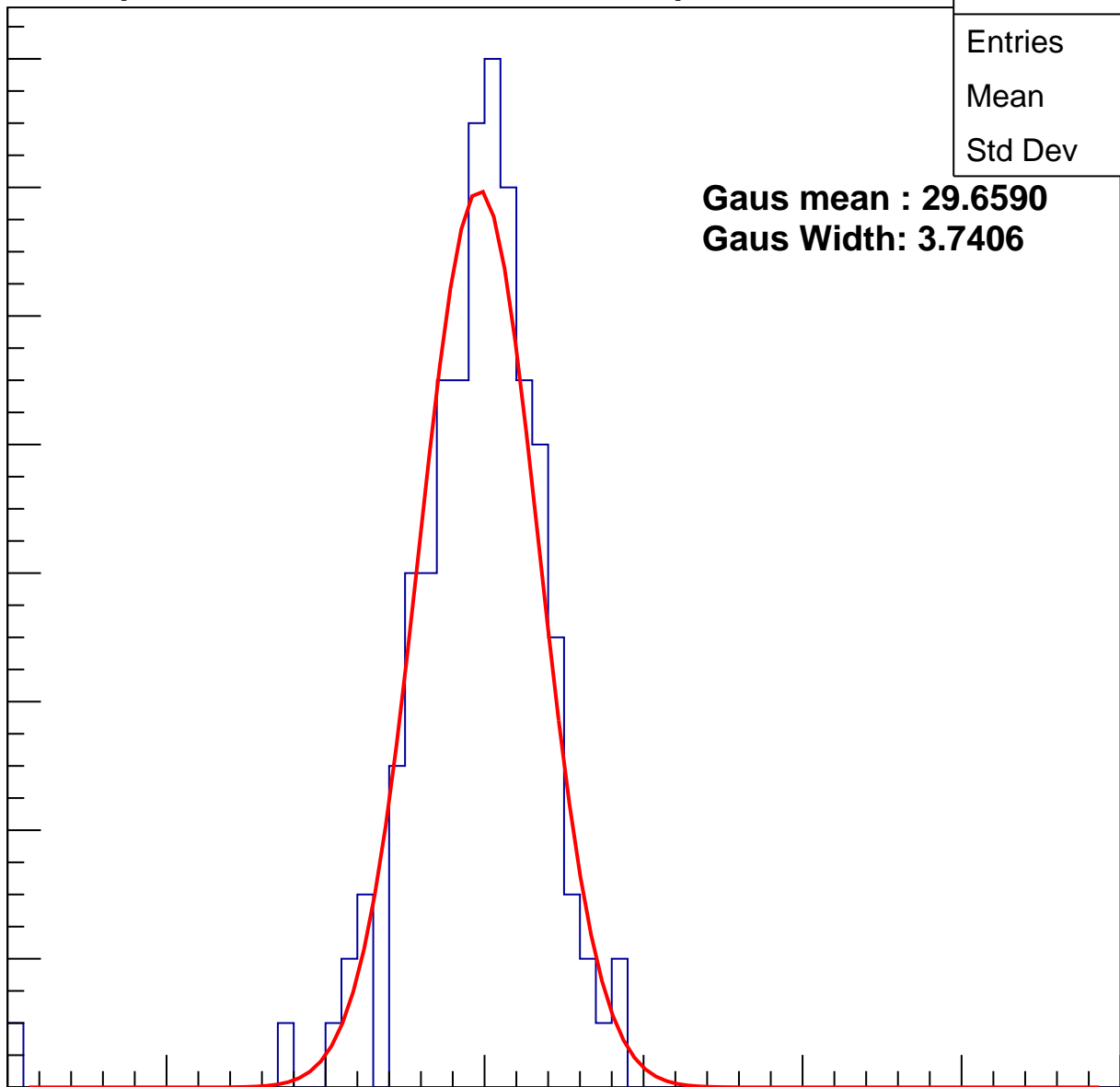
Gaus Width: 3.7406

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch46, adc1

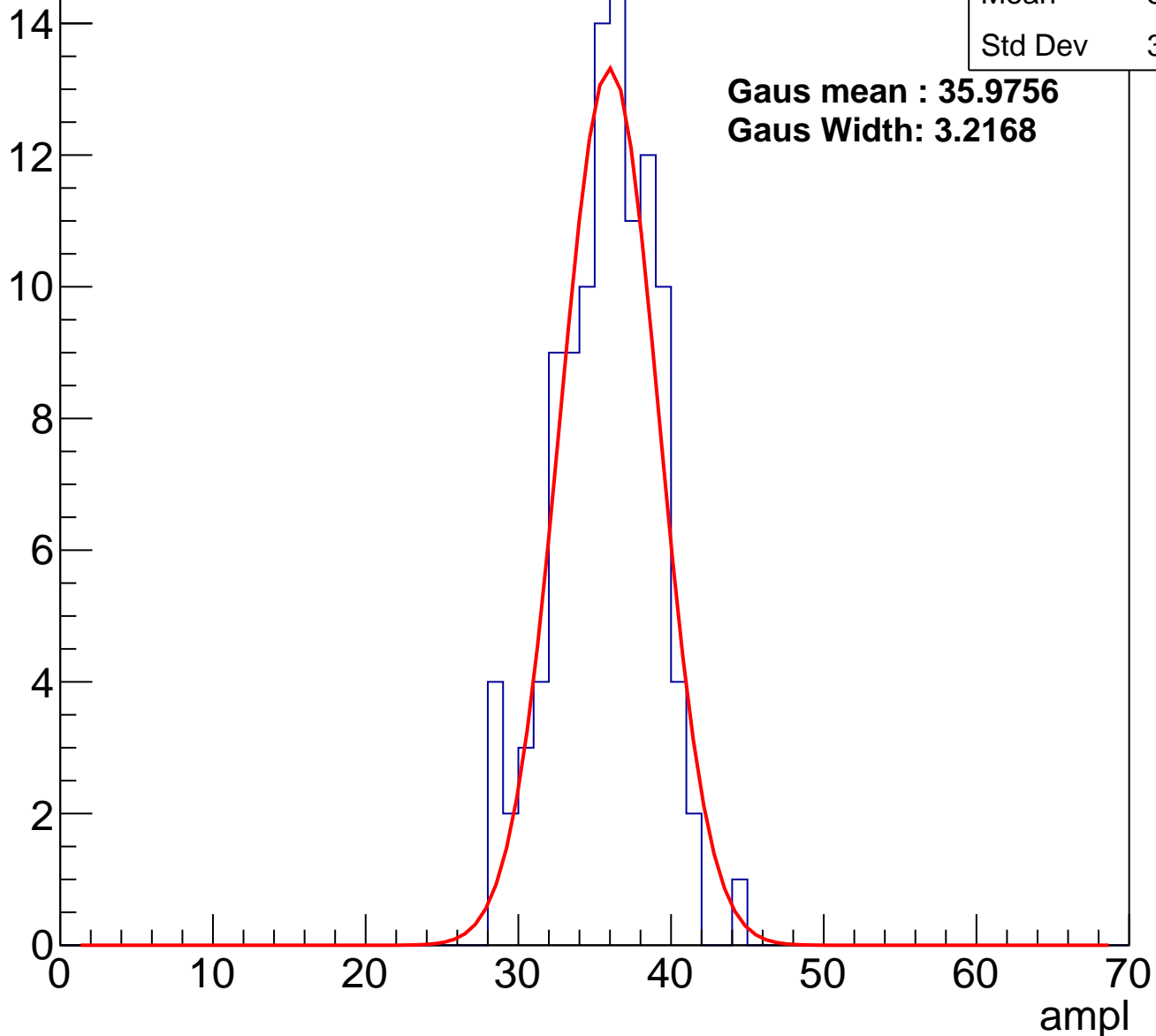
calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	35.25
Std Dev	3.172

Gaus mean : 35.9756

Gaus Width: 3.2168

Entry



B1L001S, U14-ch46, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	41.82
Std Dev	3.556

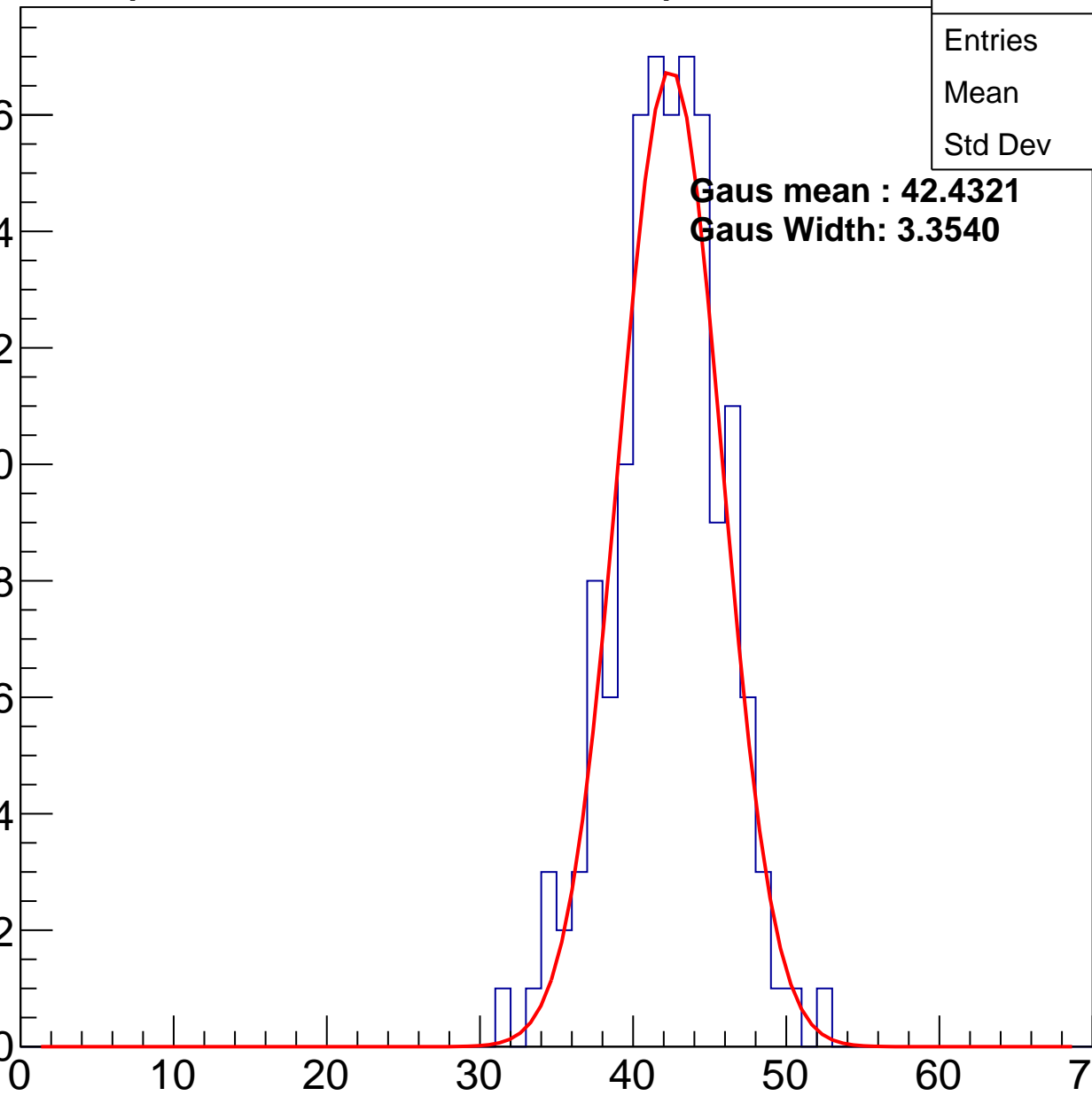
Gaus mean : 42.4321

Gaus Width: 3.3540

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#2, port C2

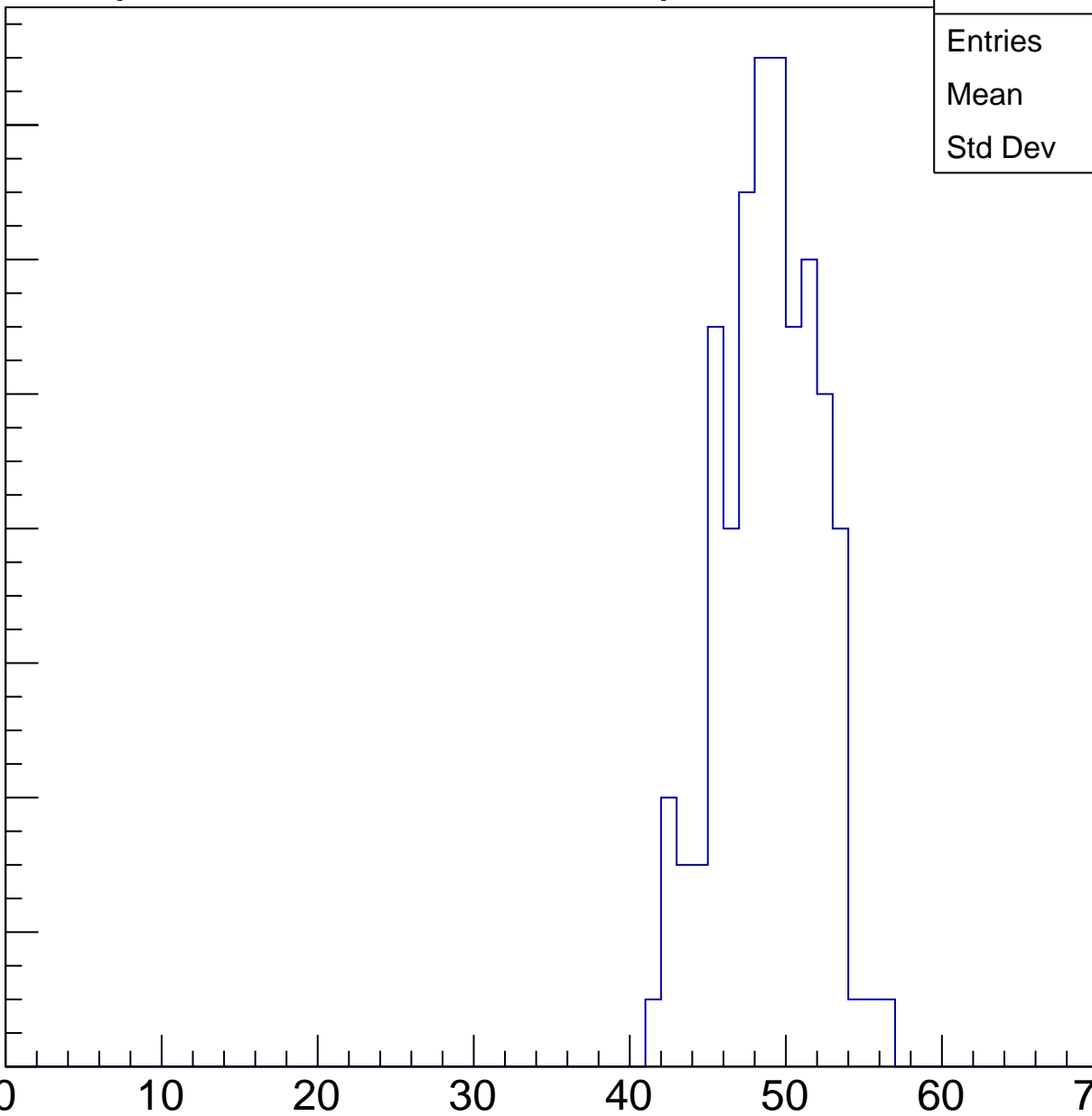
Entries	117
Mean	48.46
Std Dev	3.082

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#2, port C2

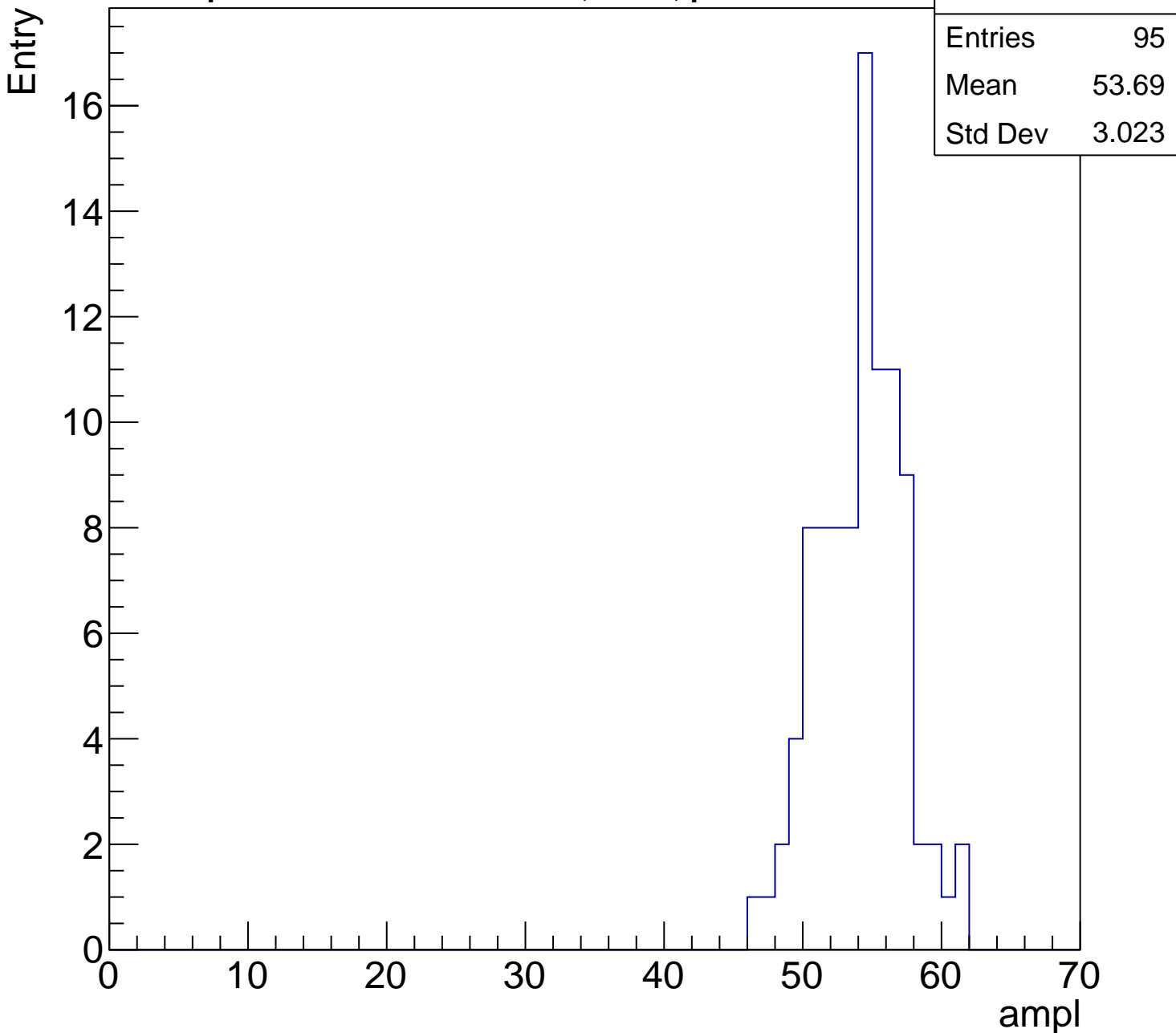
Entries	95
Mean	53.69
Std Dev	3.023

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch46, adc5

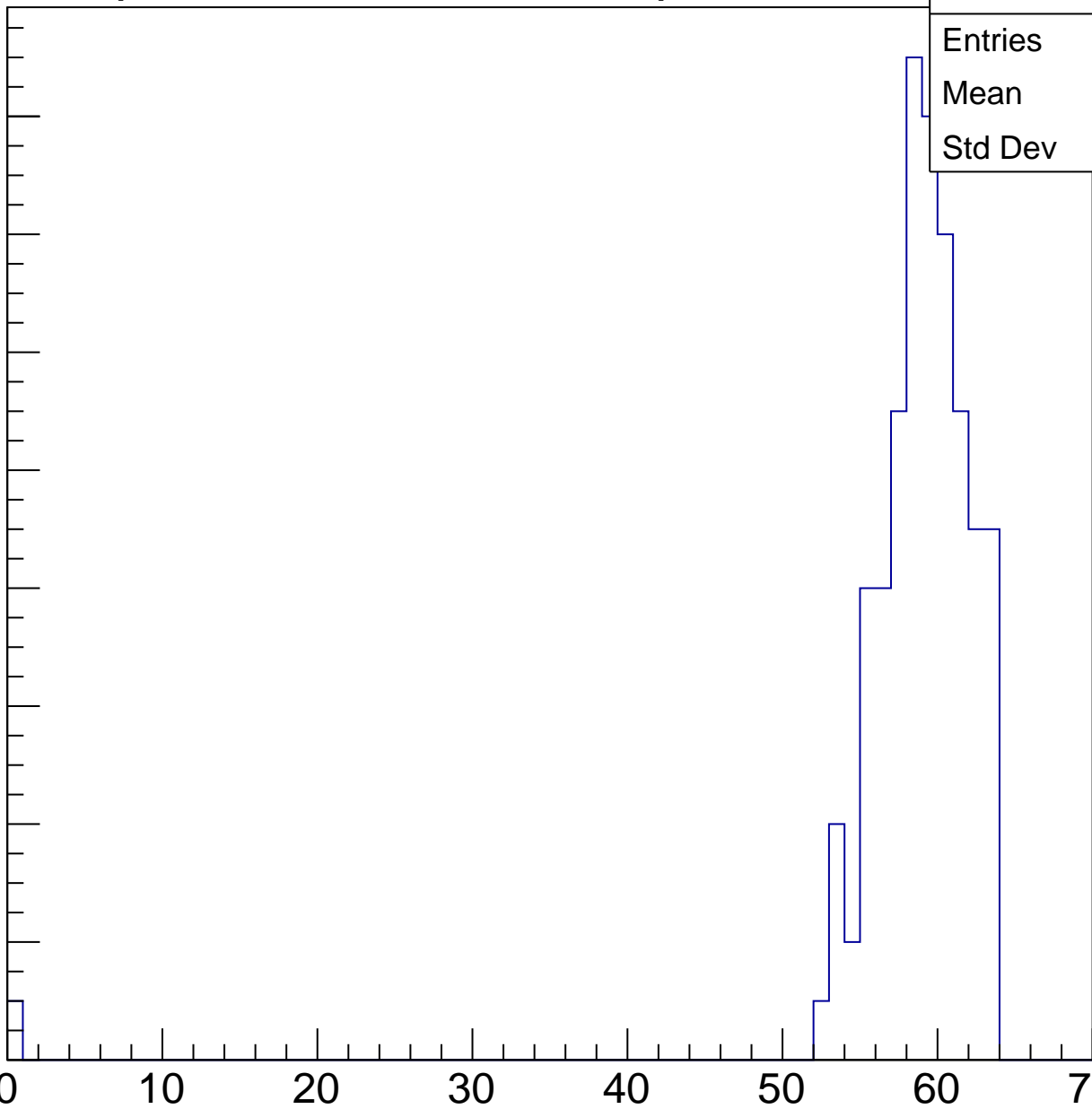
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.14
Std Dev	6.14

ampl



B1L001S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

Entries	38
Mean	59.95
Std Dev	9.942

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch47, adc0

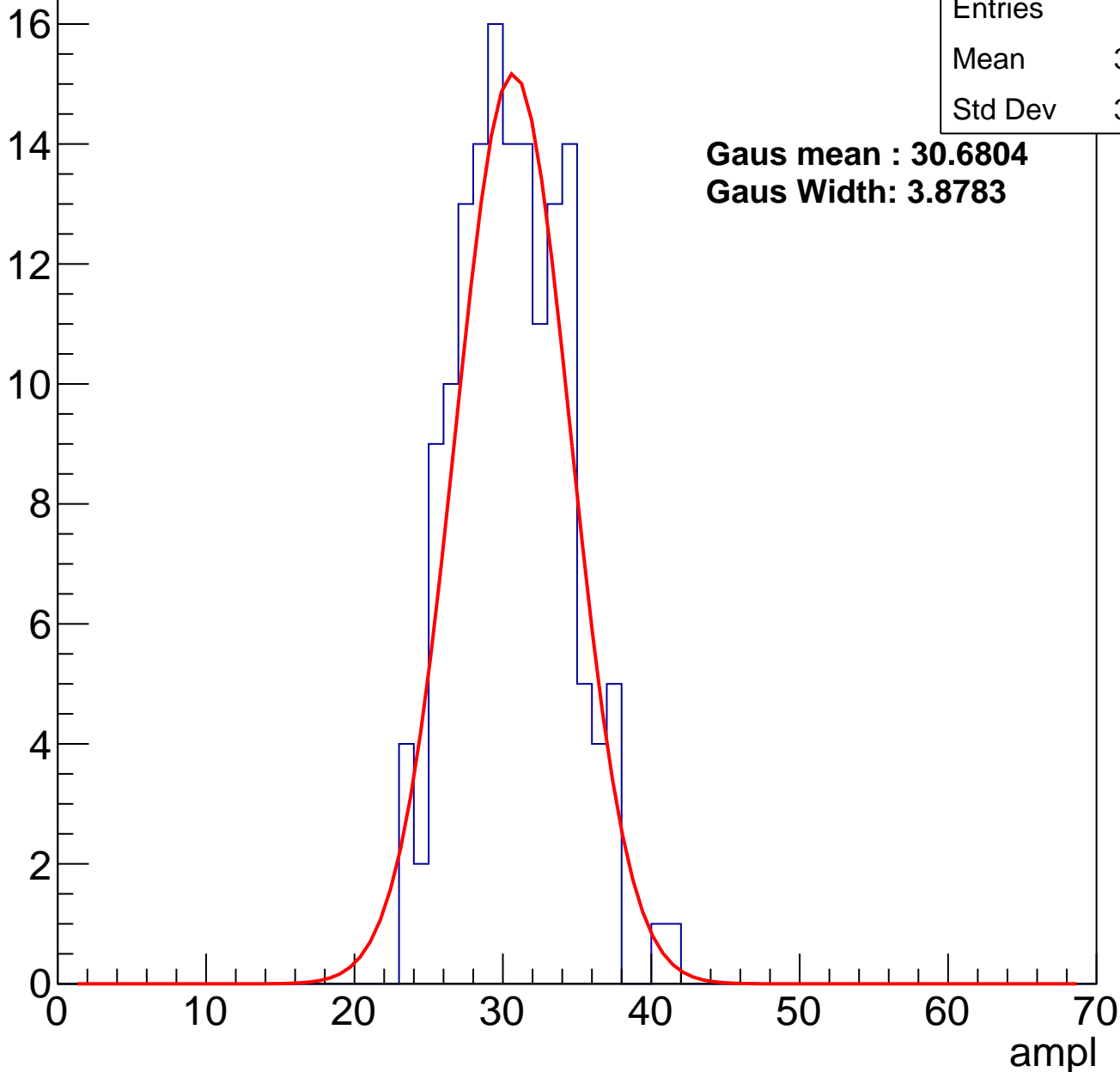
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	30.19
Std Dev	3.625

Gaus mean : 30.6804

Gaus Width: 3.8783

Entry



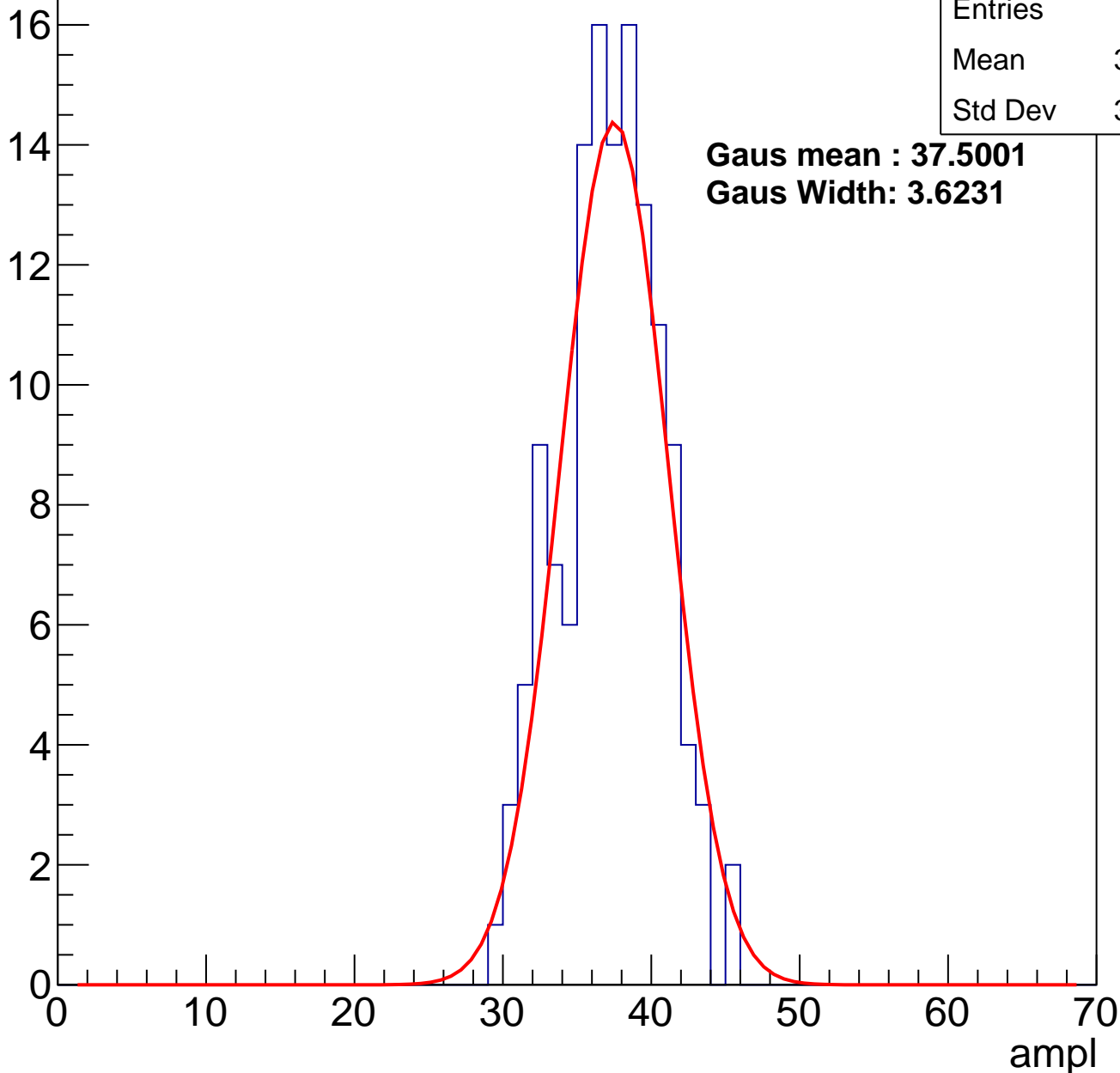
B1L001S, U14-ch47, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.78
Std Dev	3.352

Gaus mean : 37.5001
Gaus Width: 3.6231

Entry



B1L001S, U14-ch47, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	43.49
Std Dev	3.063

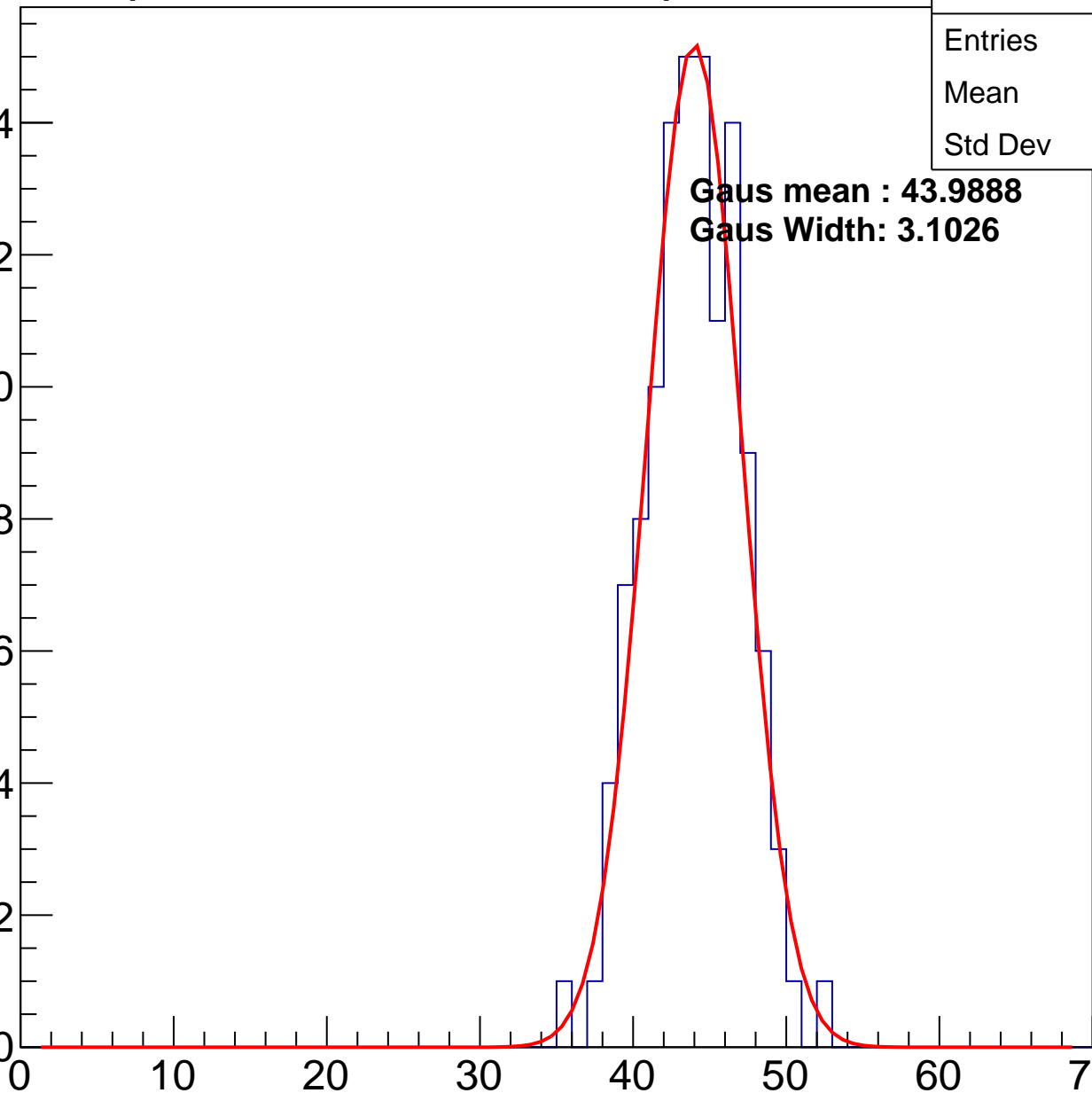
Gaus mean : 43.9888

Gaus Width: 3.1026

Entry

14
12
10
8
6
4
2
0

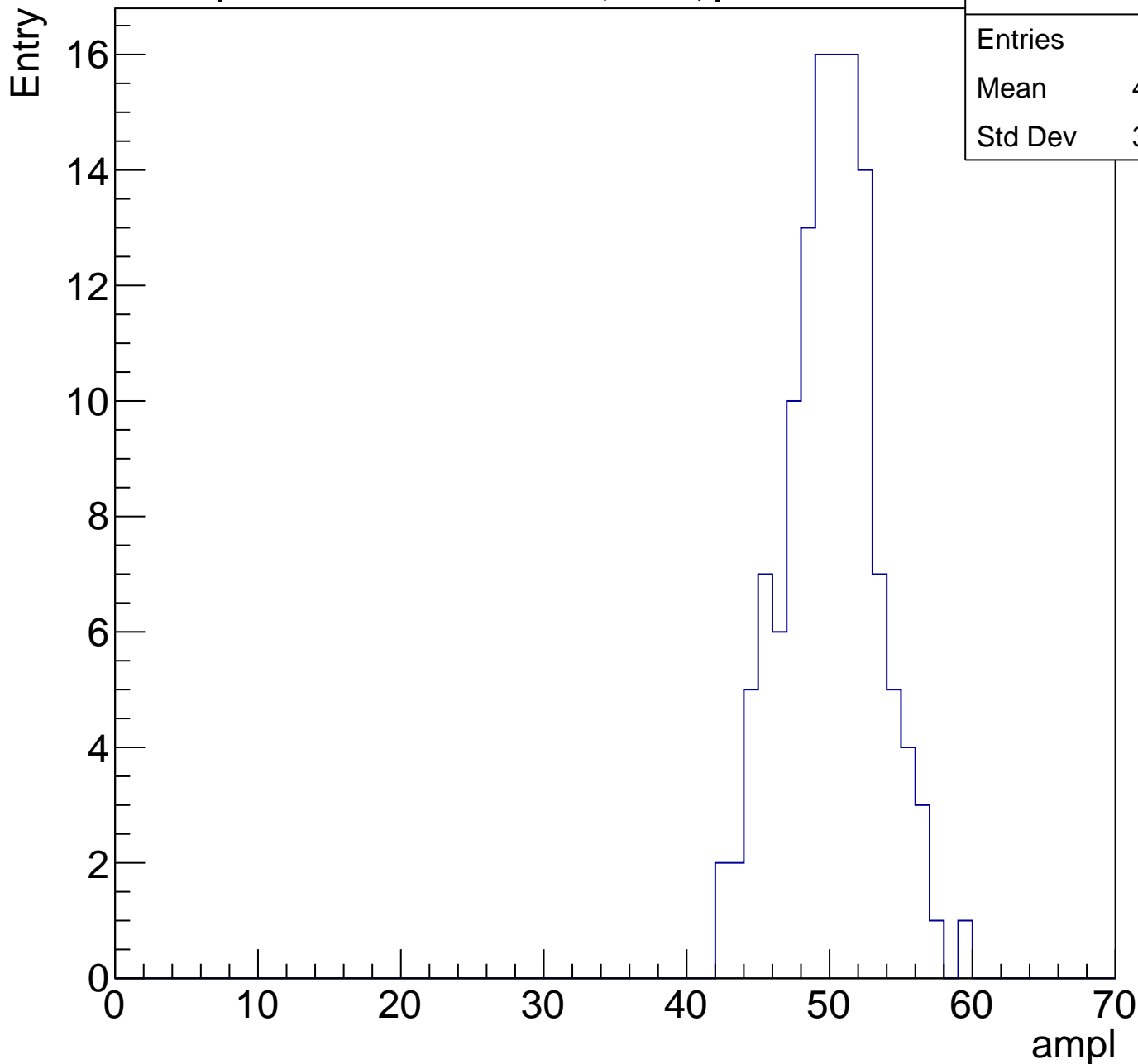
ampl



B1L001S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	49.59
Std Dev	3.268



B1L001S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#2, port C2

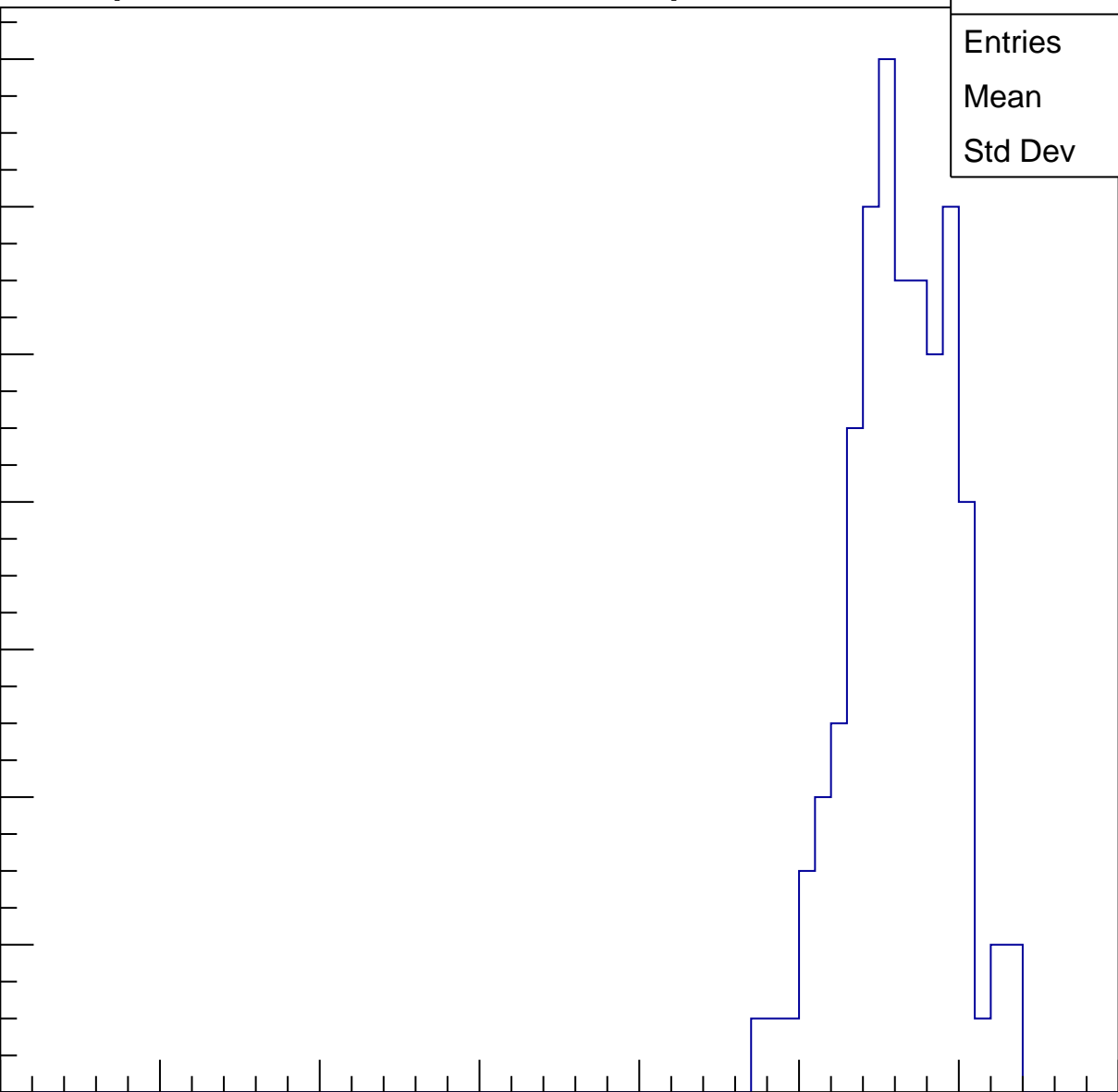
Entries	107
Mean	55.84
Std Dev	3.213

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch47, adc5

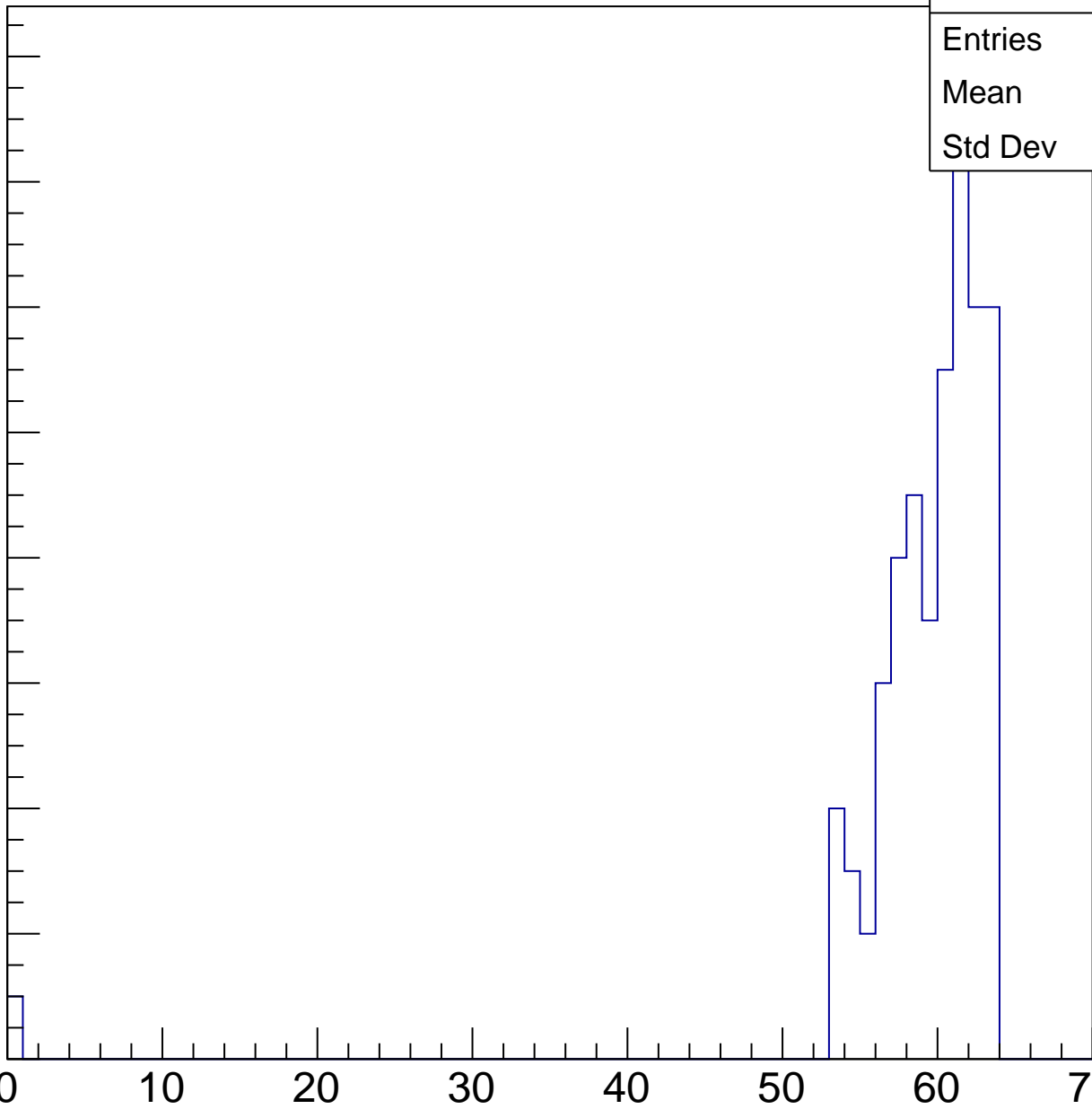
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	91
Mean	58.76
Std Dev	6.791

ampl

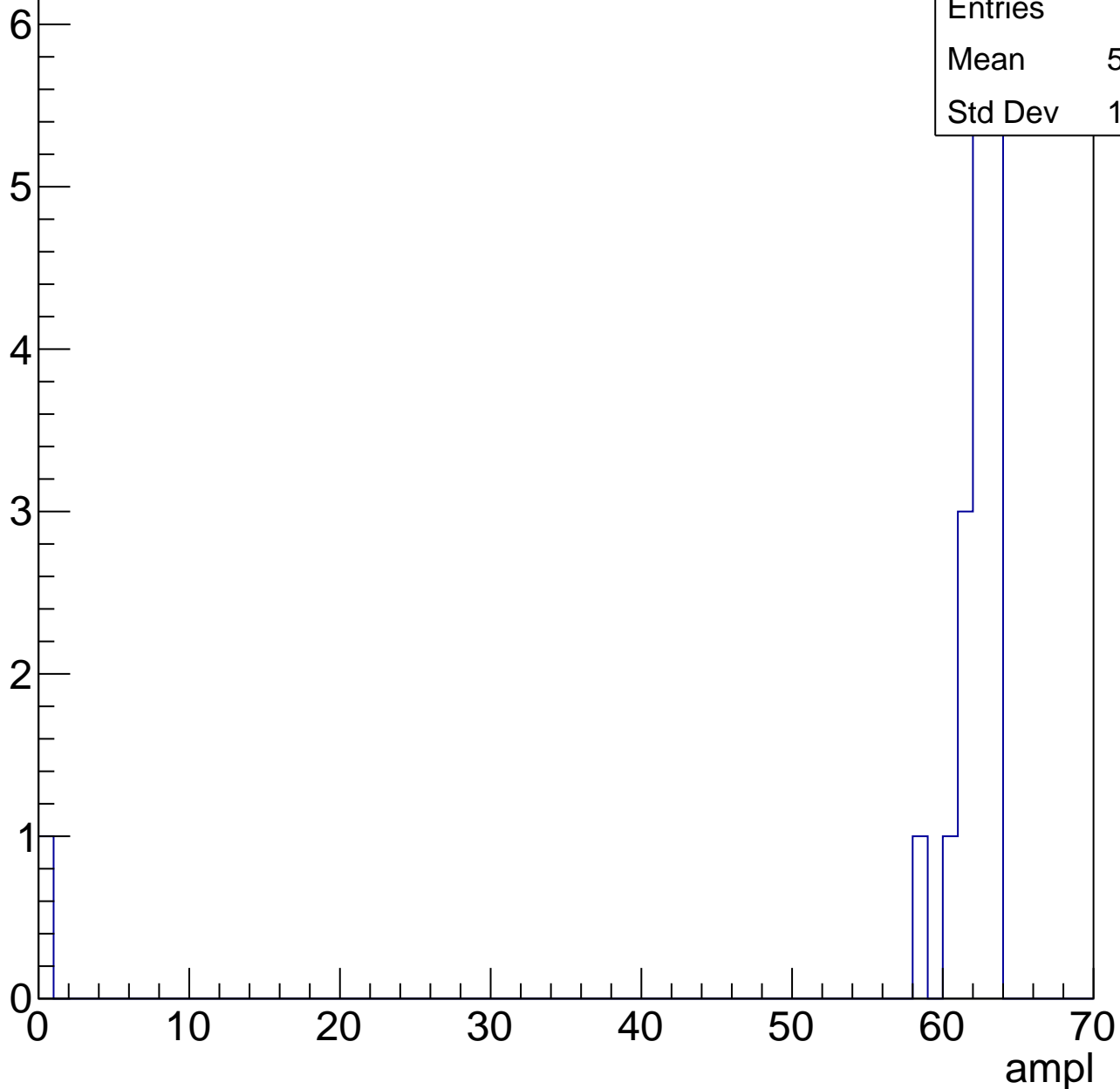


B1L001S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.39
Std Dev	14.22



B1L001S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch48, adc0

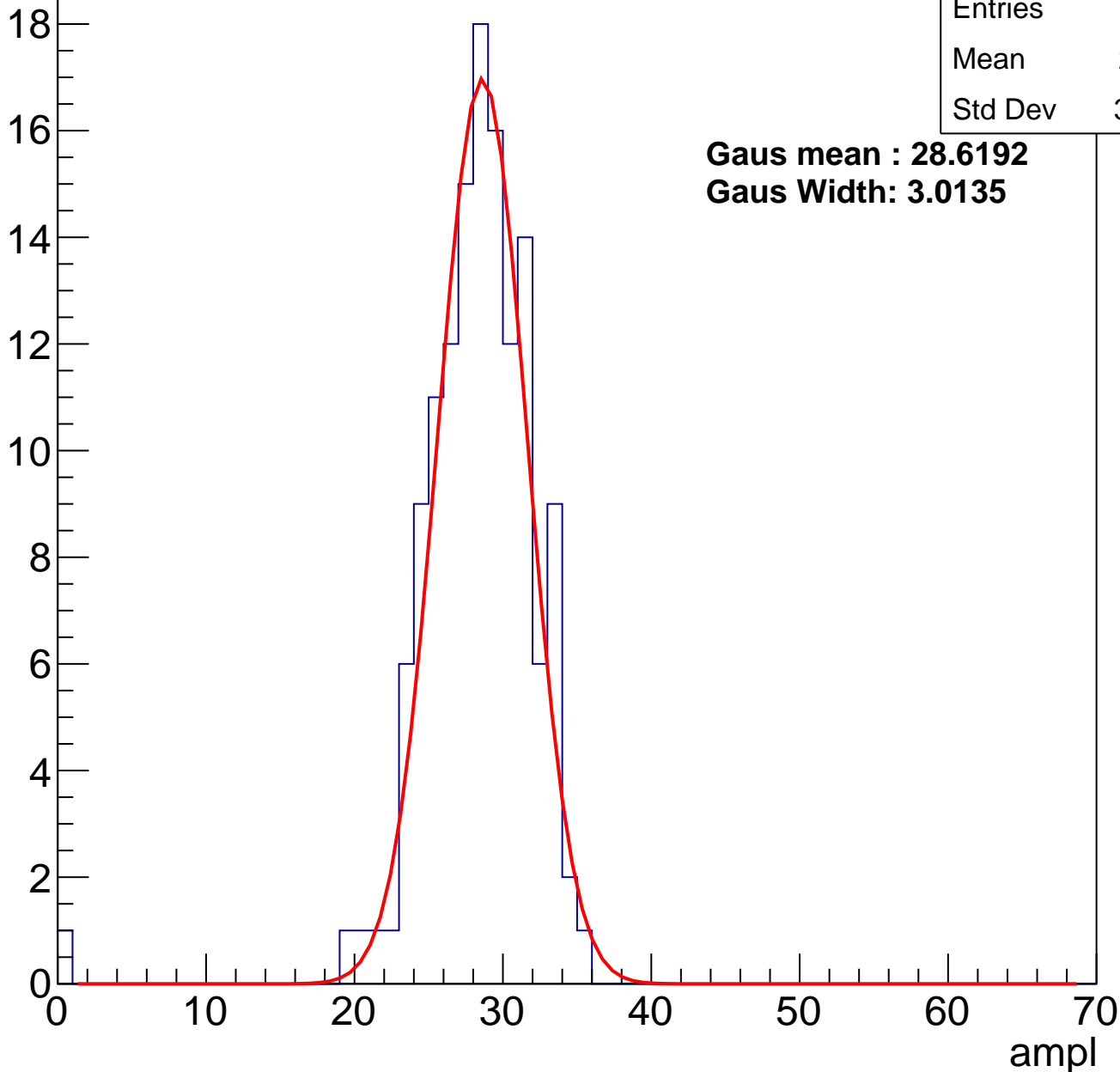
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	27.81
Std Dev	3.927

Gaus mean : 28.6192

Gaus Width: 3.0135

Entry

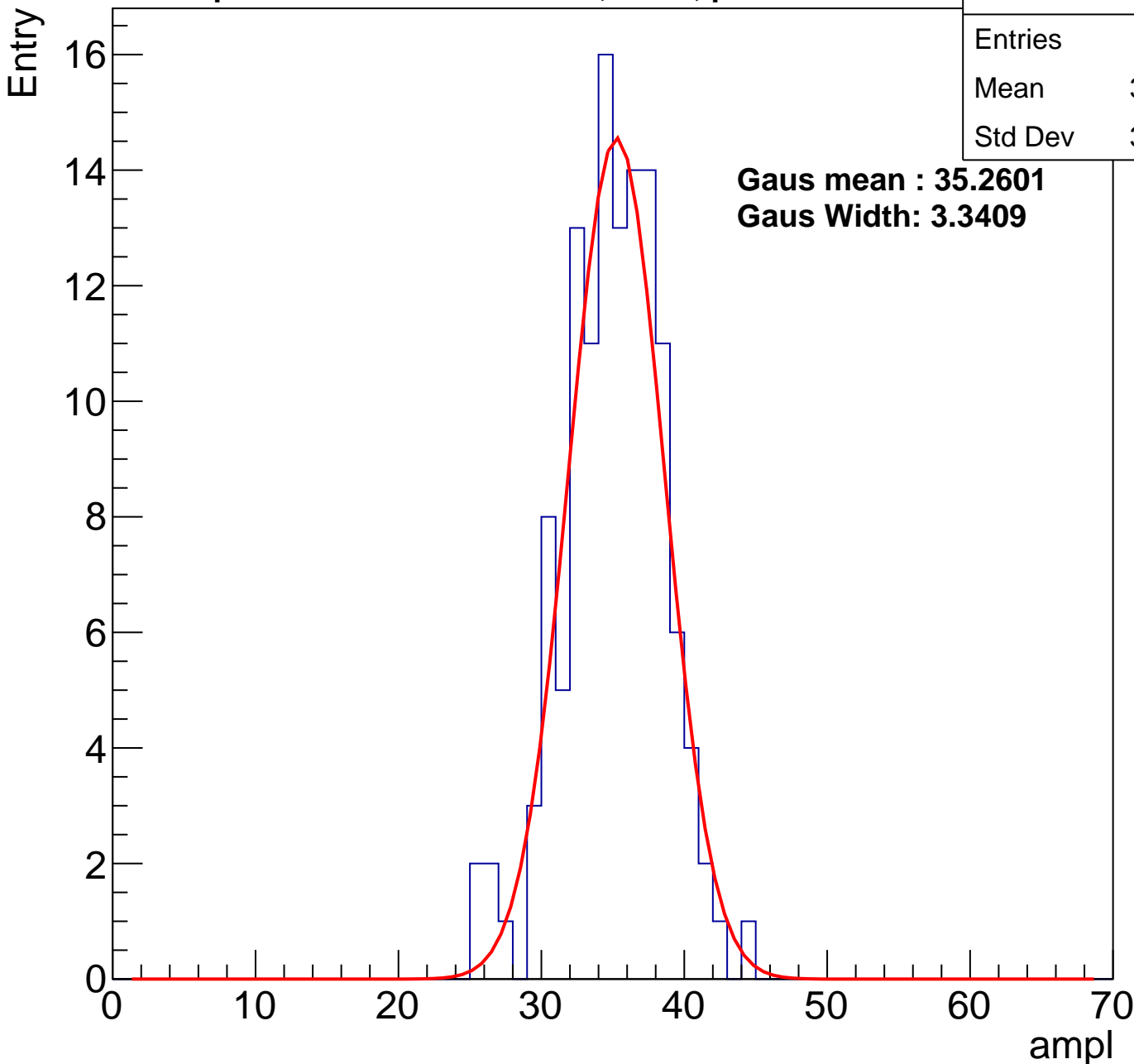


B1L001S, U14-ch48, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	34.57
Std Dev	3.477

Gaus mean : 35.2601
Gaus Width: 3.3409



B1L001S, U14-ch48, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	41.25
Std Dev	3.591

Gaus mean : 41.5208

Gaus Width: 3.6763

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

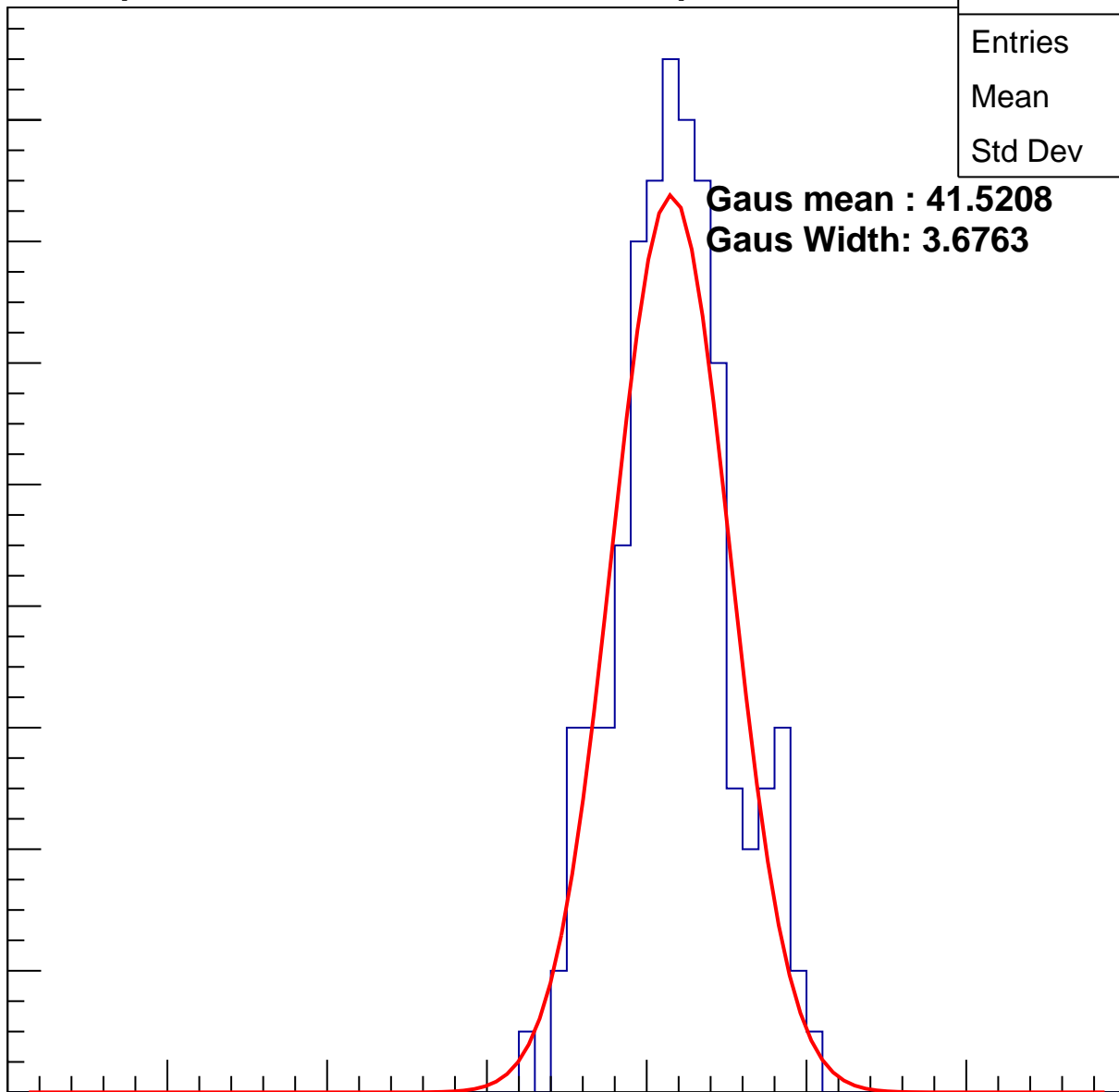
40

50

60

70

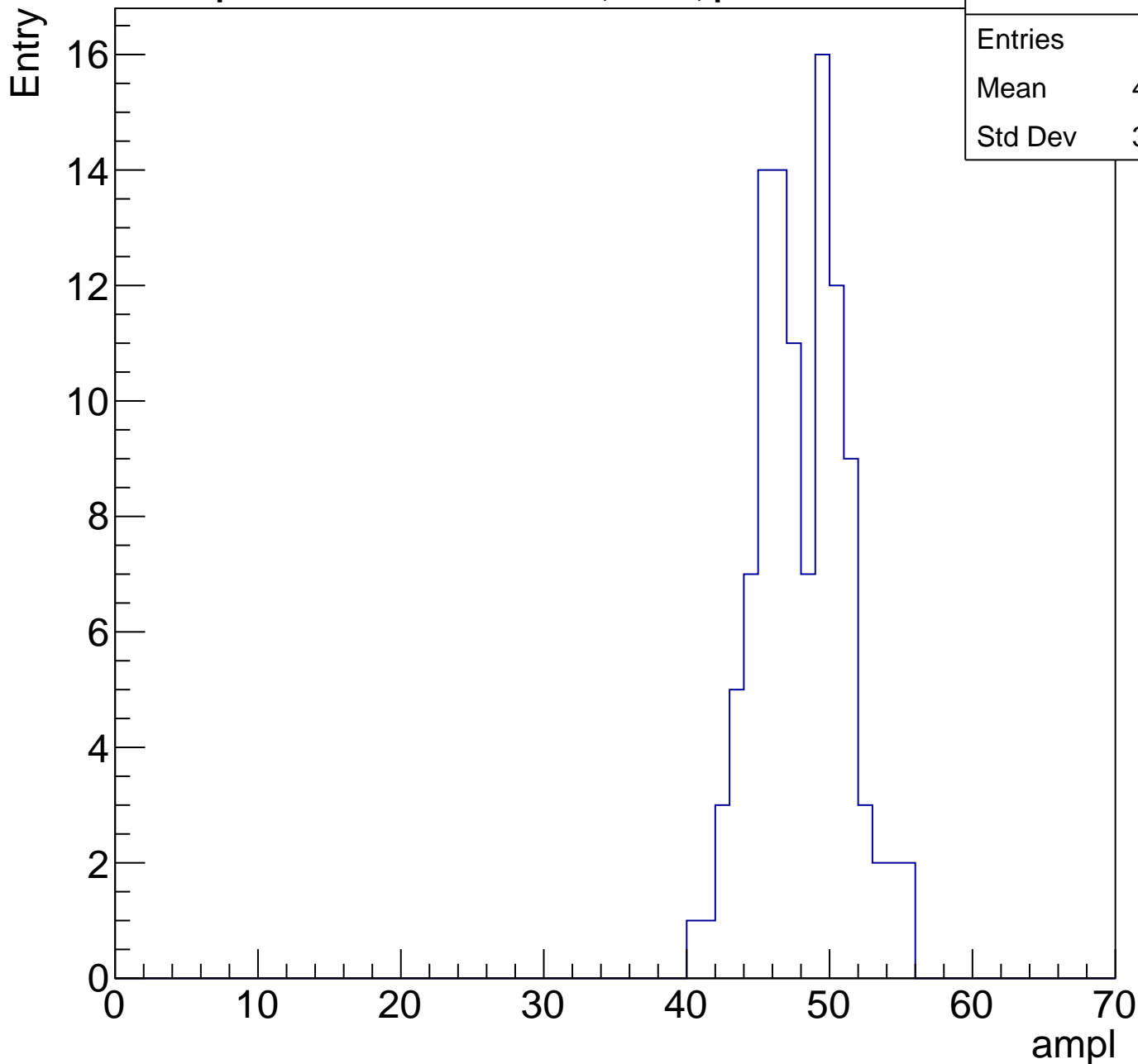
ampl



B1L001S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	47.52
Std Dev	3.088



B1L001S, U14-ch48, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	53.01
Std Dev	3.457

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

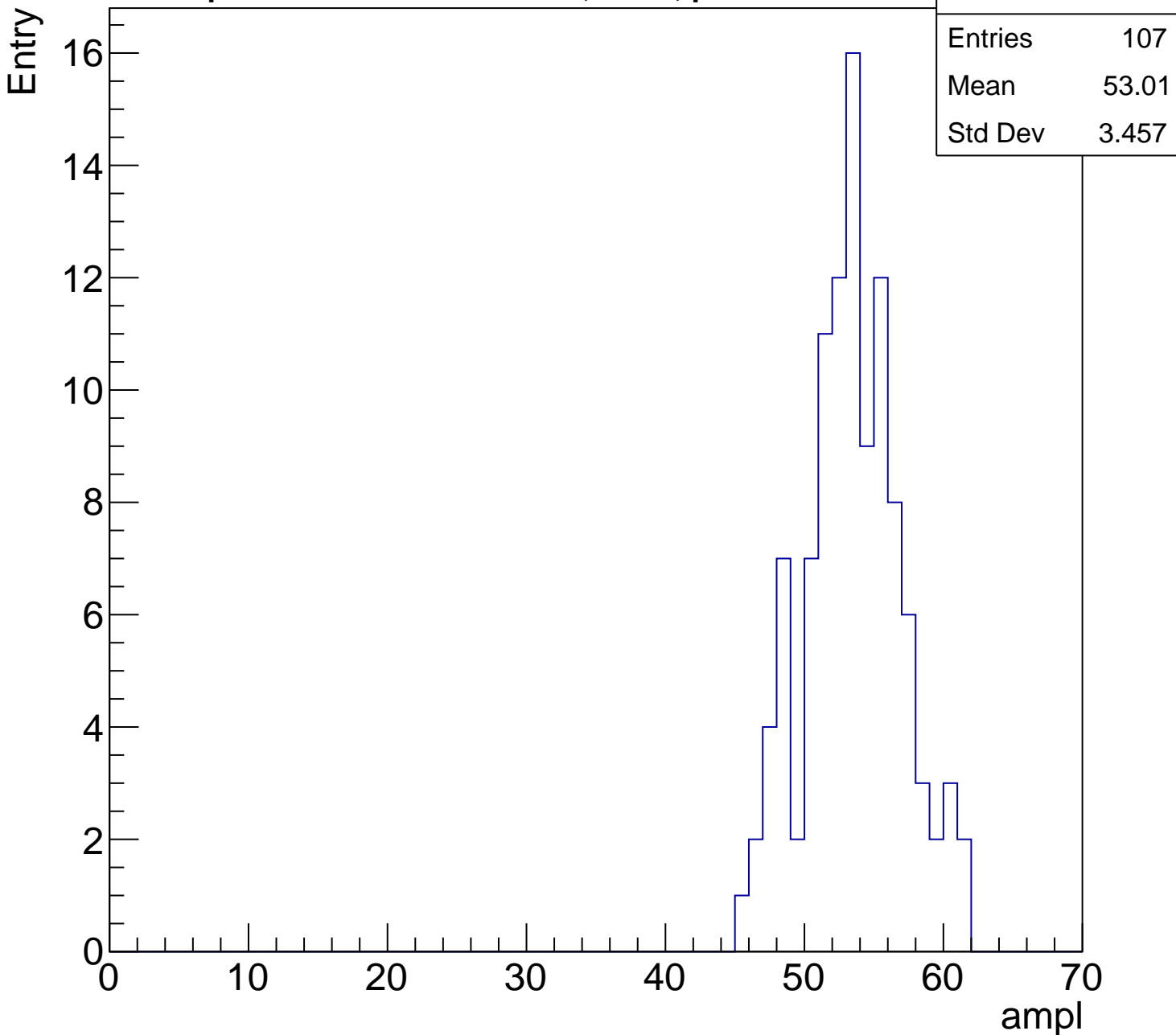
40

50

60

70

ampl

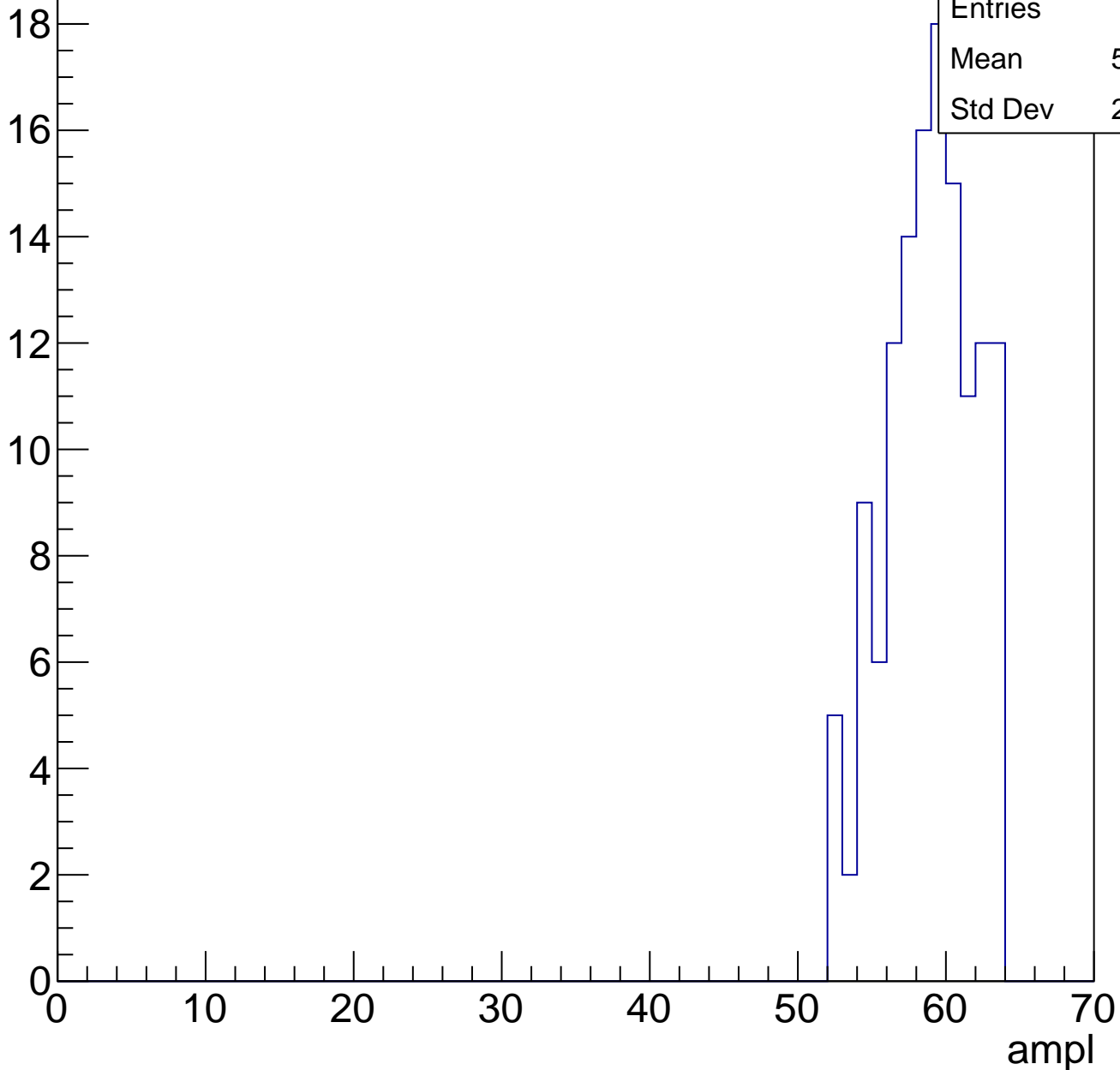


B1L001S, U14-ch48, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	58.43
Std Dev	2.932

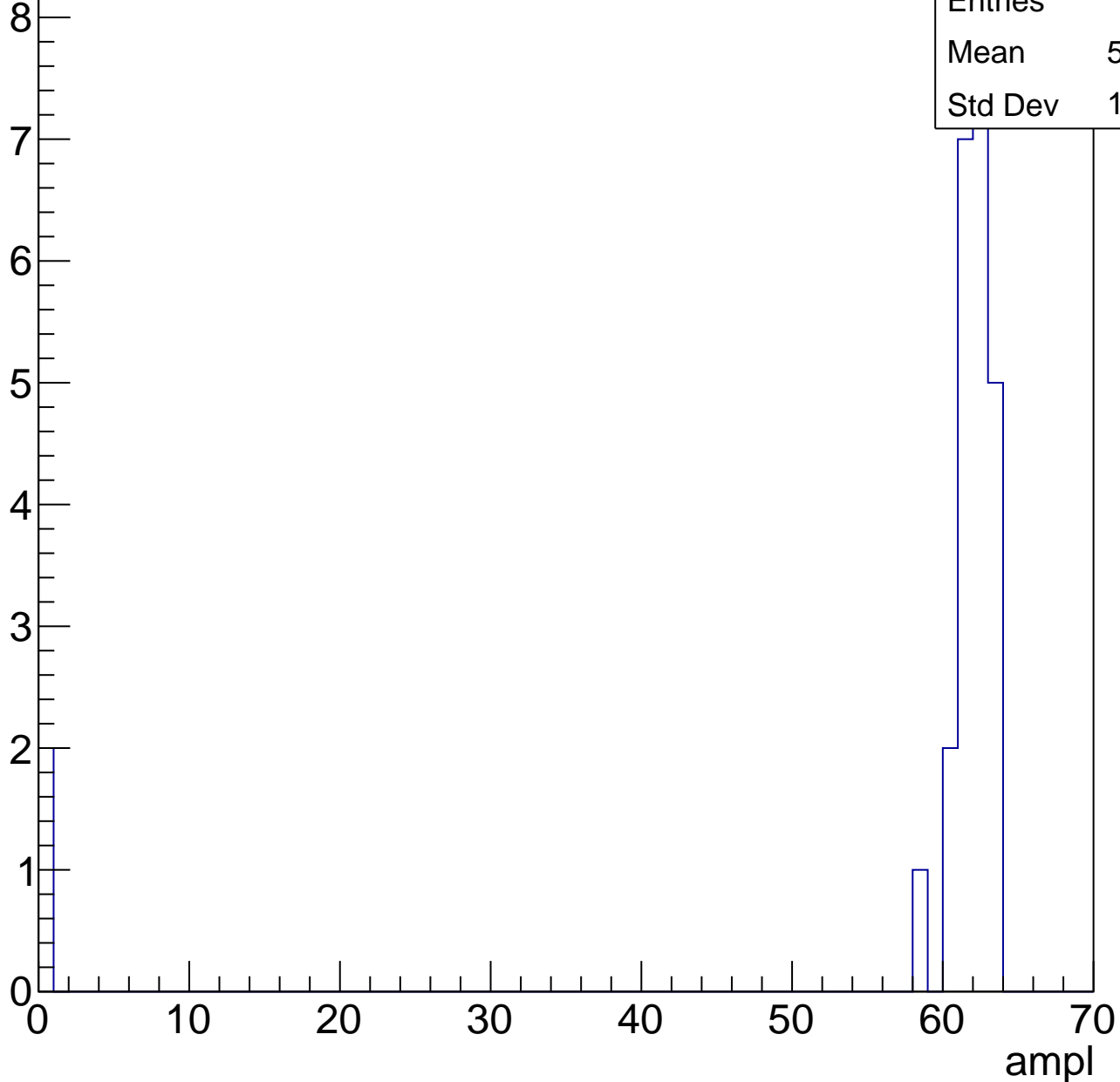
Entry



B1L001S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#2, port C2

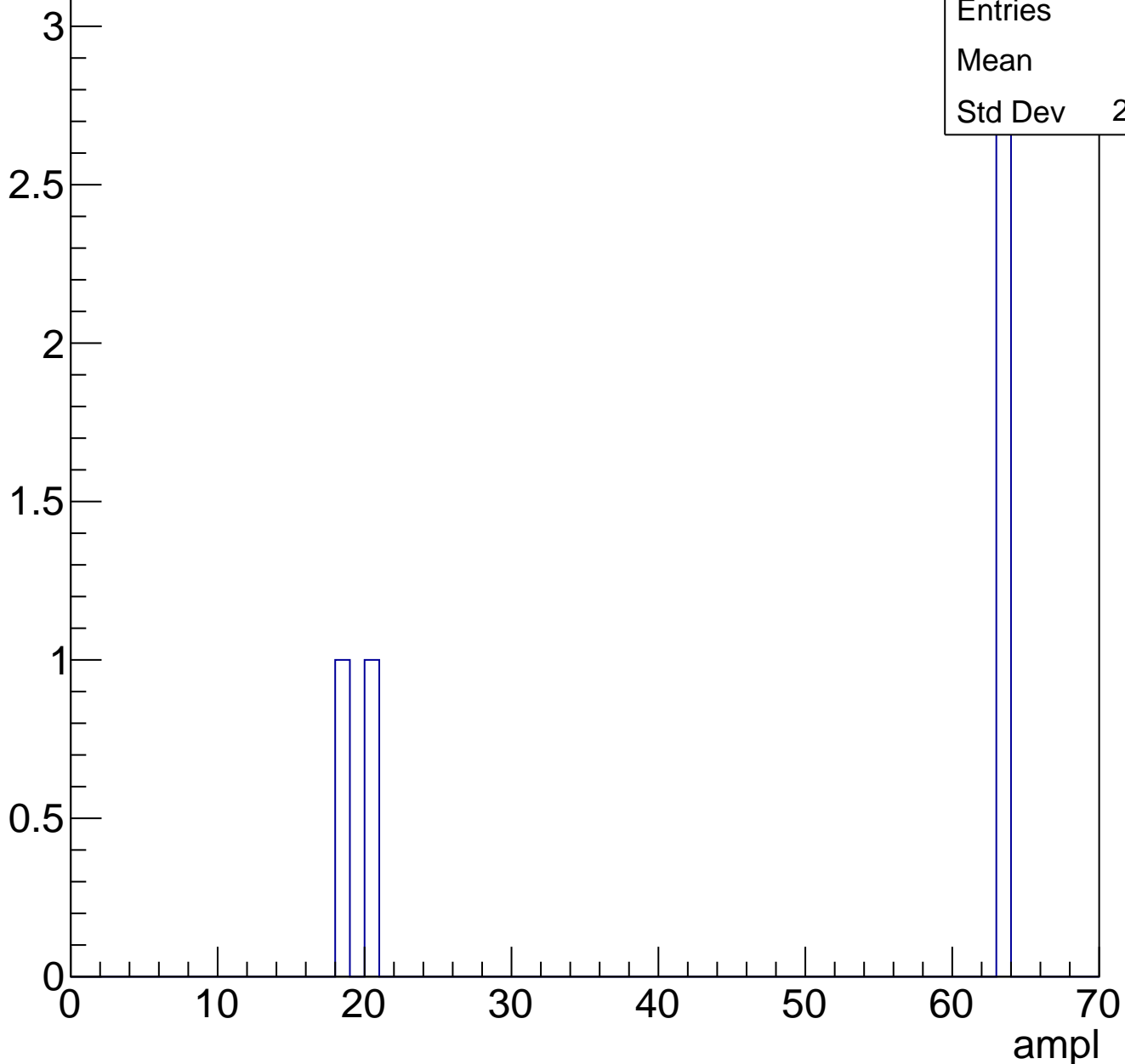
Entry



B1L001S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch49, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	28.49
Std Dev	3.315

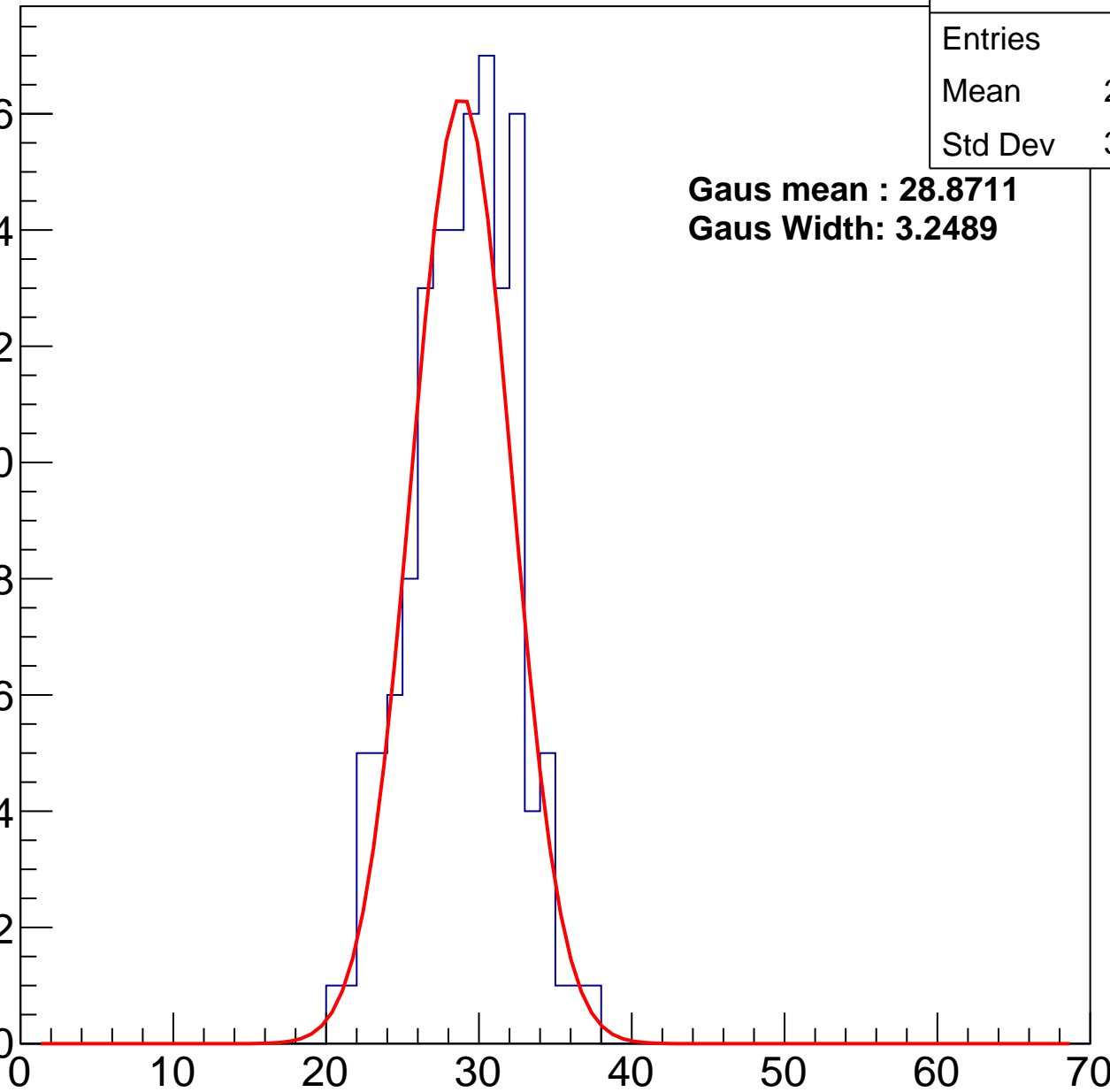
Gaus mean : 28.8711

Gaus Width: 3.2489

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch49, adc1

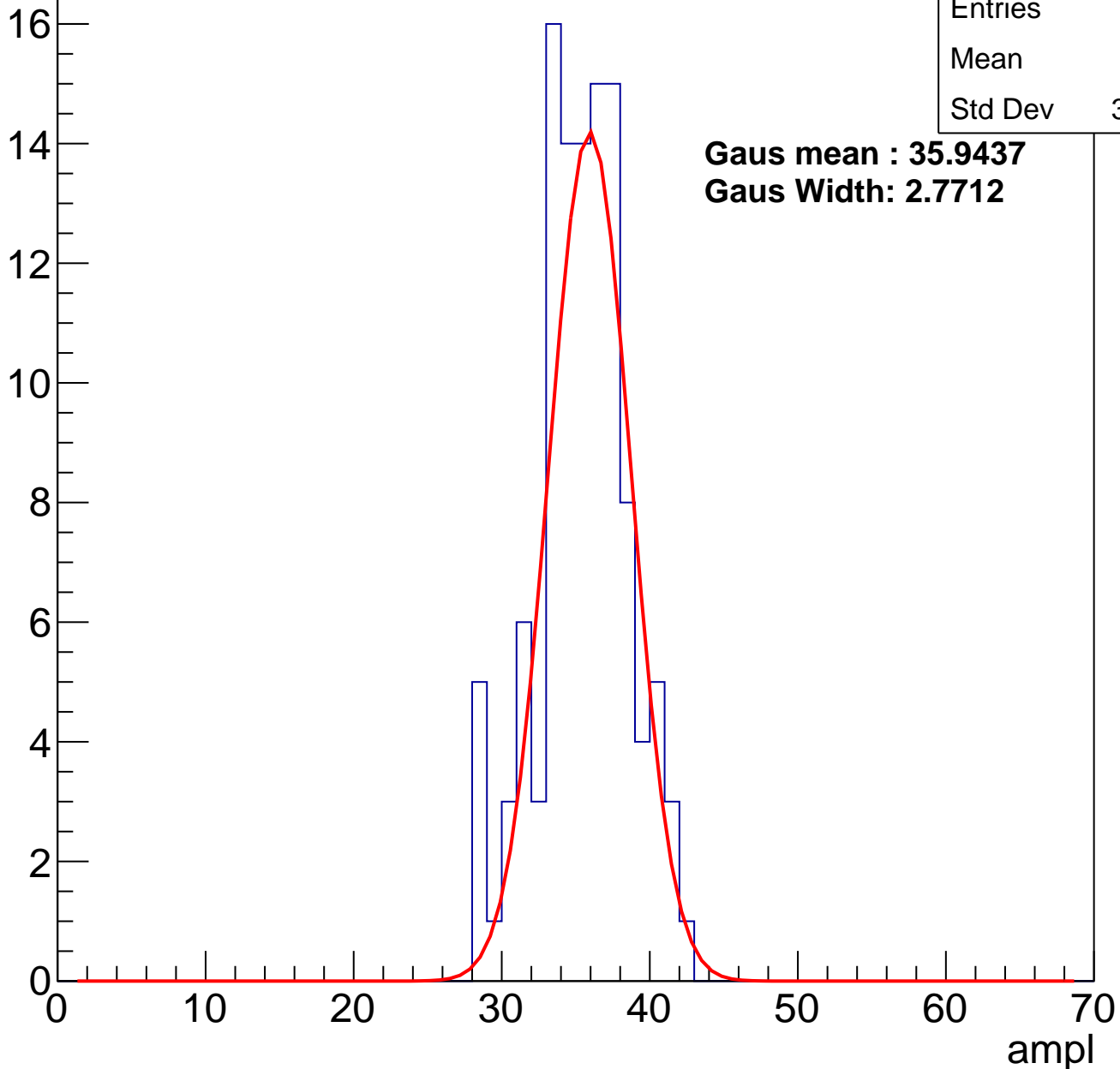
calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	35
Std Dev	3.048

Gaus mean : 35.9437

Gaus Width: 2.7712

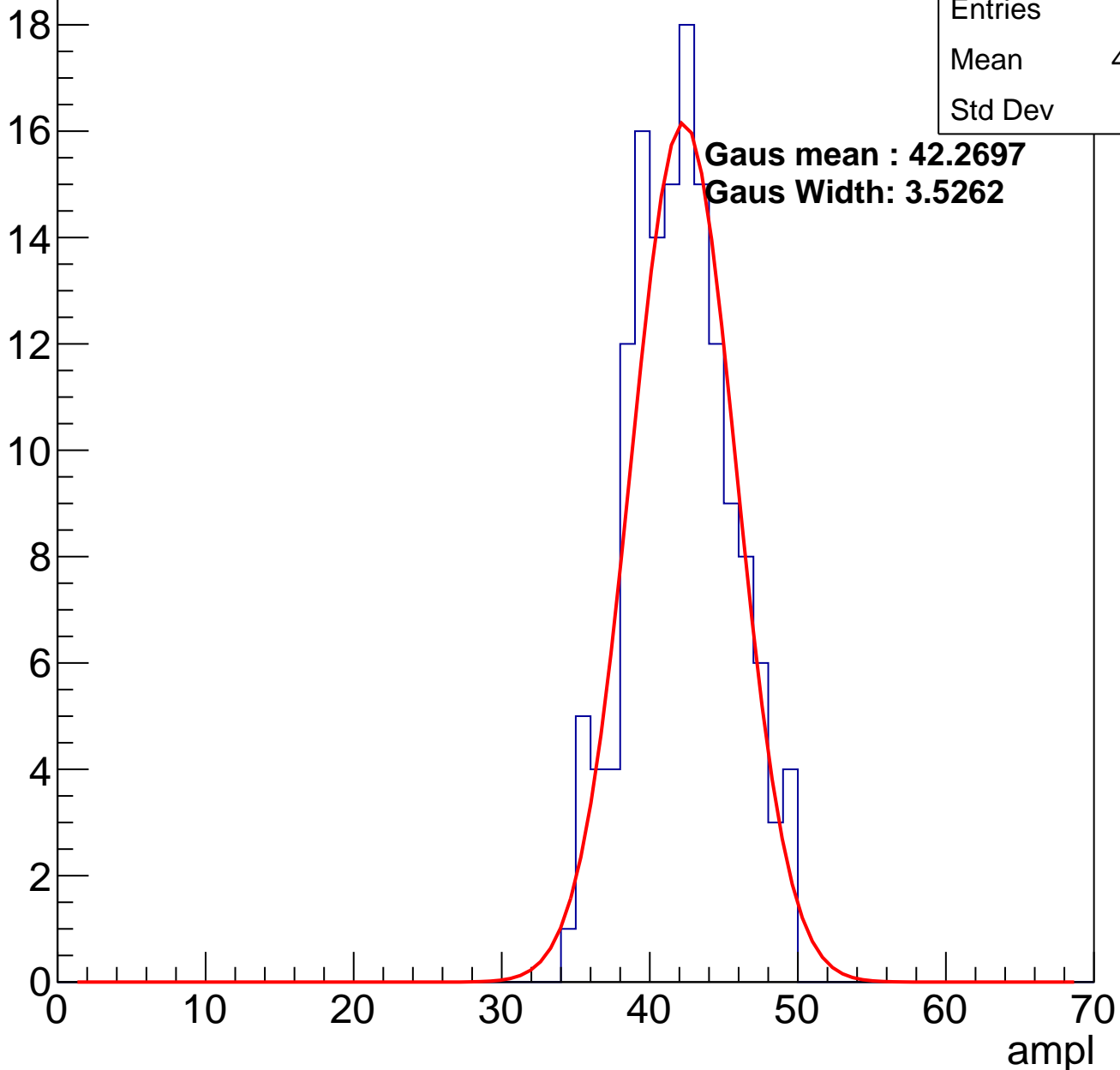
Entry



B1L001S, U14-ch49, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

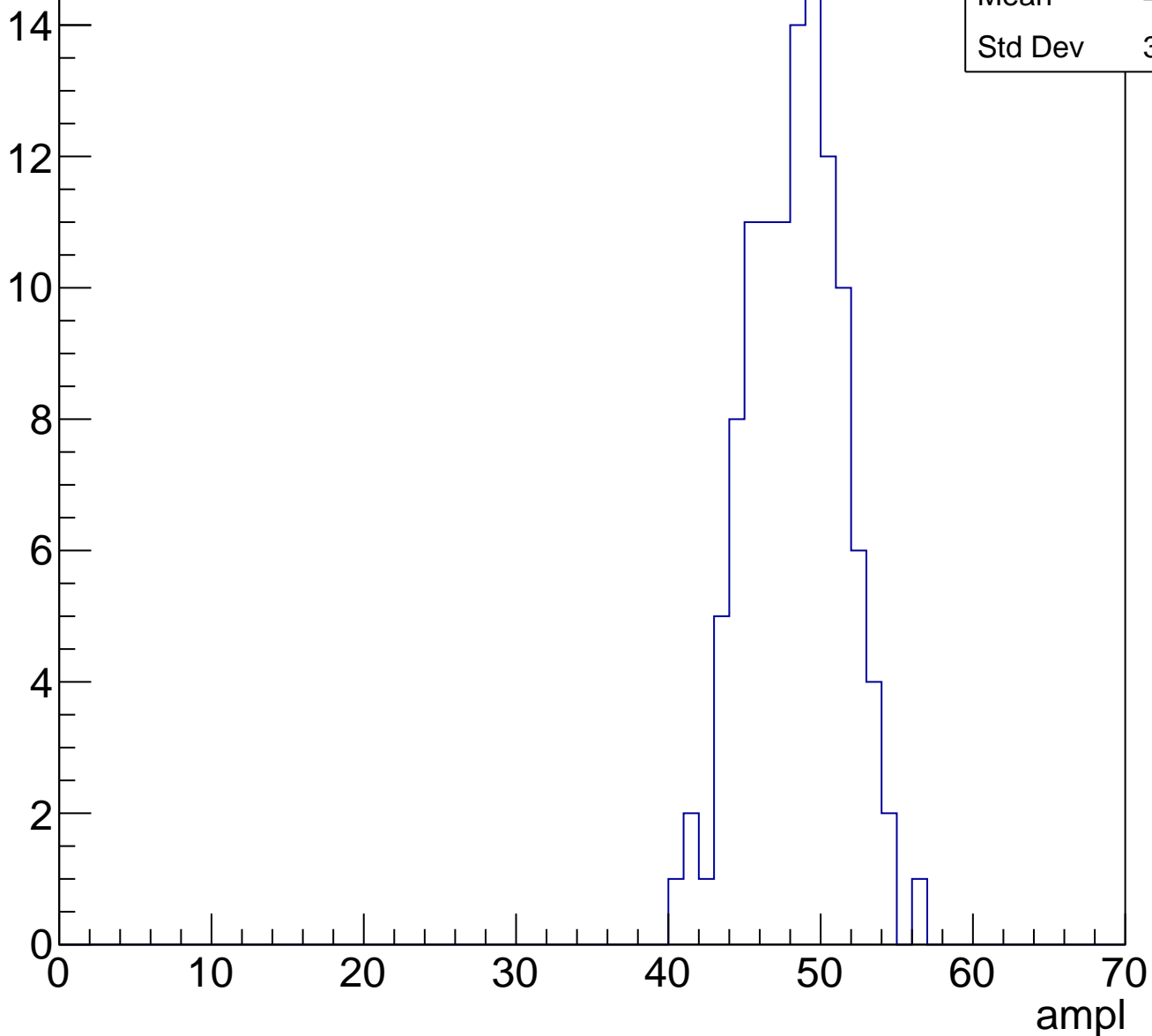


B1L001S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	47.84
Std Dev	3.088

Entry



B1L001S, U14-ch49, adc4

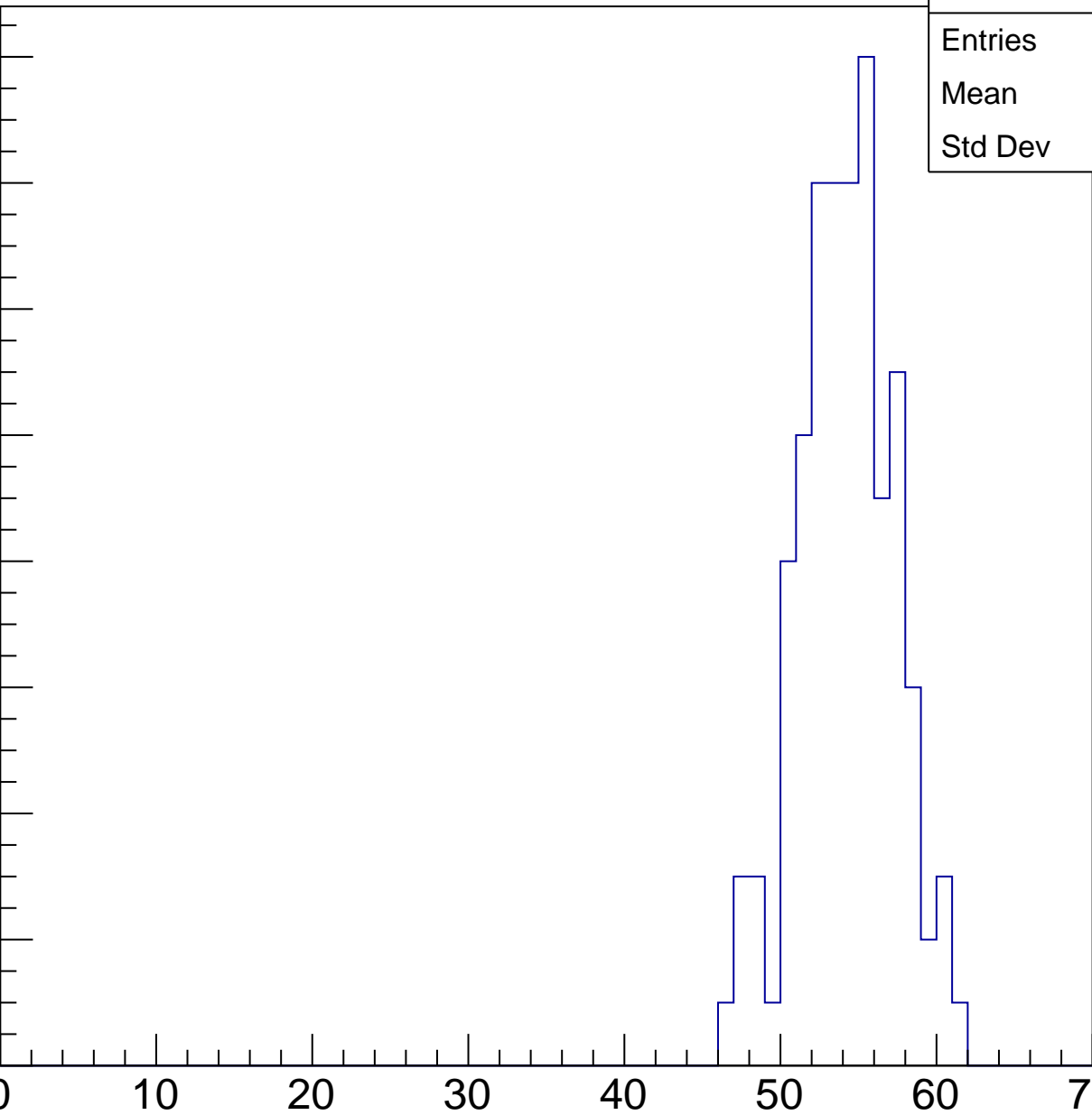
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	116
Mean	53.74
Std Dev	3.049

ampl



B1L001S, U14-ch49, adc5

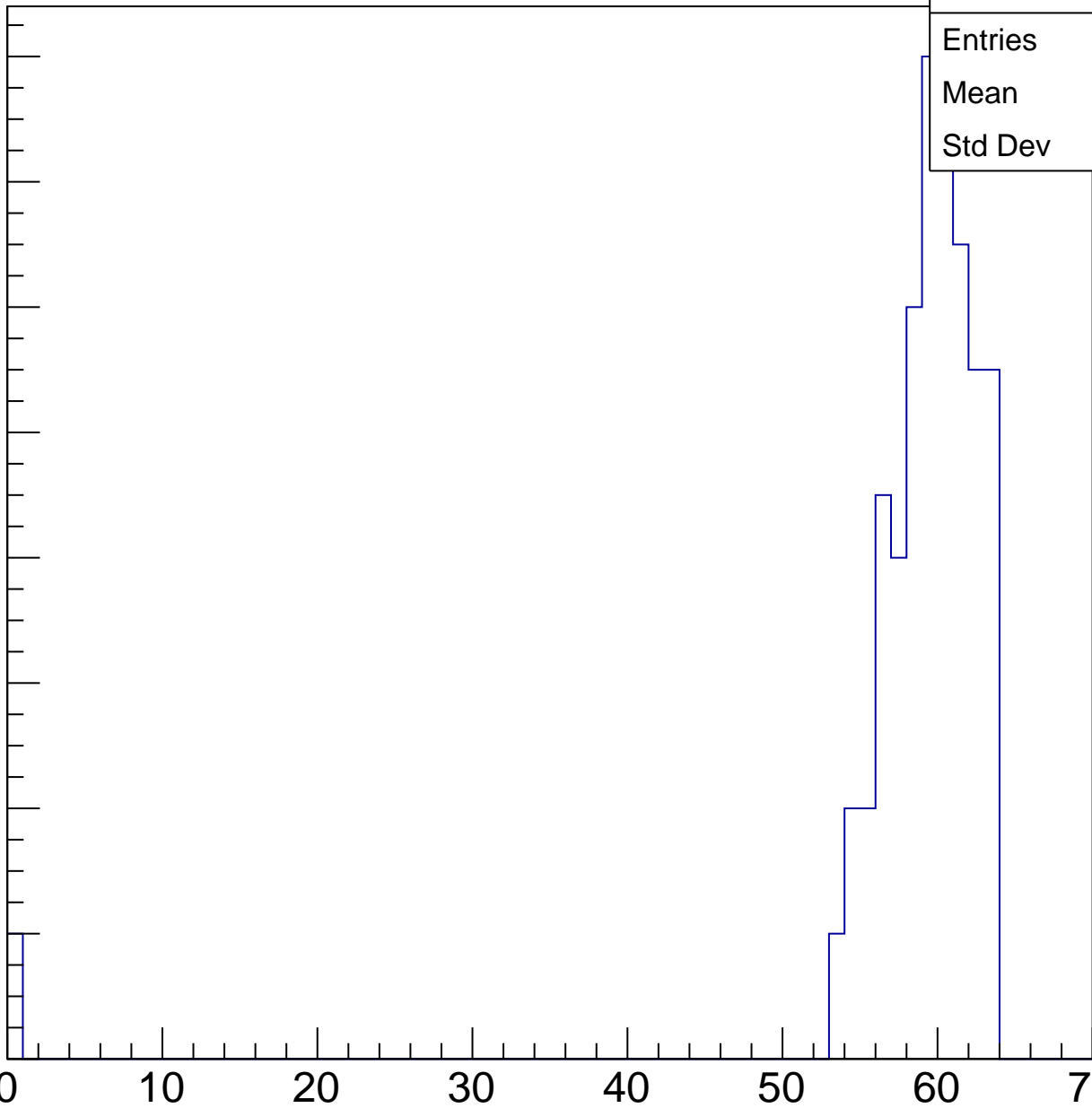
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	107
Mean	58.04
Std Dev	8.409

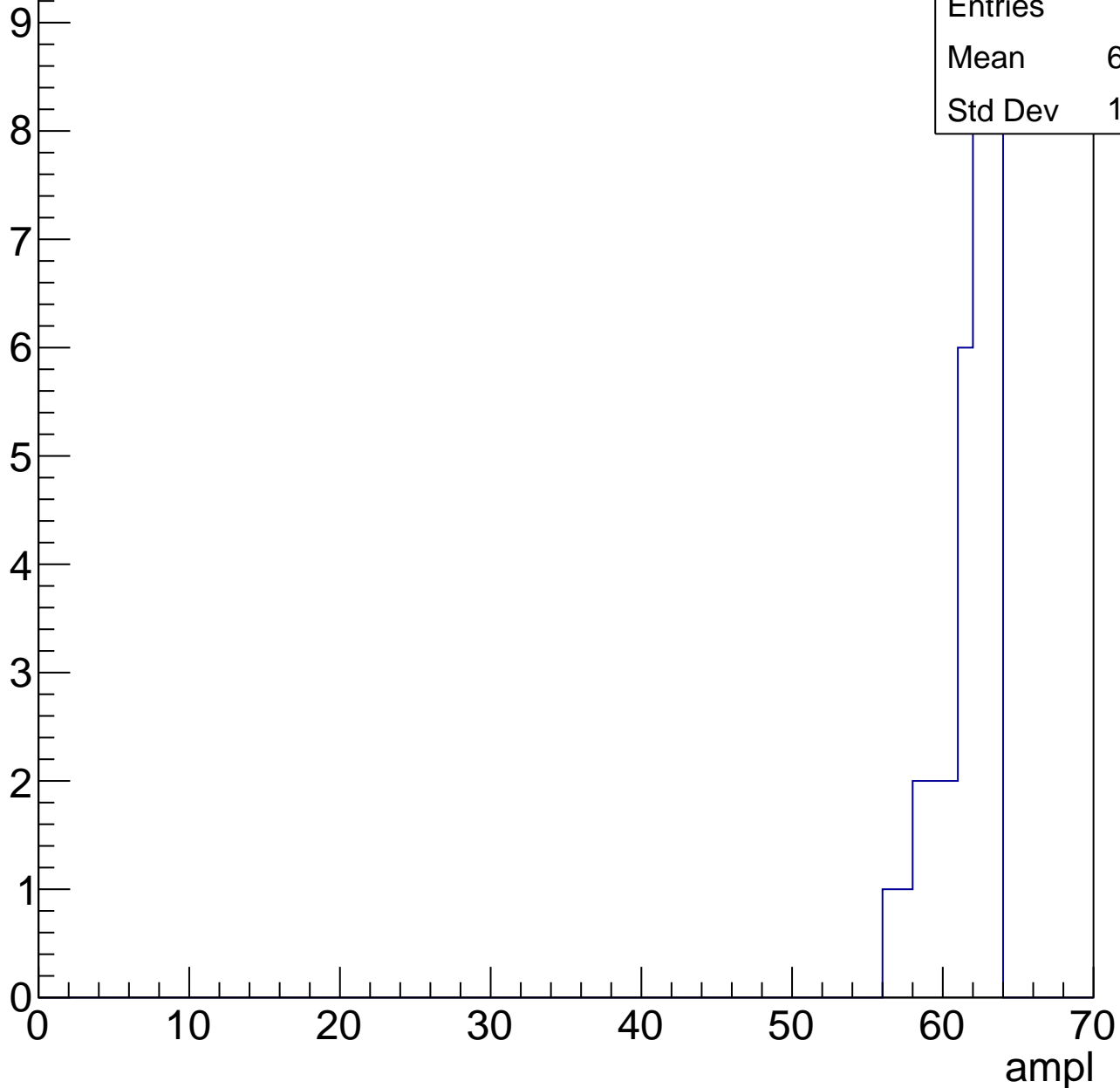
ampl



B1L001S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch50, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

135

Mean

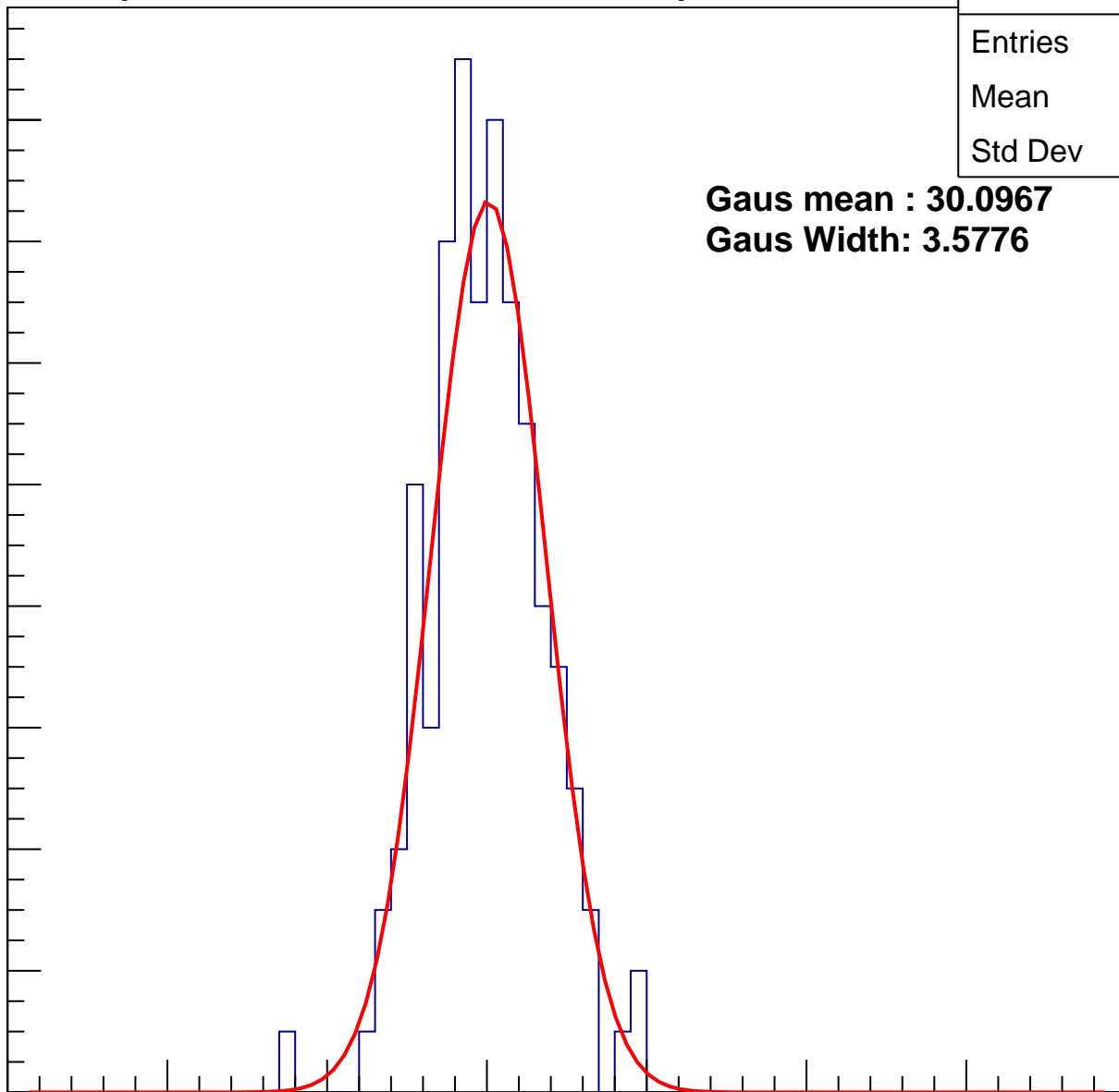
29.46

Std Dev

3.588

Gaus mean : 30.0967

Gaus Width: 3.5776



B1L001S, U14-ch50, adc1

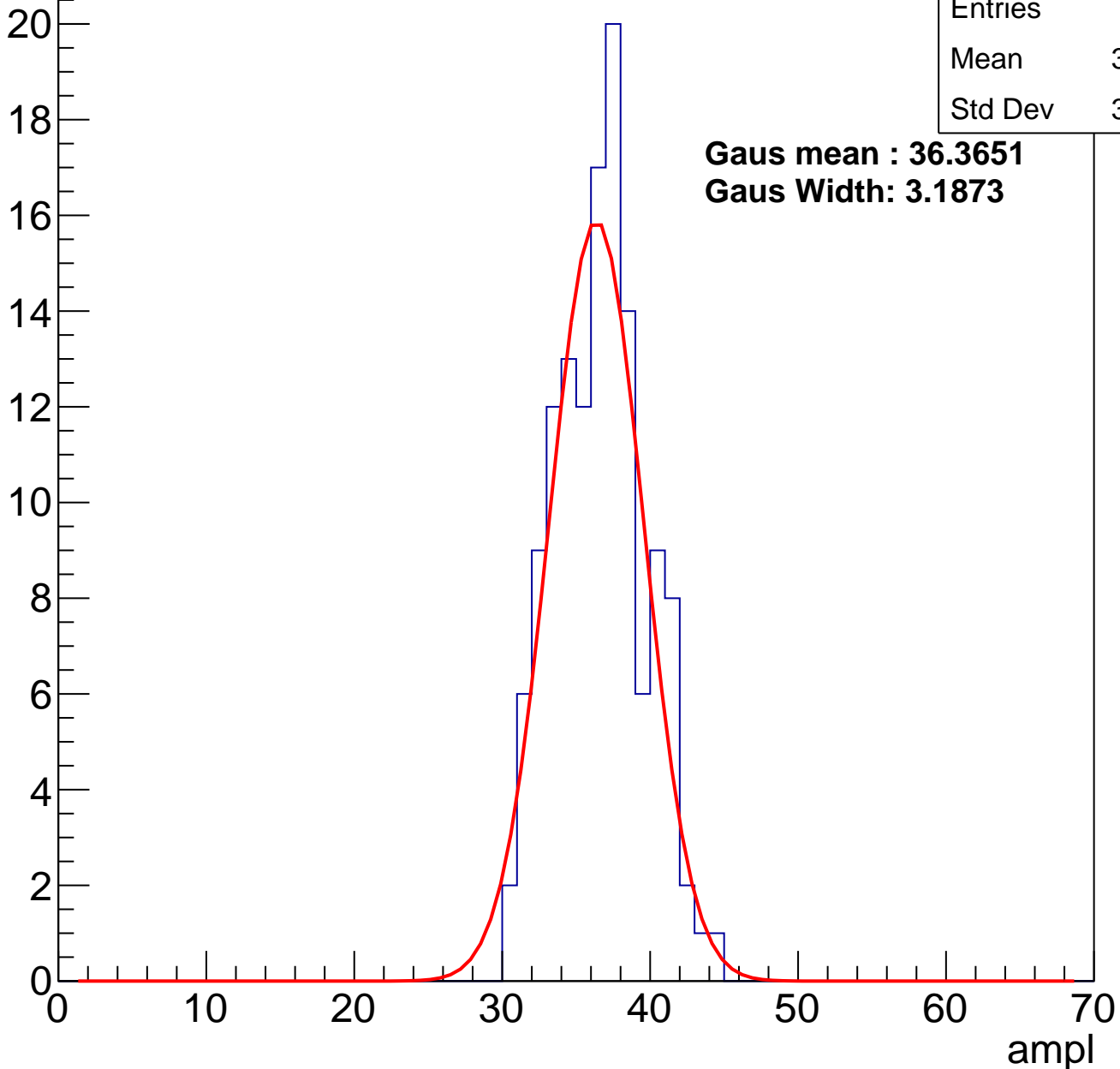
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	36.13
Std Dev	3.009

Gaus mean : 36.3651

Gaus Width: 3.1873

Entry



B1L001S, U14-ch50, adc2

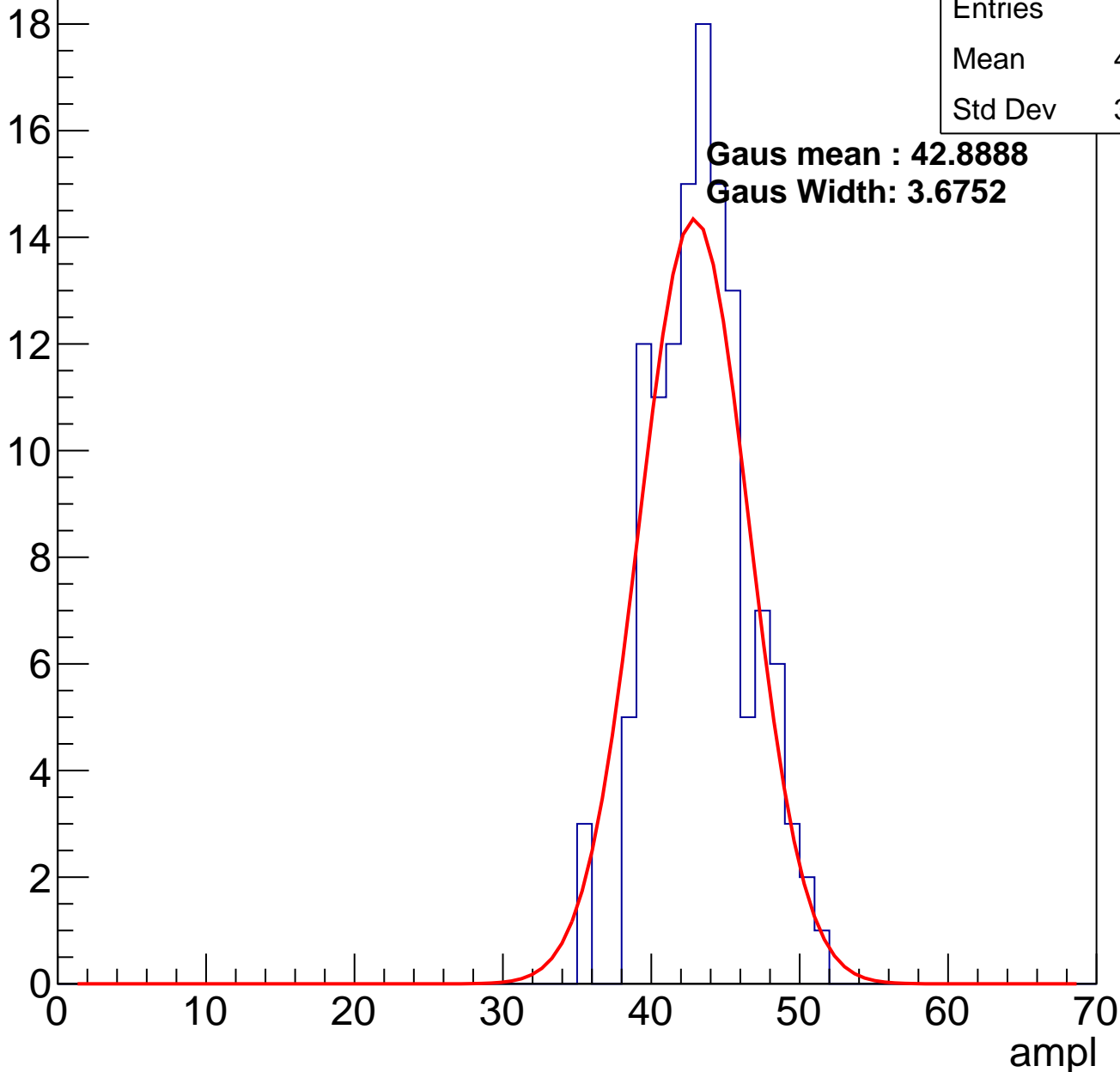
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	42.88
Std Dev	3.203

Gaus mean : 42.8888

Gaus Width: 3.6752

Entry

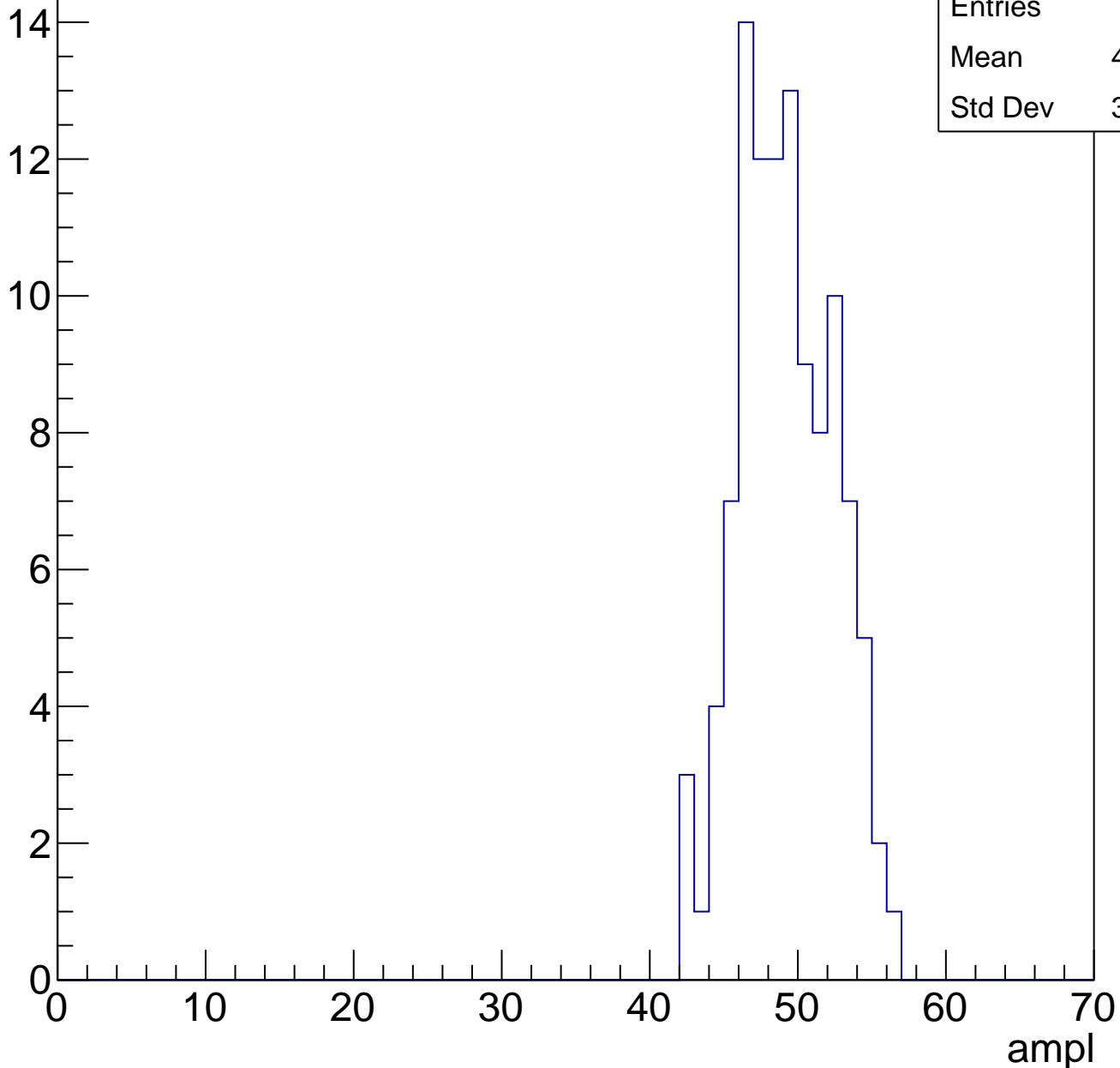


B1L001S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	48.76
Std Dev	3.138

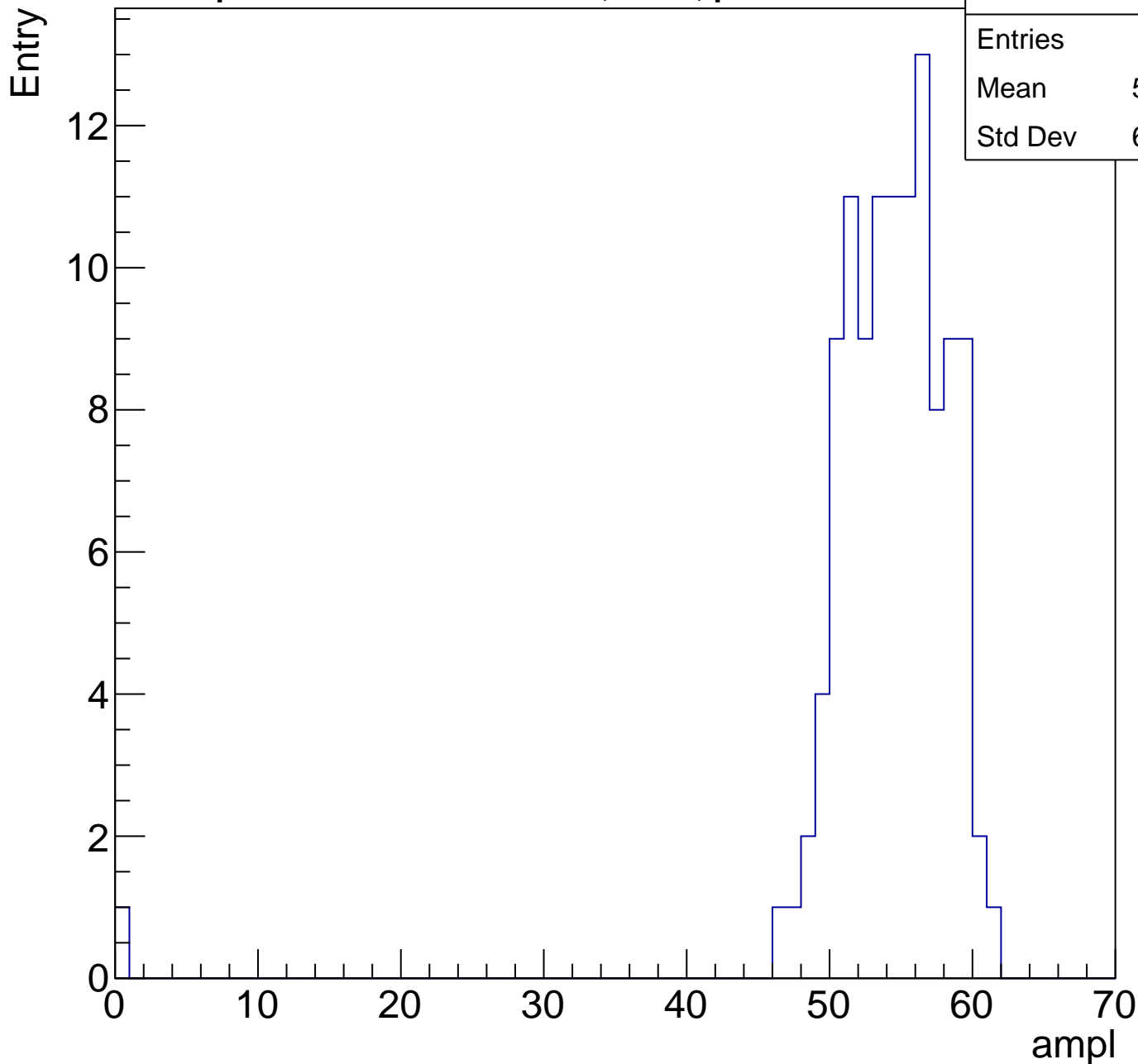
Entry



B1L001S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#2, port C2

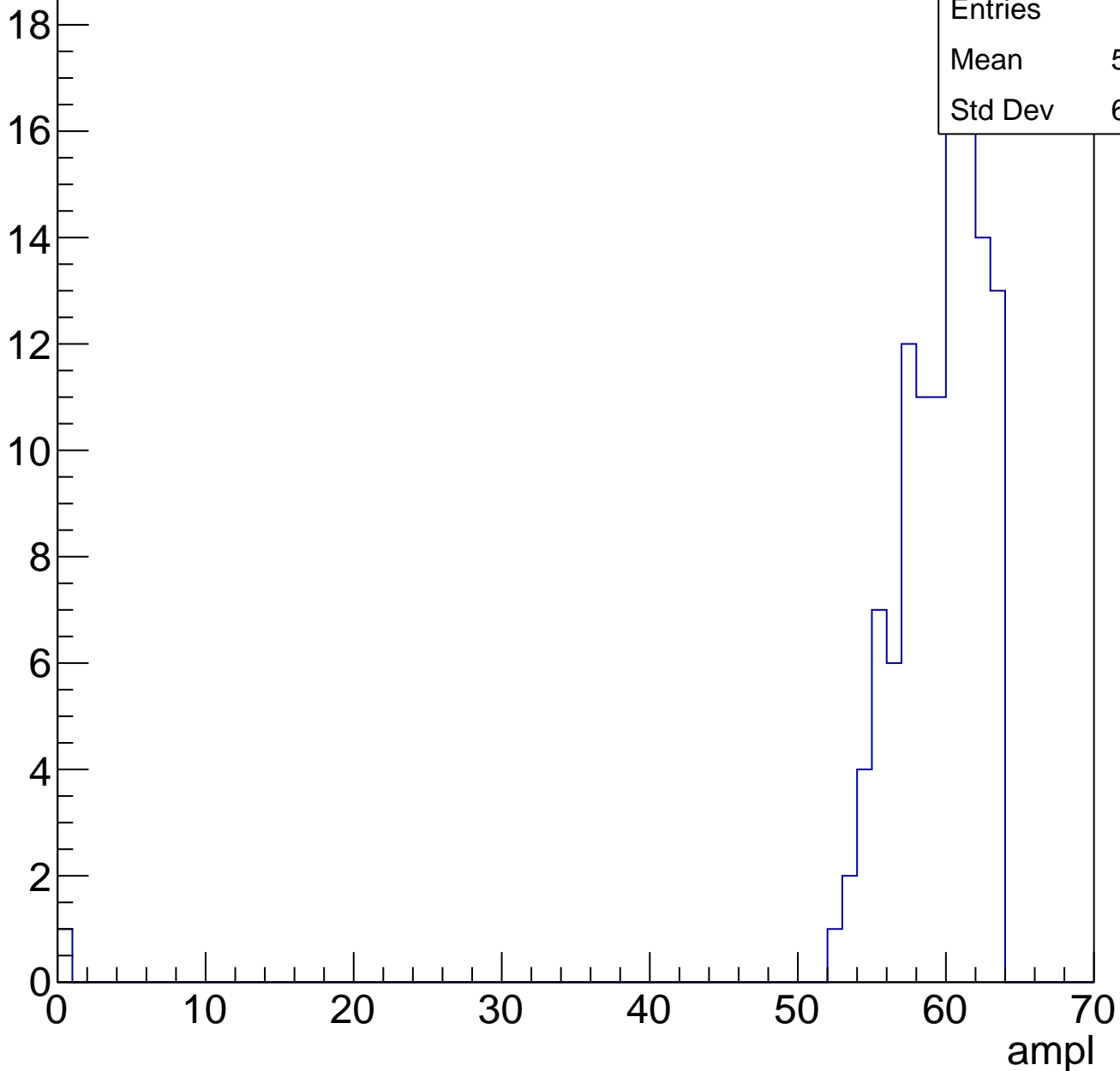
Entries	113
Mean	53.66
Std Dev	6.026



B1L001S, U14-ch50, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

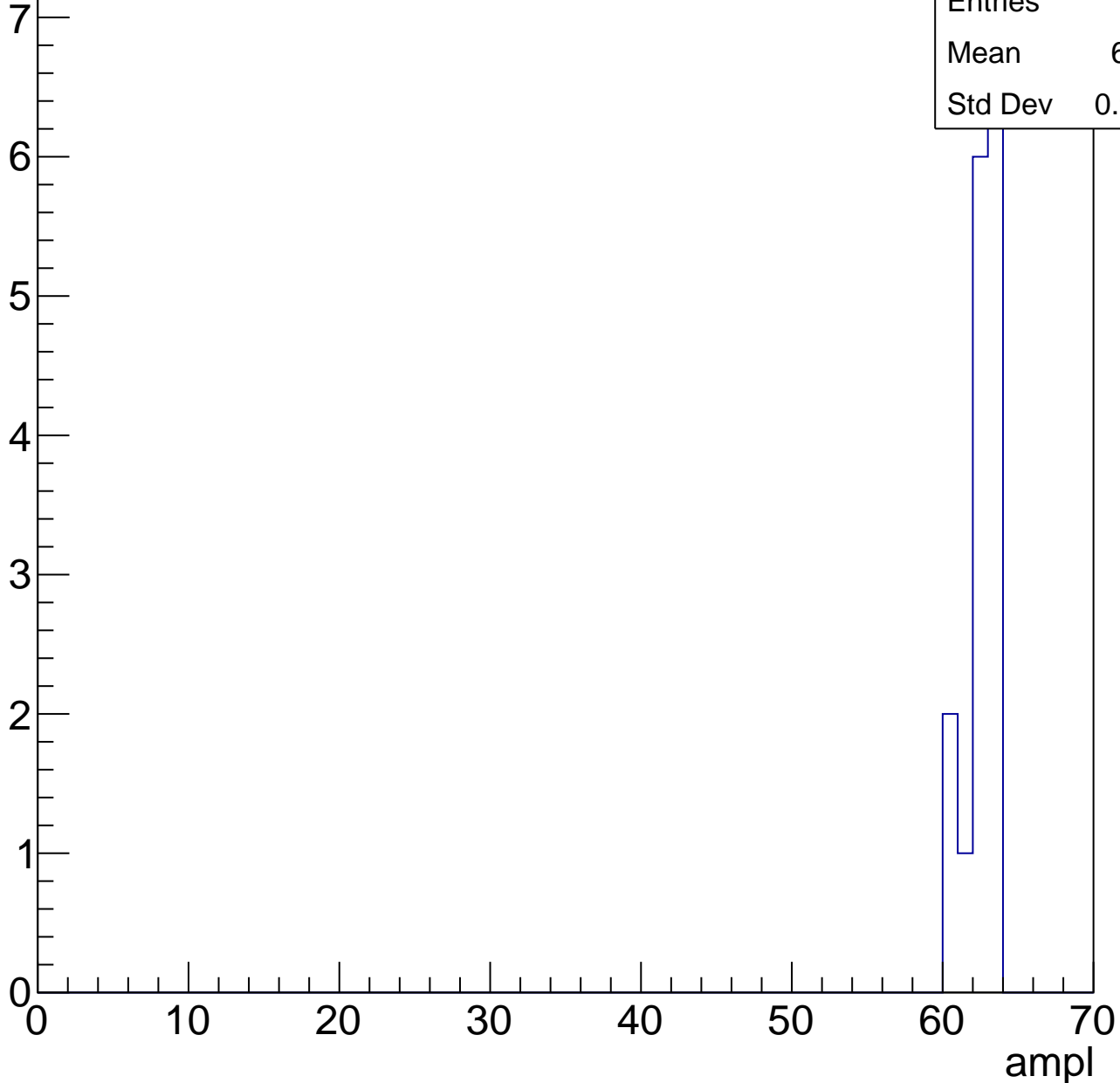


B1L001S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	16
Mean	62.12
Std Dev	0.9922



B1L001S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

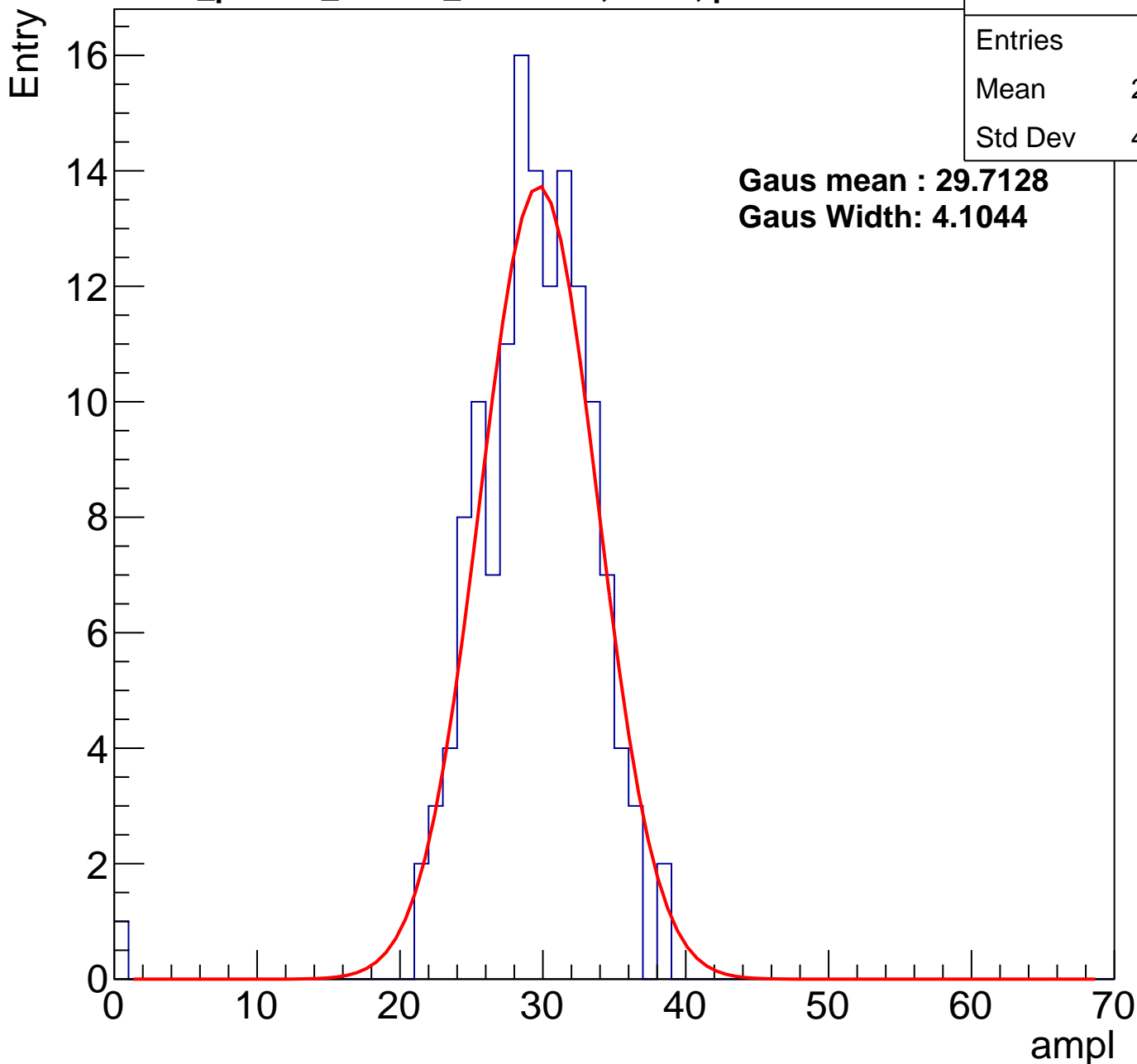
B1L001S, U14-ch51, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.89
Std Dev	4.396

Gaus mean : 29.7128

Gaus Width: 4.1044



B1L001S, U14-ch51, adc1

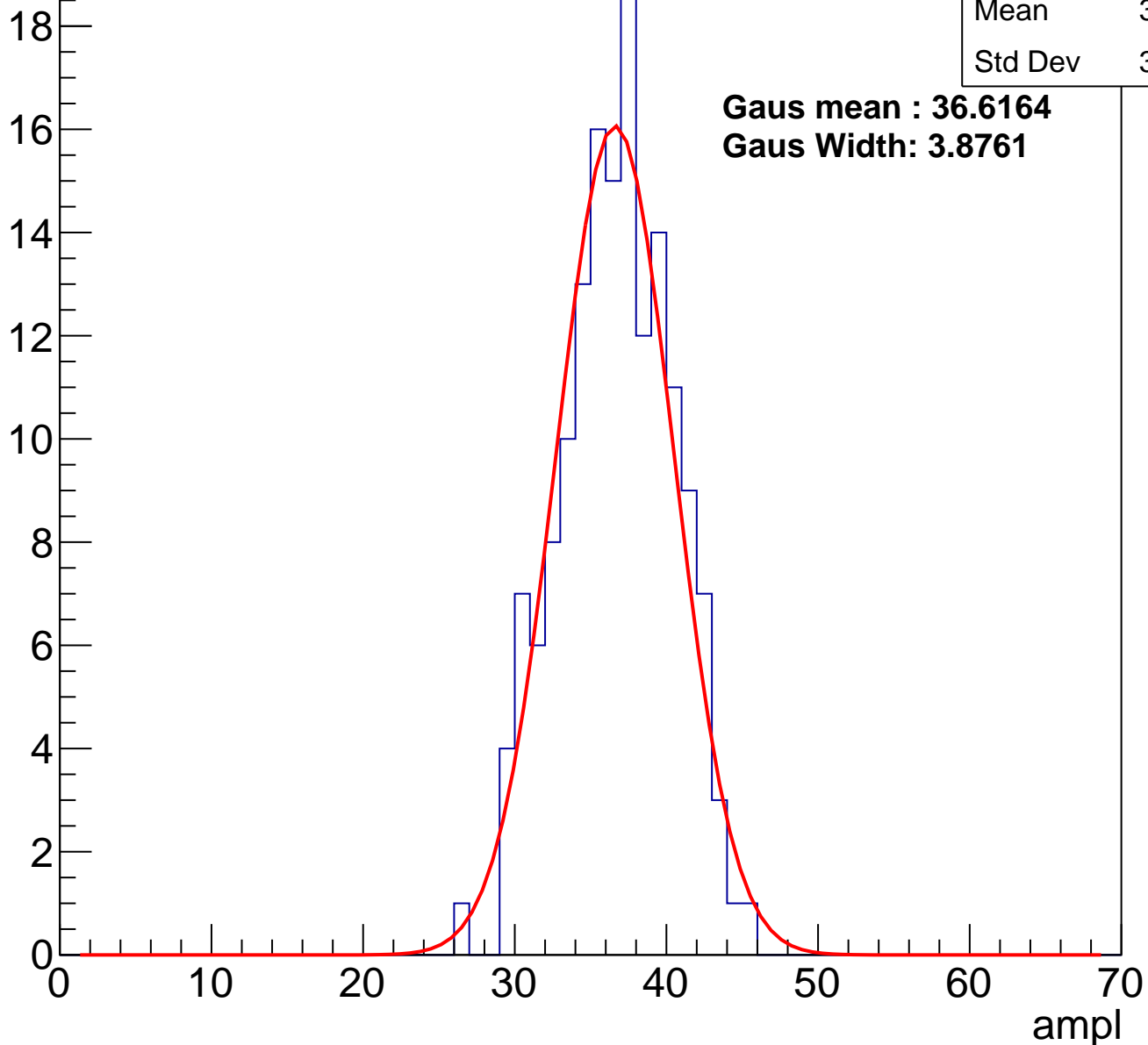
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	36.25
Std Dev	3.668

Gaus mean : 36.6164

Gaus Width: 3.8761

Entry



B1L001S, U14-ch51, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

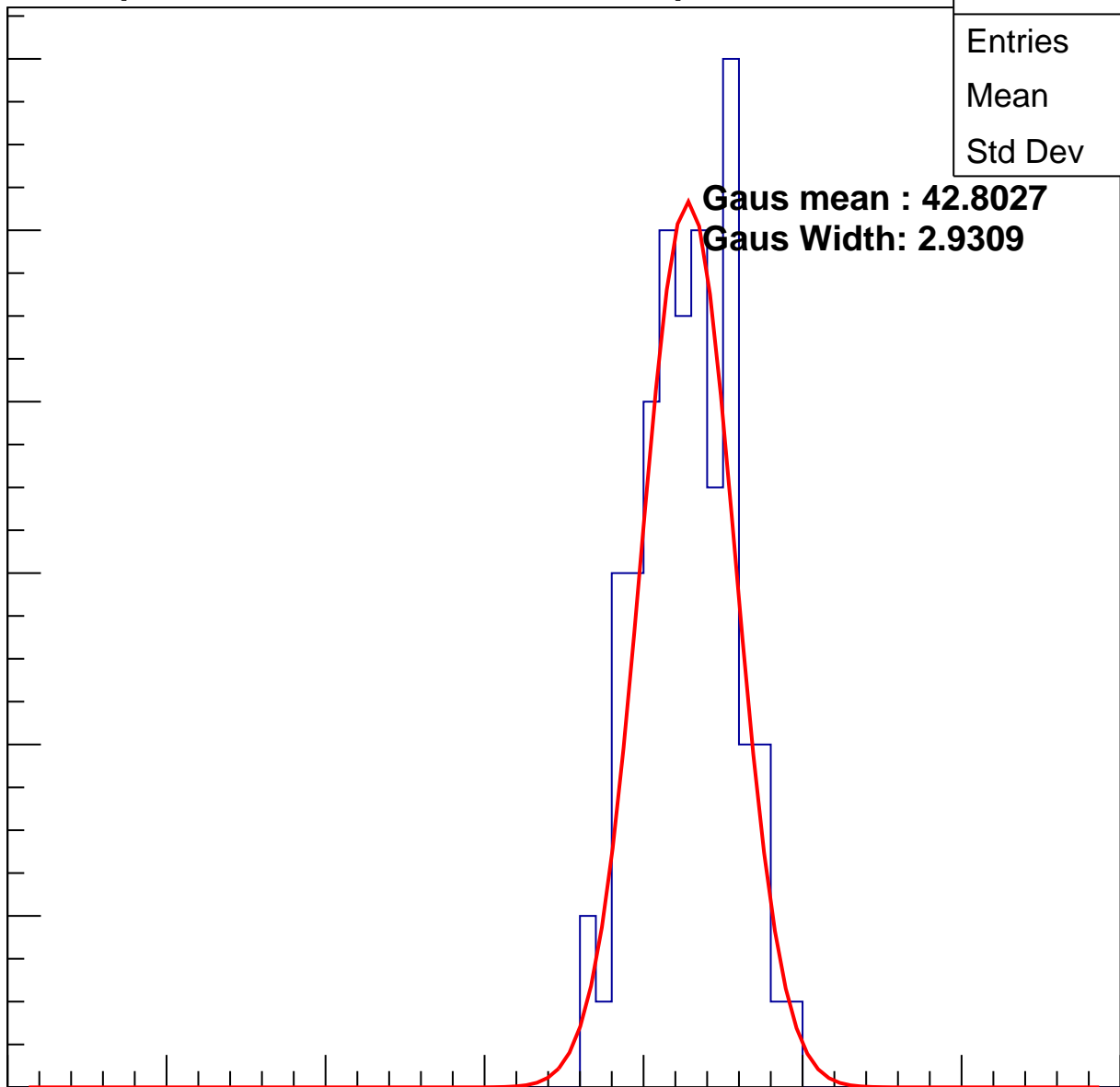
Entries	81
Mean	42.3
Std Dev	2.891

Gaus mean : 42.8027

Gaus Width: 2.9309

0 10 20 30 40 50 60 70

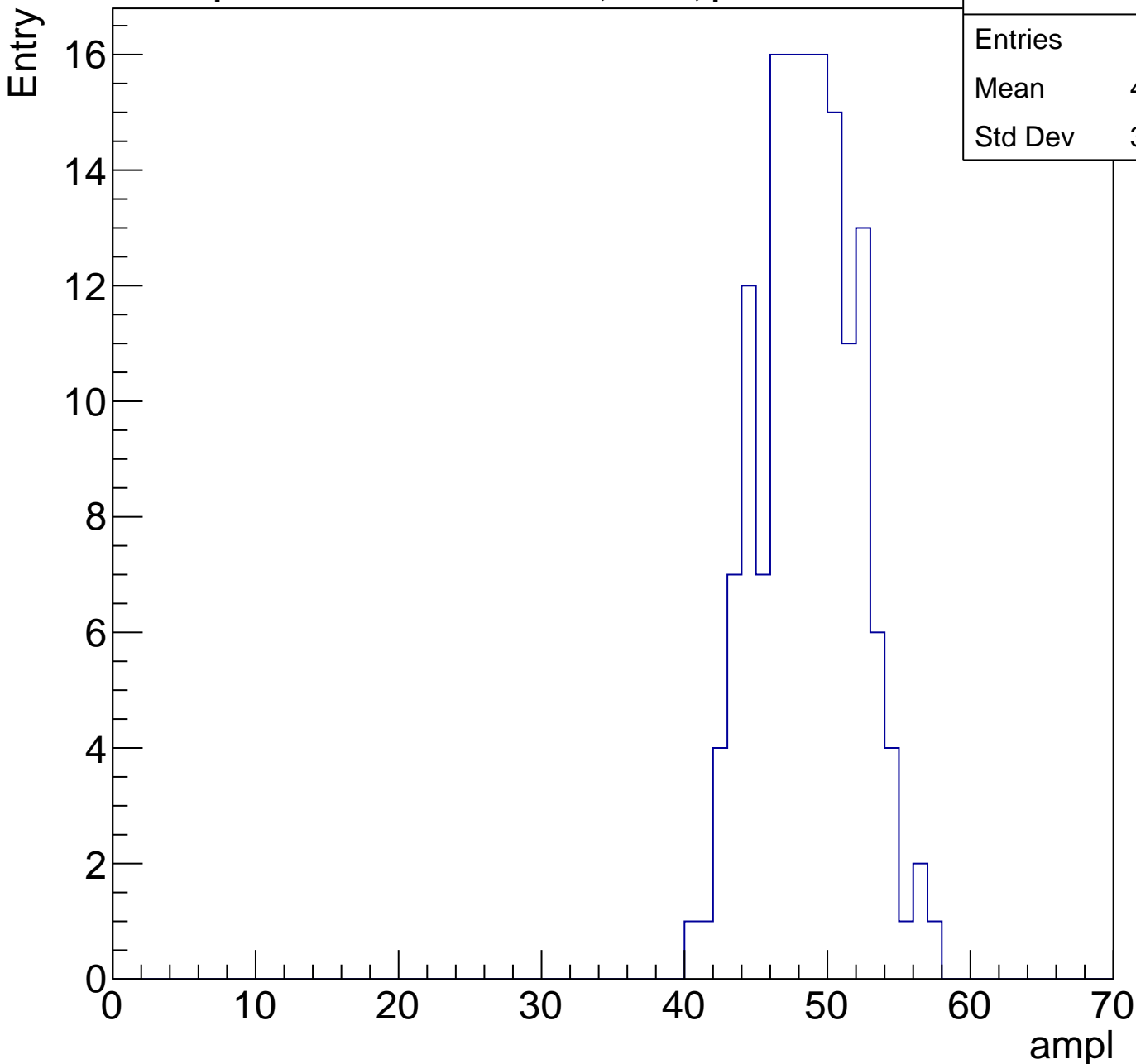
ampl



B1L001S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	48.17
Std Dev	3.375



B1L001S, U14-ch51, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	54.86
Std Dev	3.066

Entry

16
14
12
10
8
6
4
2
0

0

10

20

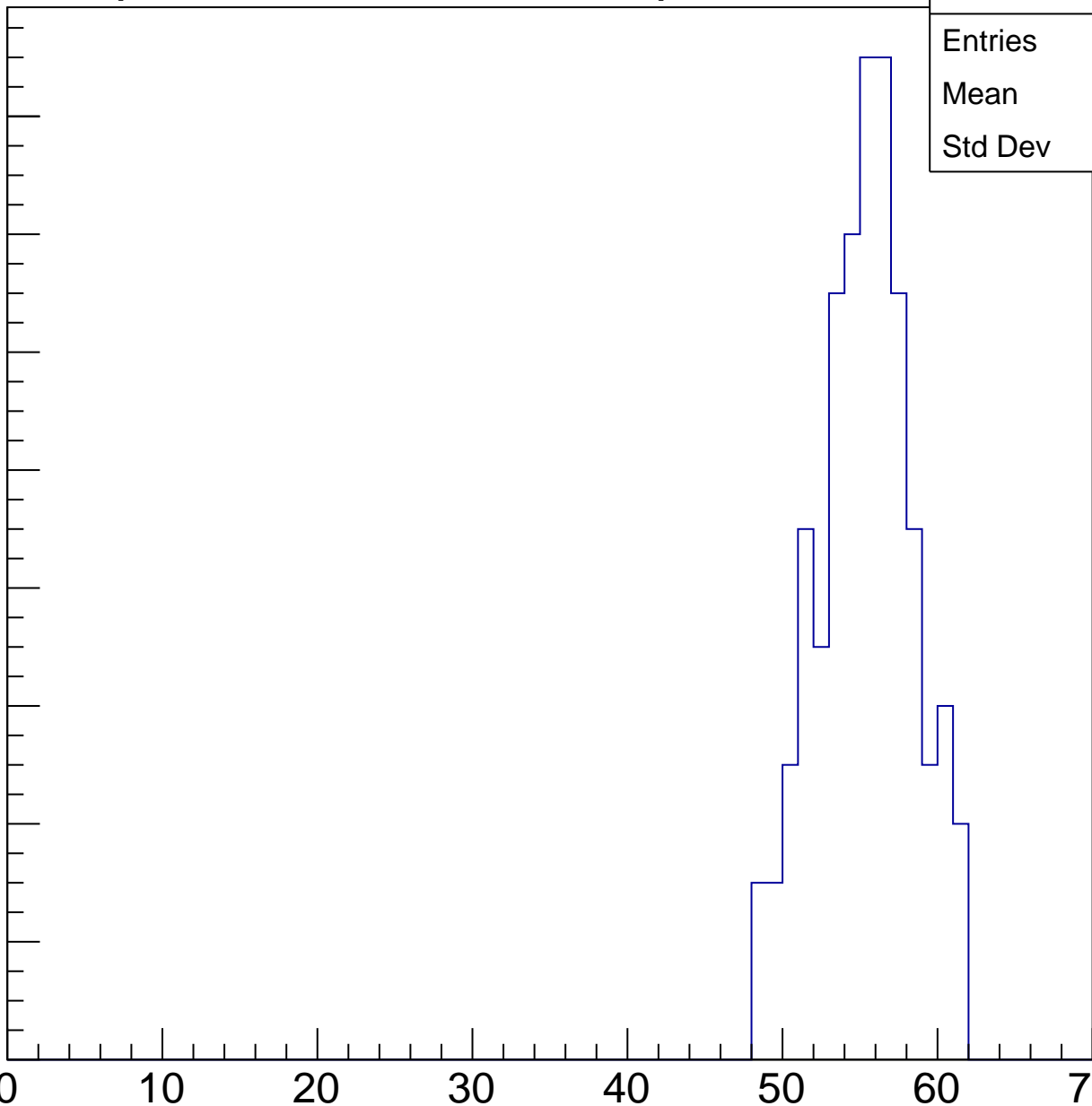
30

40

50

60

ampl



B1L001S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

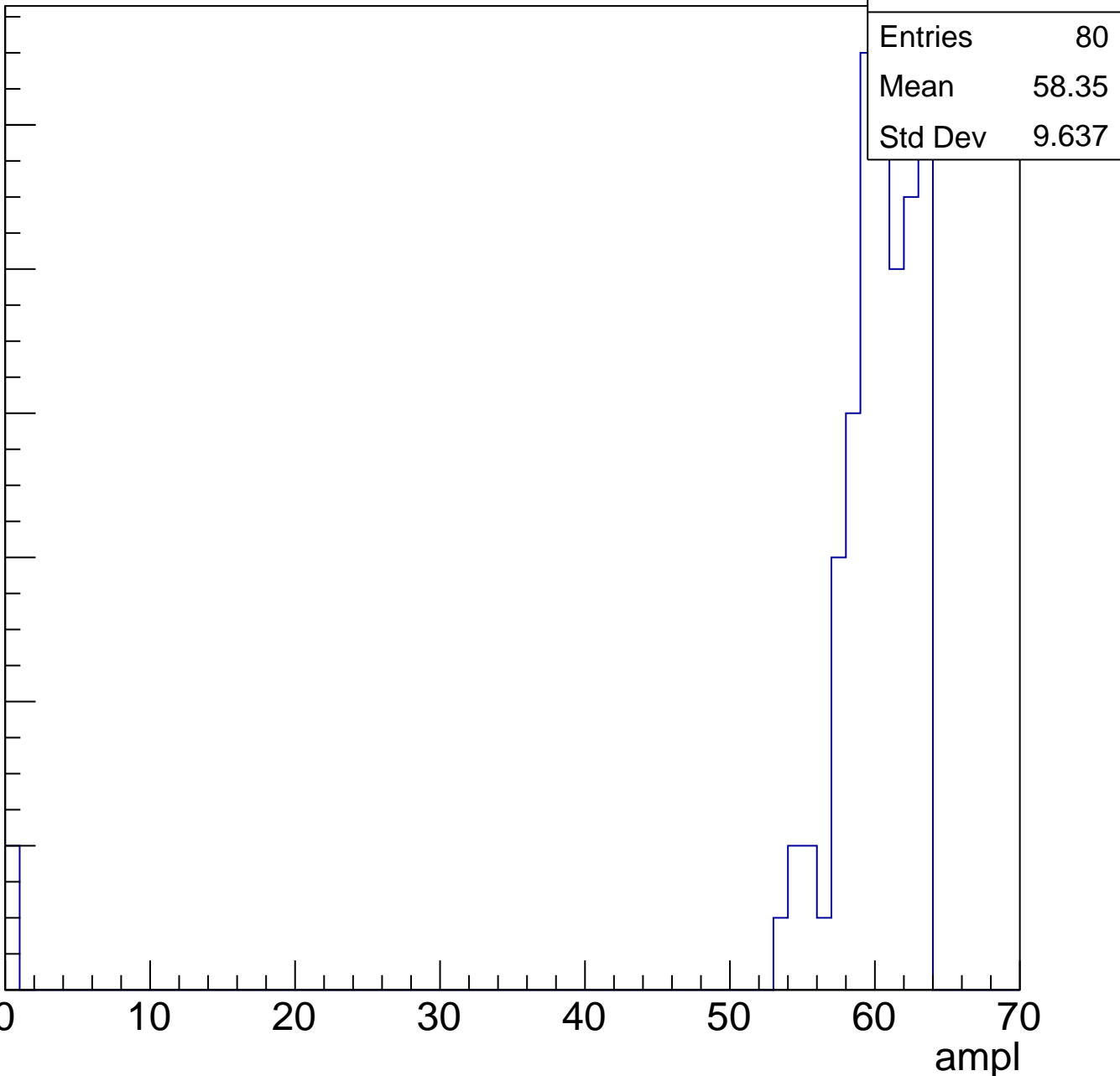
40

50

60

ampl

Entries	80
Mean	58.35
Std Dev	9.637

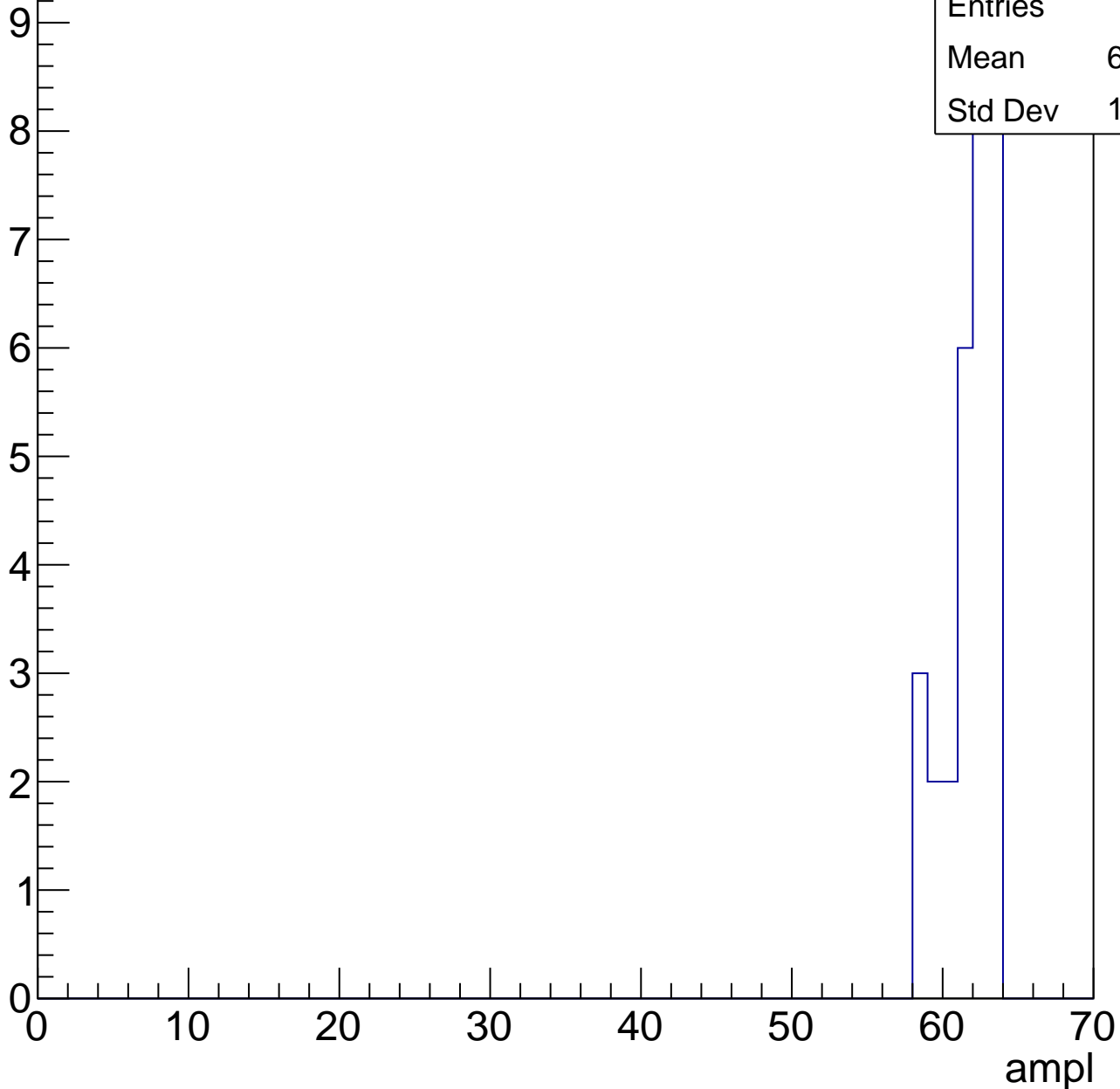


B1L001S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	30
Mean	61.33
Std Dev	1.578



B1L001S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch52, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.86
Std Dev	2.975

Gaus mean : 30.3373

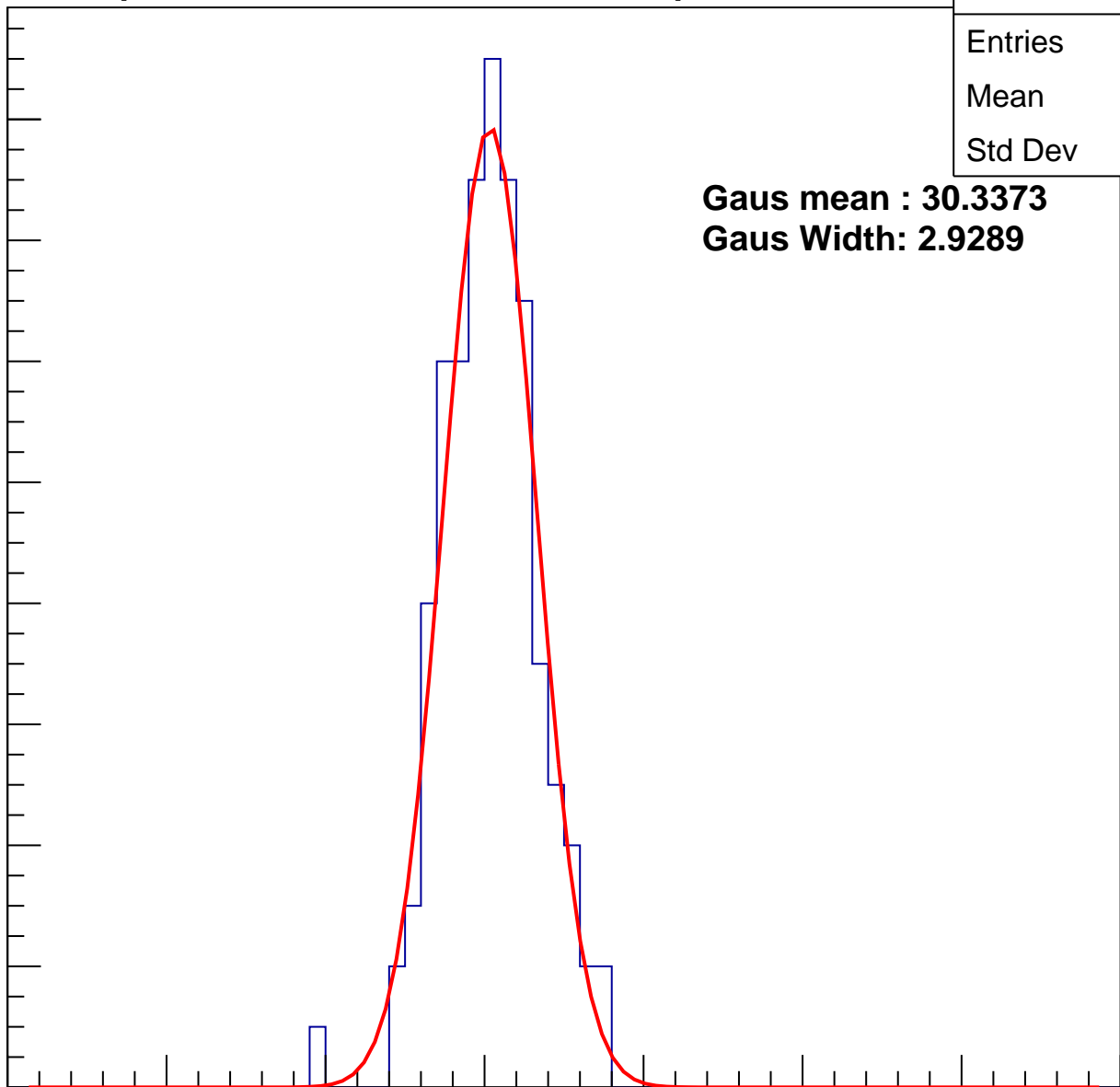
Gaus Width: 2.9289

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



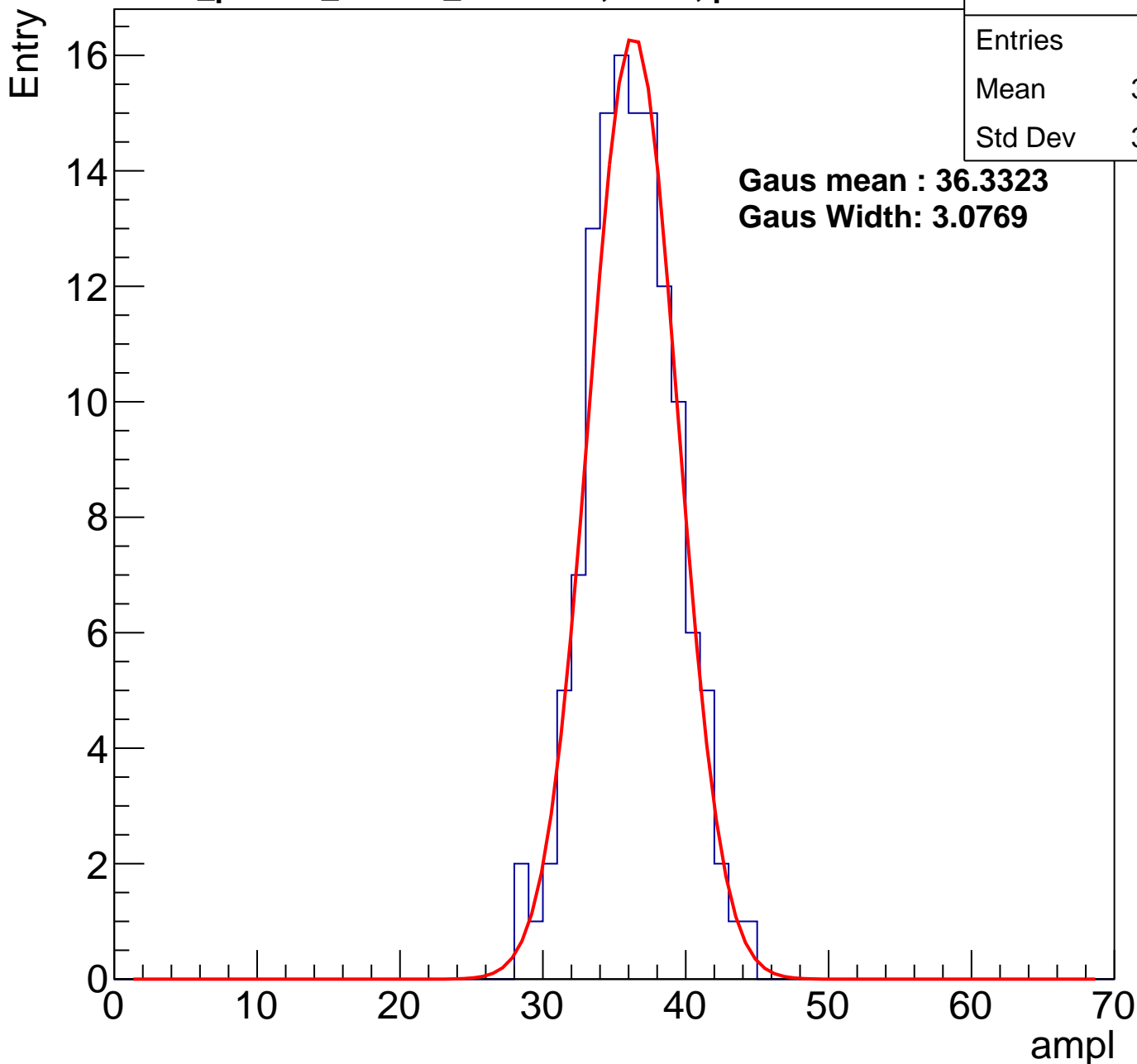
B1L001S, U14-ch52, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	35.78
Std Dev	3.102

Gaus mean : 36.3323

Gaus Width: 3.0769



B1L001S, U14-ch52, adc2

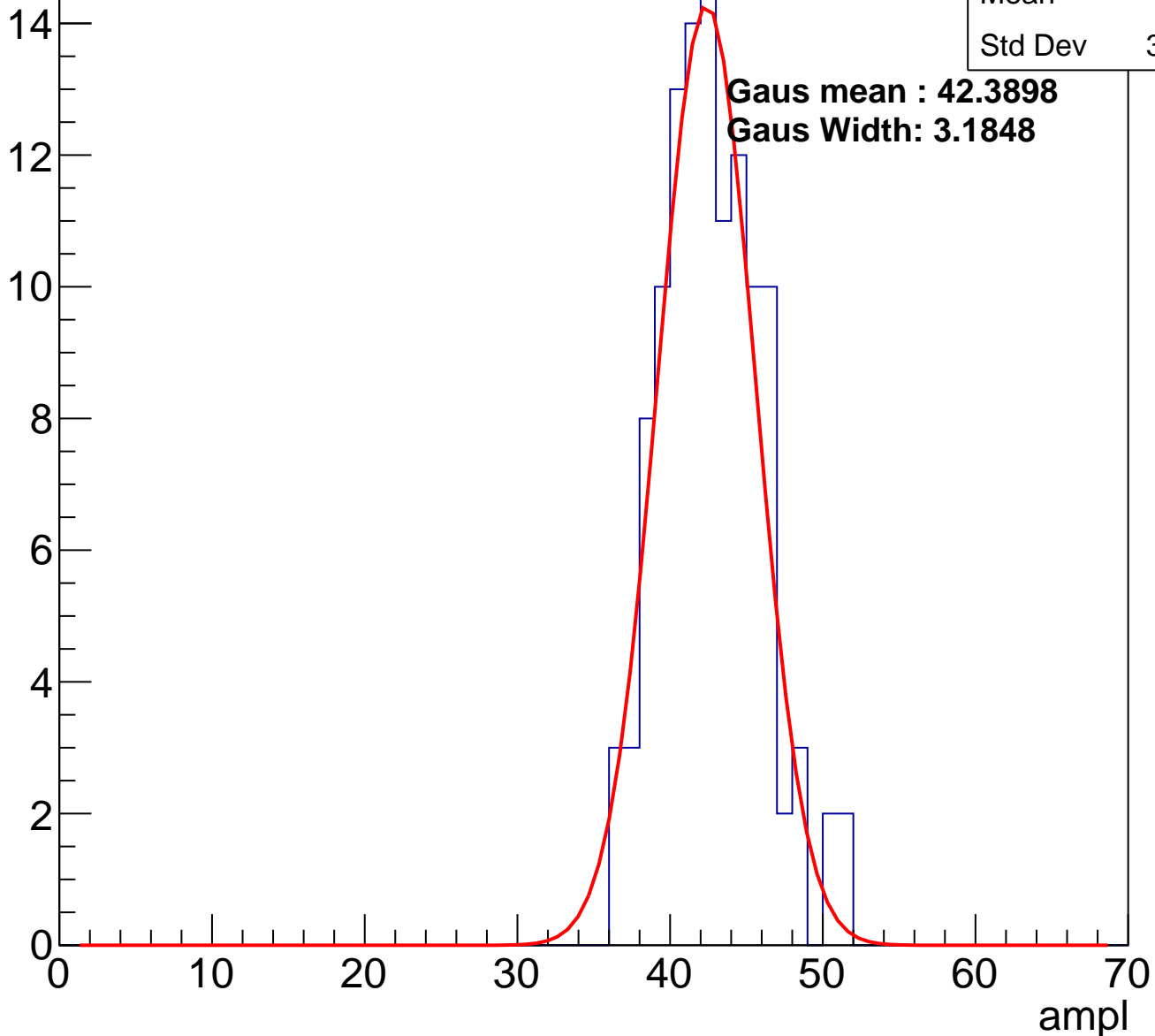
calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	42.27
Std Dev	3.209

Gaus mean : 42.3898

Gaus Width: 3.1848

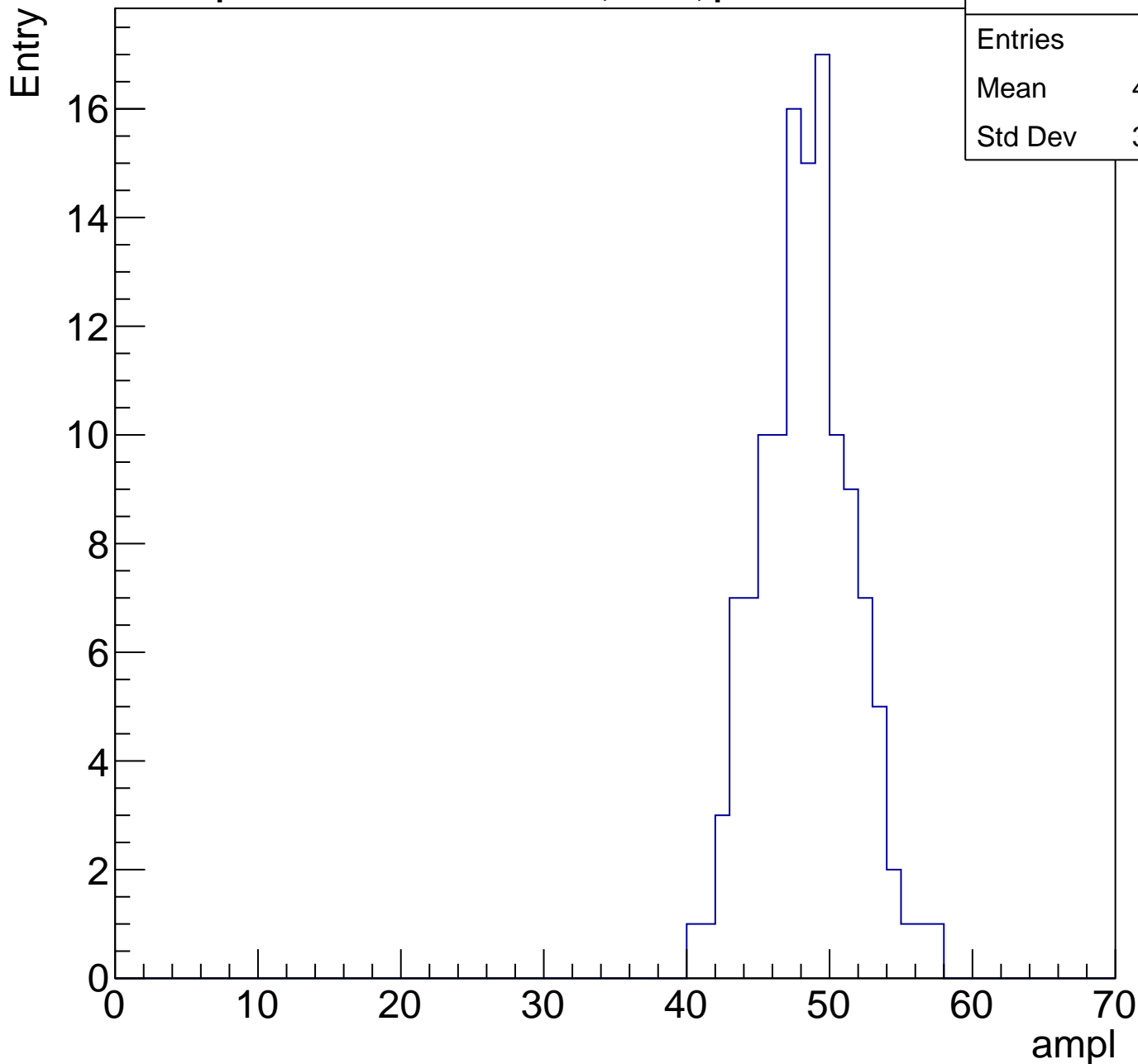
Entry



B1L001S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	47.93
Std Dev	3.244



B1L001S, U14-ch52, adc4

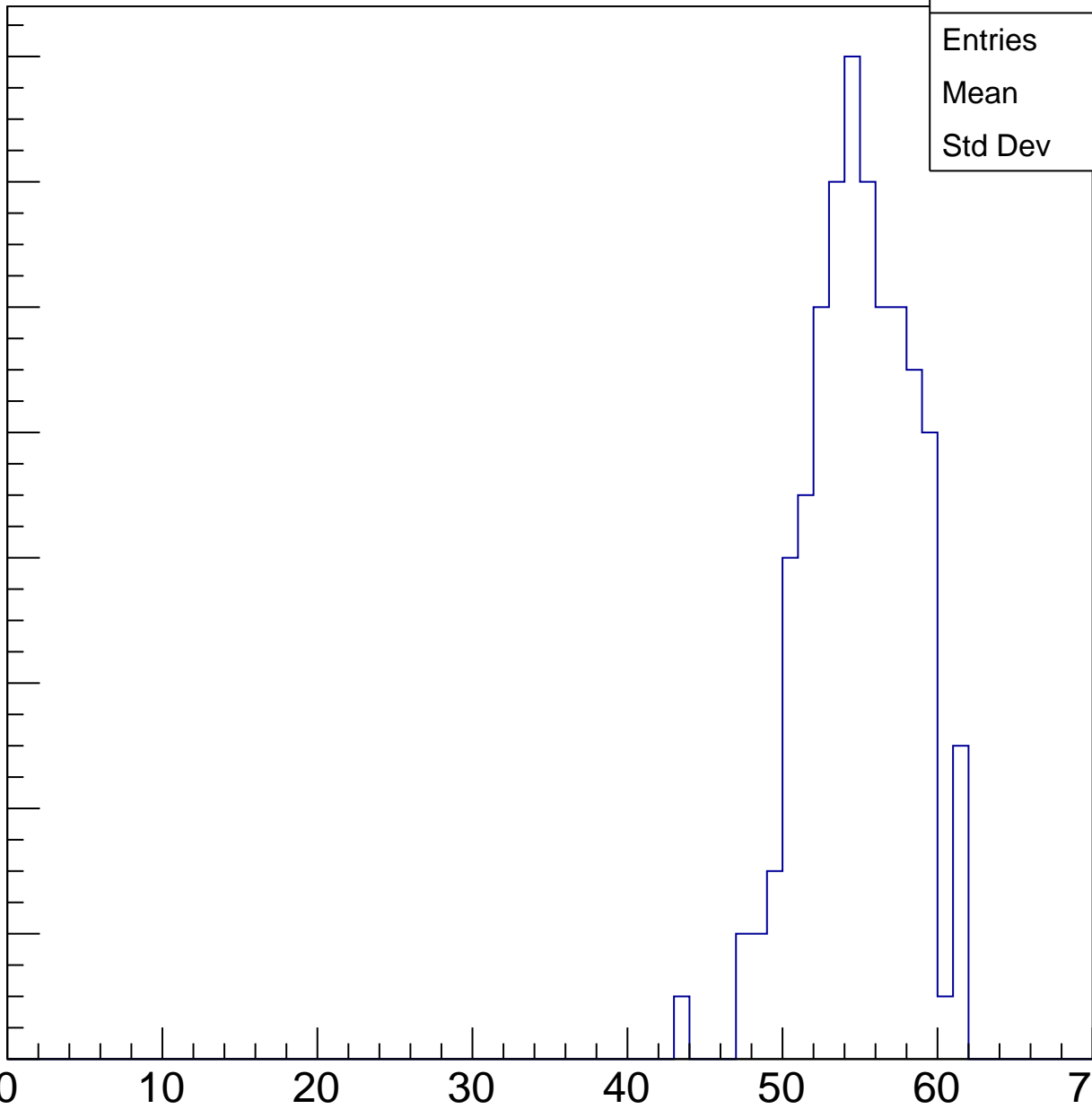
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	132
Mean	54.45
Std Dev	3.356

ampl



B1L001S, U14-ch52, adc5

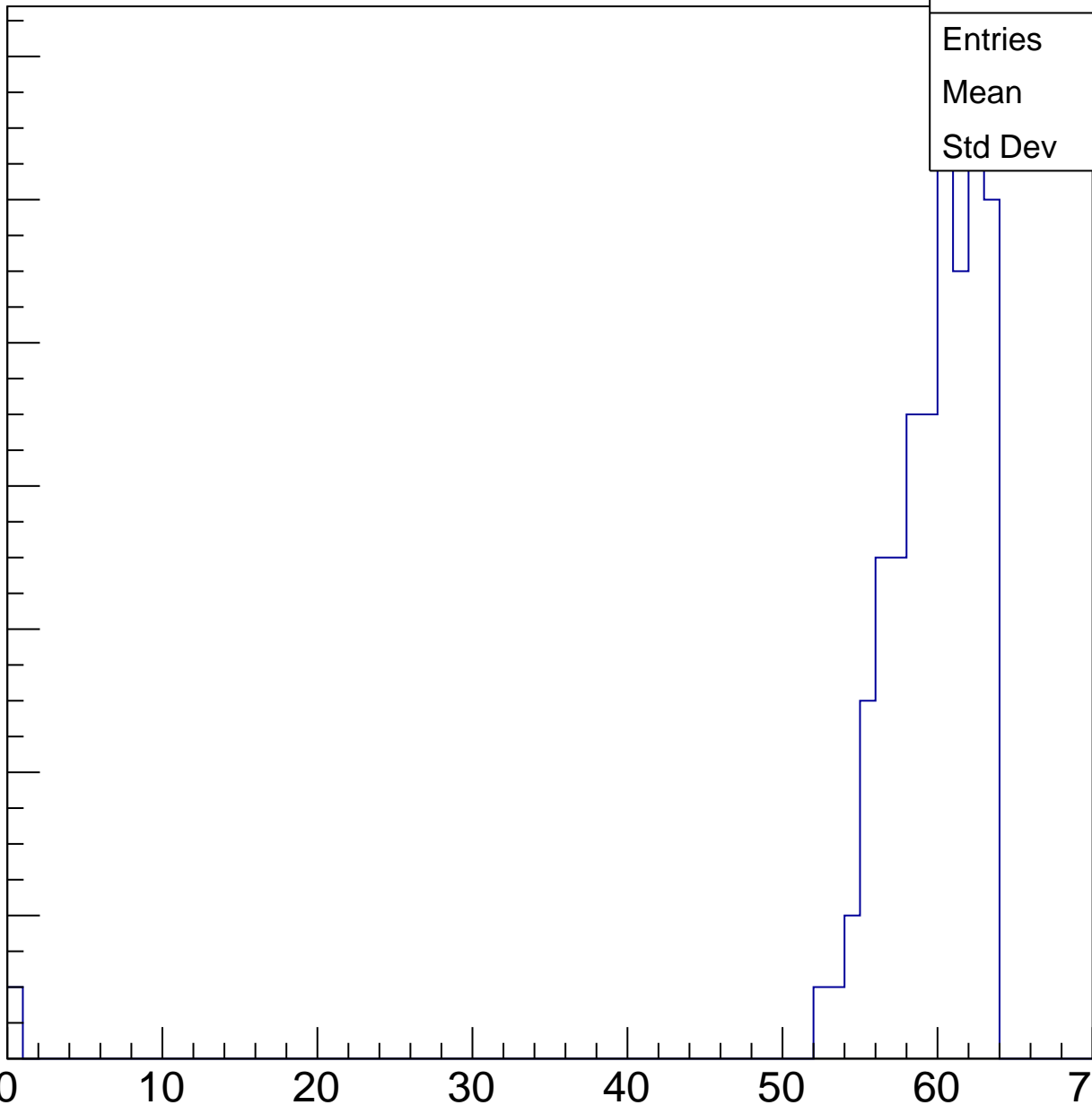
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	92
Mean	58.77
Std Dev	6.729

ampl

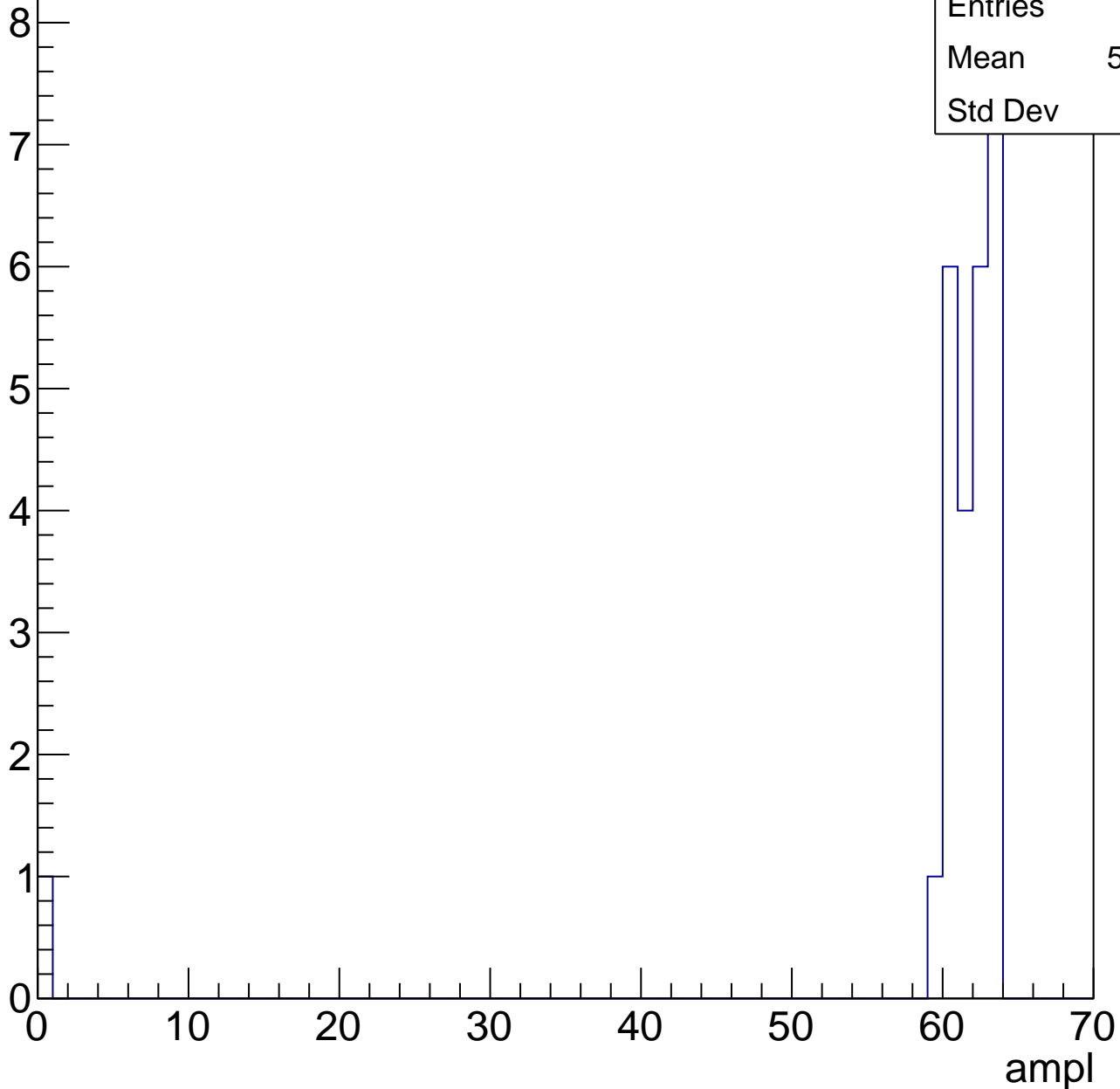


B1L001S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	59.19
Std Dev	11.9



B1L001S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch53, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	29.39
Std Dev	3.326

Gaus mean : 29.6513

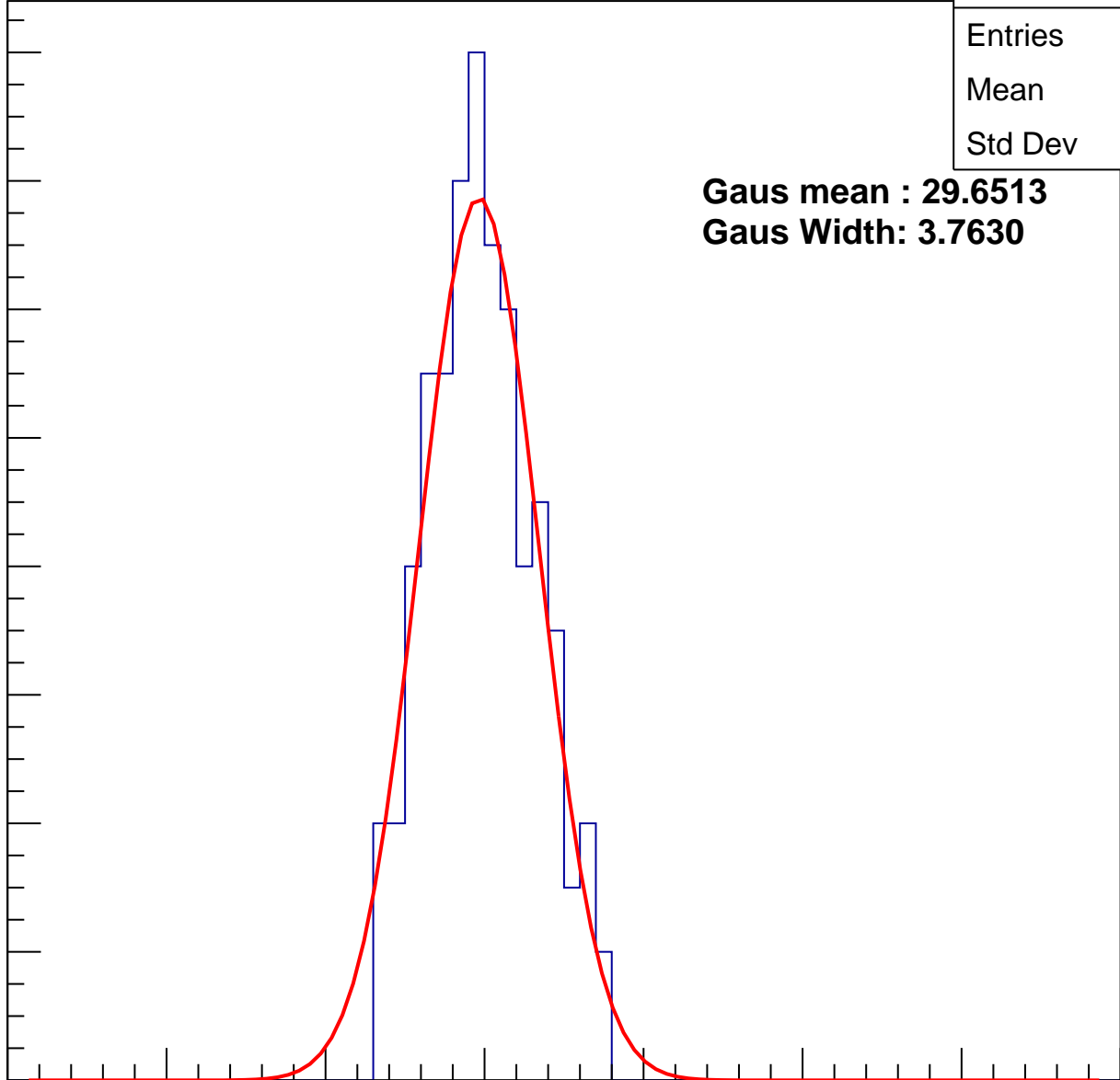
Gaus Width: 3.7630

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch53, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	34.82
Std Dev	3.471

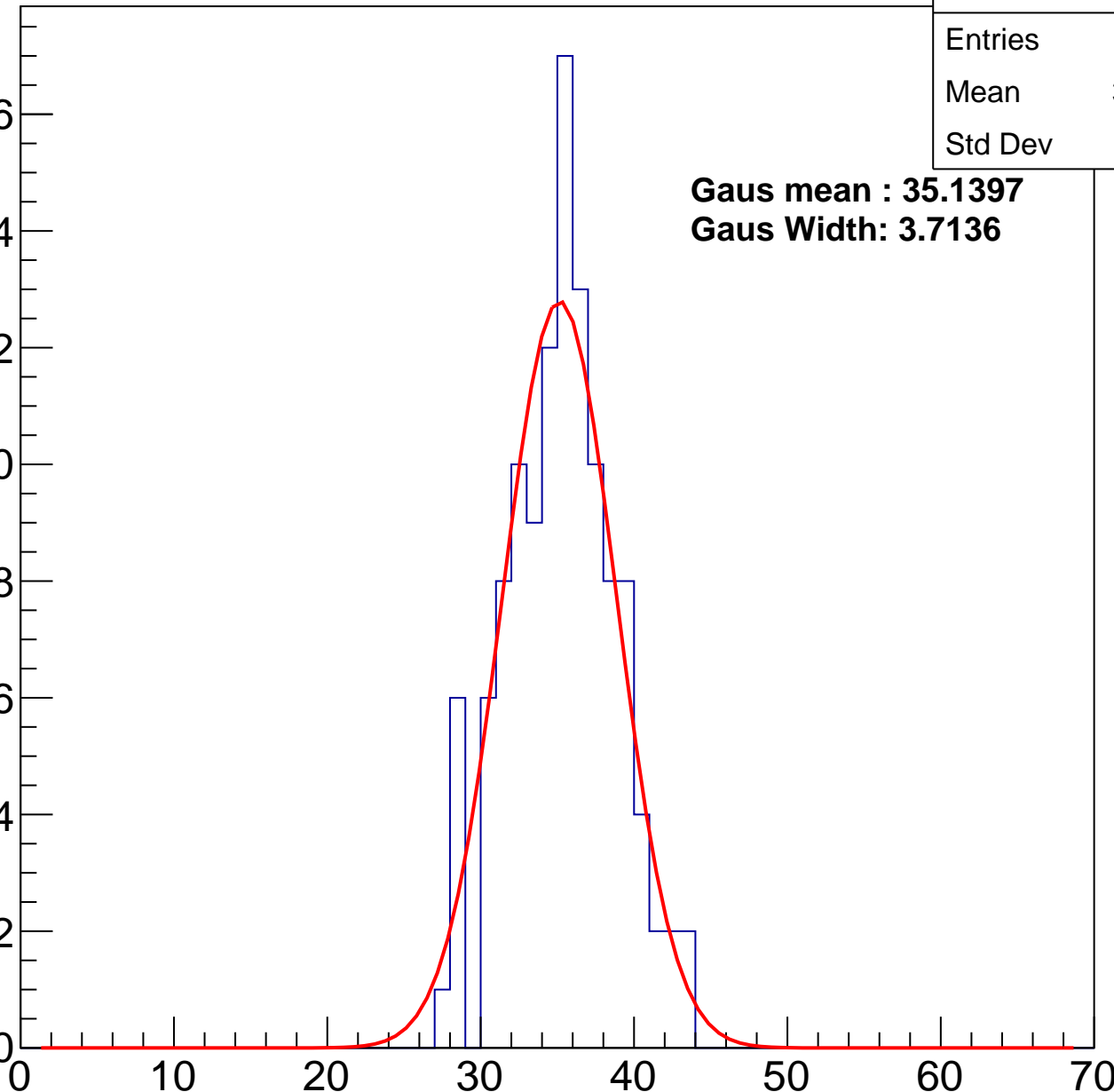
Gaus mean : 35.1397

Gaus Width: 3.7136

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch53, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	41.61
Std Dev	3.573

Gaus mean : 41.9340

Gaus Width: 3.4474

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

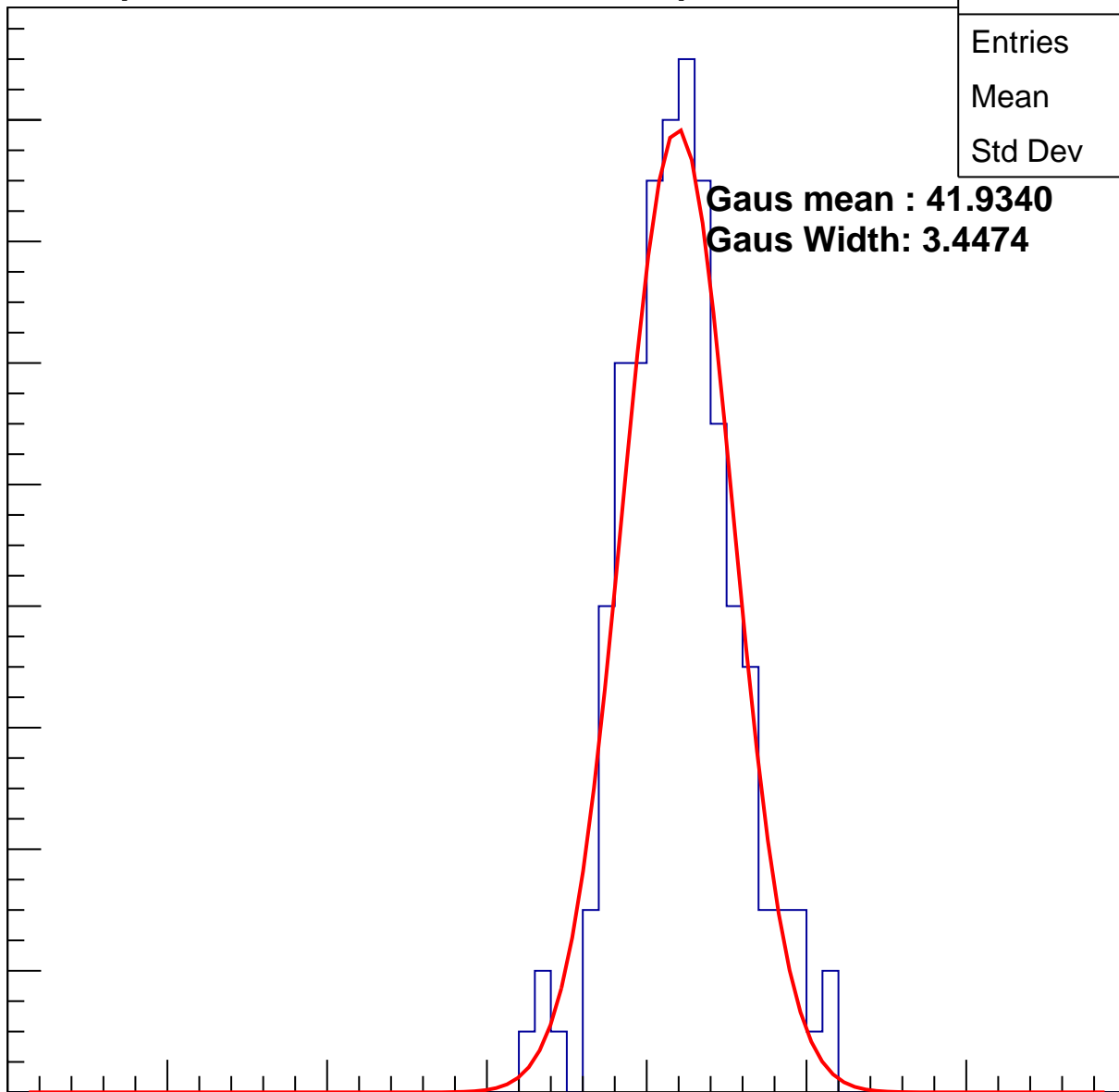
40

50

60

70

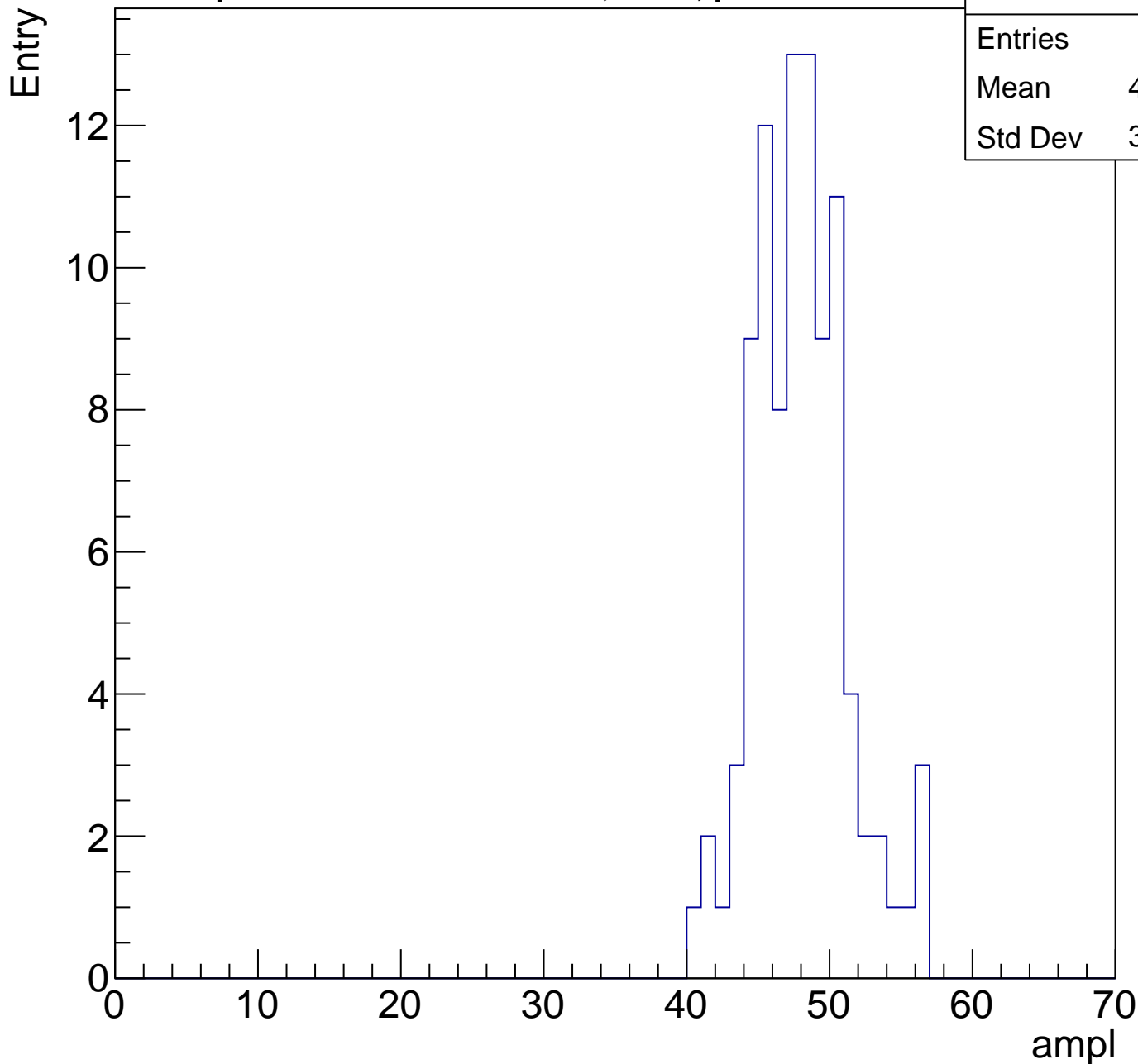
ampl



B1L001S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#2, port C2

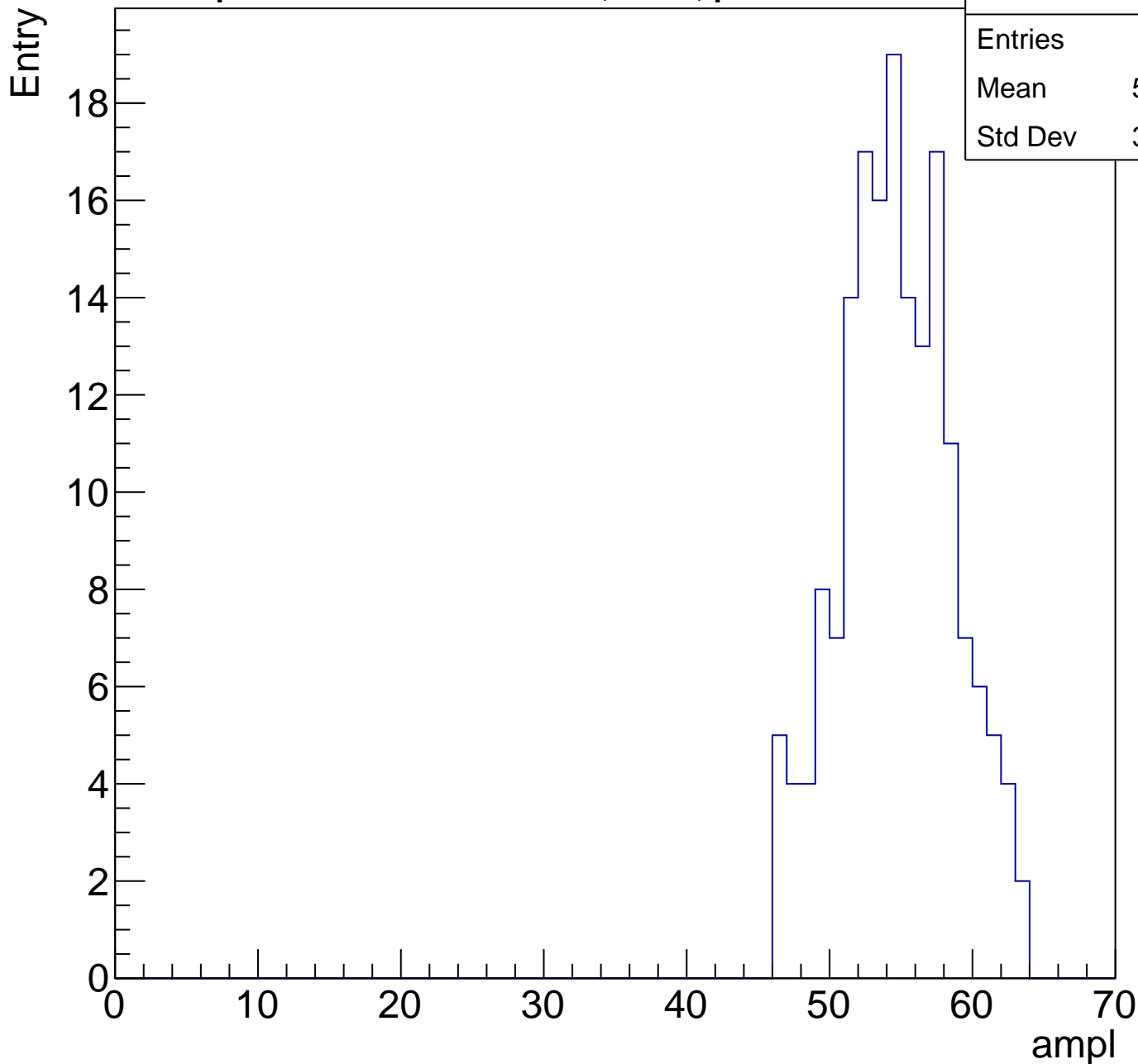
Entries	95
Mean	47.52
Std Dev	3.237



B1L001S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	173
Mean	54.23
Std Dev	3.893

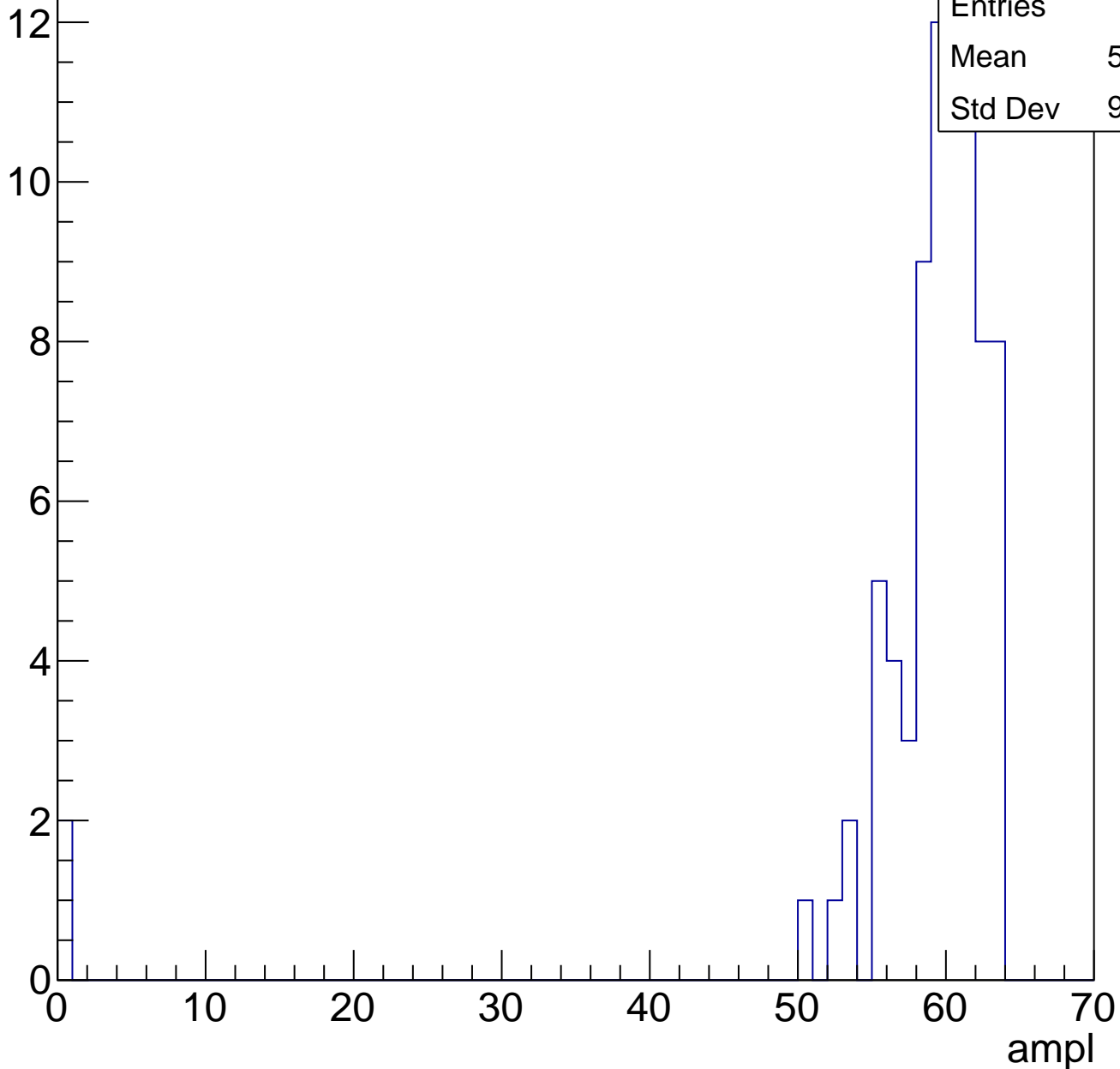


B1L001S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	79
Mean	57.72
Std Dev	9.699

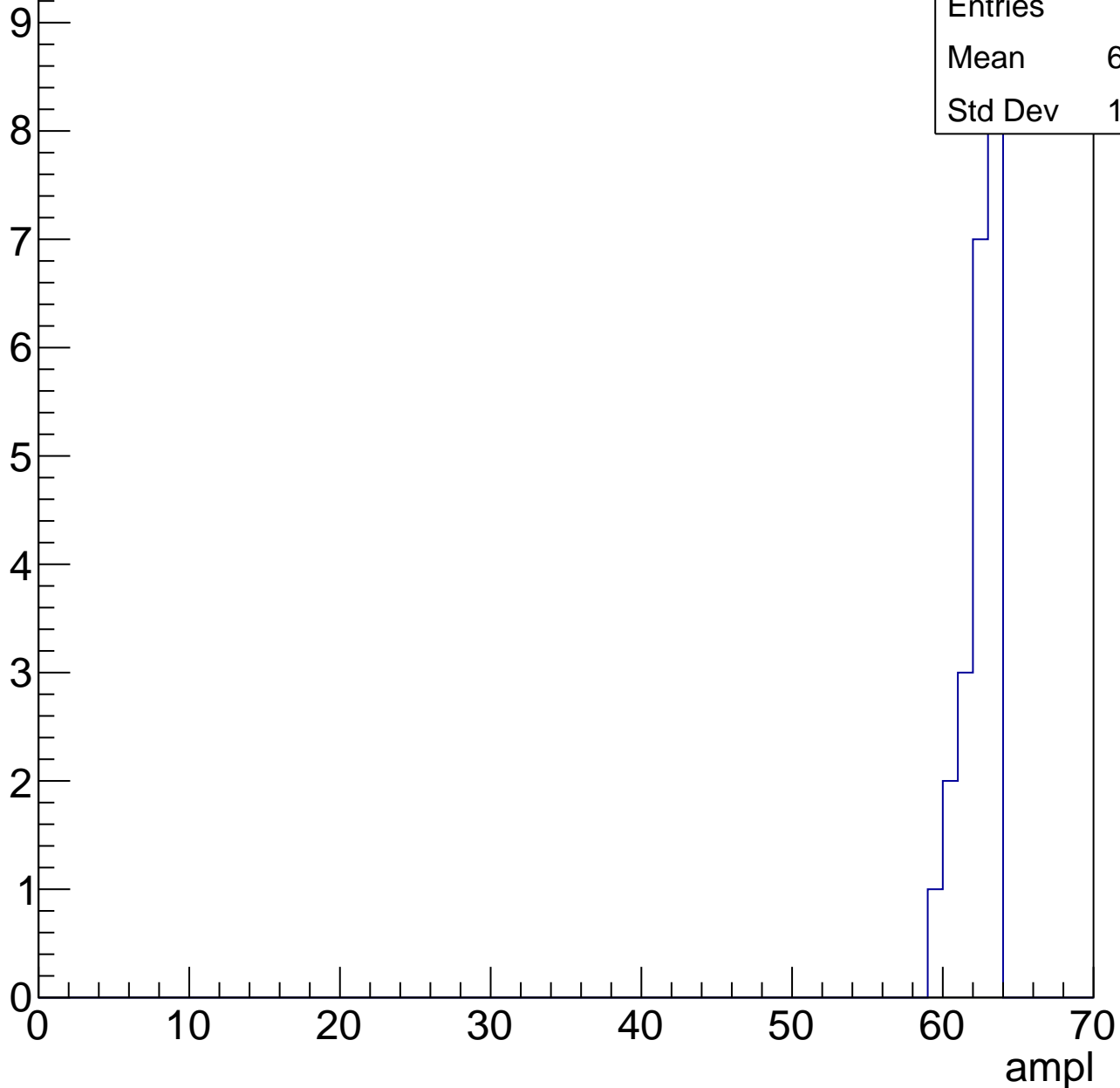
Entry



B1L001S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#2, port C2

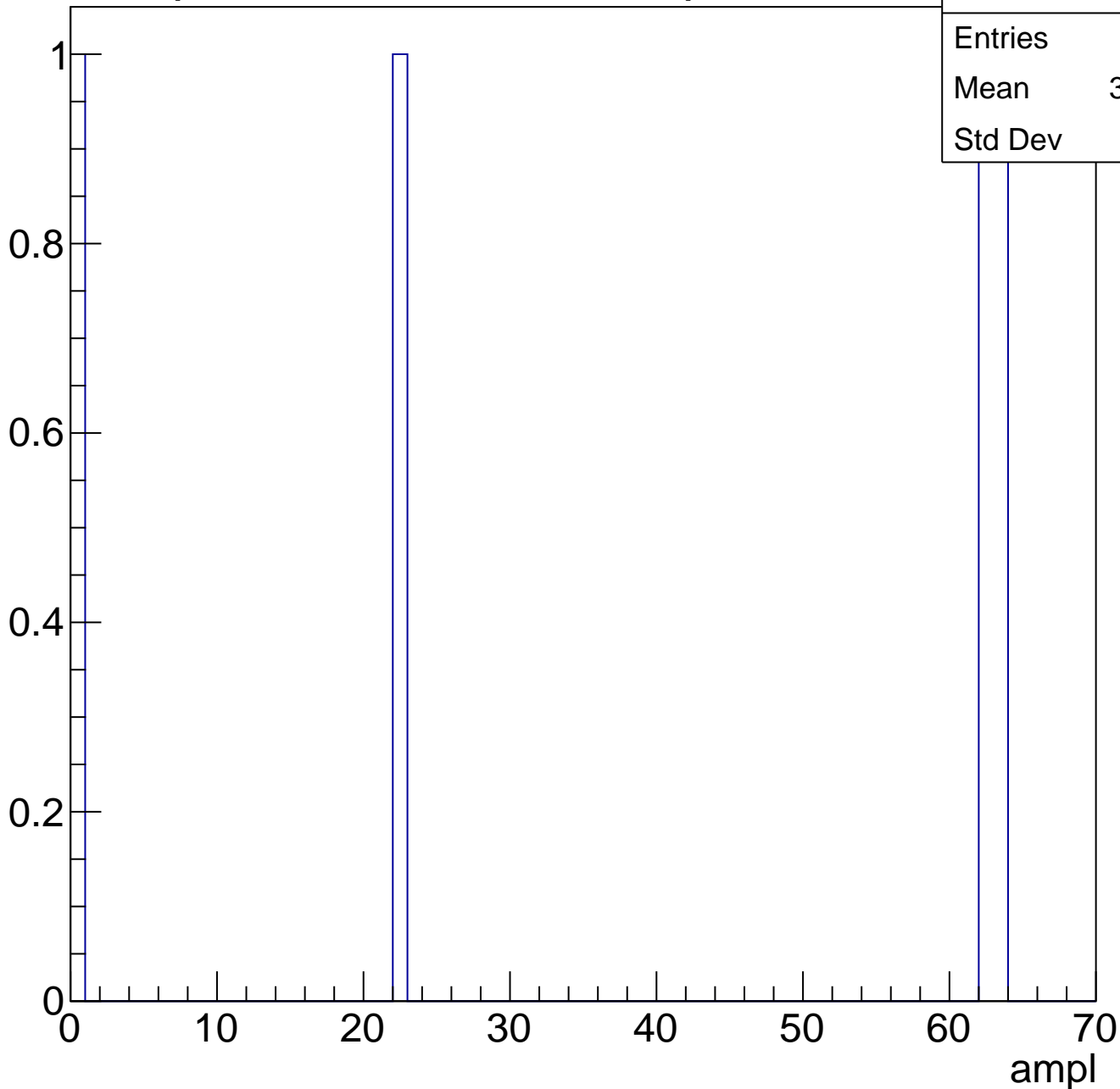
Entry



B1L001S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch54, adc0

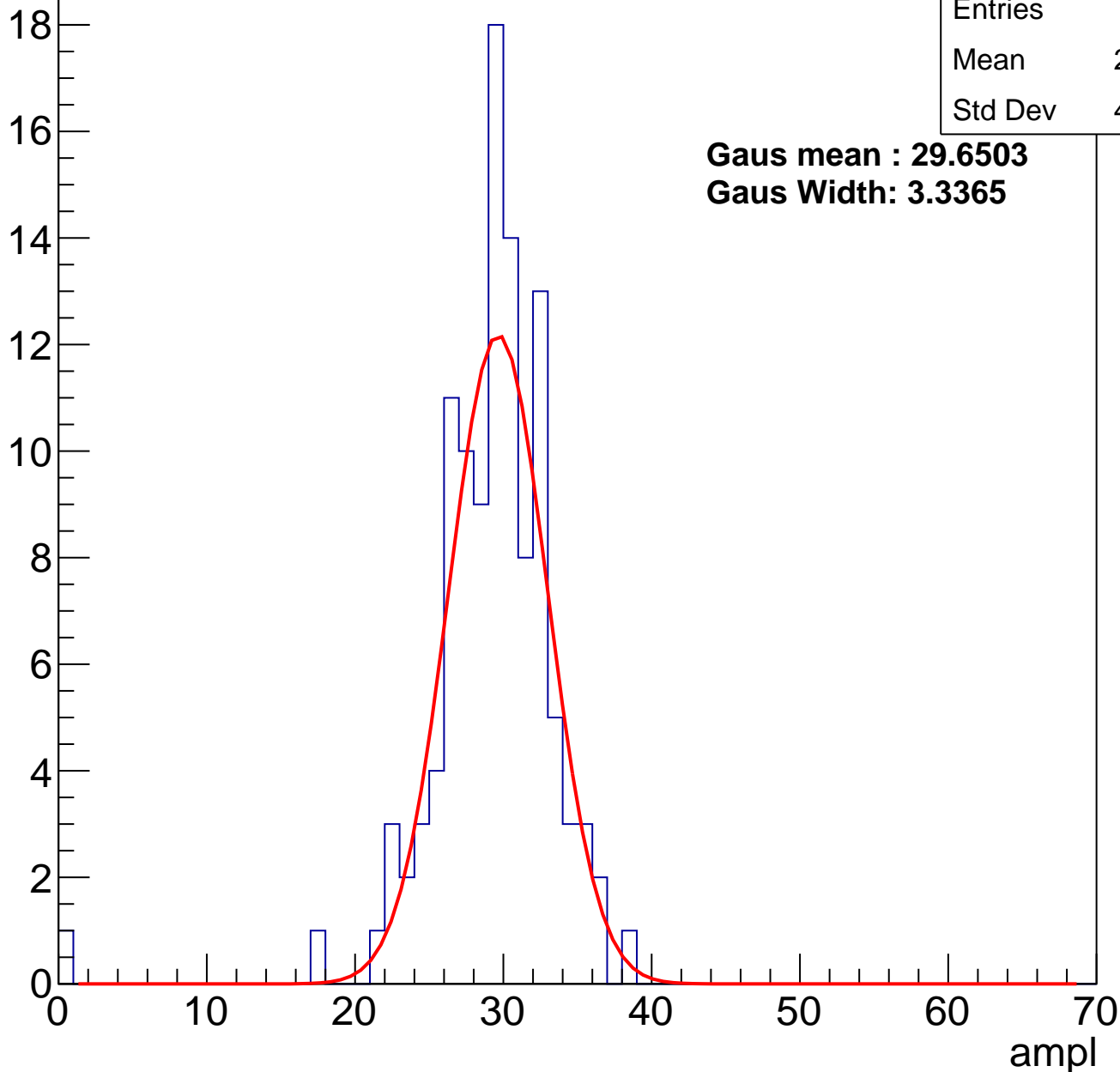
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	28.73
Std Dev	4.383

Gaus mean : 29.6503

Gaus Width: 3.3365

Entry



B1L001S, U14-ch54, adc1

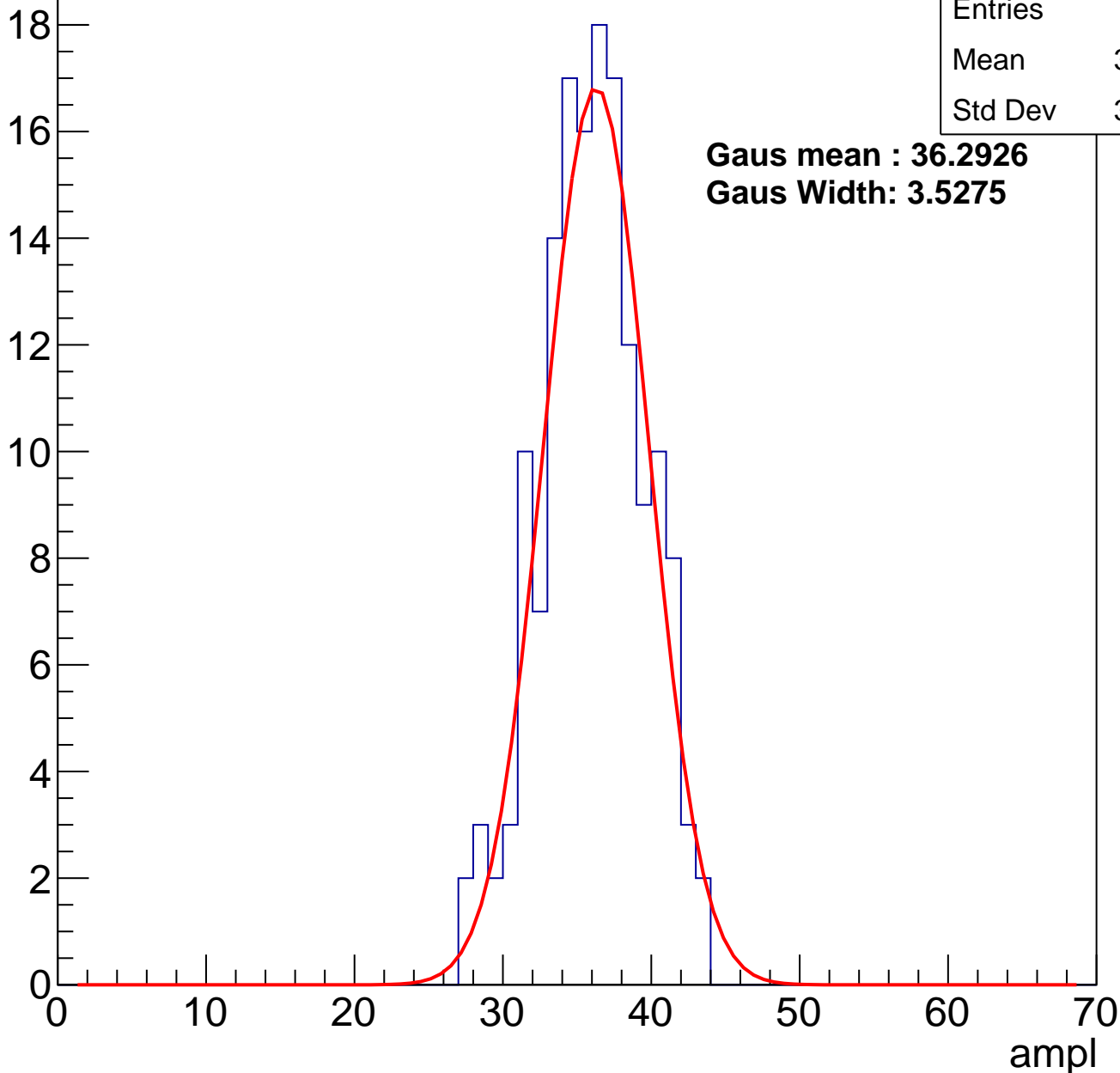
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	35.58
Std Dev	3.446

Gaus mean : 36.2926

Gaus Width: 3.5275

Entry



B1L001S, U14-ch54, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	42.67
Std Dev	3.179

Gaus mean : 43.1865

Gaus Width: 3.3999

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

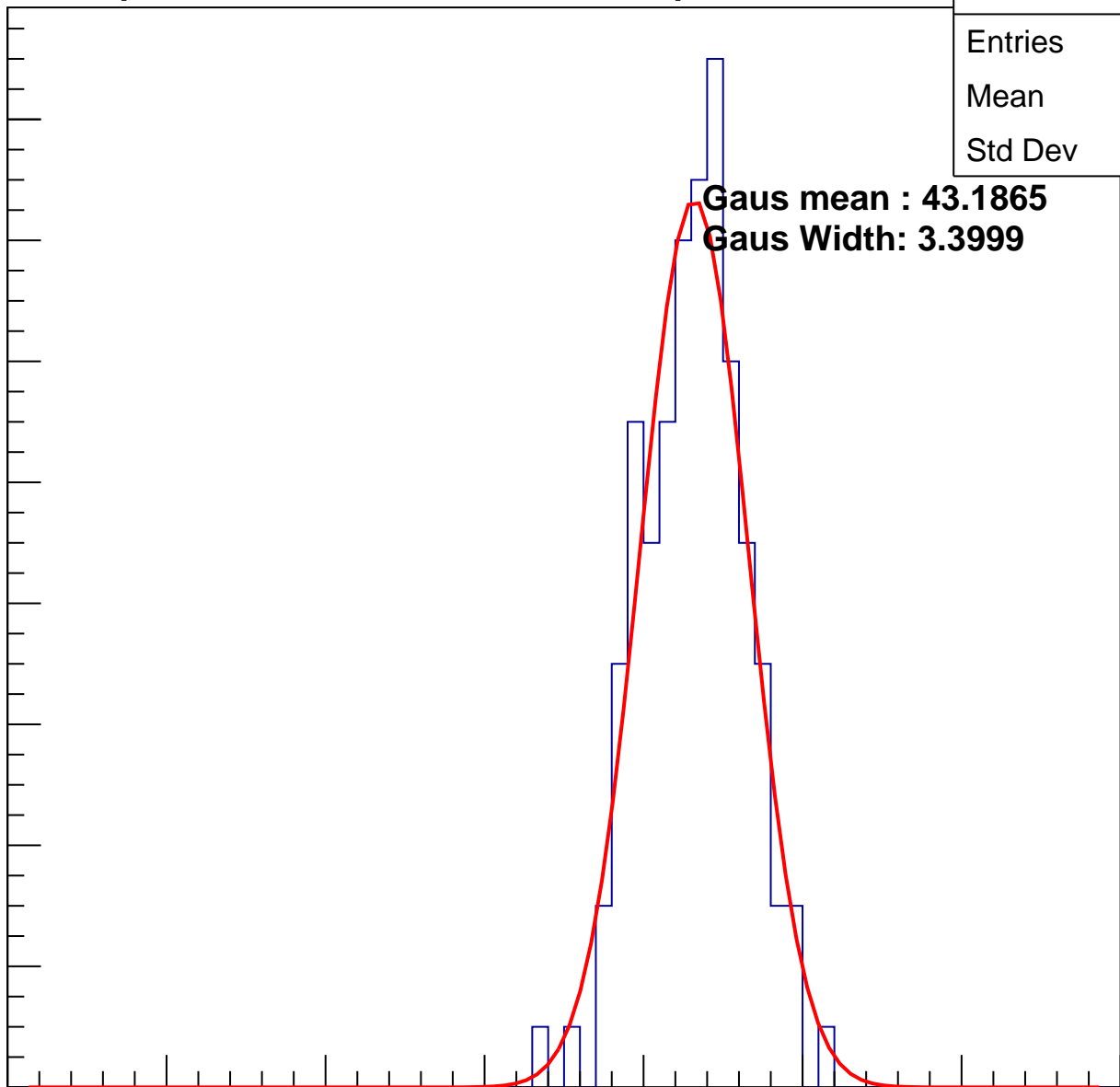
40

50

60

70

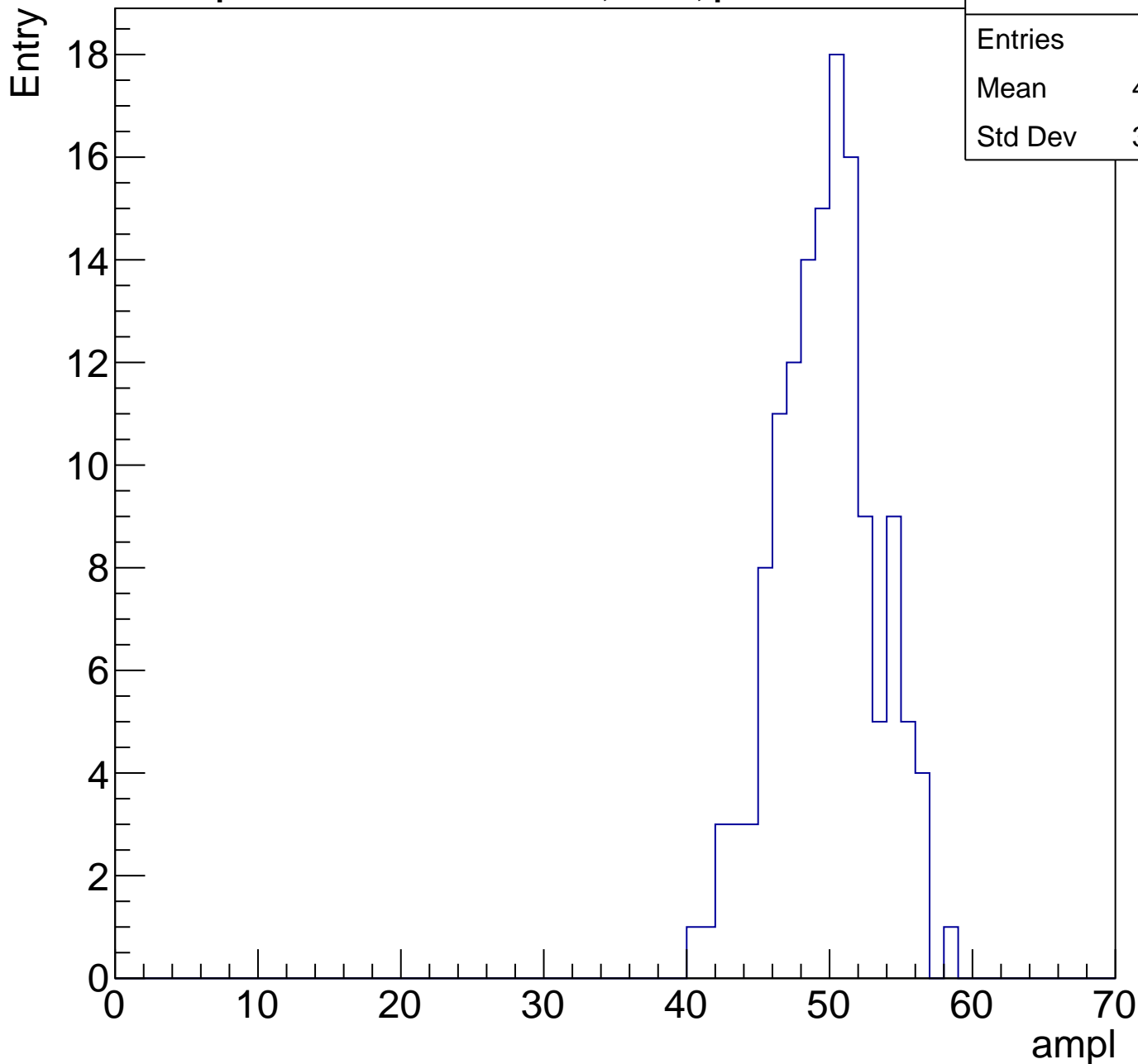
ampl



B1L001S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	49.25
Std Dev	3.489



B1L001S, U14-ch54, adc4

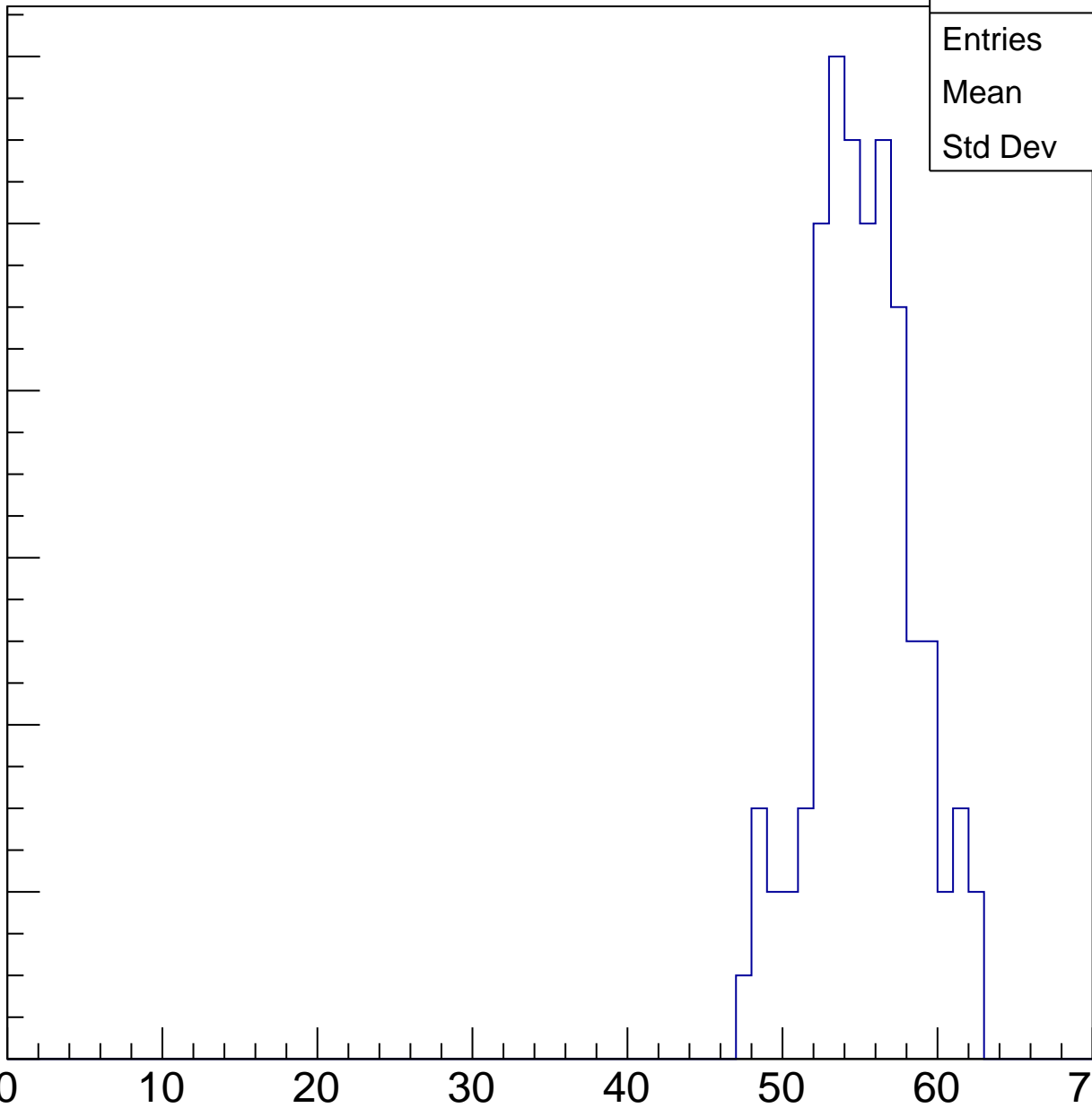
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	91
Mean	54.76
Std Dev	3.236

ampl



B1L001S, U14-ch54, adc5

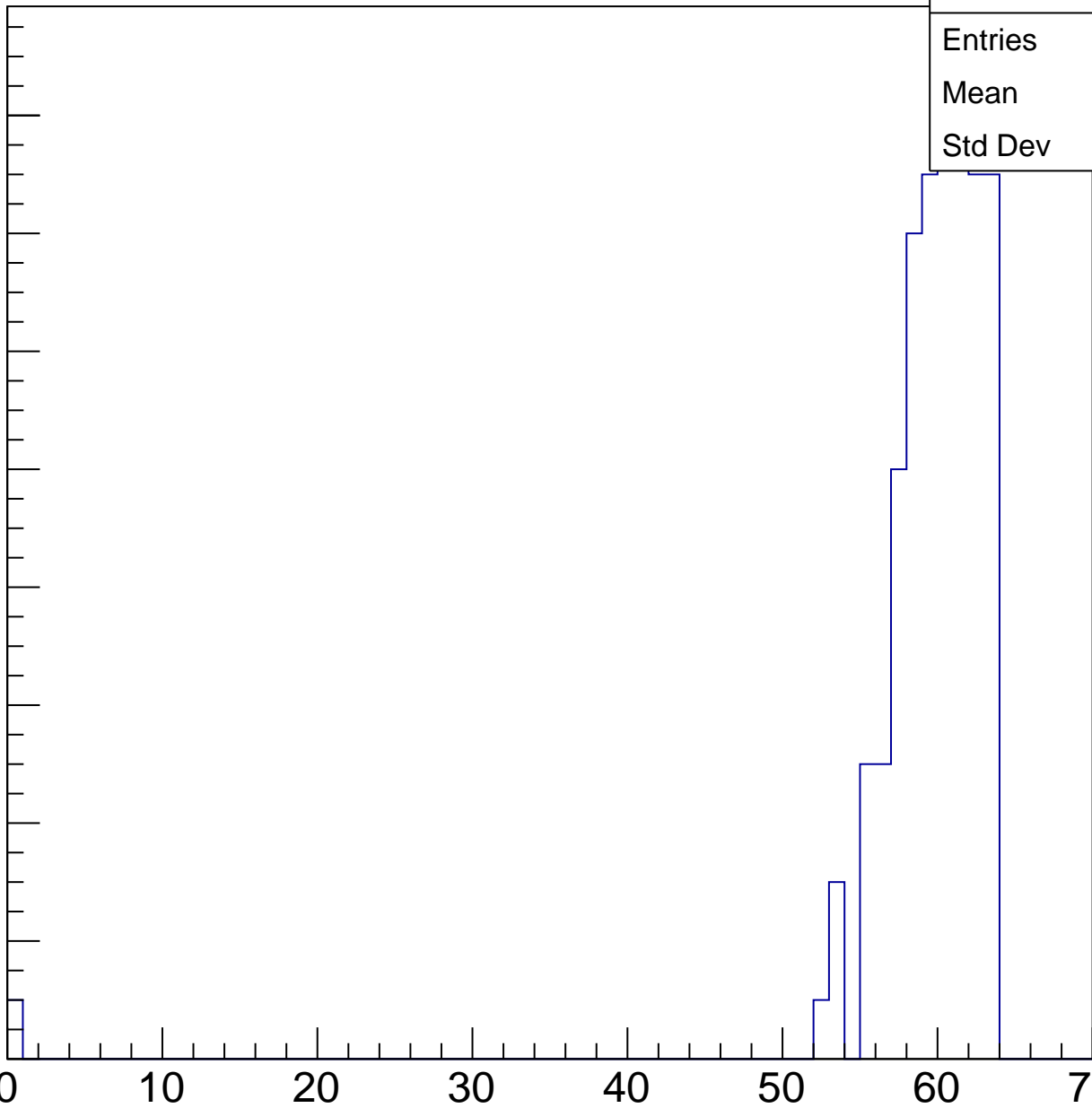
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	117
Mean	59.01
Std Dev	6.043

ampl

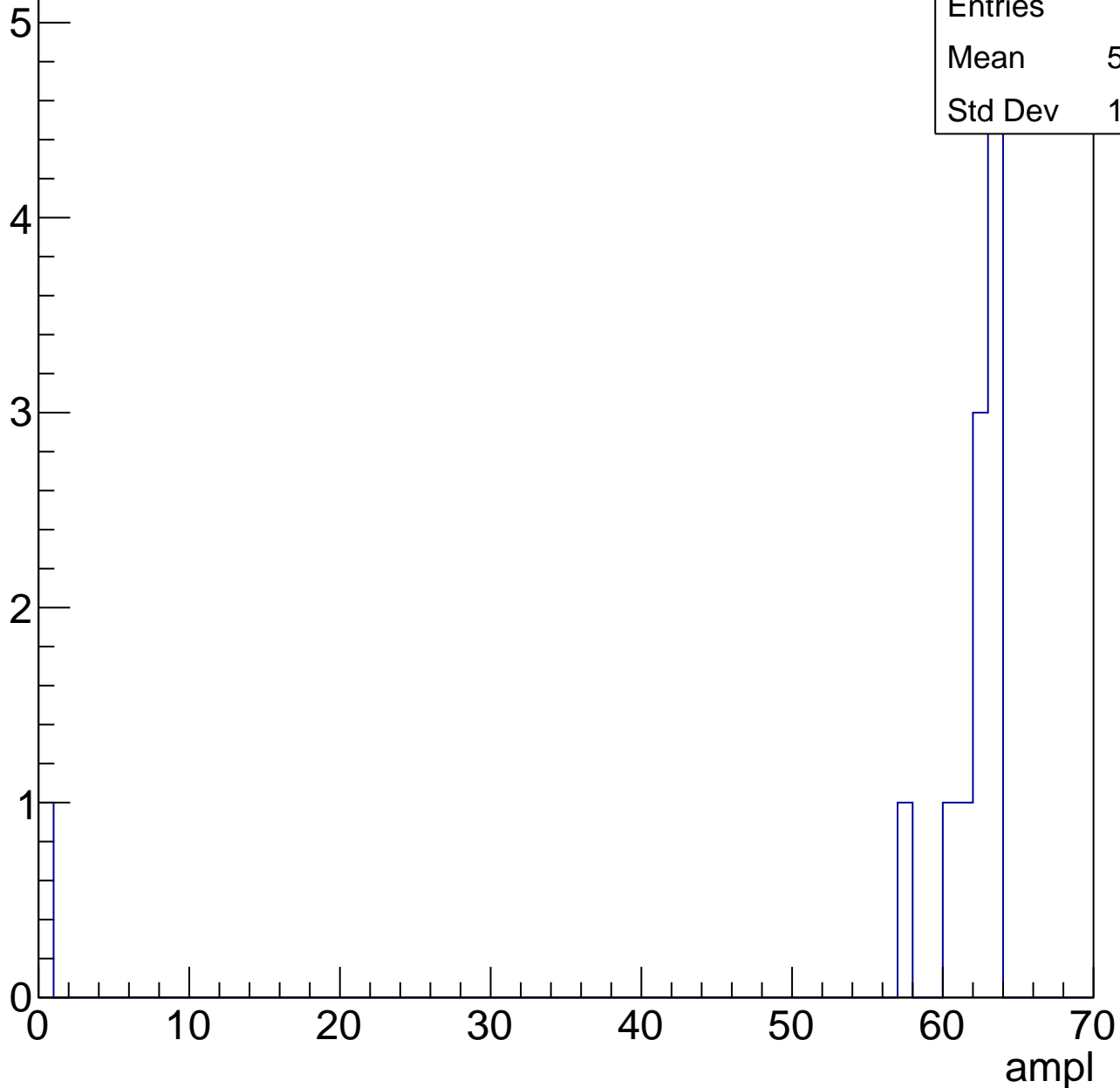


B1L001S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	56.58
Std Dev	17.14



B1L001S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch55, adc0

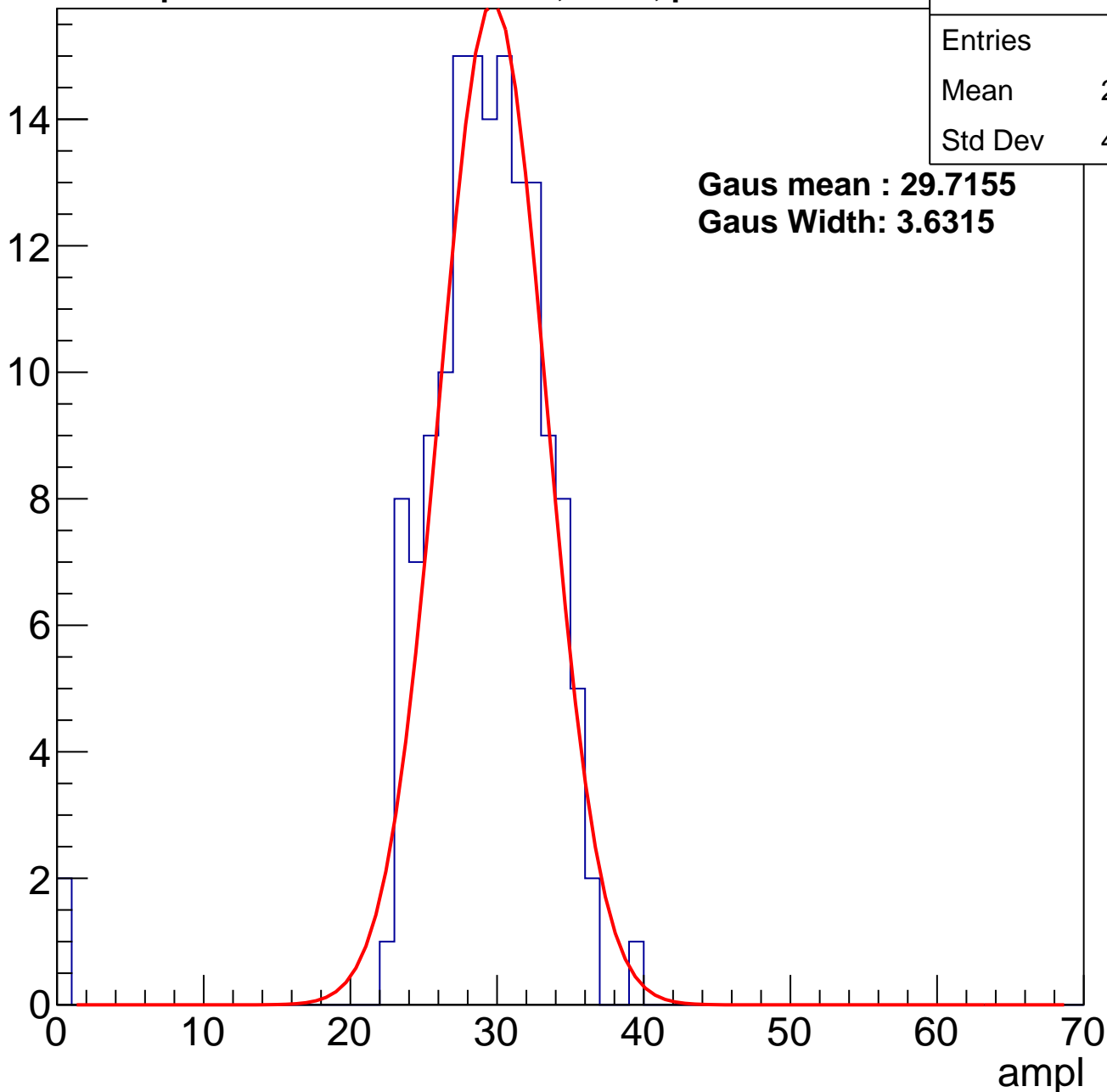
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	28.67
Std Dev	4.804

Gaus mean : 29.7155

Gaus Width: 3.6315

Entry



B1L001S, U14-ch55, adc1

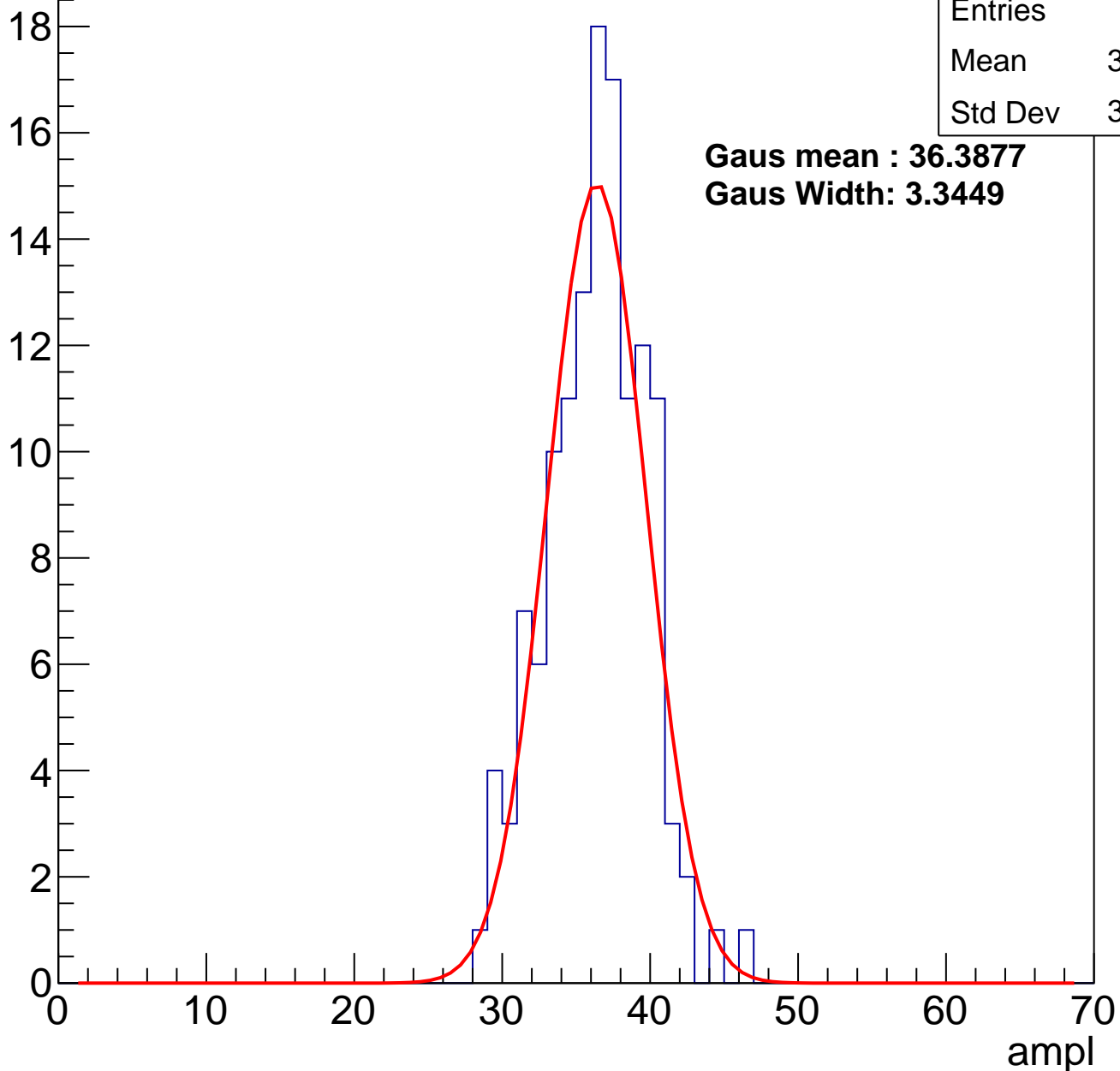
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.89
Std Dev	3.316

Gaus mean : 36.3877

Gaus Width: 3.3449

Entry



B1L001S, U14-ch55, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	41.87
Std Dev	3.199

Gaus mean : 42.3883

Gaus Width: 3.4136

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch55, adc3

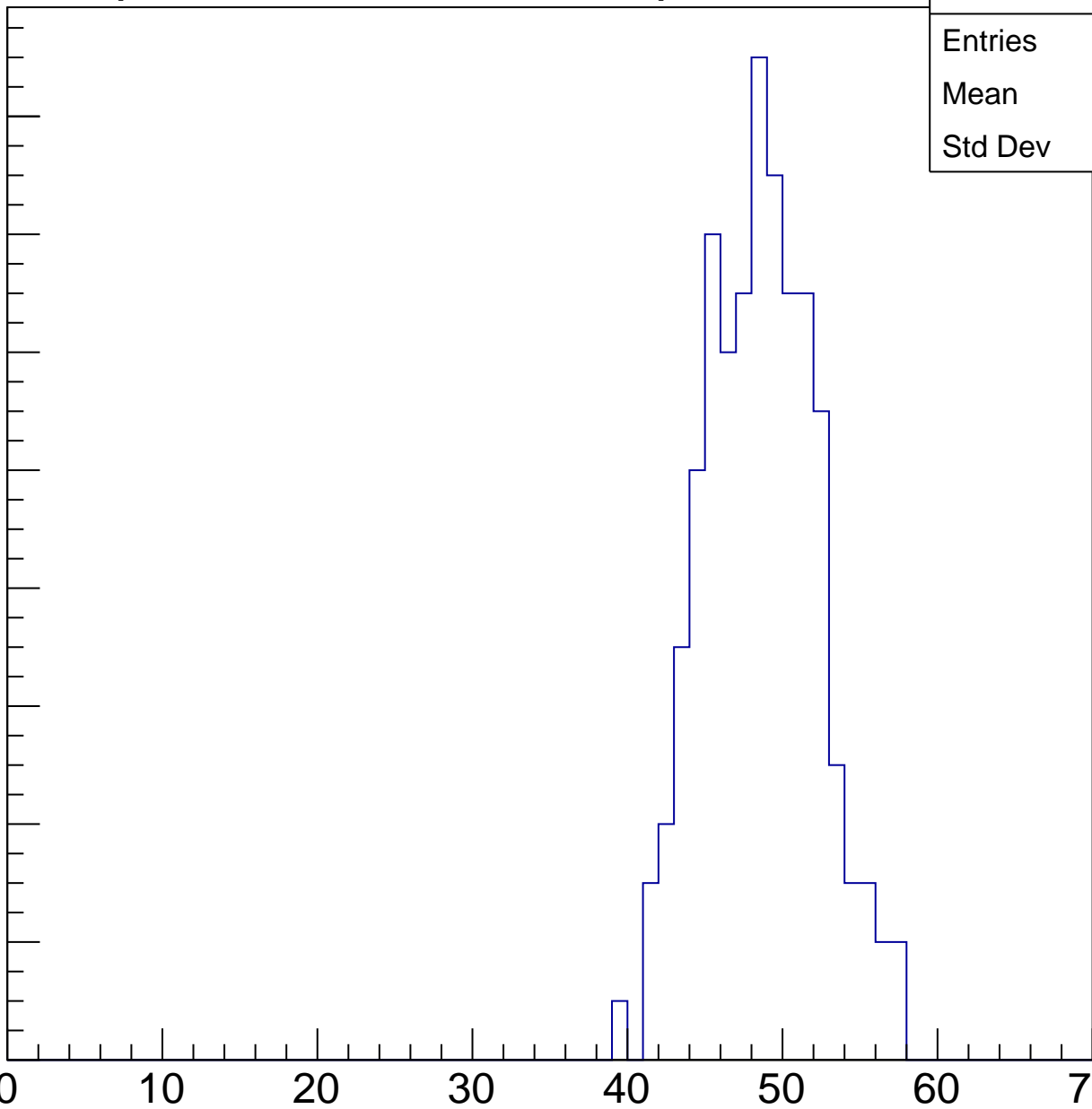
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	148
Mean	48.09
Std Dev	3.604

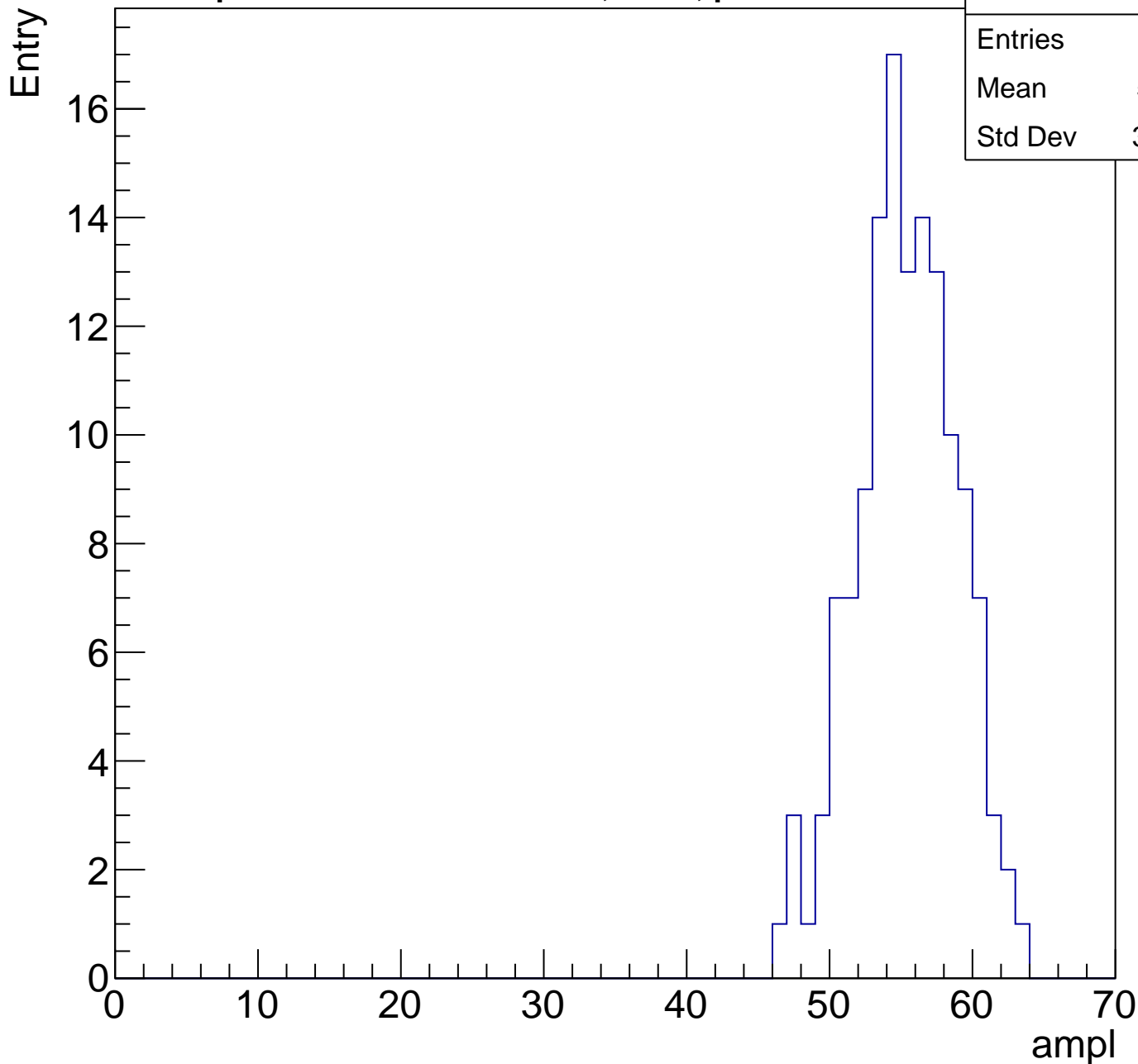
ampl



B1L001S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	54.91
Std Dev	3.472



B1L001S, U14-ch55, adc5

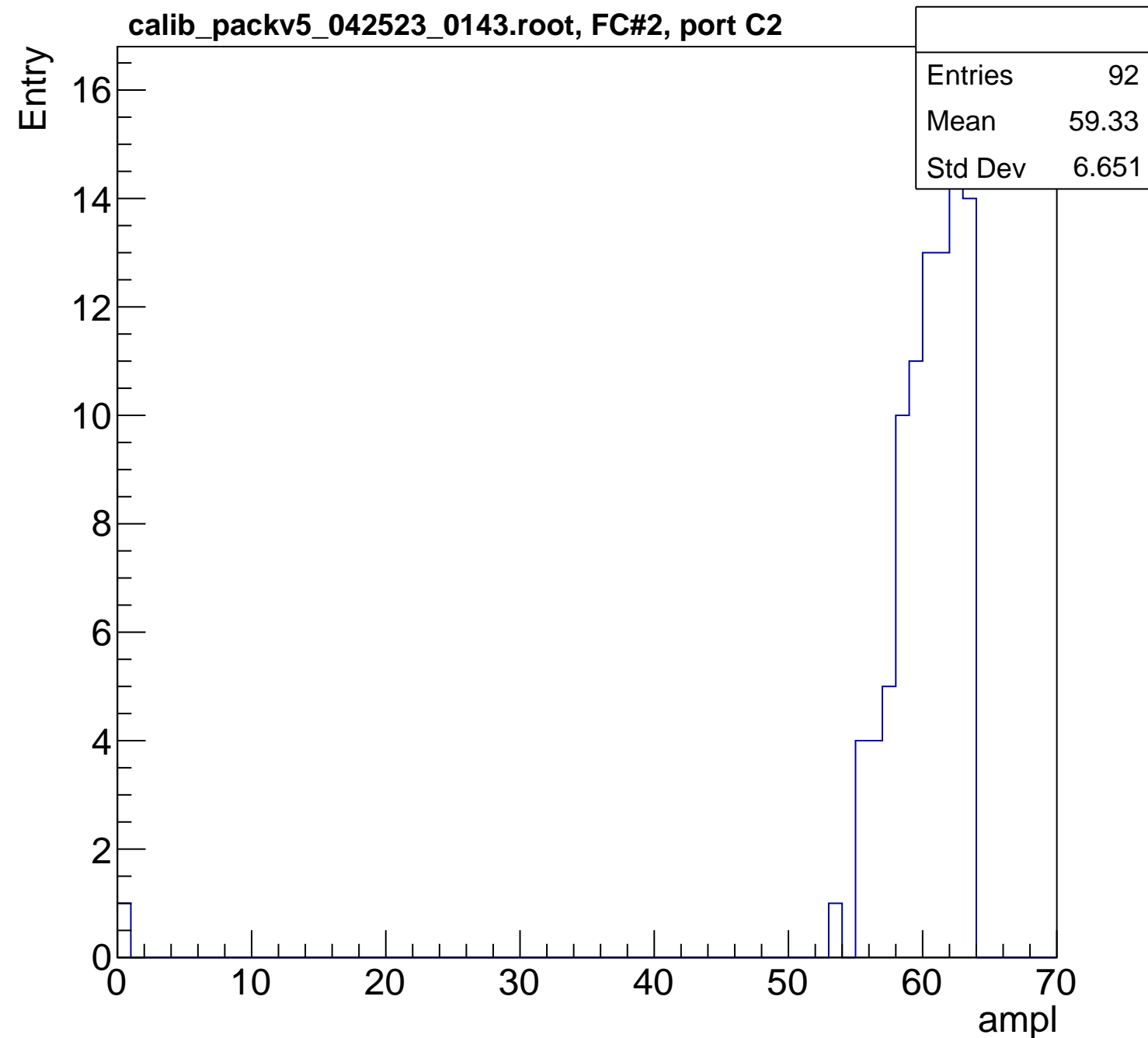
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	92
Mean	59.33
Std Dev	6.651

ampl

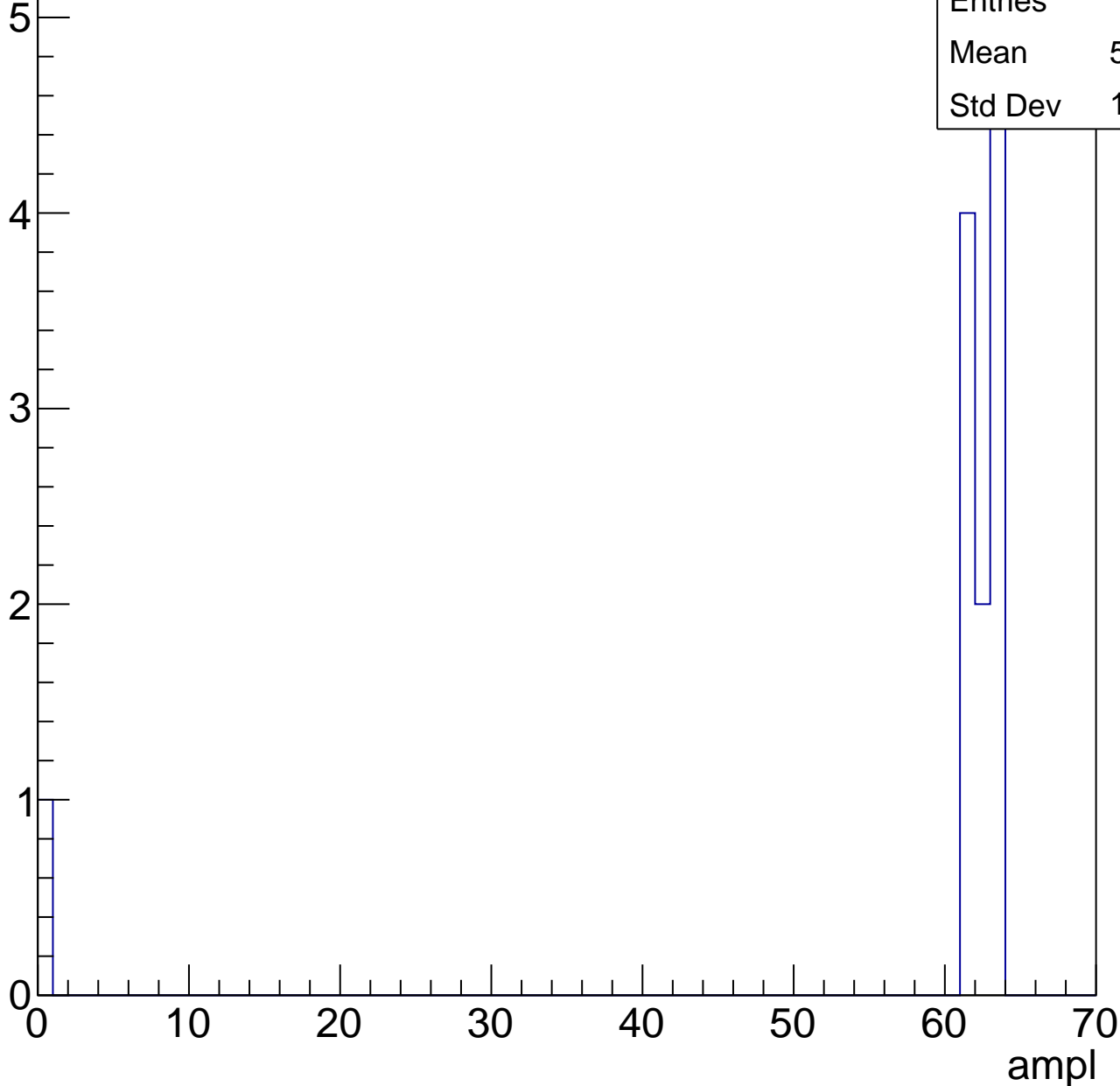


B1L001S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	56.92
Std Dev	17.18



B1L001S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch56, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	28.61
Std Dev	3.928

Gaus mean : 29.3687

Gaus Width: 3.0515

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

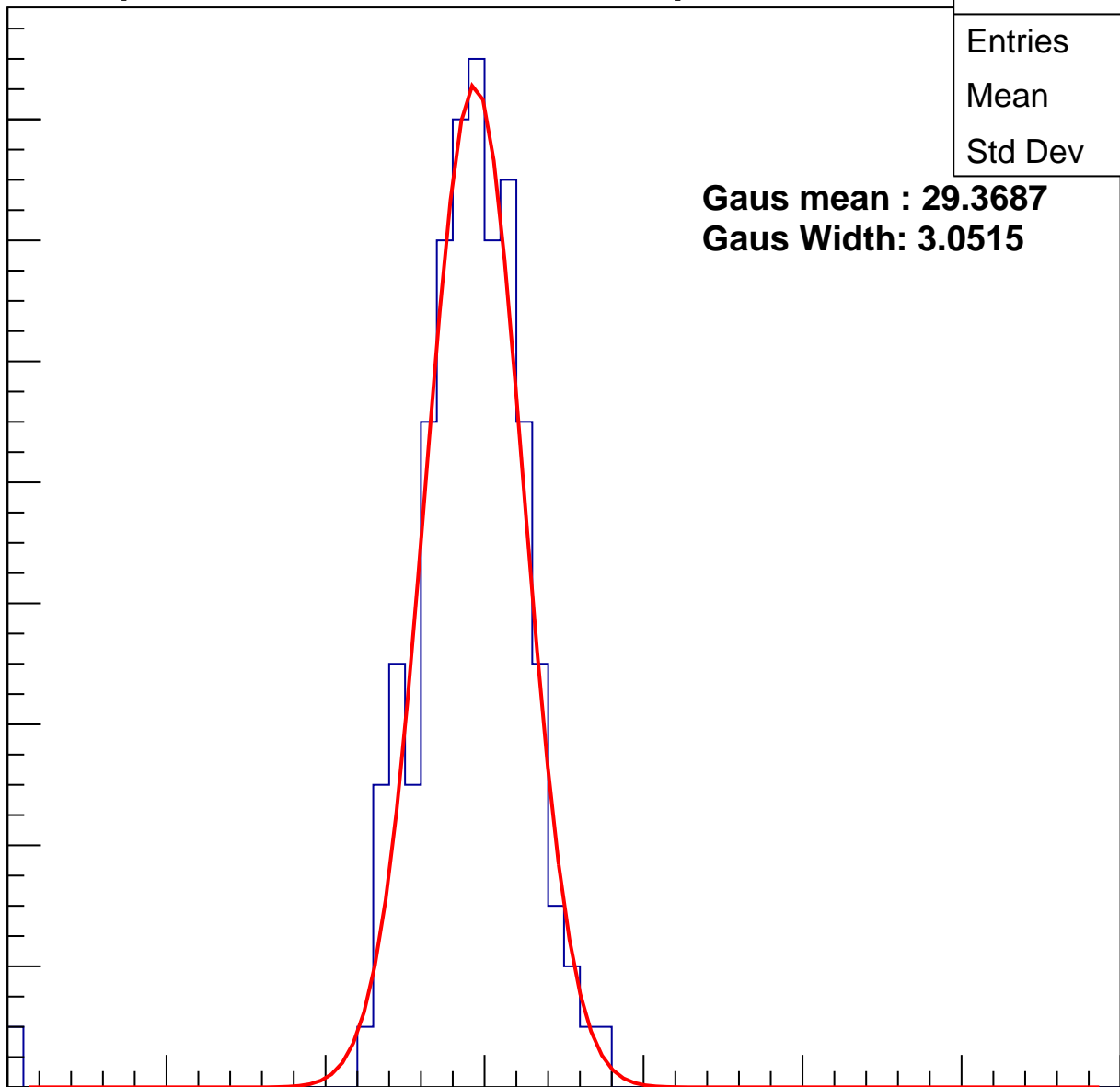
40

50

60

70

ampl



B1L001S, U14-ch56, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.68
Std Dev	3.348

Gaus mean : 36.2697

Gaus Width: 3.2670

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

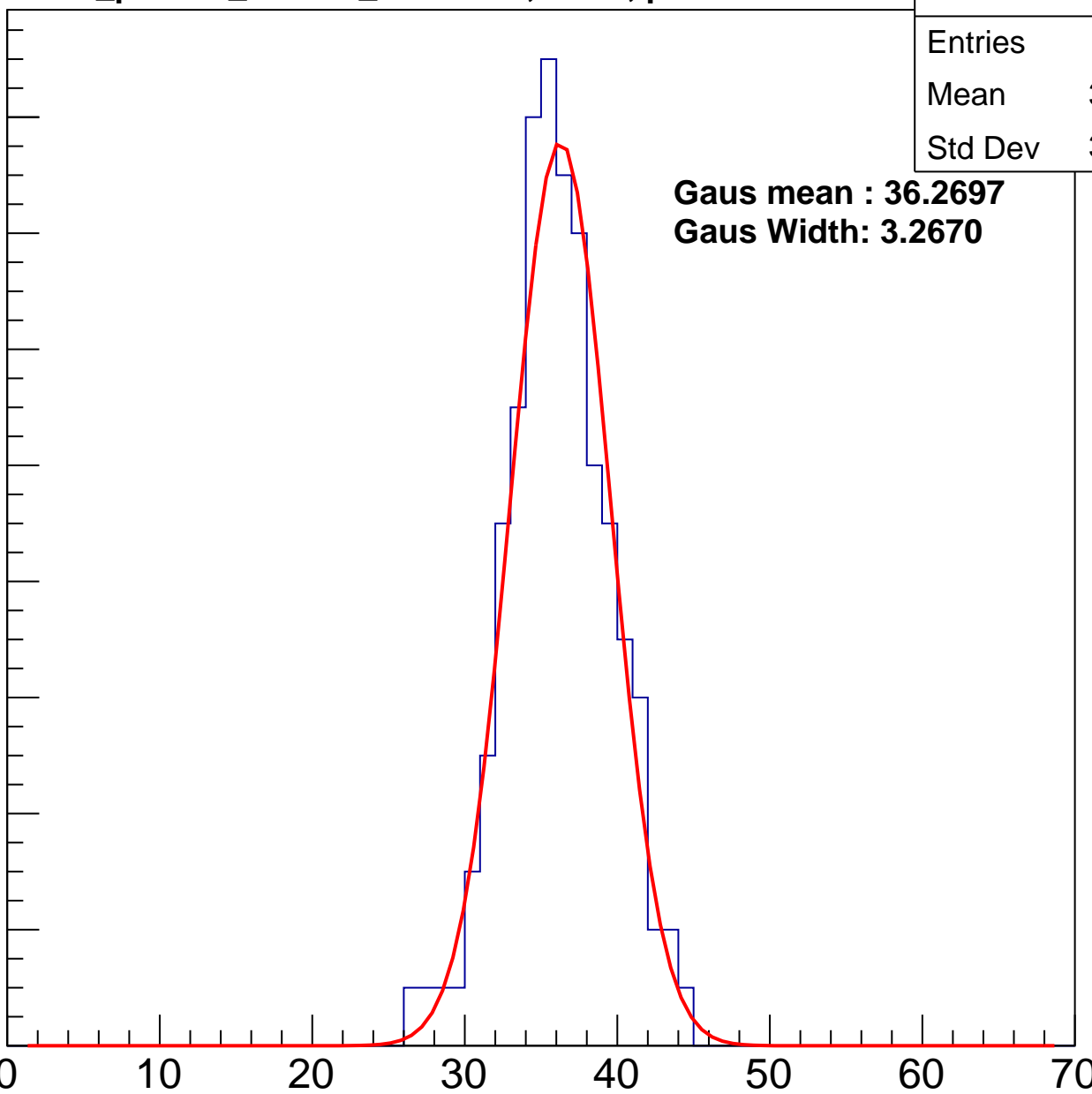
40

50

60

70

ampl



B1L001S, U14-ch56, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

107

Mean

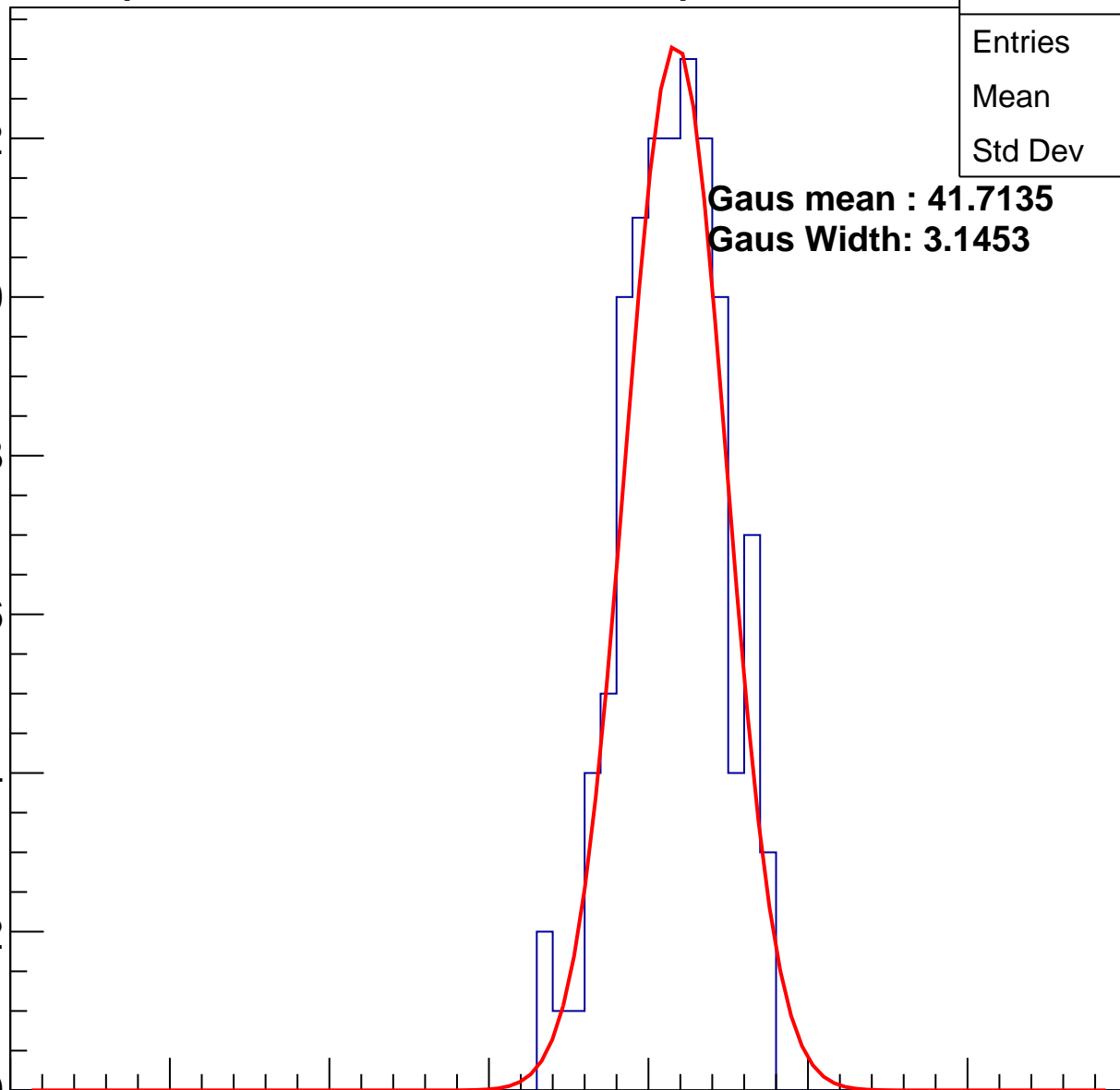
41.03

Std Dev

3.113

Gaus mean : 41.7135

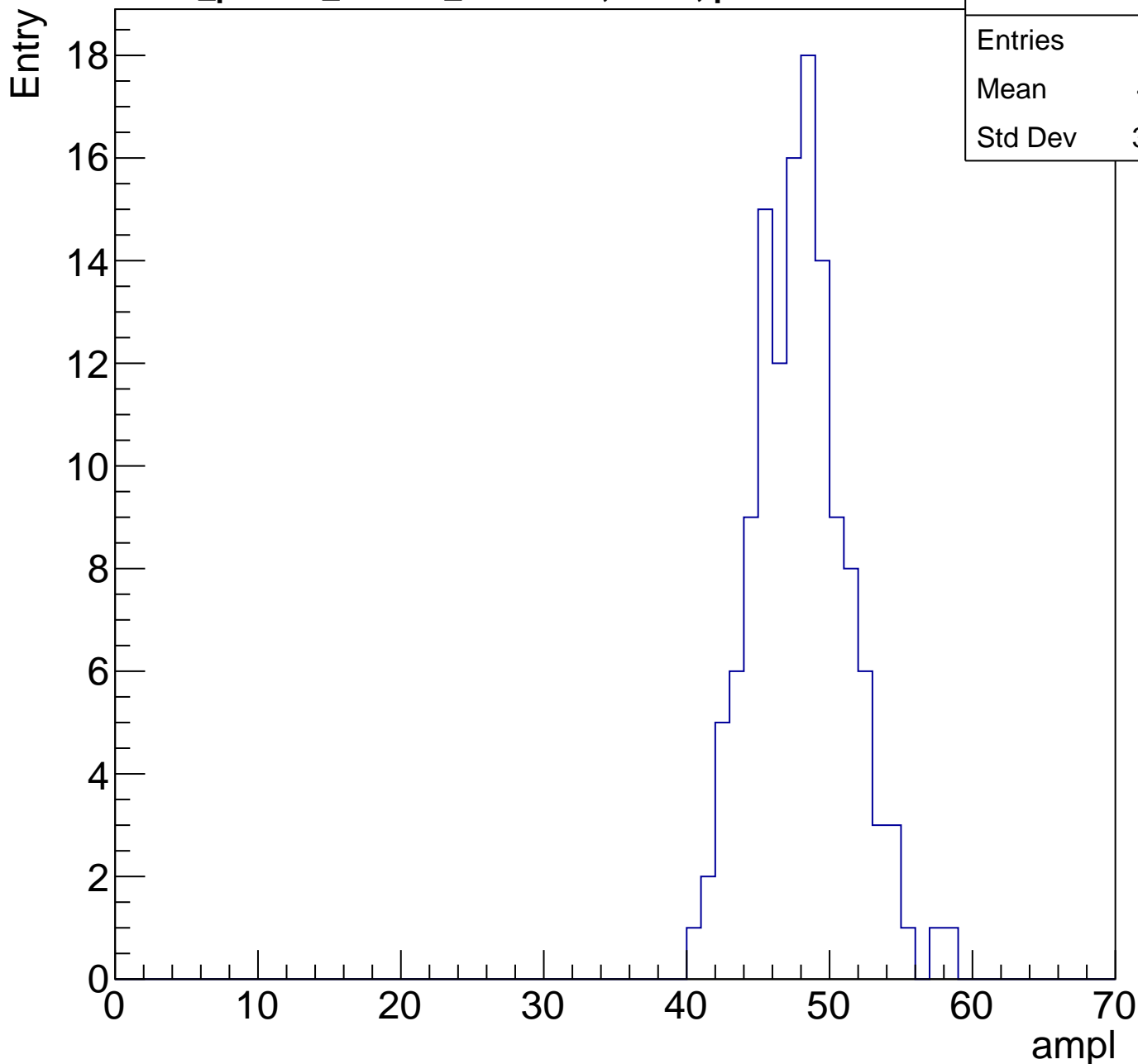
Gaus Width: 3.1453



B1L001S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	47.51
Std Dev	3.322



B1L001S, U14-ch56, adc4

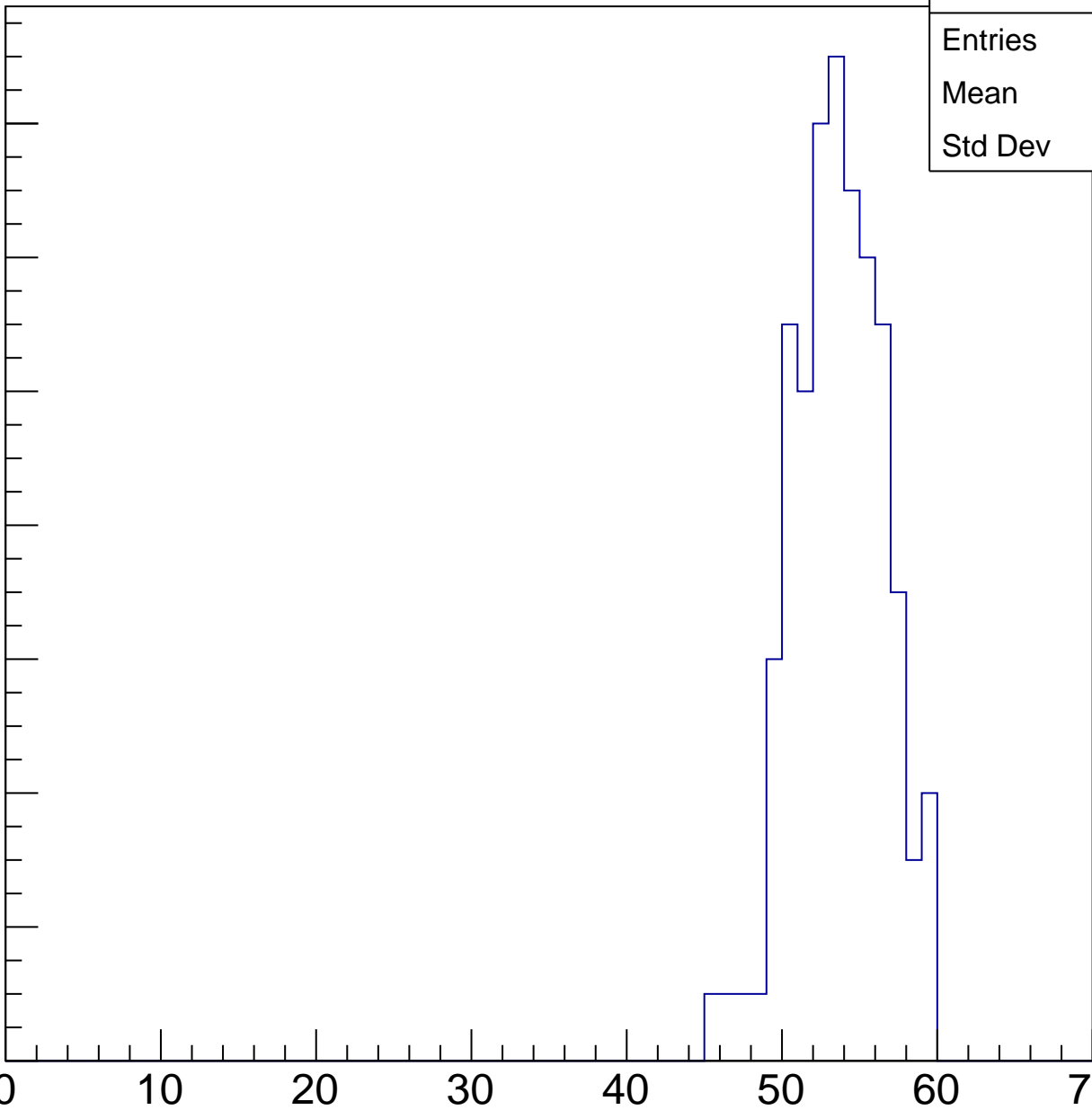
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	53.18
Std Dev	2.877

ampl



B1L001S, U14-ch56, adc5

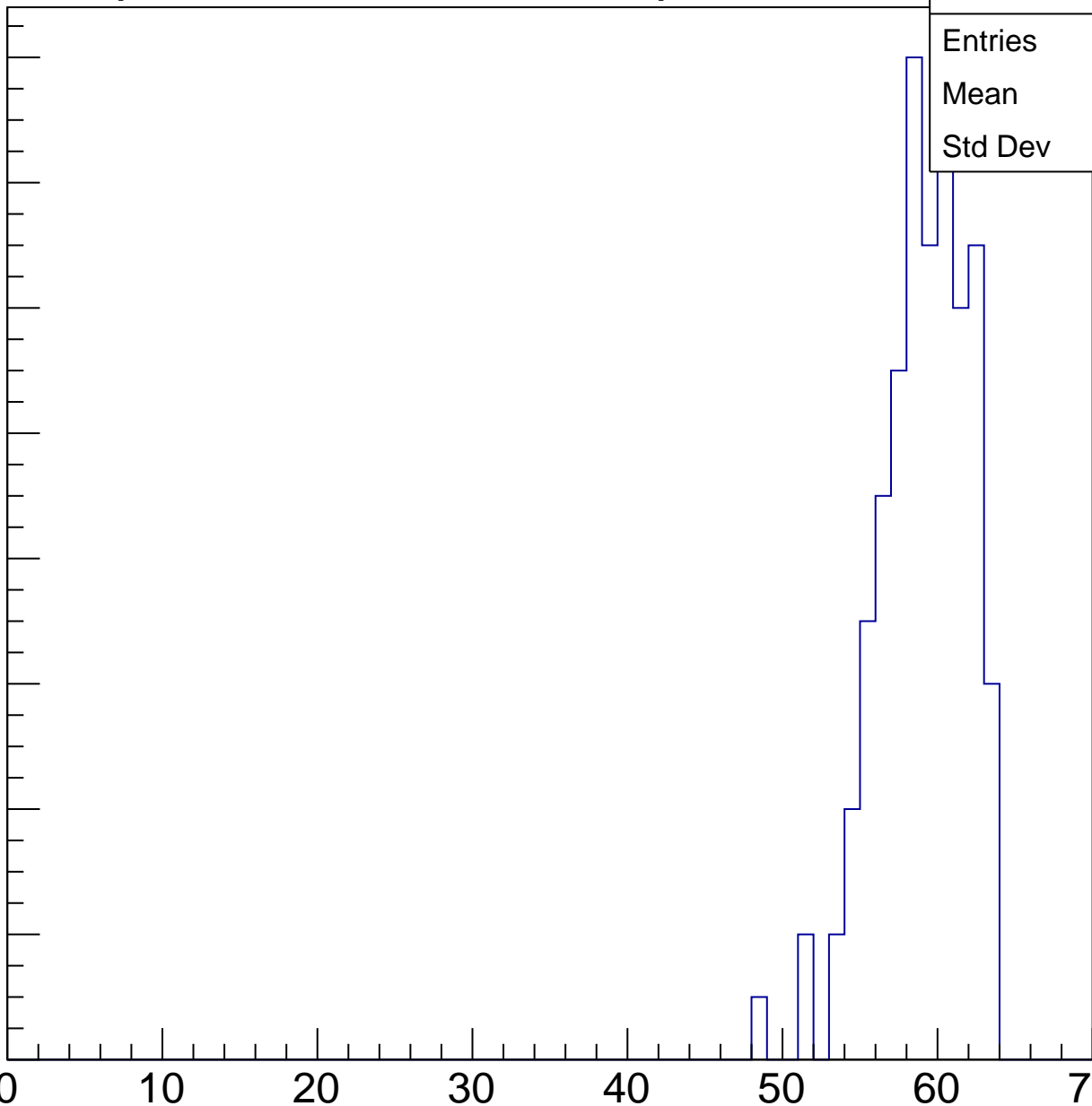
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	112
Mean	58.56
Std Dev	2.881

ampl



B1L001S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

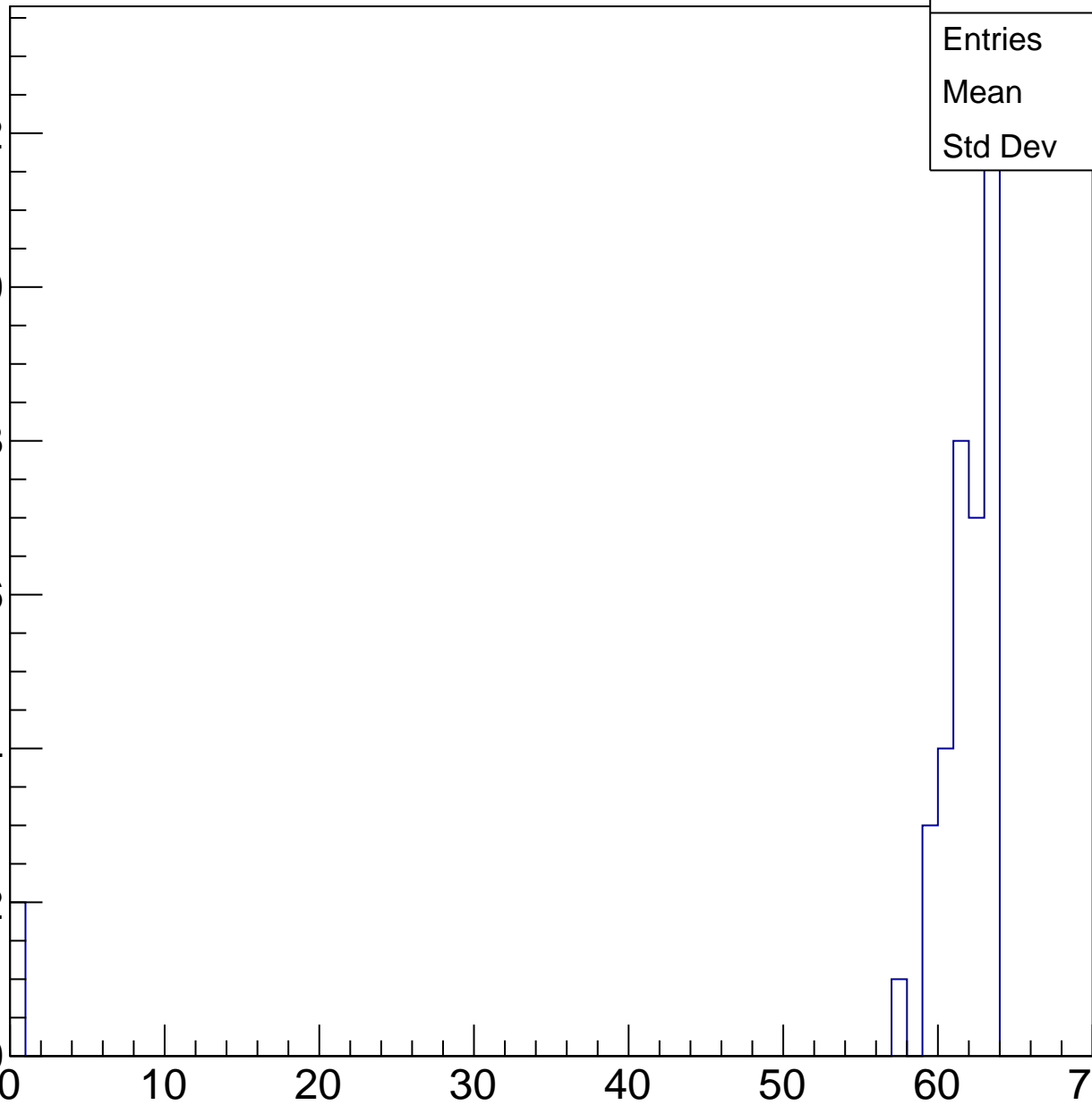
4

2

0

Entries	38
Mean	58.29
Std Dev	13.82

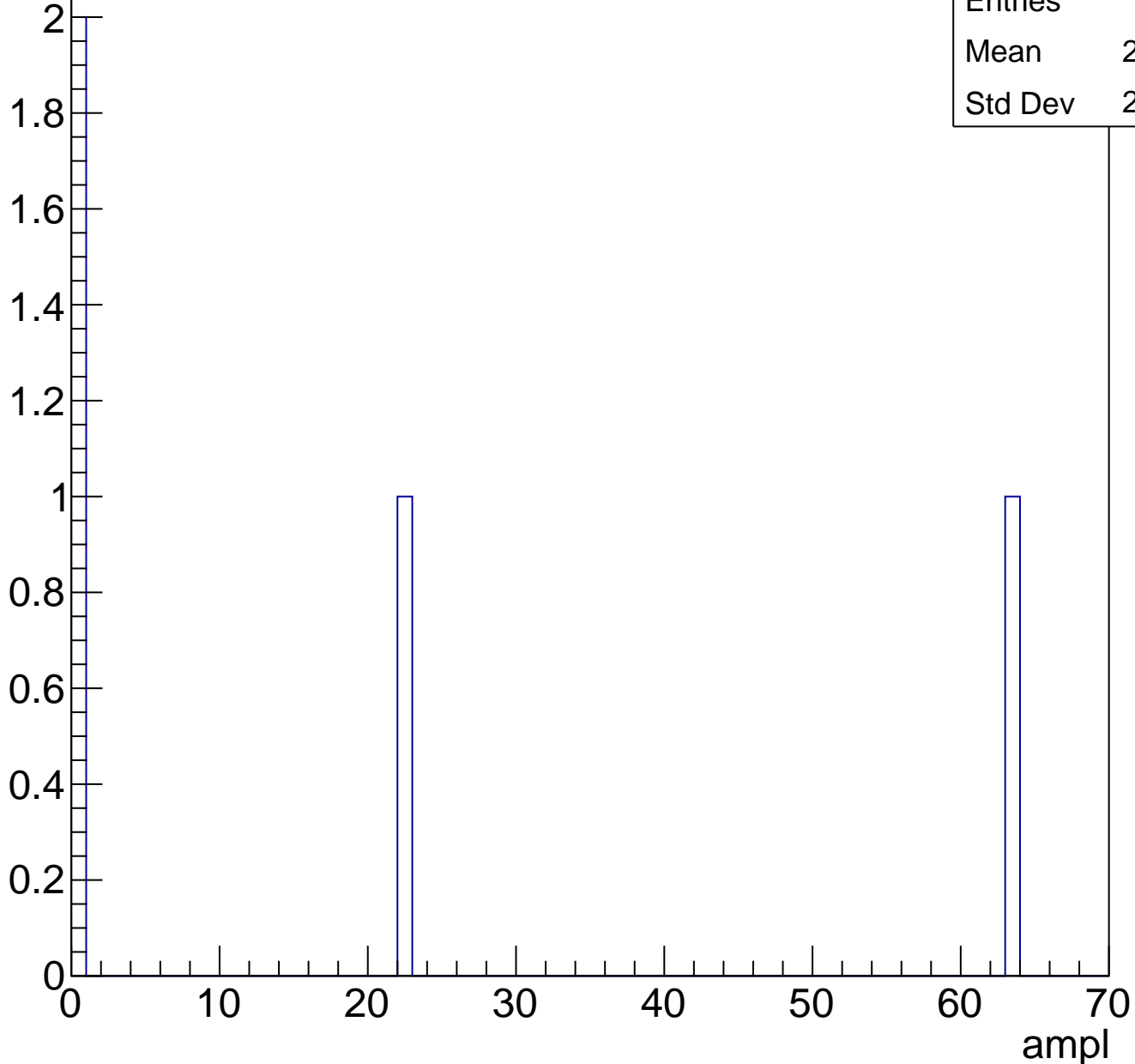
ampl



B1L001S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B1L001S, U14-ch57, adc0

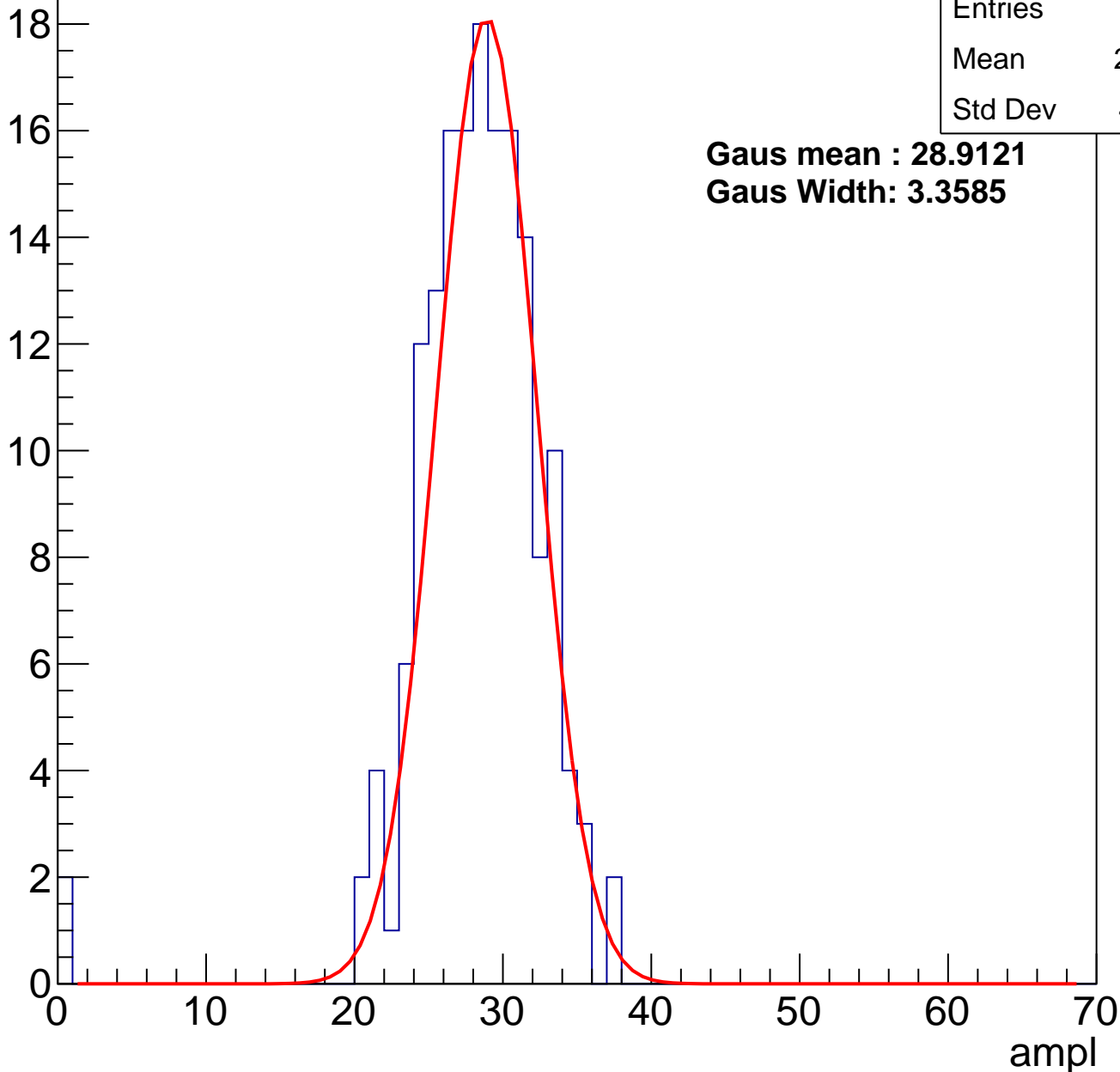
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	27.78
Std Dev	4.631

Gaus mean : 28.9121

Gaus Width: 3.3585

Entry



B1L001S, U14-ch57, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	35.25
Std Dev	3.603

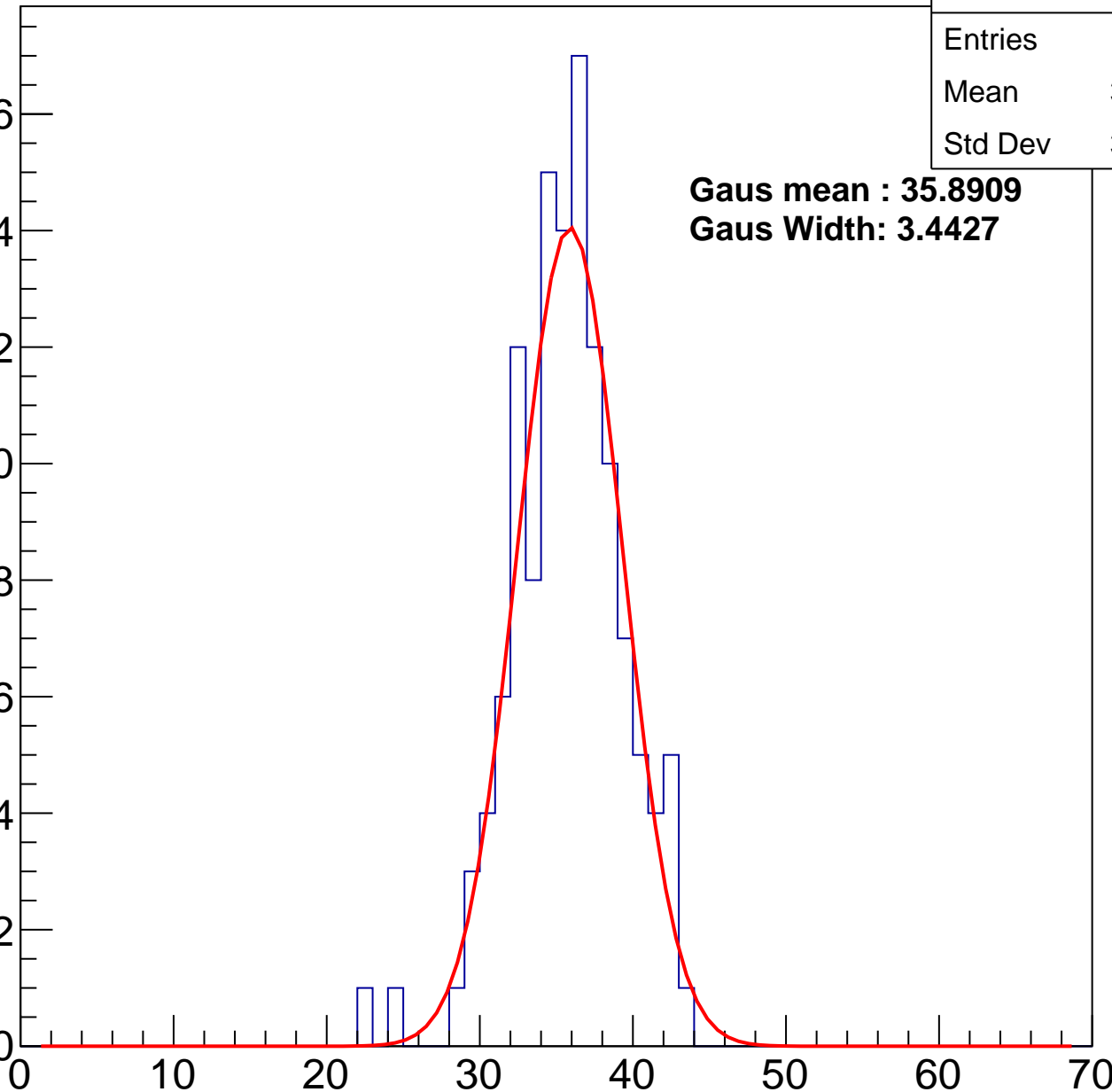
Gaus mean : 35.8909

Gaus Width: 3.4427

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch57, adc2

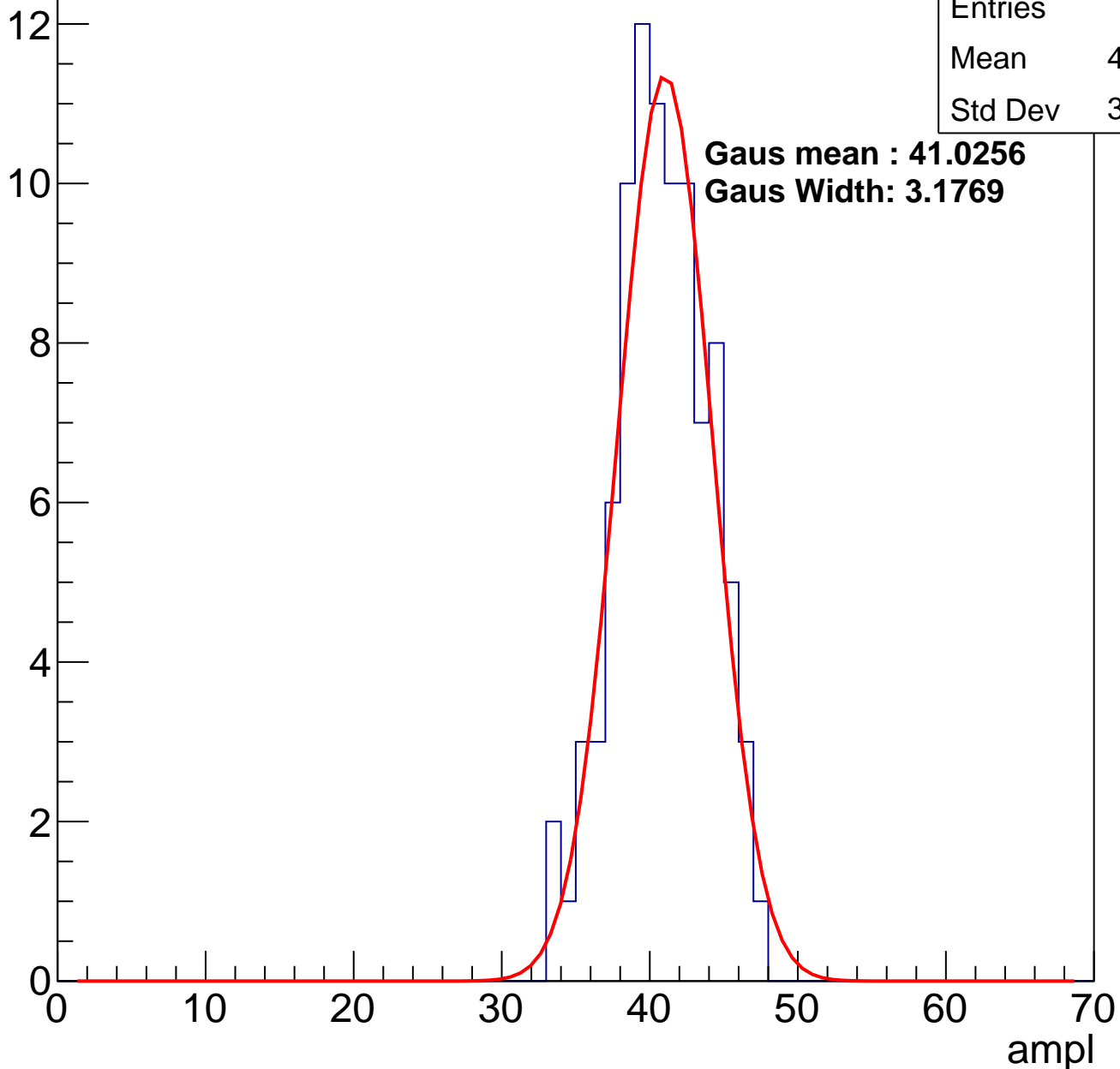
calib_packv5_042523_0143.root, FC#2, port C2

Entries	92
Mean	40.39
Std Dev	3.079

Gaus mean : 41.0256

Gaus Width: 3.1769

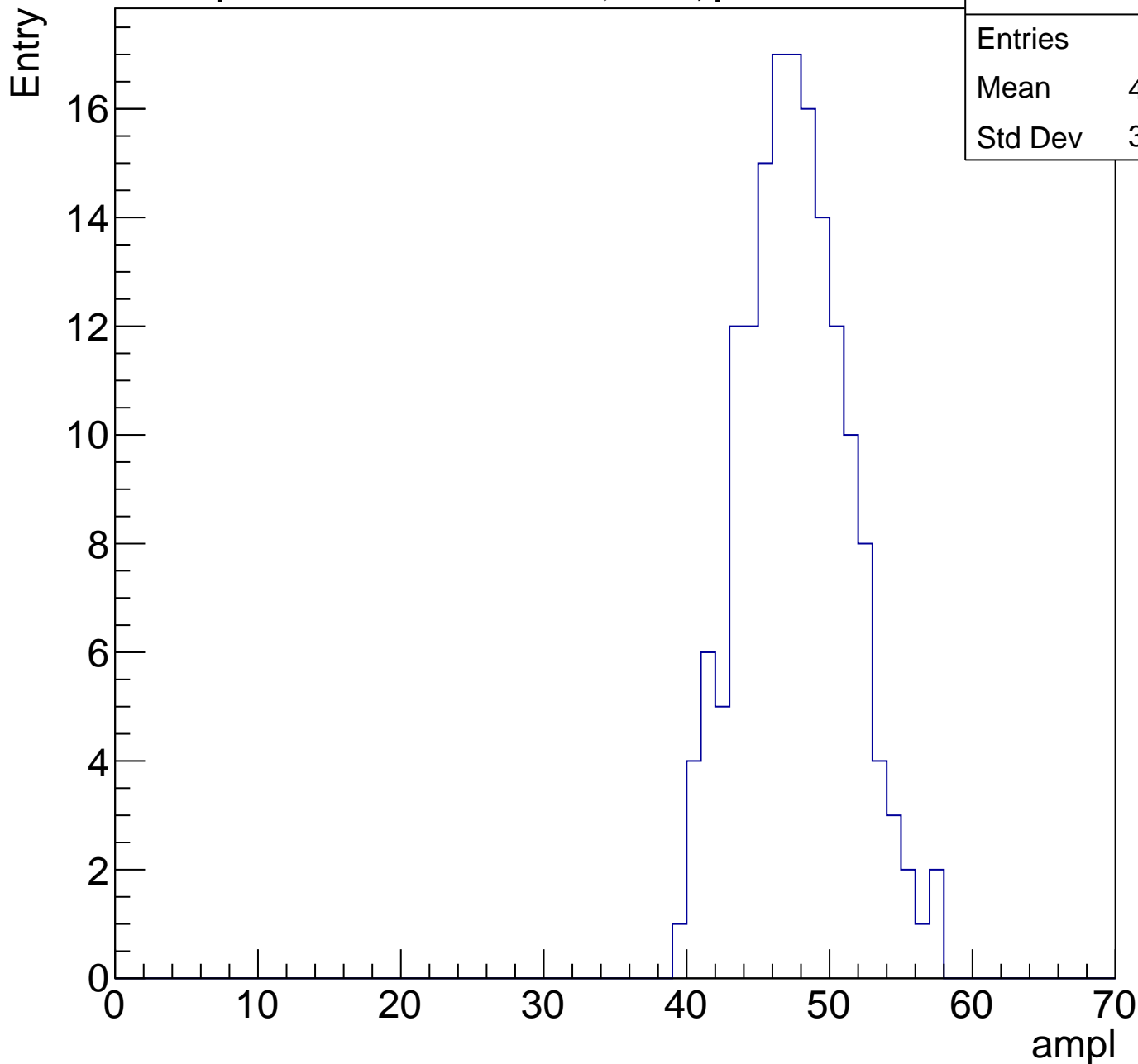
Entry



B1L001S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#2, port C2

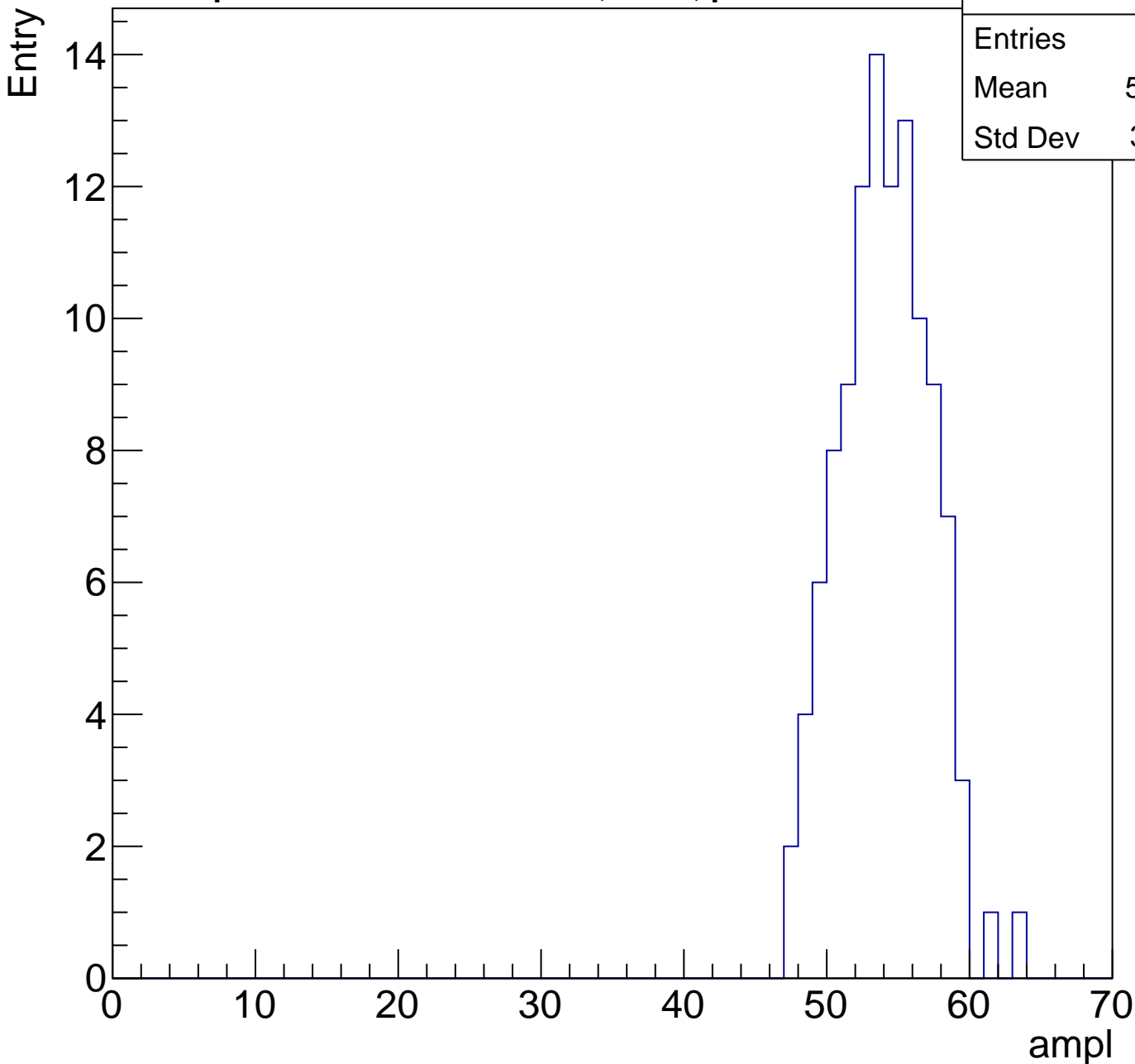
Entries	161
Mean	47.14
Std Dev	3.709



B1L001S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#2, port C2

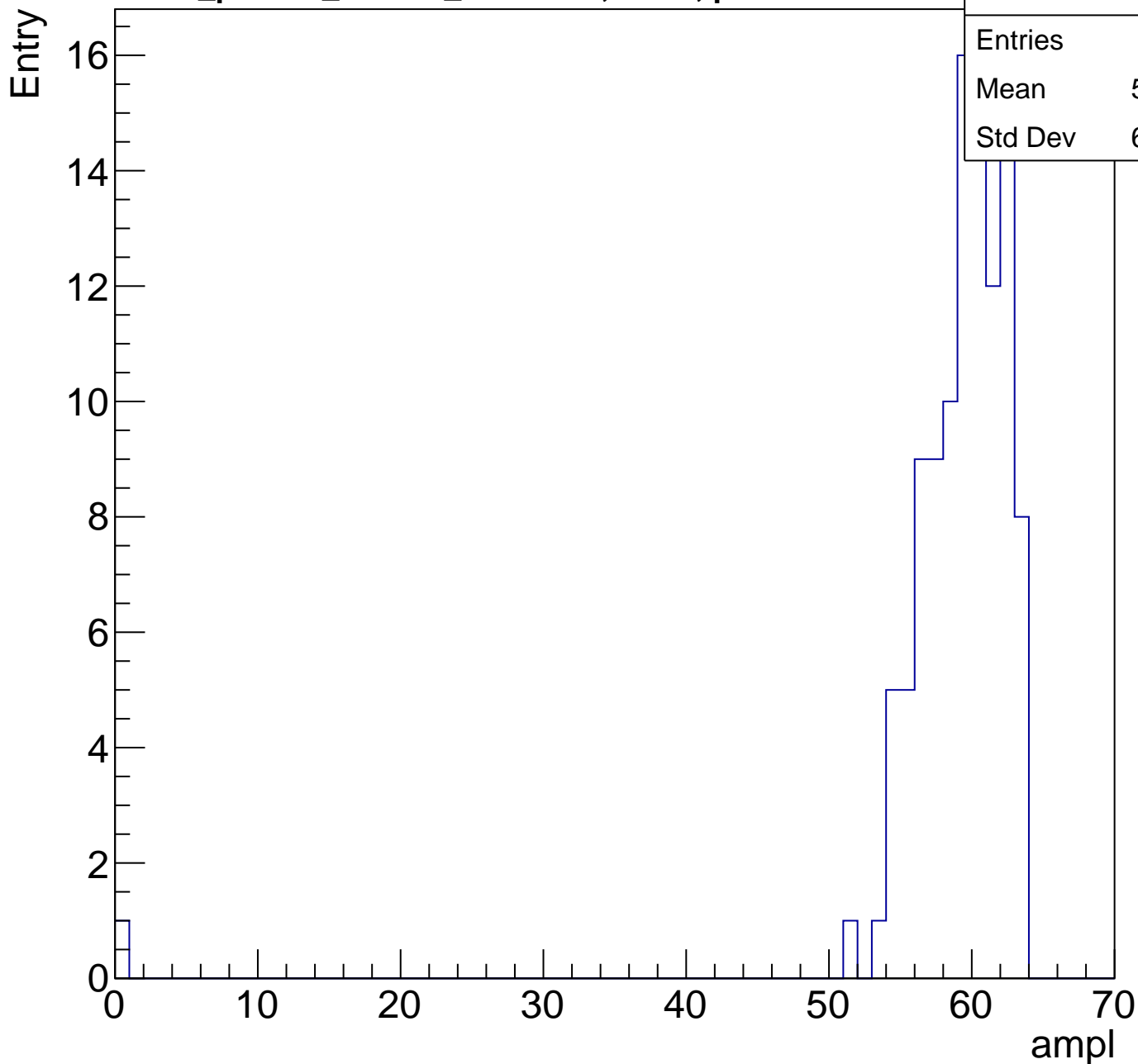
Entries	111
Mean	53.59
Std Dev	3.141



B1L001S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	58.47
Std Dev	6.272

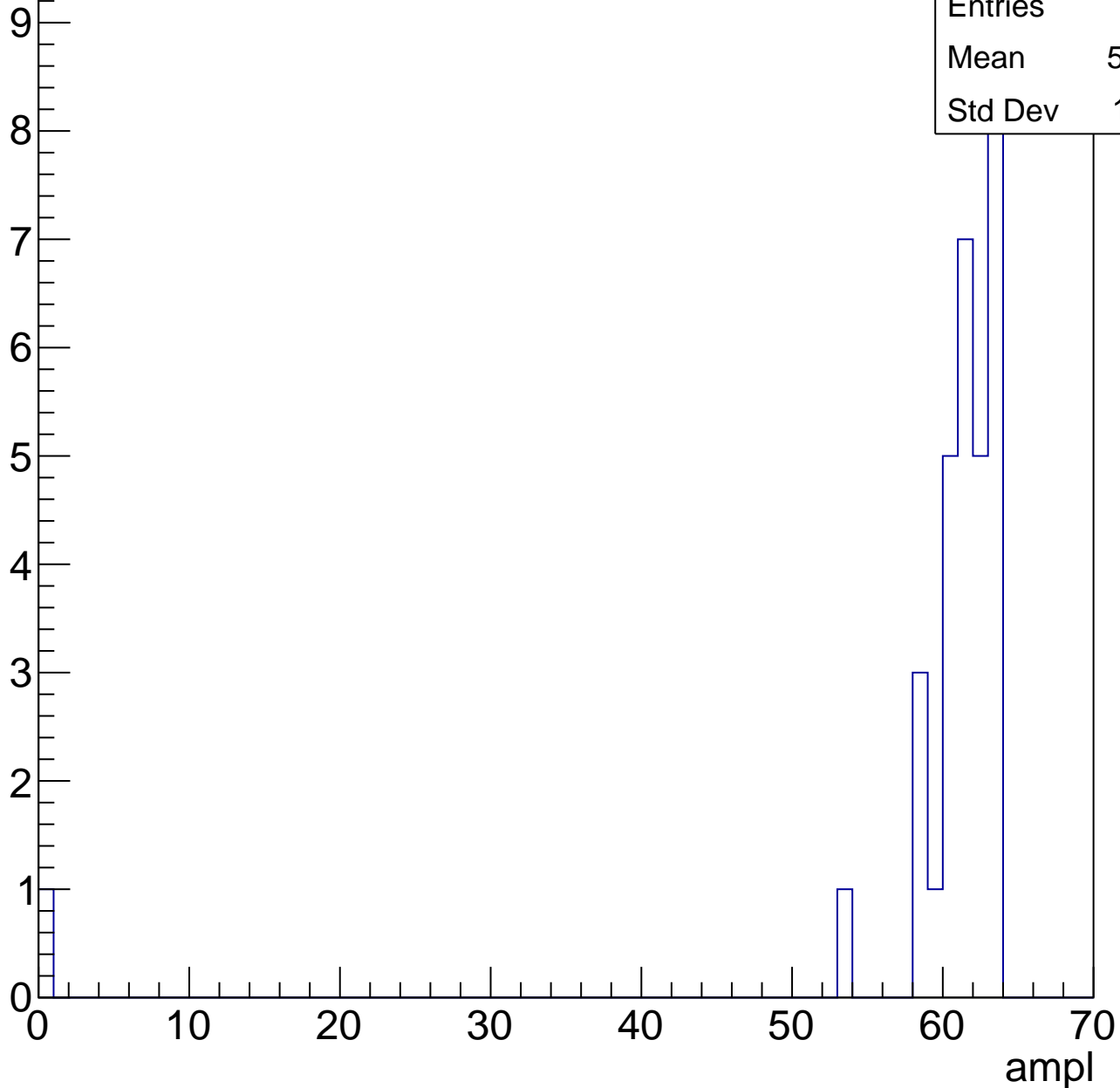


B1L001S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

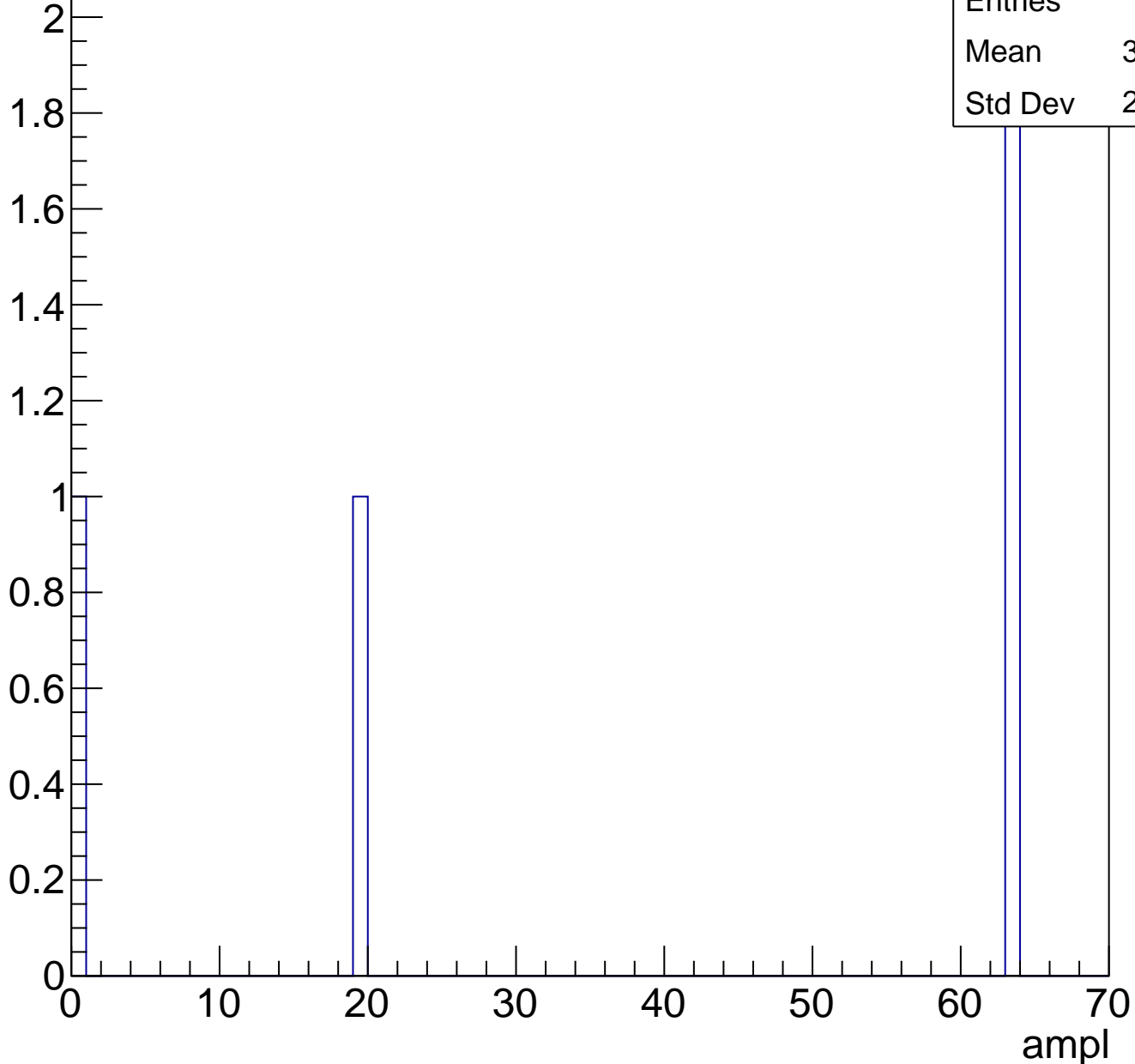
Entries	32
Mean	59.06
Std Dev	10.81



B1L001S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	36.25
Std Dev	27.58

B1L001S, U14-ch58, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	30.86
Std Dev	3.463

Gaus mean : 31.3181

Gaus Width: 3.7425

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

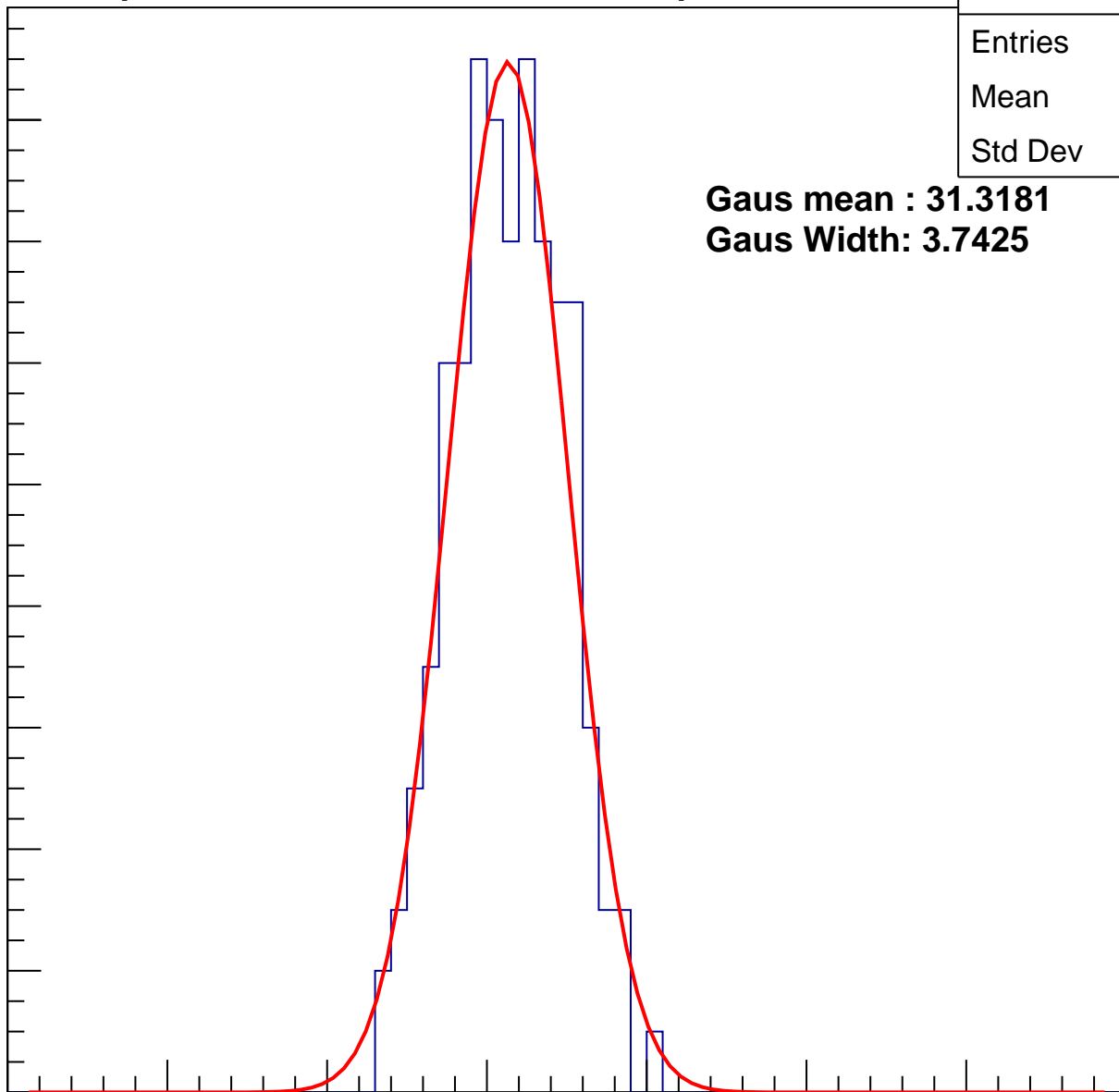
40

50

60

70

ampl



B1L001S, U14-ch58, adc1

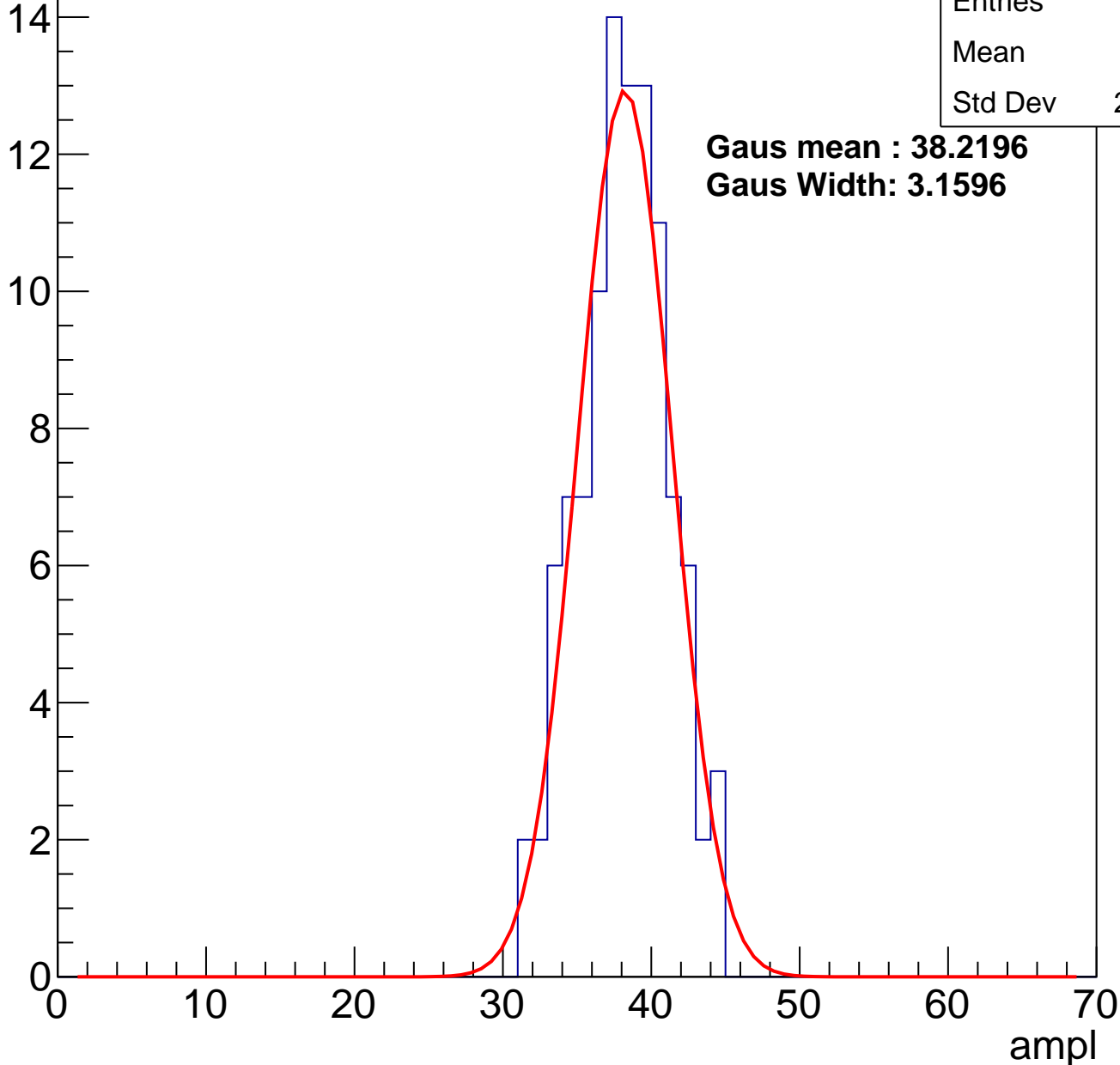
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	37.7
Std Dev	2.978

Gaus mean : 38.2196

Gaus Width: 3.1596

Entry



B1L001S, U14-ch58, adc2

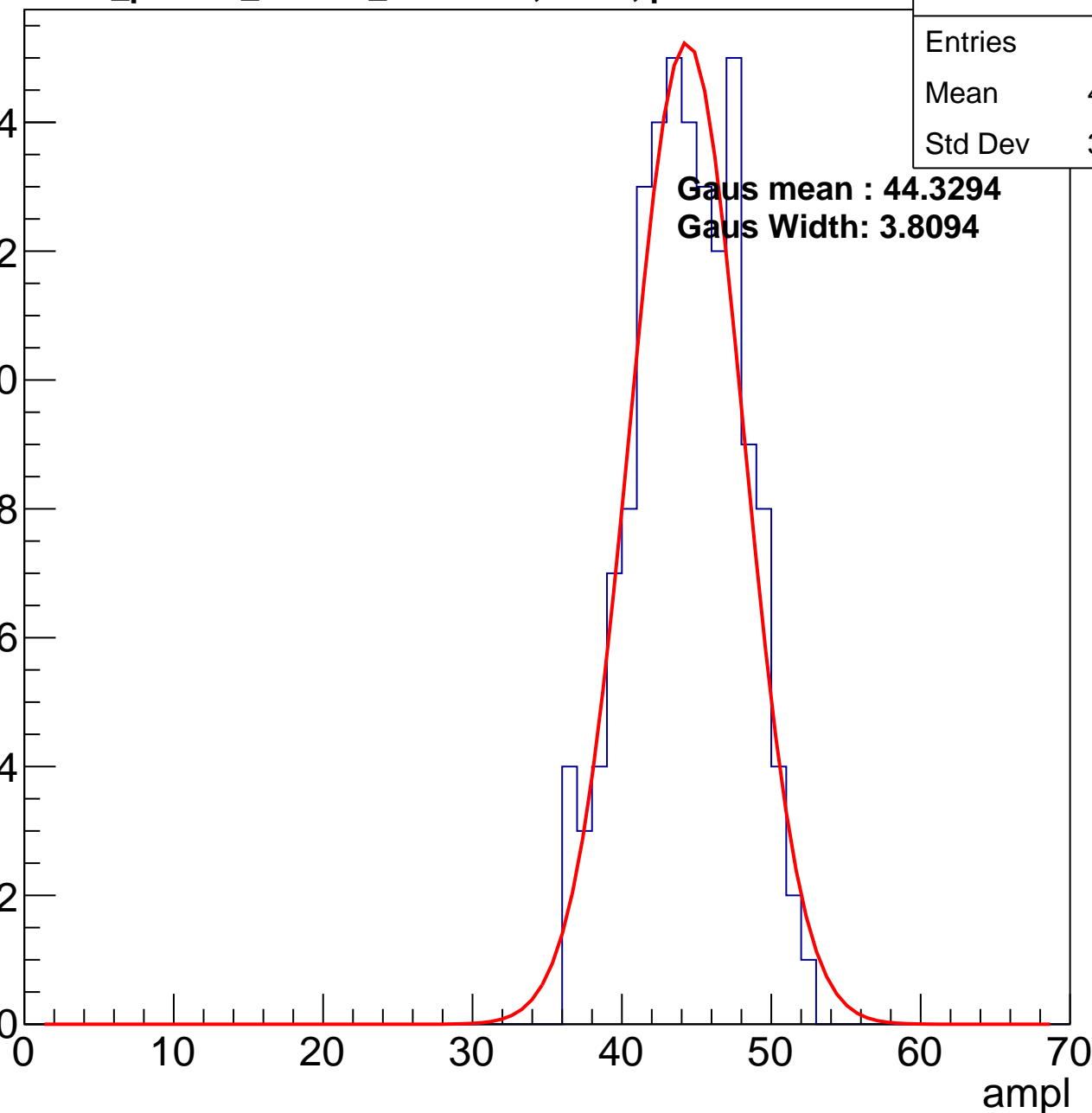
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	146
Mean	43.85
Std Dev	3.589

Gaus mean : 44.3294
Gaus Width: 3.8094

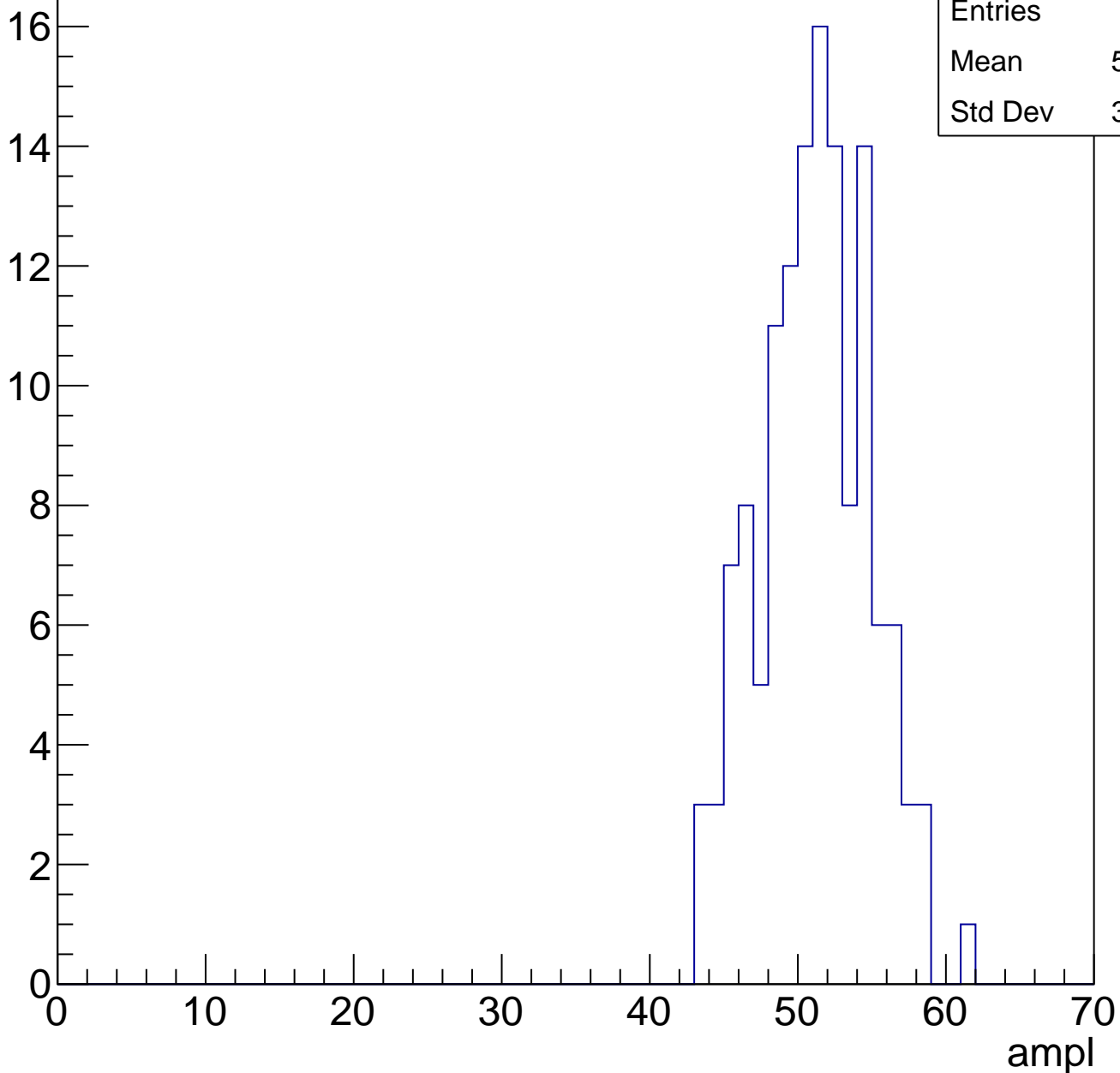


B1L001S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	50.68
Std Dev	3.656

Entry



B1L001S, U14-ch58, adc4

calib_packv5_042523_0143.root, FC#2, port C2

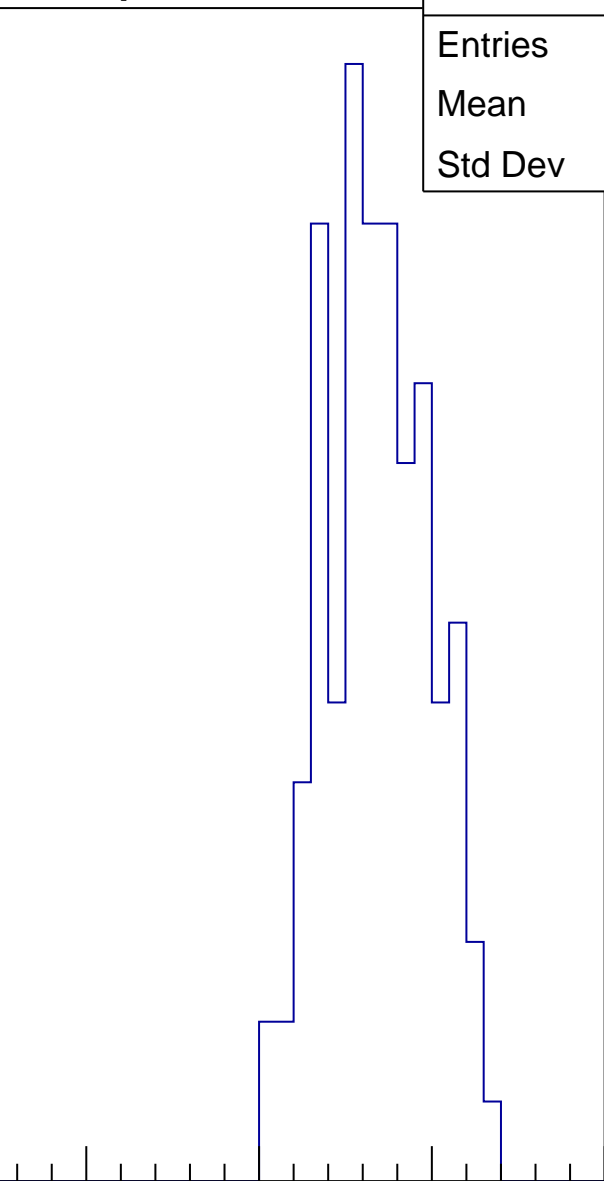
Entries	101
Mean	56.4
Std Dev	2.982

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

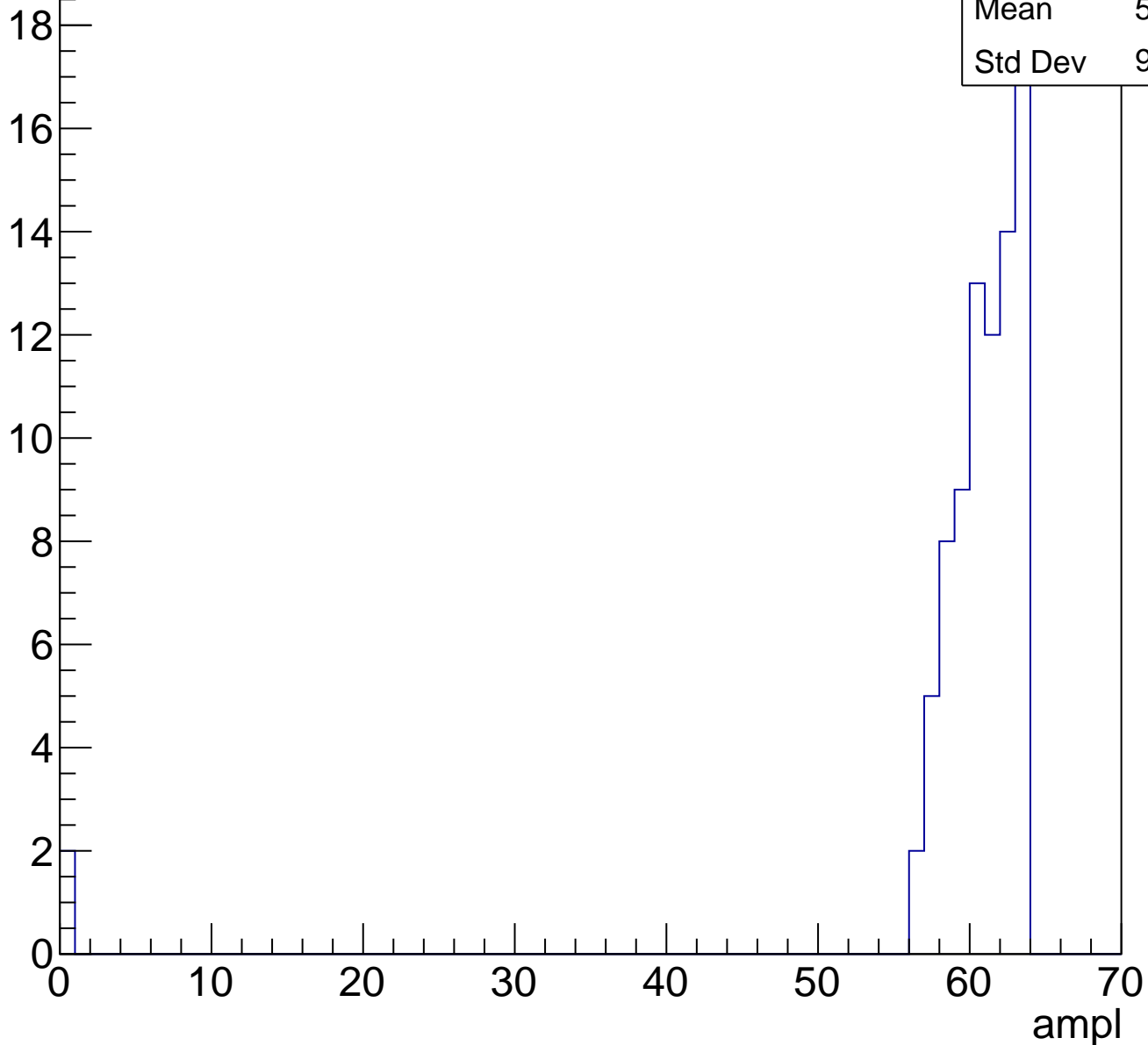
ampl



B1L001S, U14-ch58, adc5

calib_packv5_042523_0143.root, FC#2, port C2

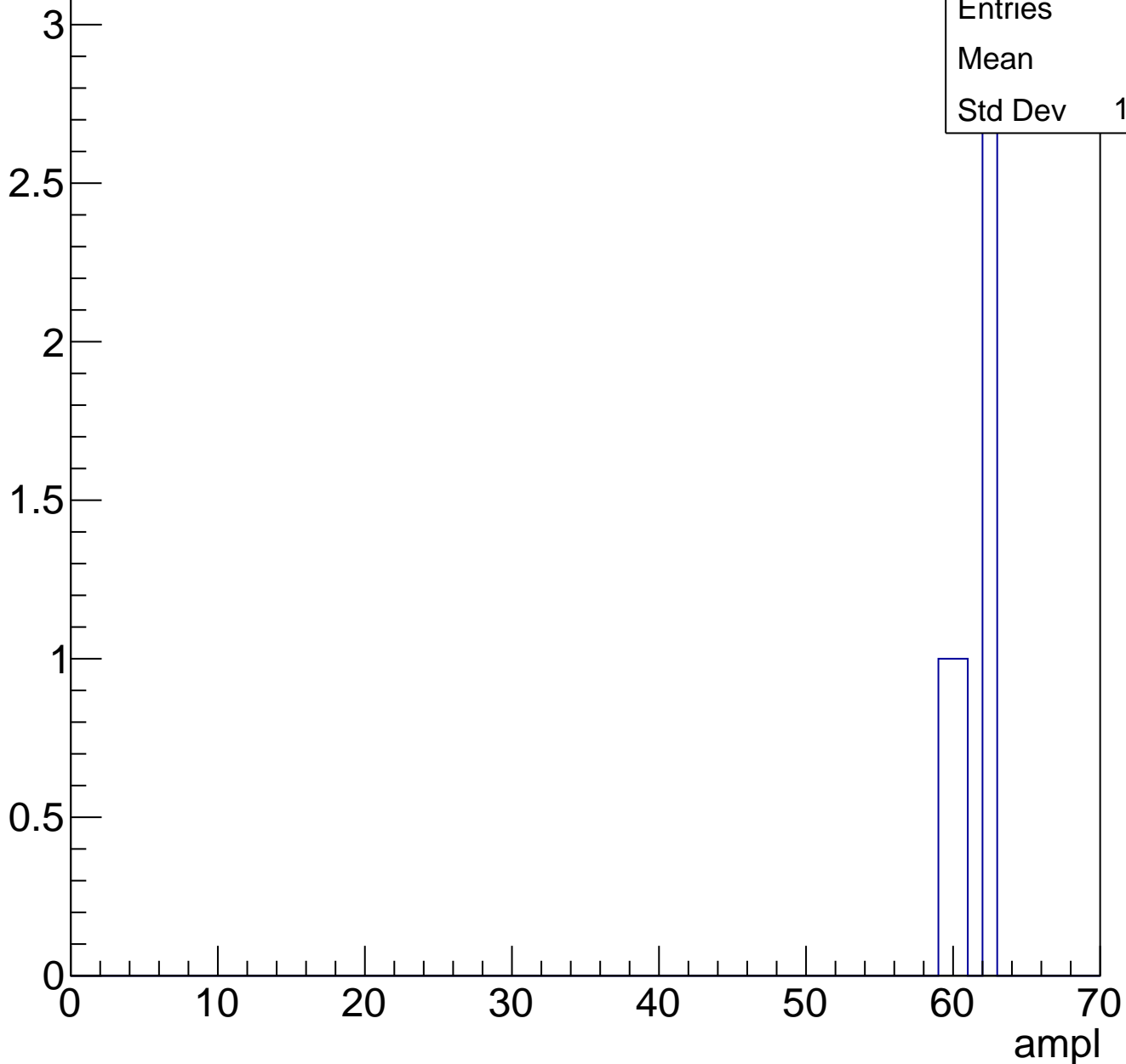
Entry



B1L001S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

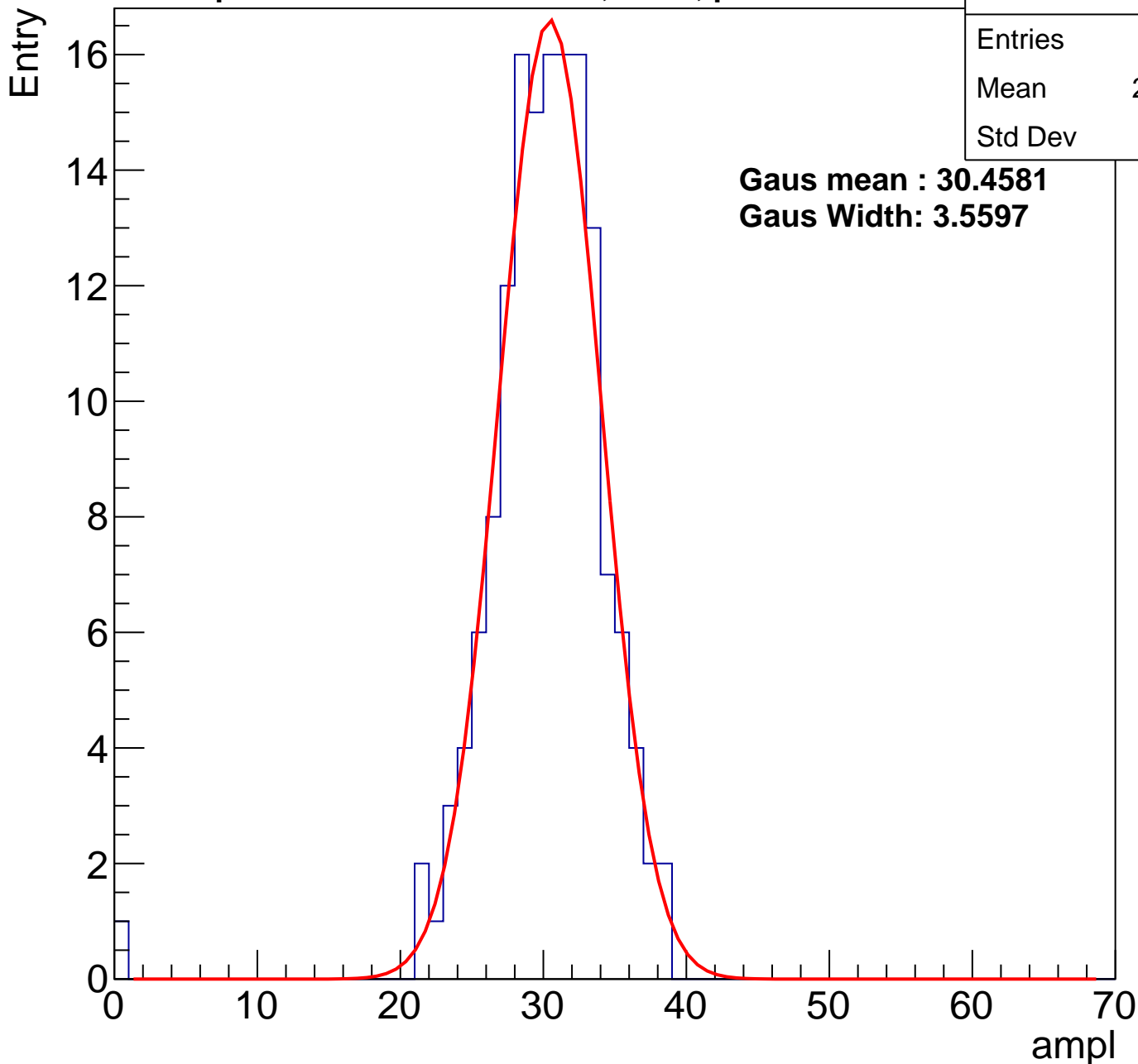
B1L001S, U14-ch59, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	29.69
Std Dev	4.24

Gaus mean : 30.4581

Gaus Width: 3.5597



B1L001S, U14-ch59, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	36.94
Std Dev	3.074

Gaus mean : 37.4772

Gaus Width: 3.2231

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch59, adc2

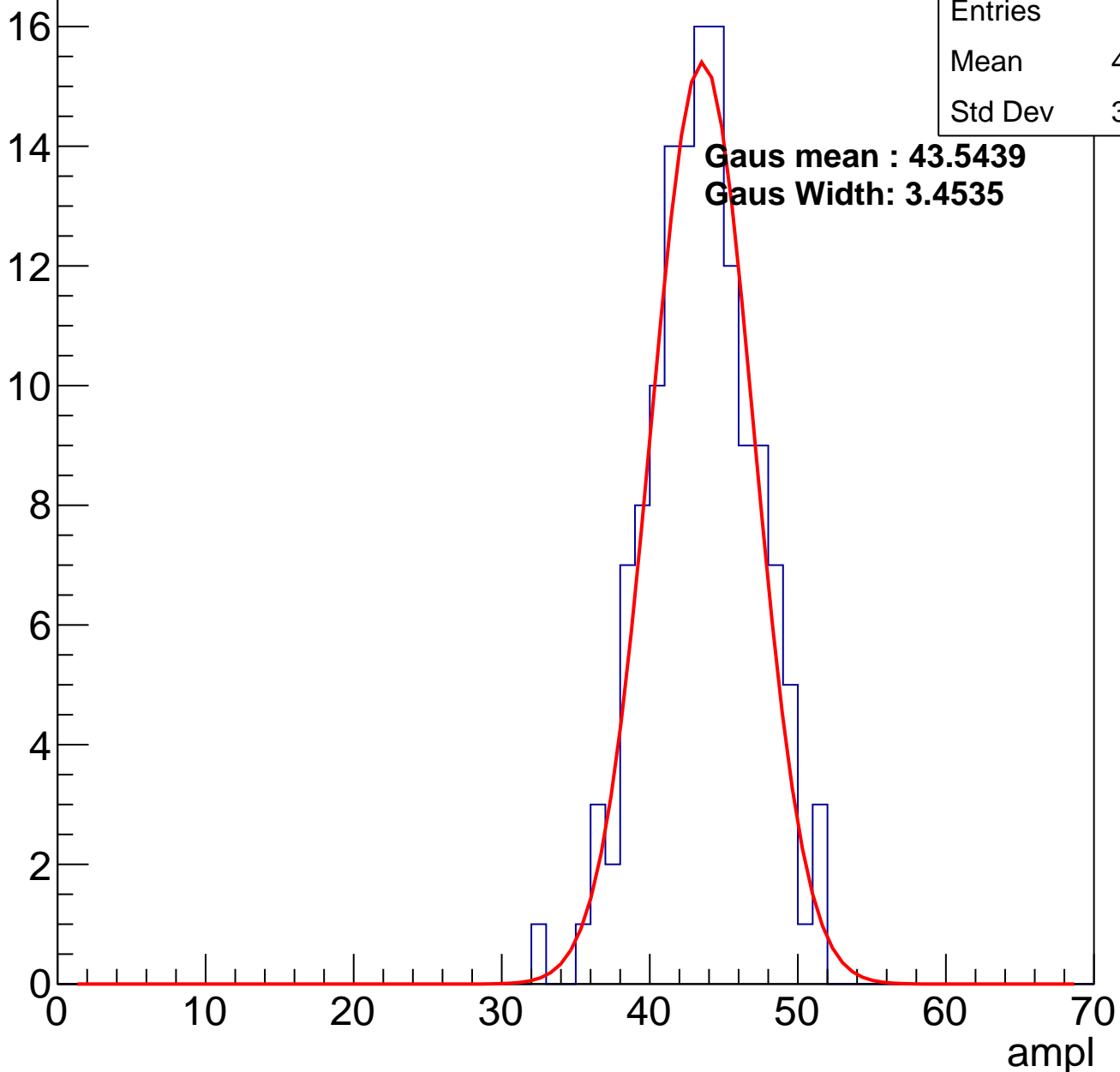
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	43.06
Std Dev	3.563

Gaus mean : 43.5439

Gaus Width: 3.4535

Entry

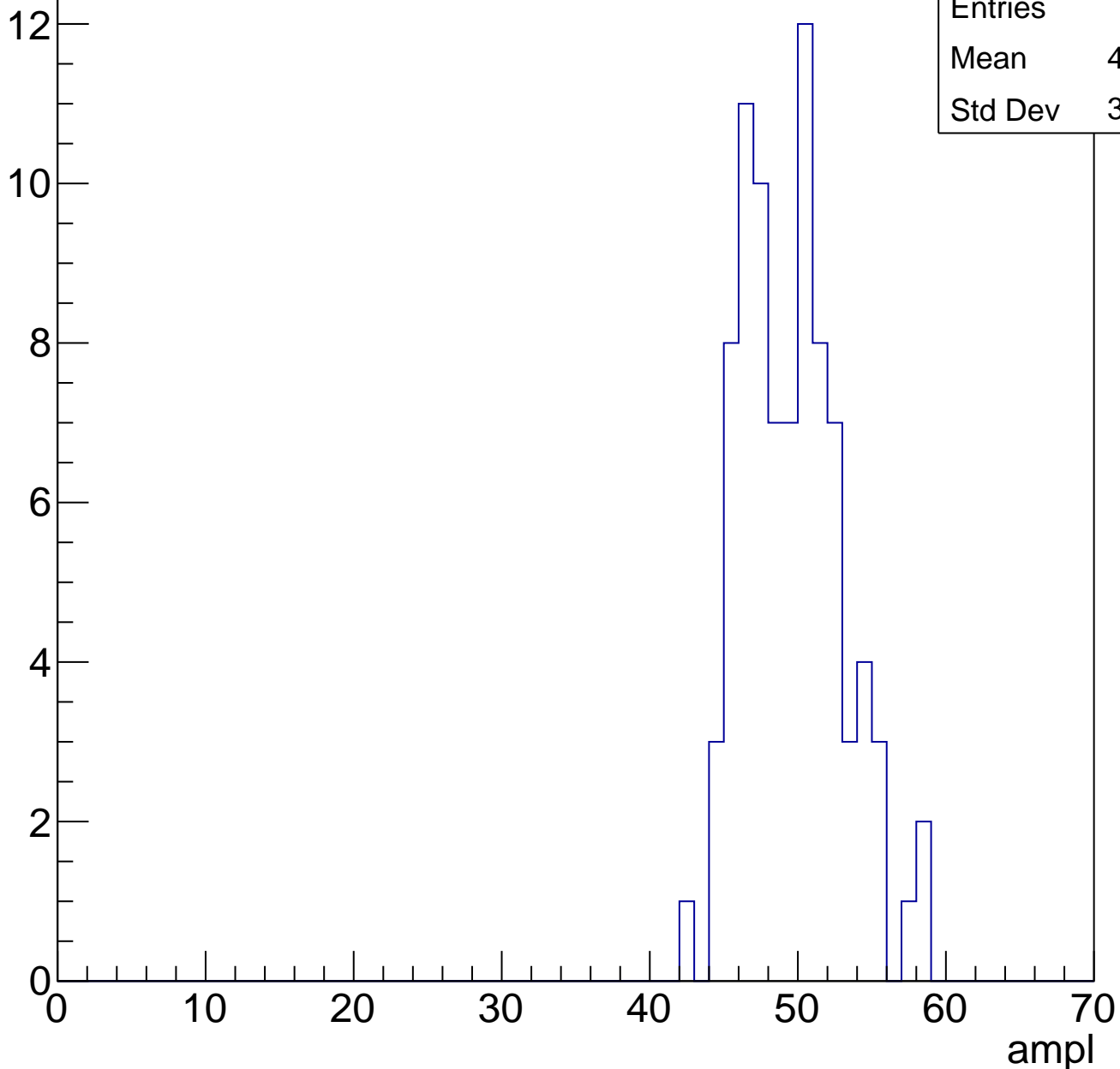


B1L001S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	87
Mean	49.13
Std Dev	3.366

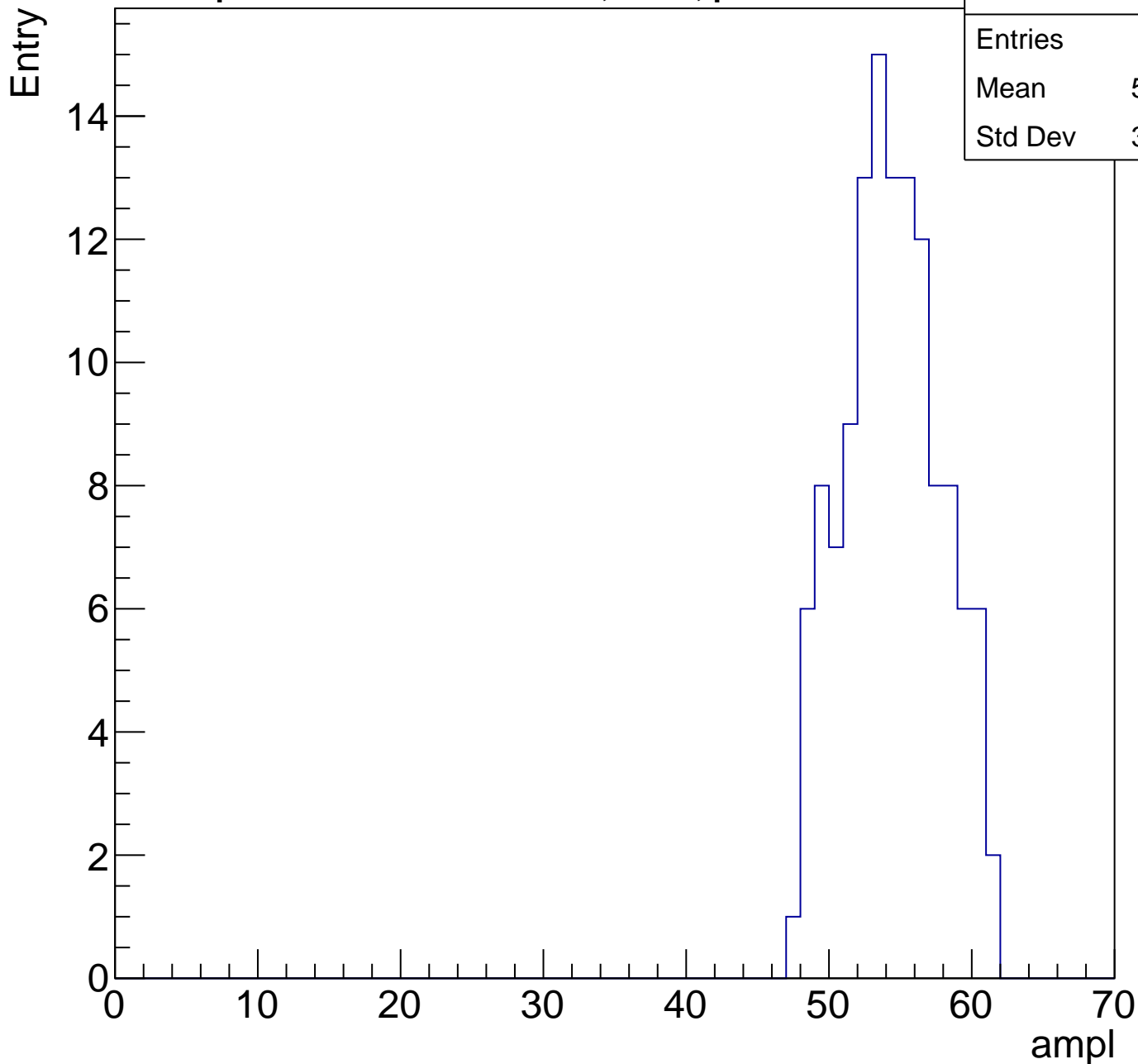
Entry



B1L001S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	53.95
Std Dev	3.379



B1L001S, U14-ch59, adc5

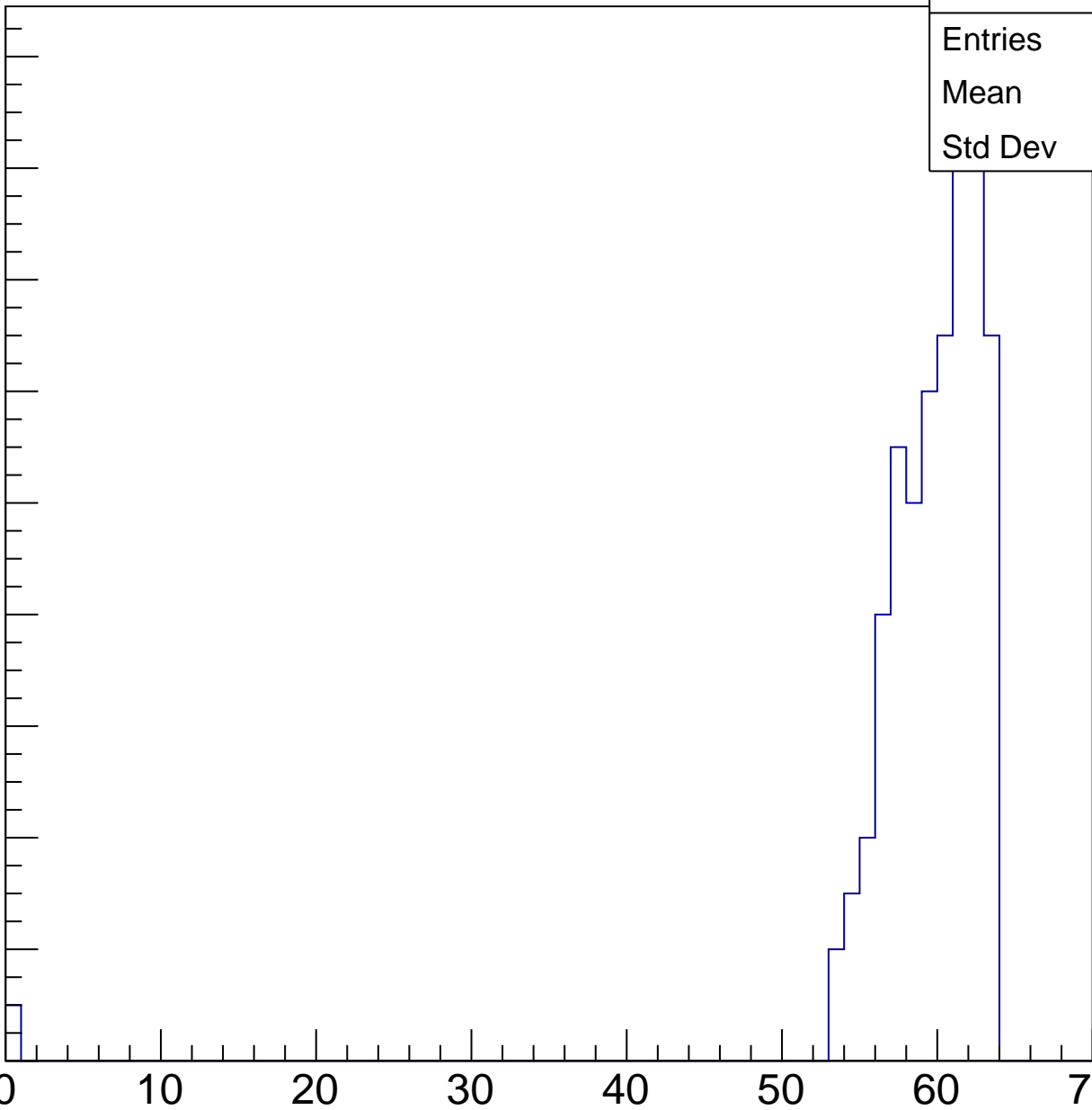
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	111
Mean	58.94
Std Dev	6.198

ampl

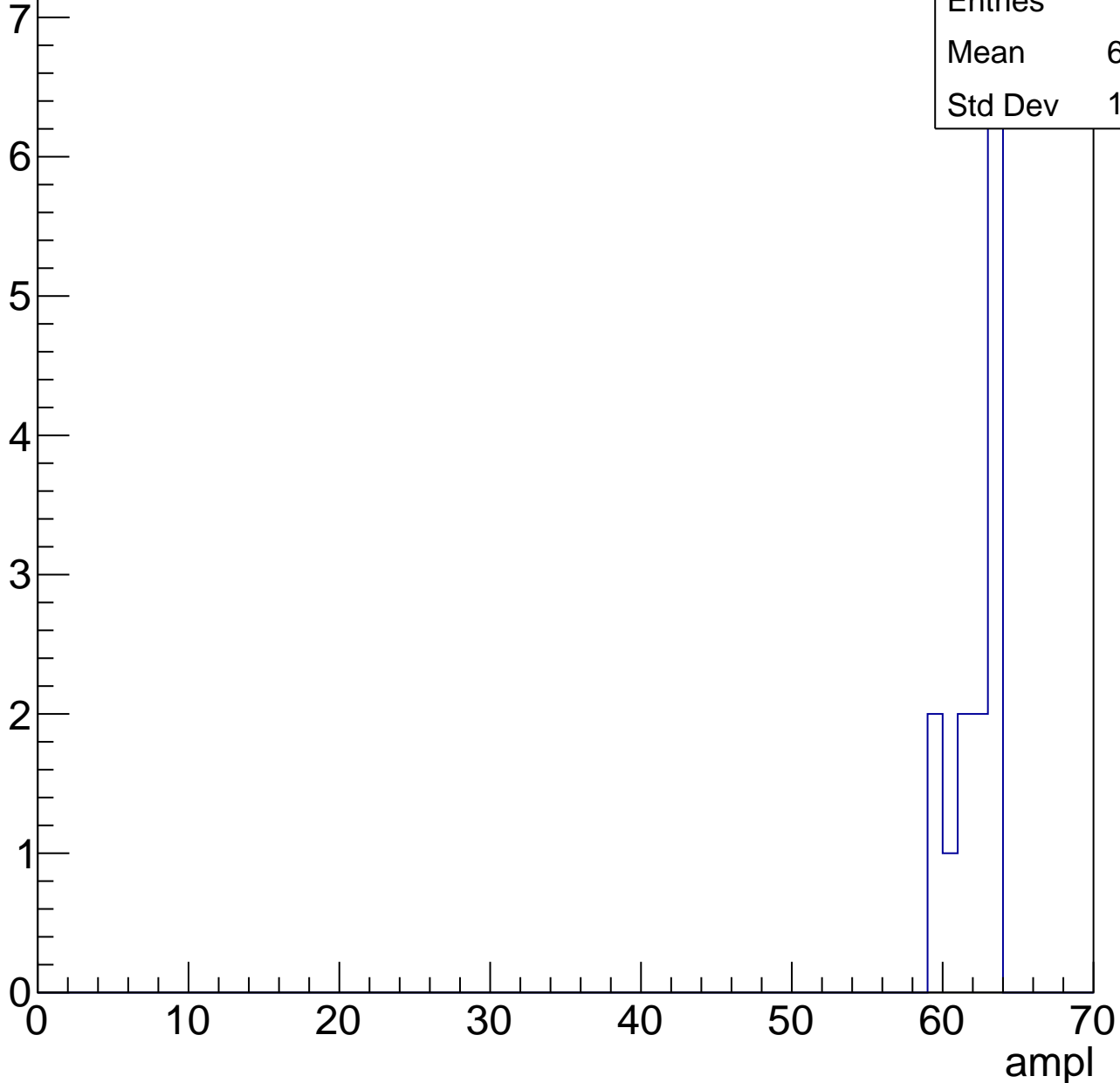


B1L001S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

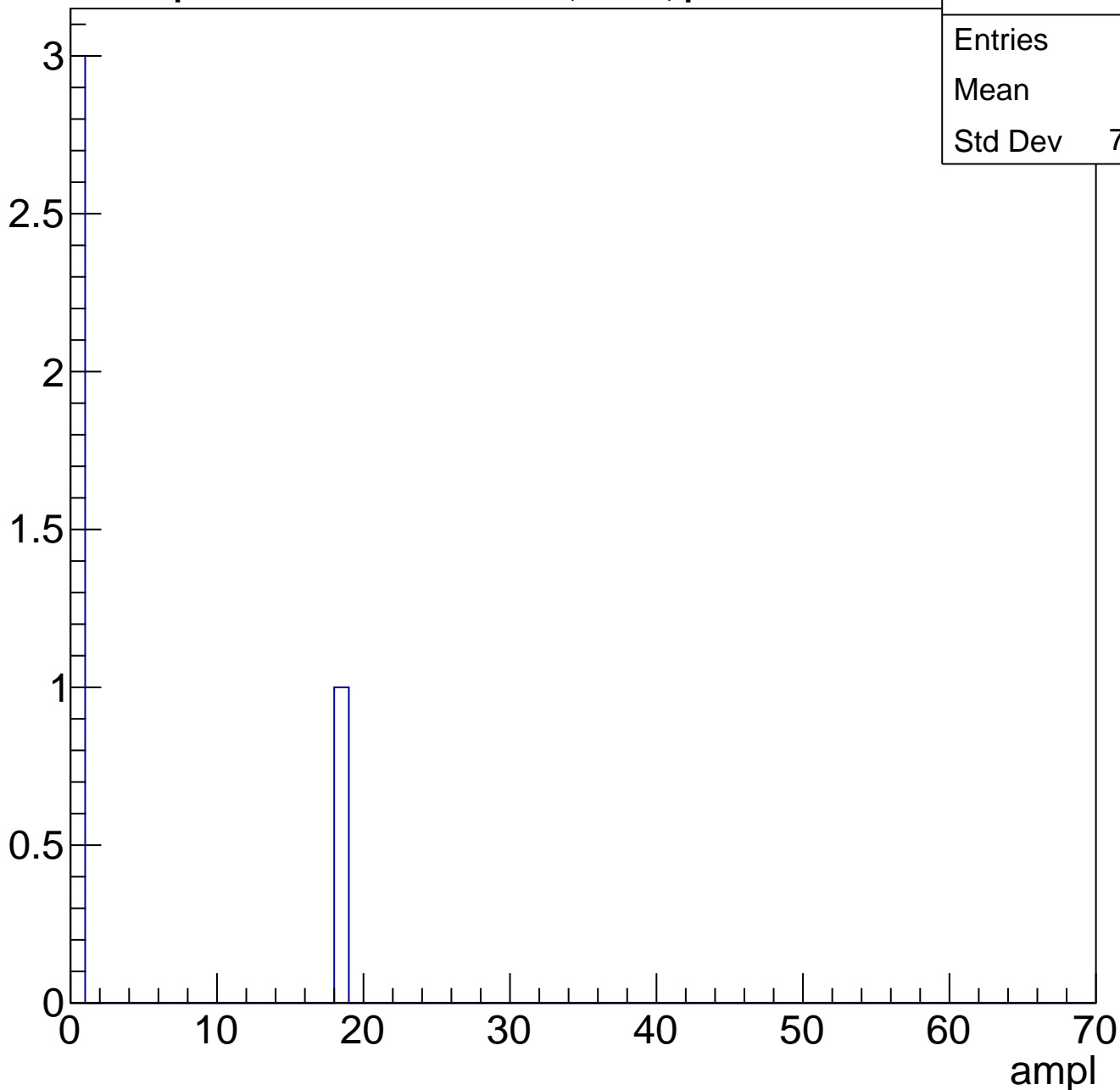
Entries	14
Mean	61.79
Std Dev	1.473



B1L001S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	4.5
Std Dev	7.794

B1L001S, U14-ch60, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	29.45
Std Dev	4.086

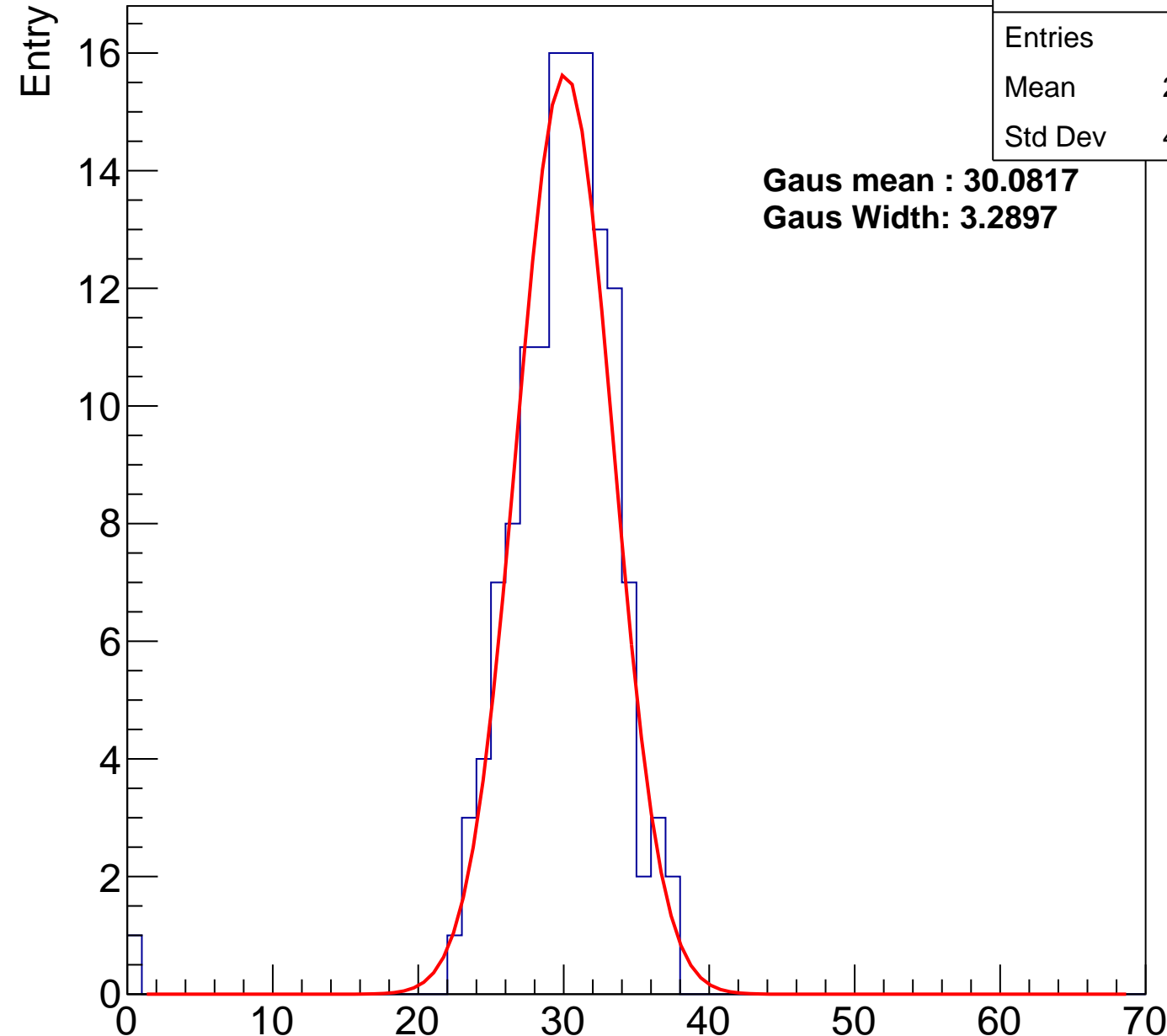
Gaus mean : 30.0817

Gaus Width: 3.2897

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch60, adc1

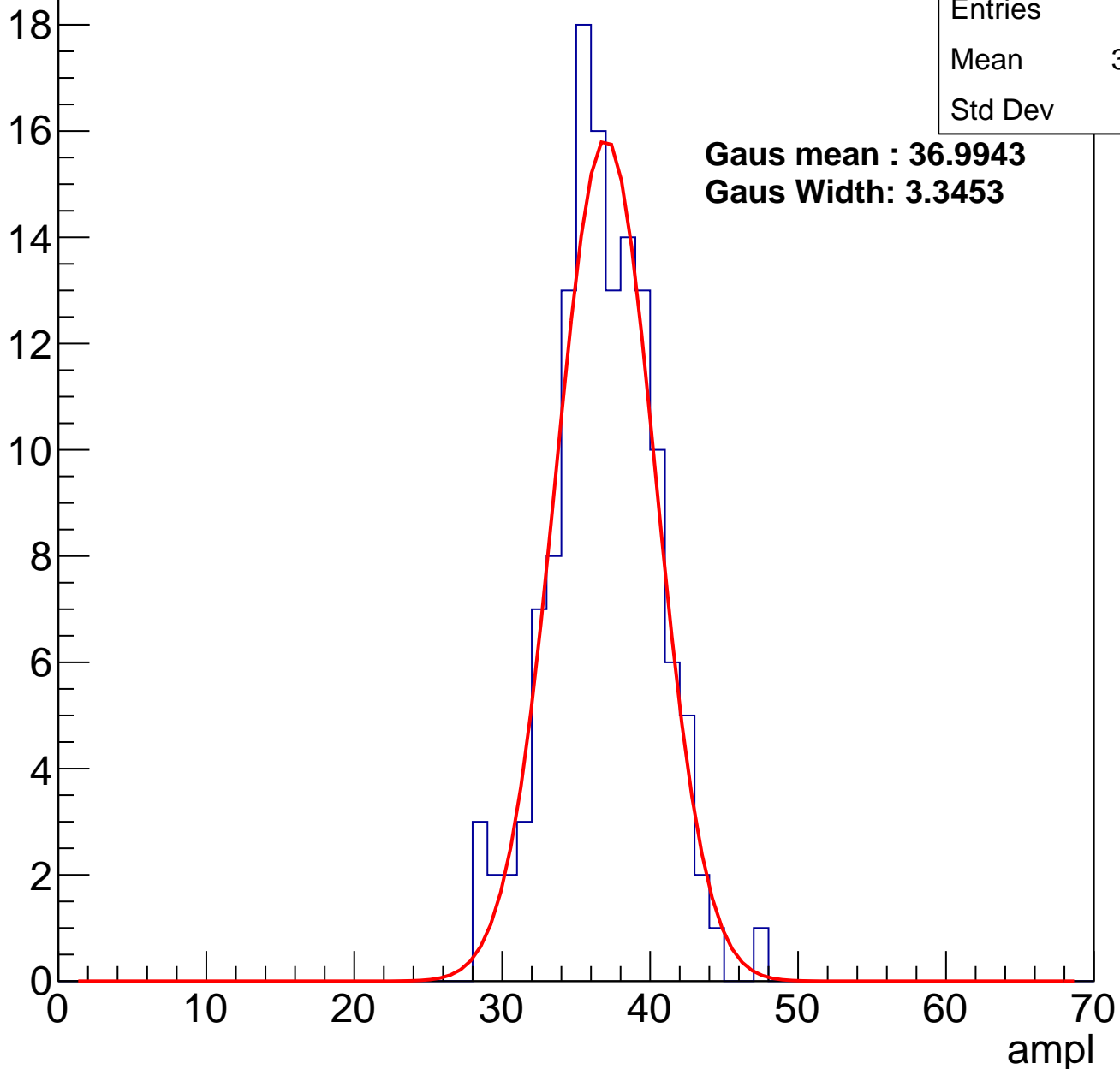
calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	36.38
Std Dev	3.45

Gaus mean : 36.9943

Gaus Width: 3.3453

Entry



B1L001S, U14-ch60, adc2

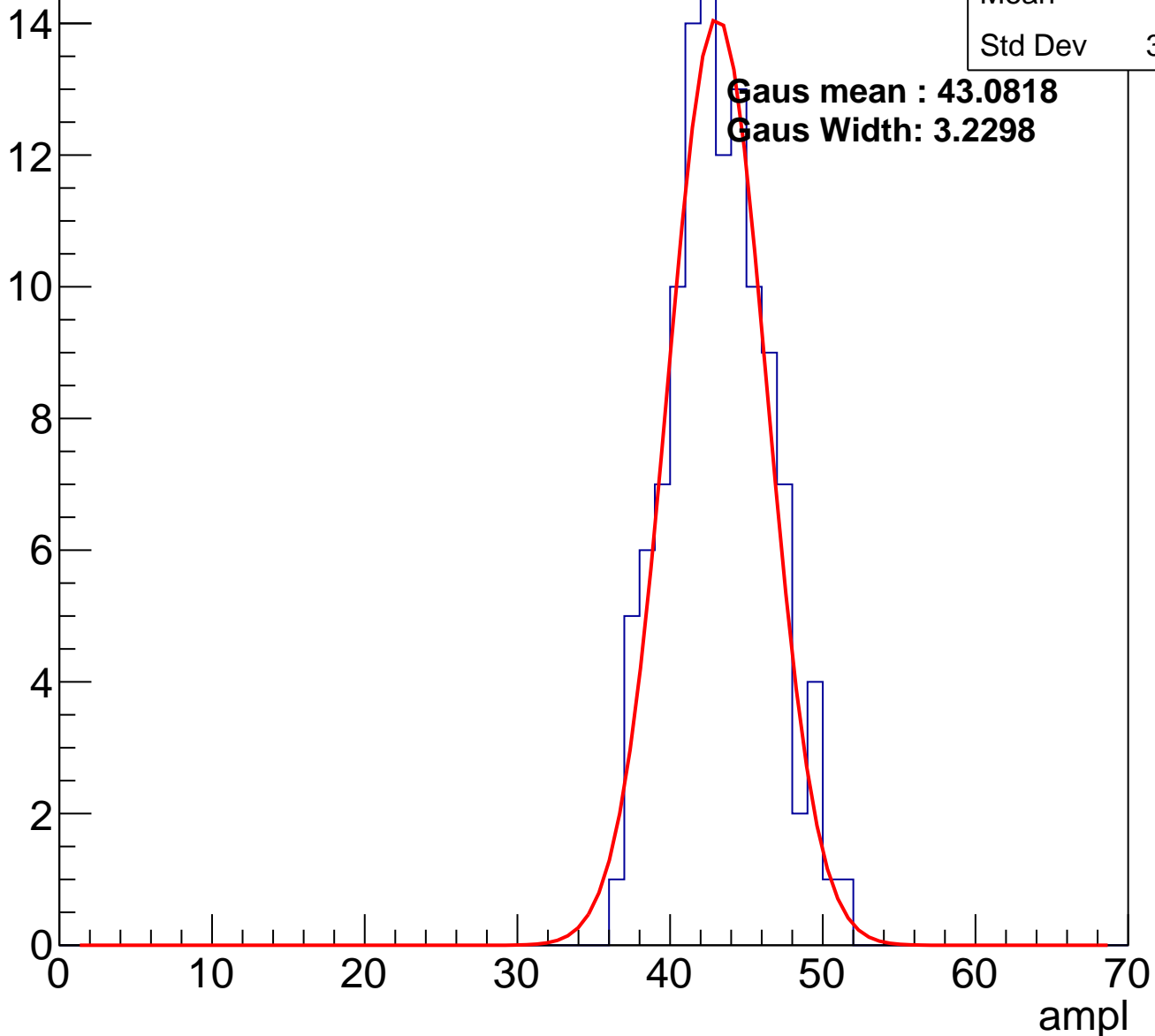
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	42.74
Std Dev	3.198

Gaus mean : 43.0818

Gaus Width: 3.2298

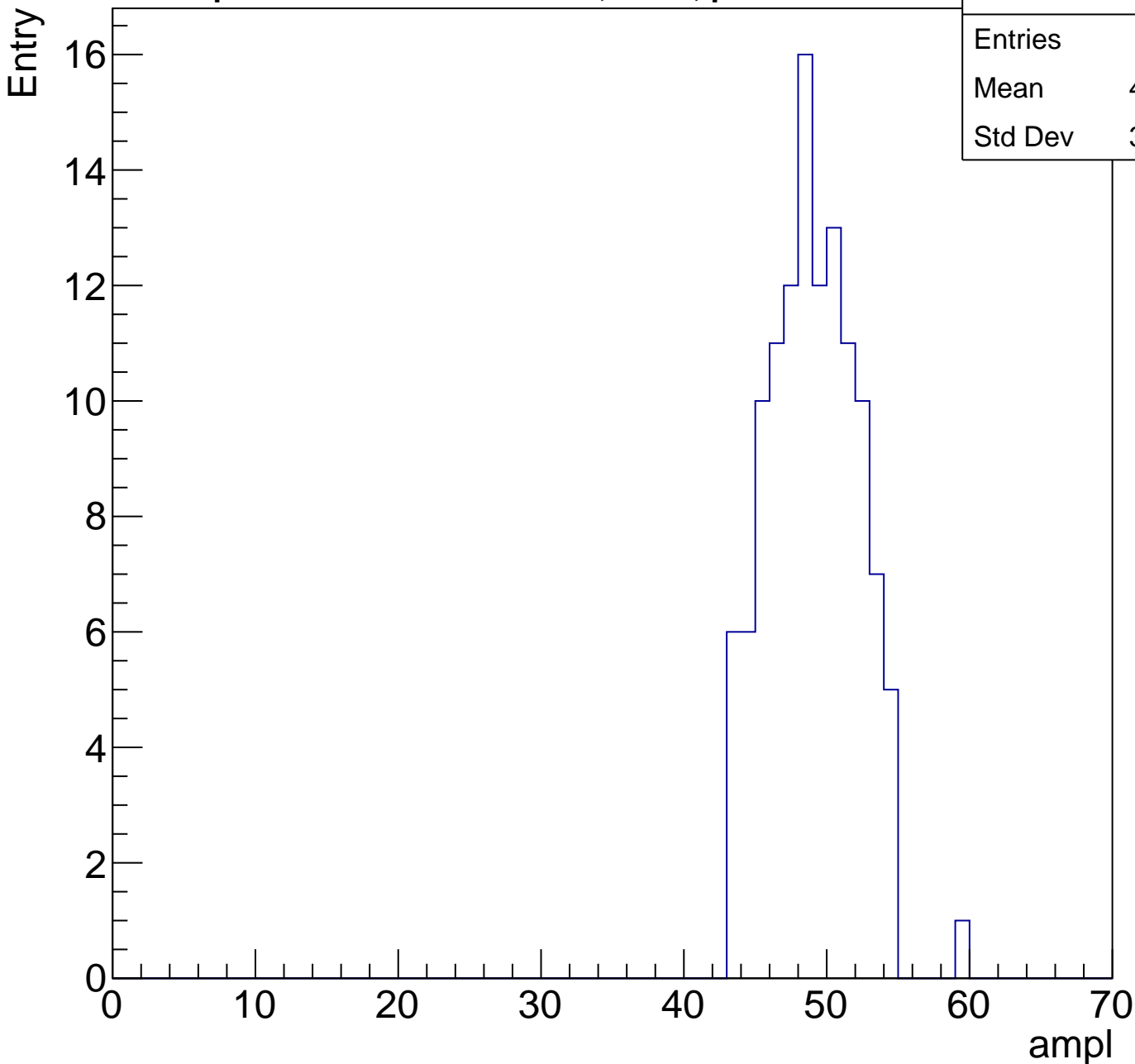
Entry



B1L001S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	48.58
Std Dev	3.097



B1L001S, U14-ch60, adc4

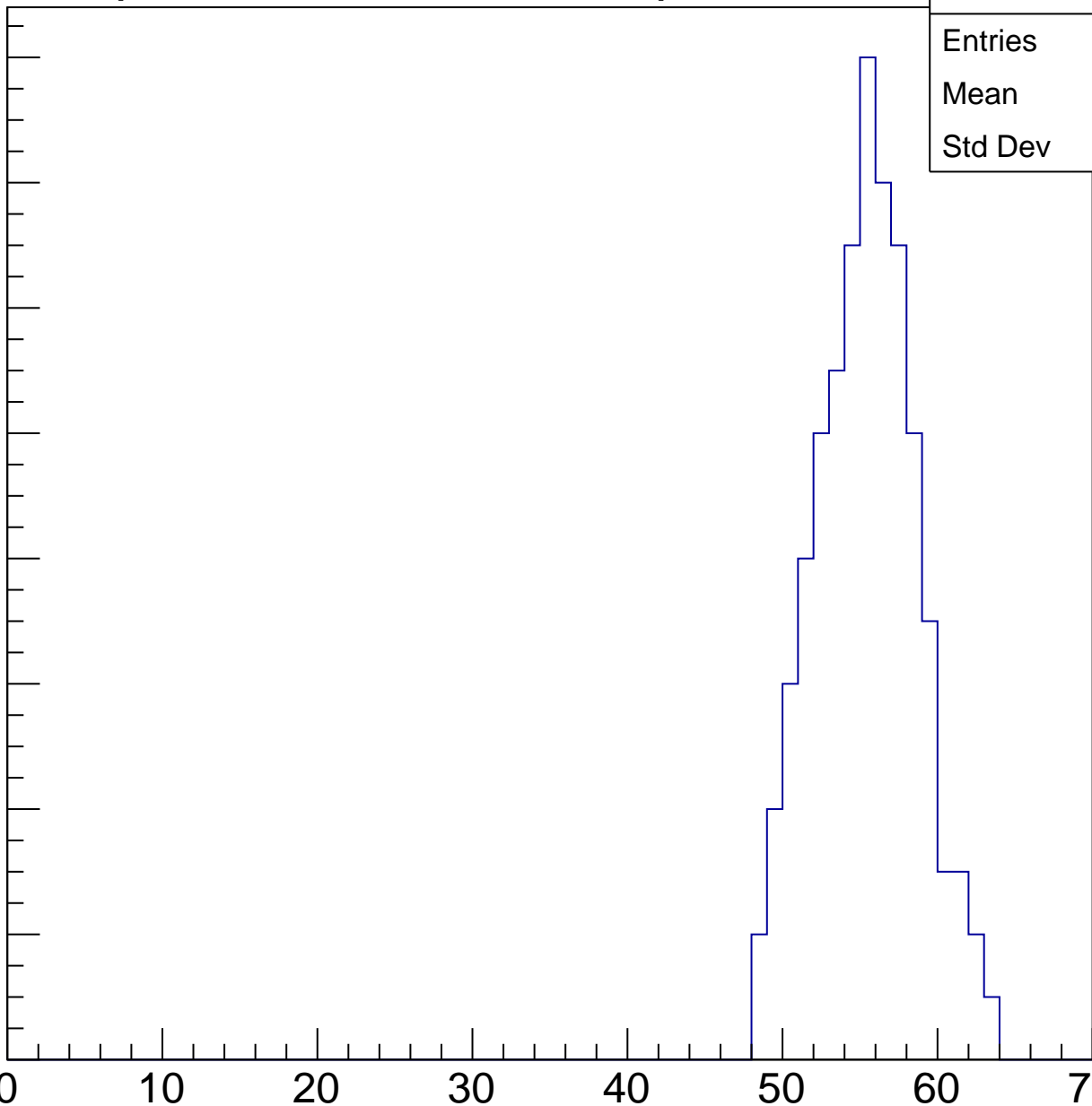
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	123
Mean	54.9
Std Dev	3.224

ampl



B1L001S, U14-ch60, adc5

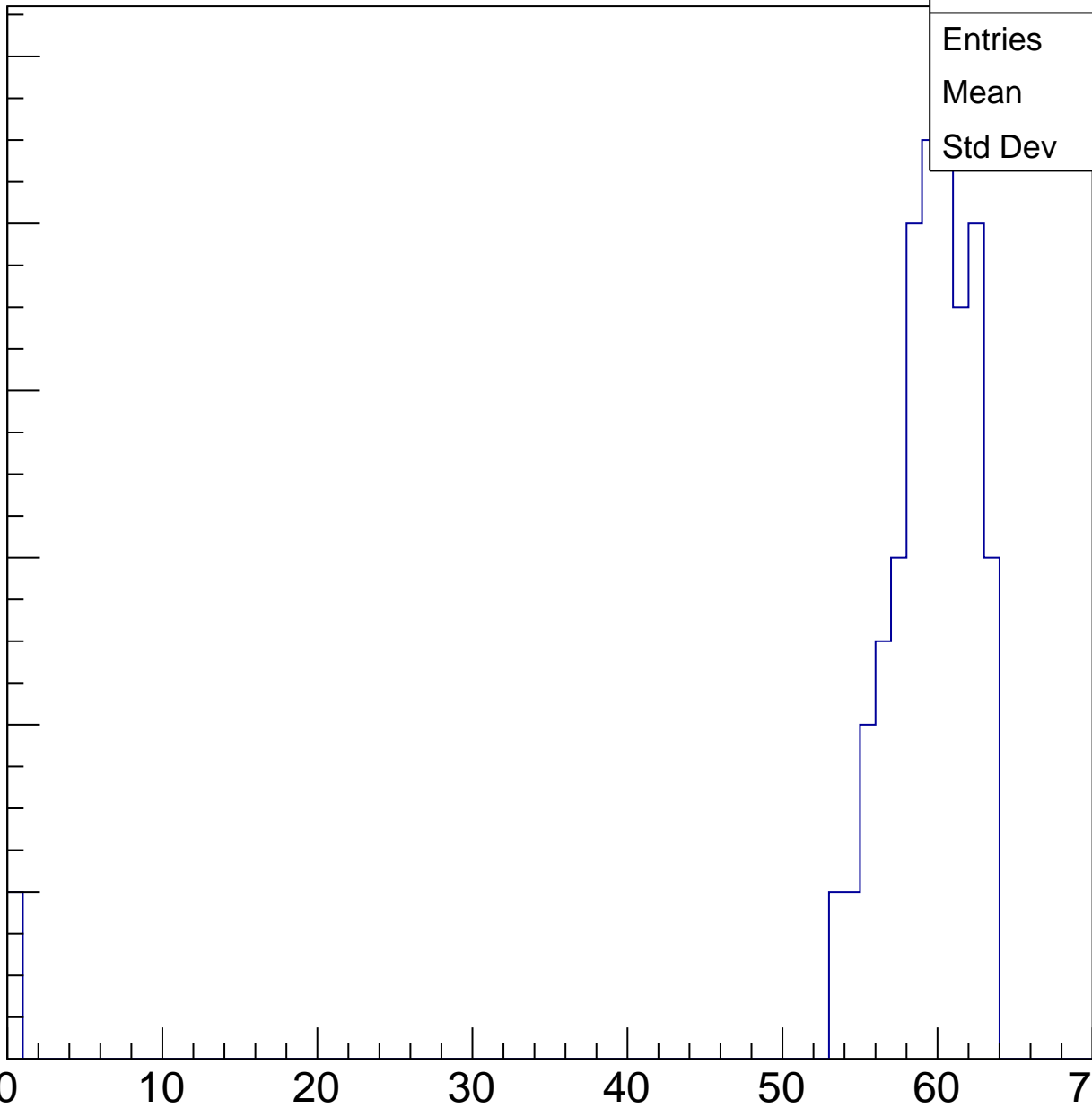
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	79
Mean	57.62
Std Dev	9.62

ampl



B1L001S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

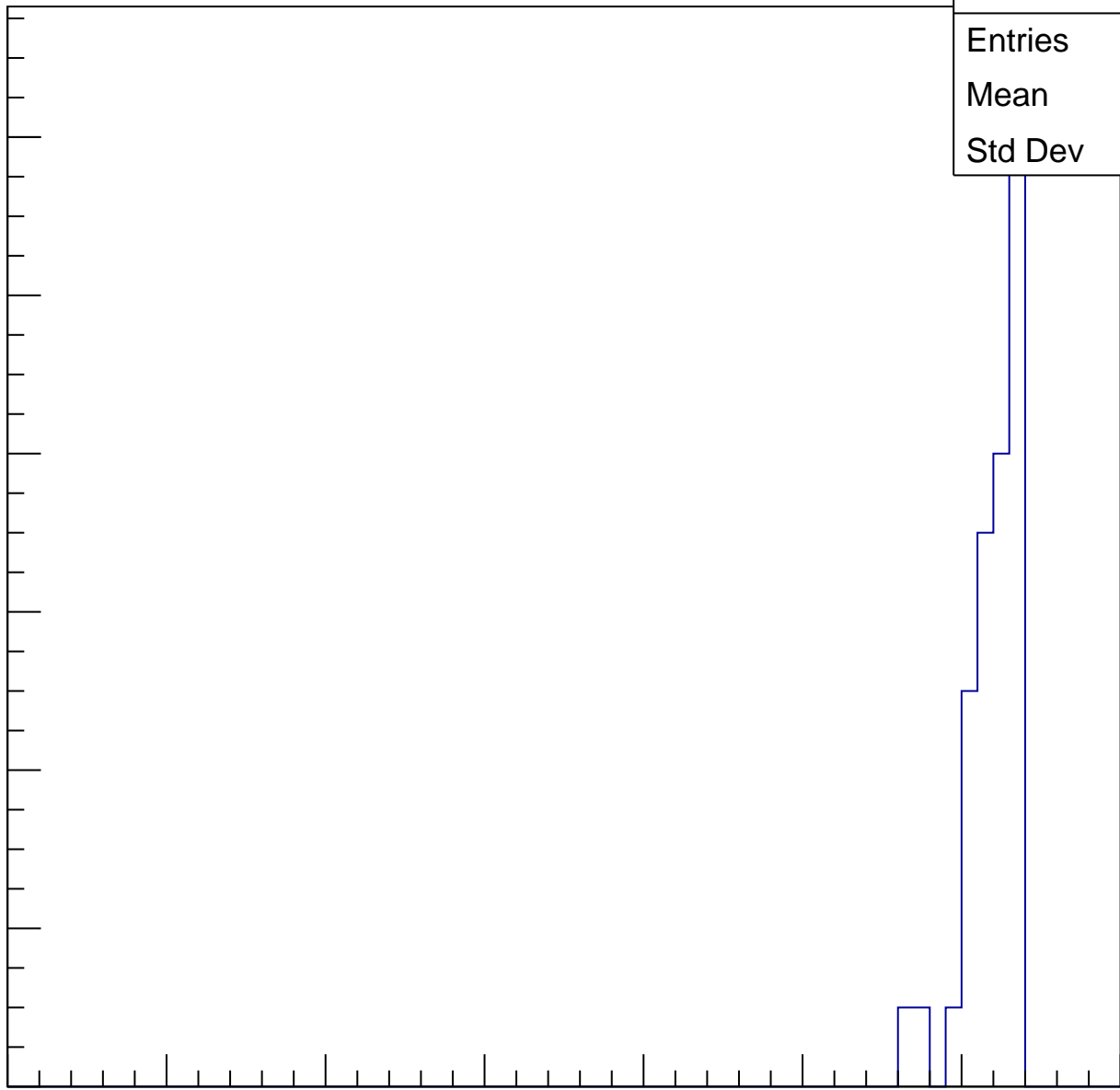
2

0

Entries	36
Mean	61.5
Std Dev	1.675

ampl

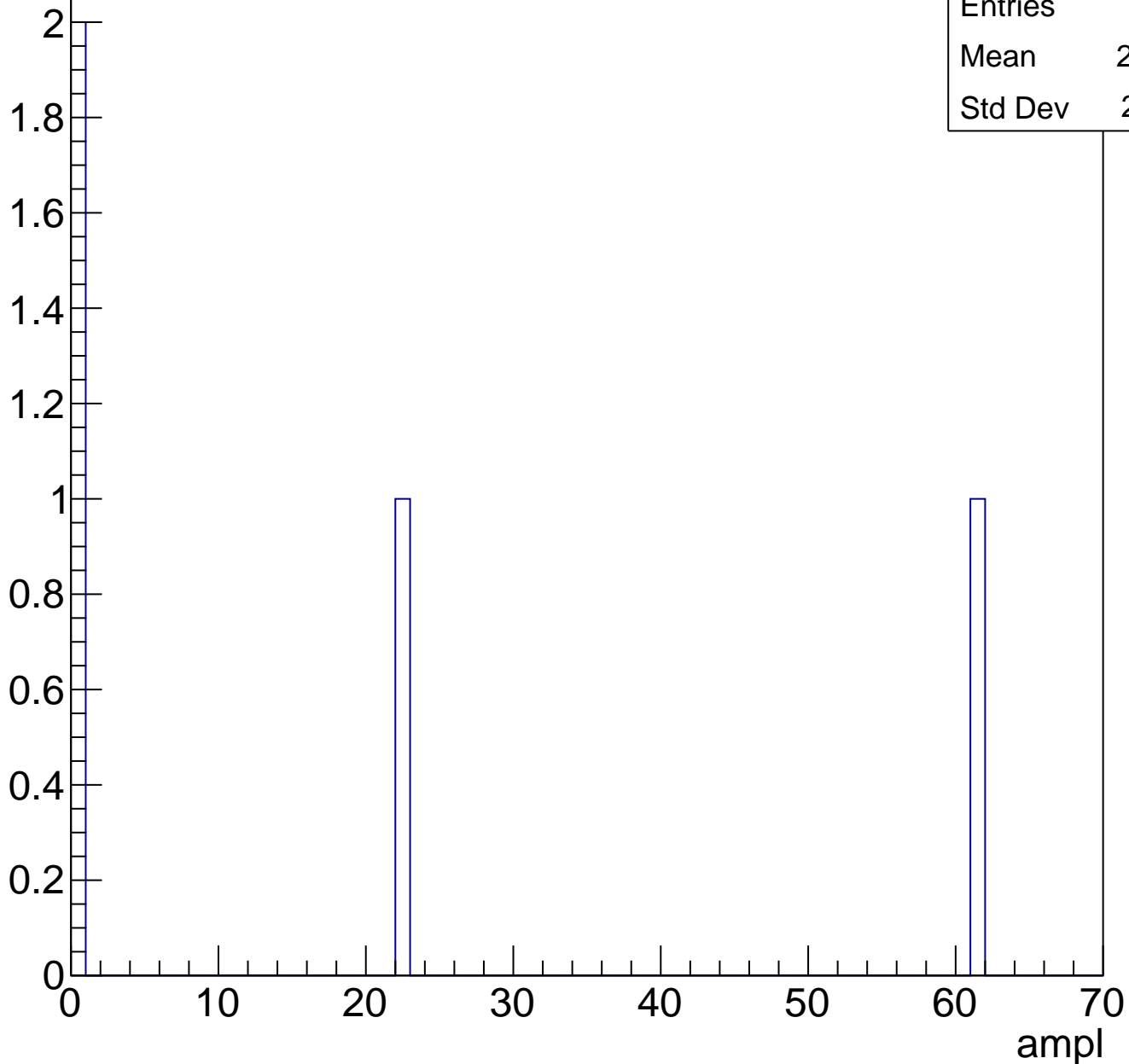
0 10 20 30 40 50 60 70



B1L001S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	20.75
Std Dev	24.91

B1L001S, U14-ch61, adc0

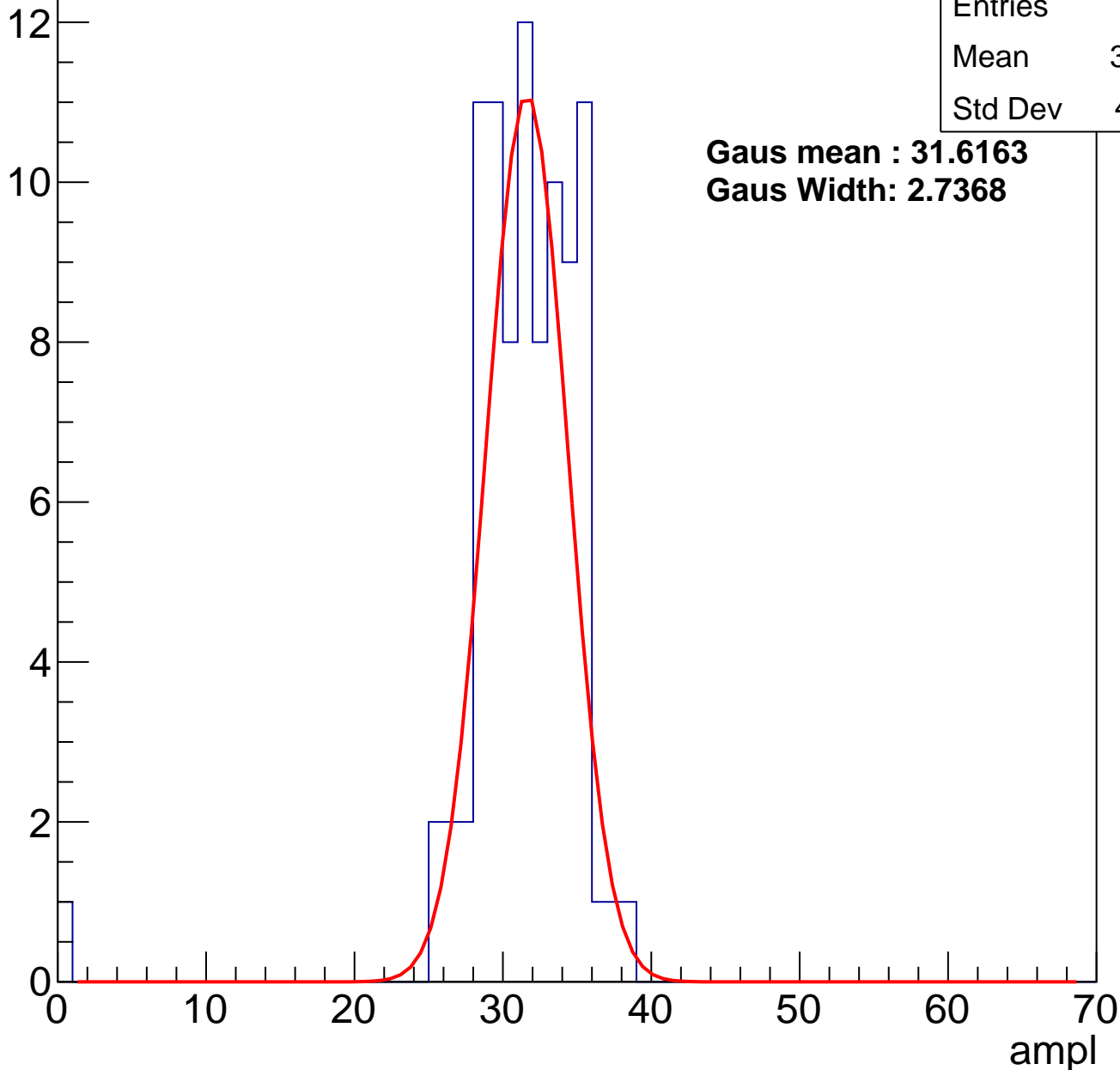
calib_packv5_042523_0143.root, FC#2, port C2

Entries	90
Mean	30.92
Std Dev	4.321

Gaus mean : 31.6163

Gaus Width: 2.7368

Entry



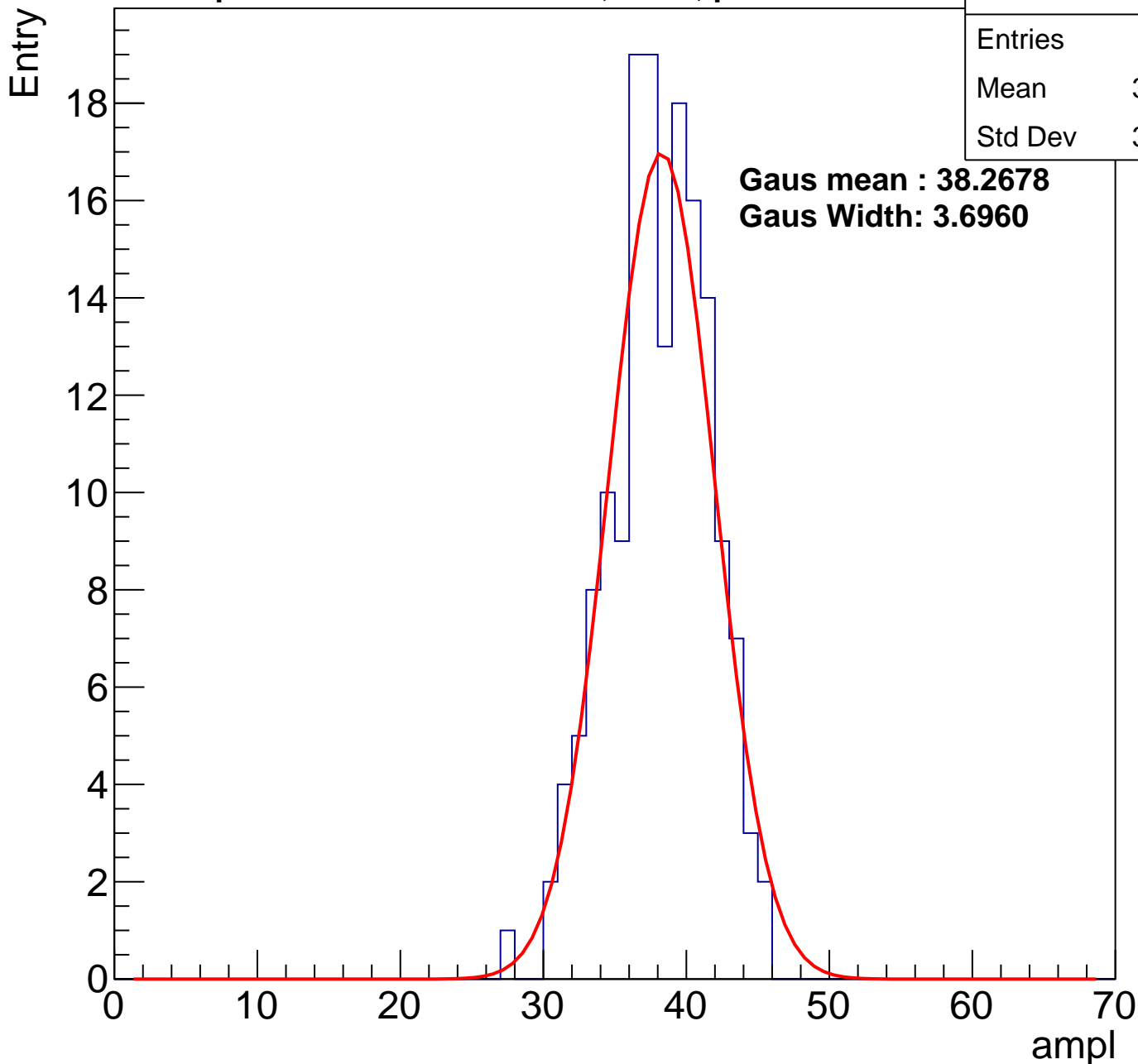
B1L001S, U14-ch61, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	37.66
Std Dev	3.435

Gaus mean : 38.2678

Gaus Width: 3.6960

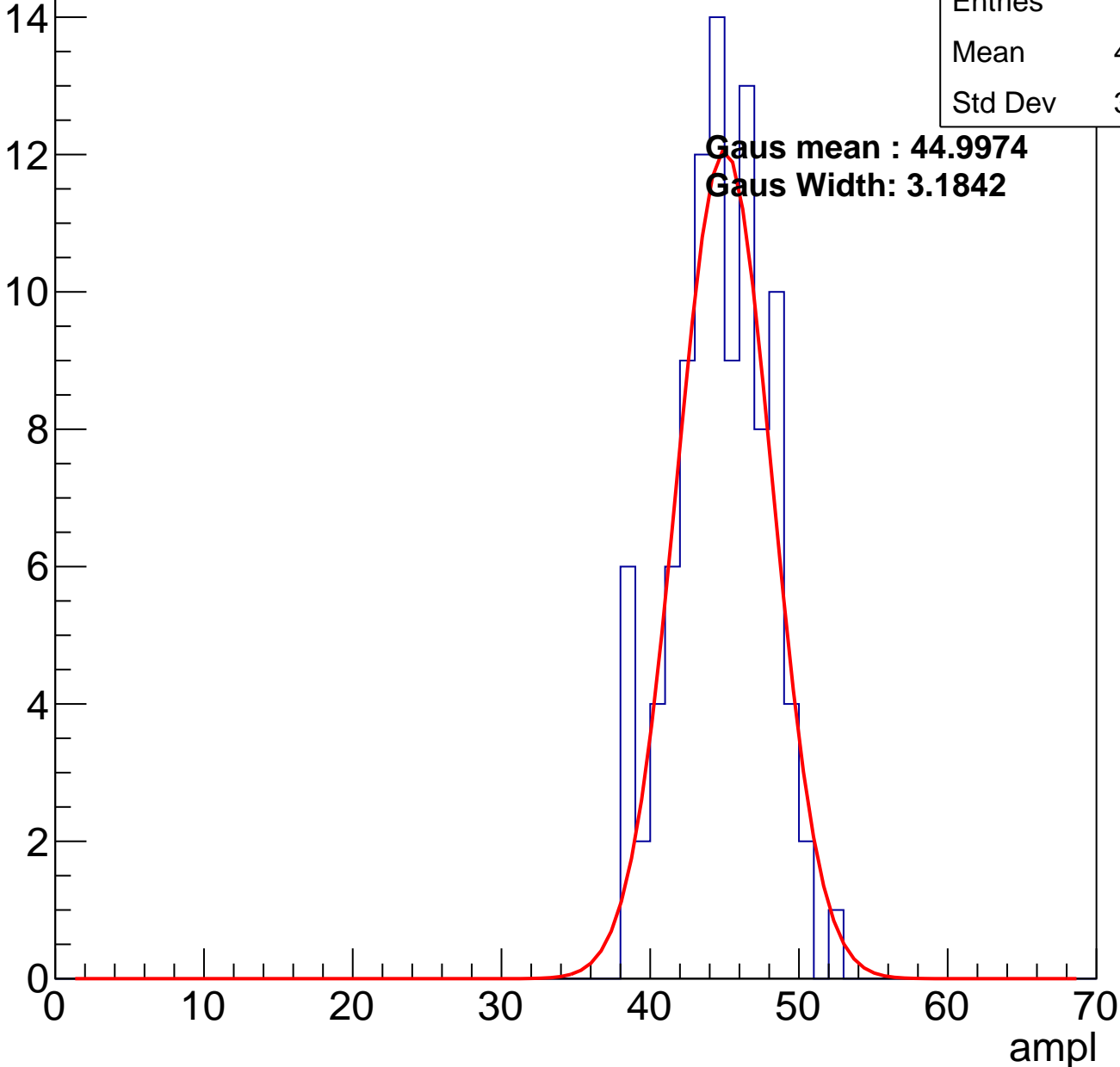


B1L001S, U14-ch61, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	44.29
Std Dev	3.086

Entry



B1L001S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#2, port C2

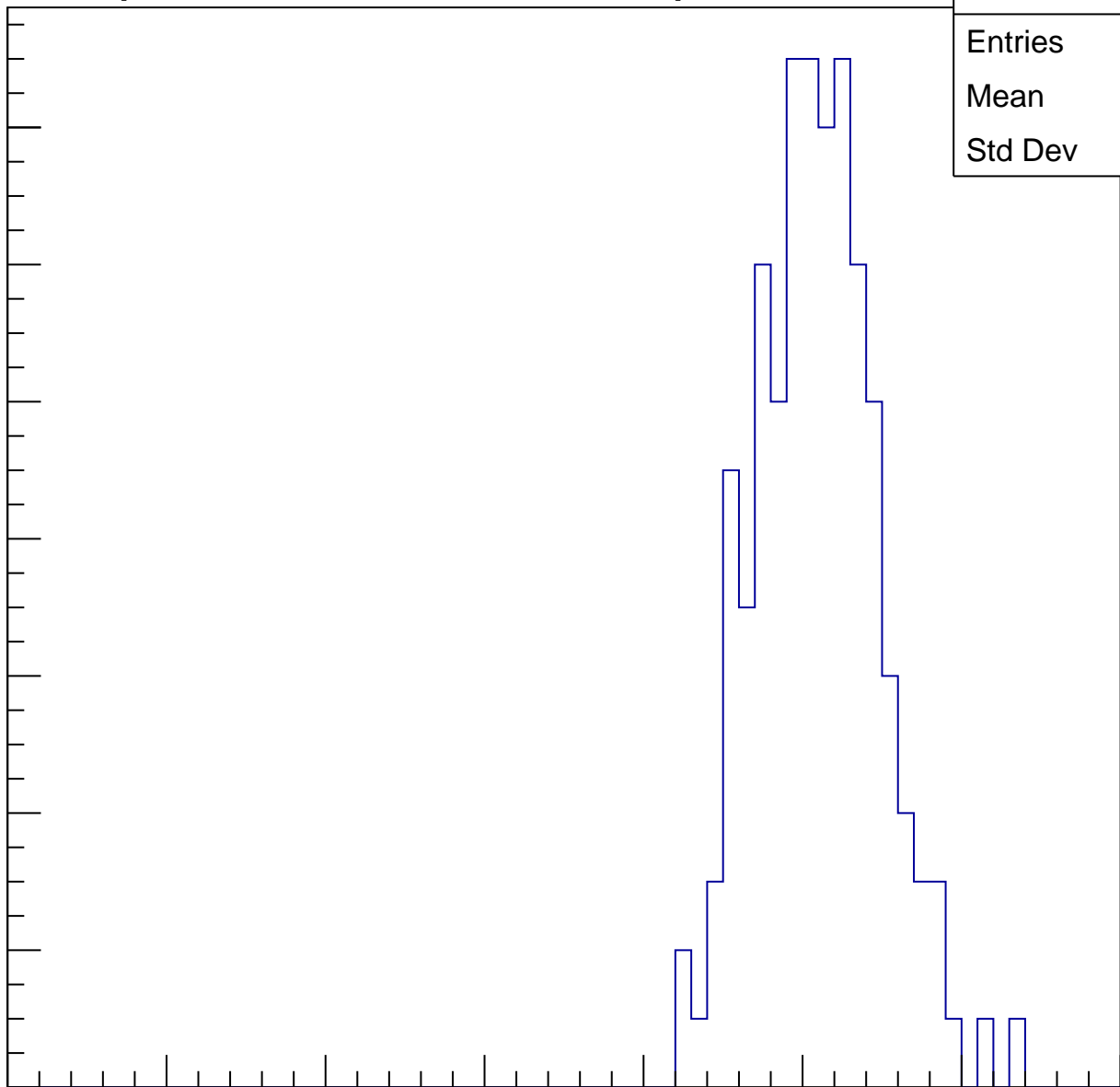
Entries	144
Mean	50.47
Std Dev	3.819

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

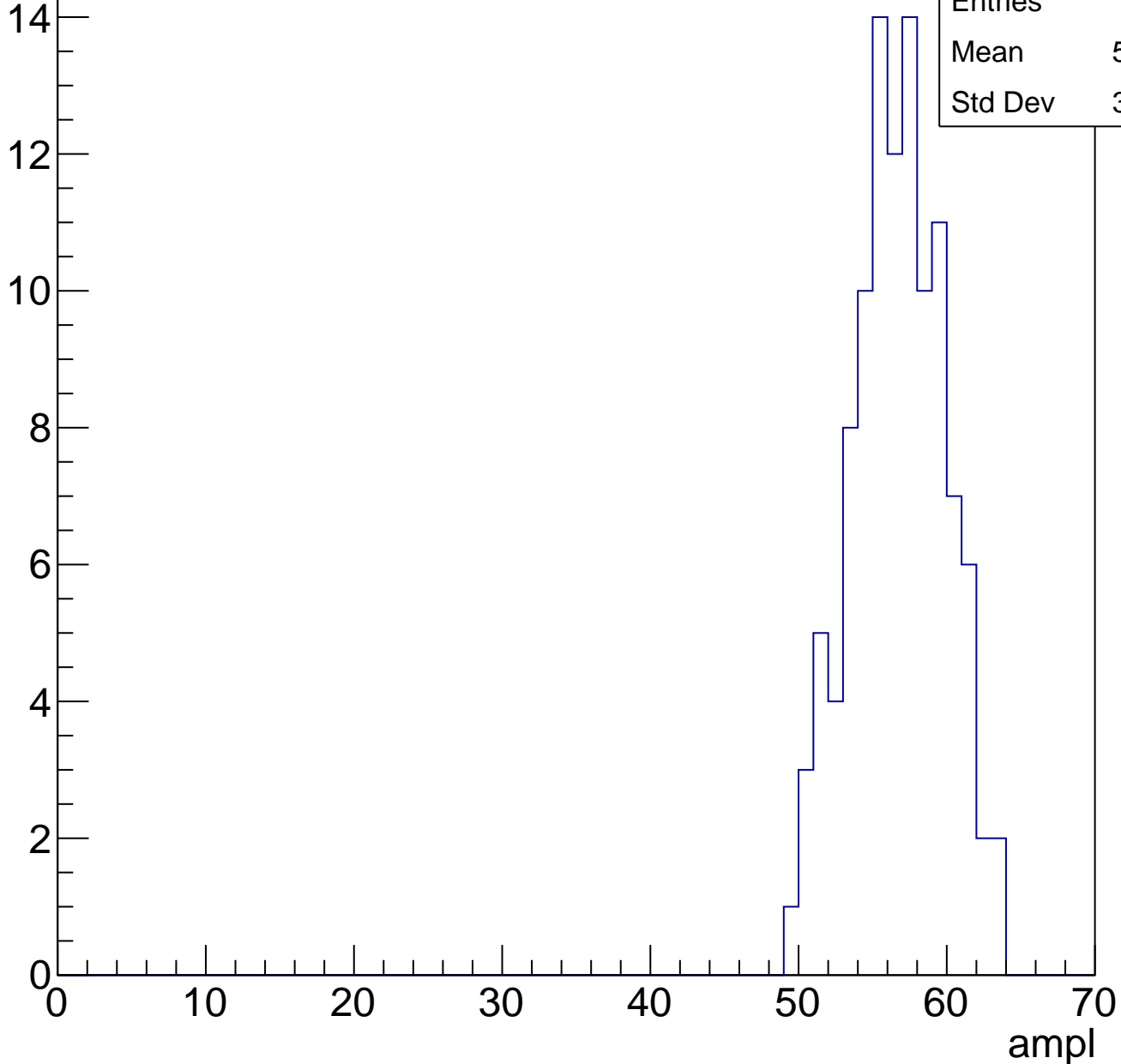
ampl



B1L001S, U14-ch61, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	109
Mean	56.25
Std Dev	3.104

B1L001S, U14-ch61, adc5

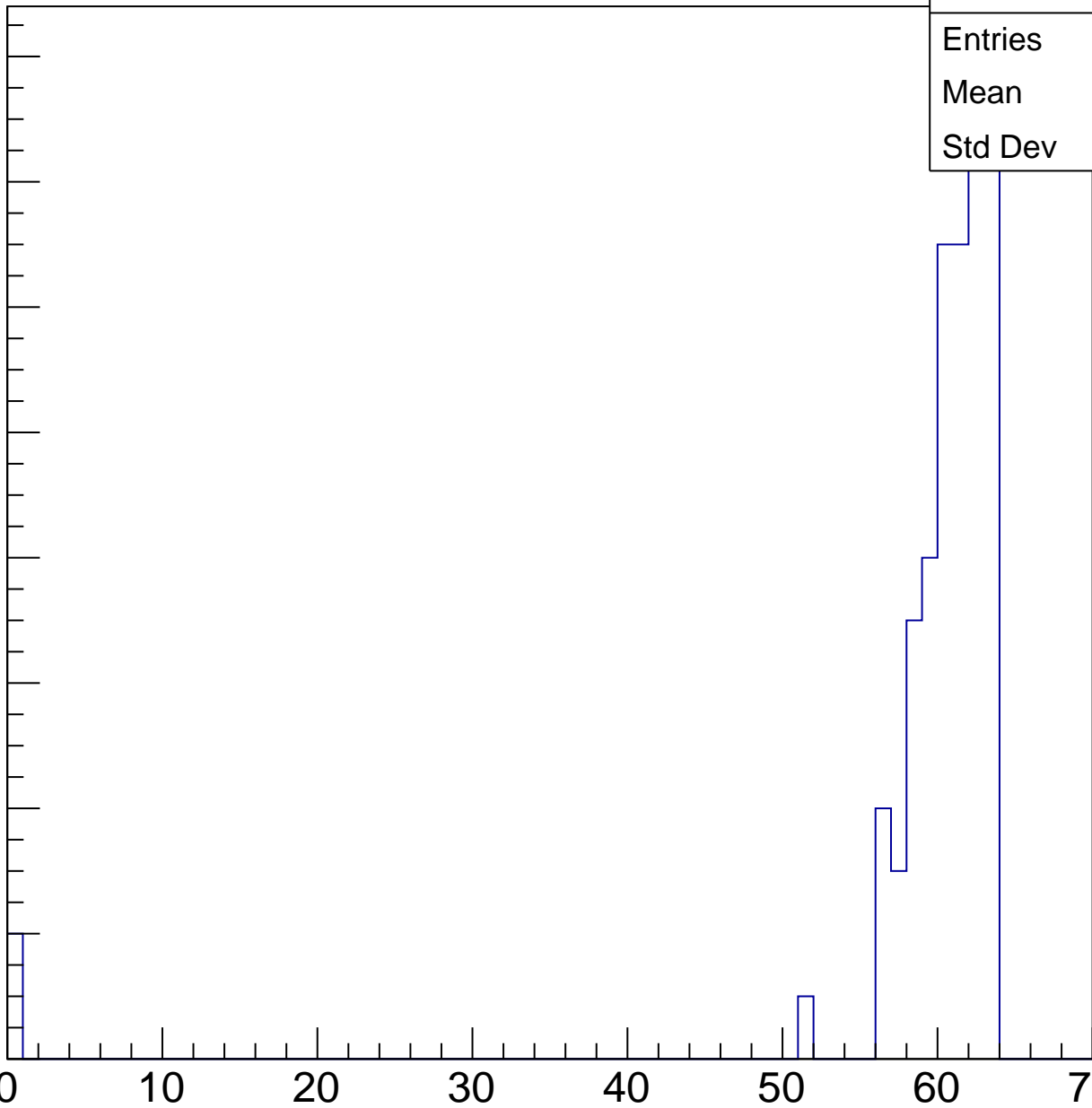
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	83
Mean	59
Std Dev	9.535

ampl



B1L001S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U14-ch62, adc0

calib_packv5_042523_0143.root, FC#2, port C2

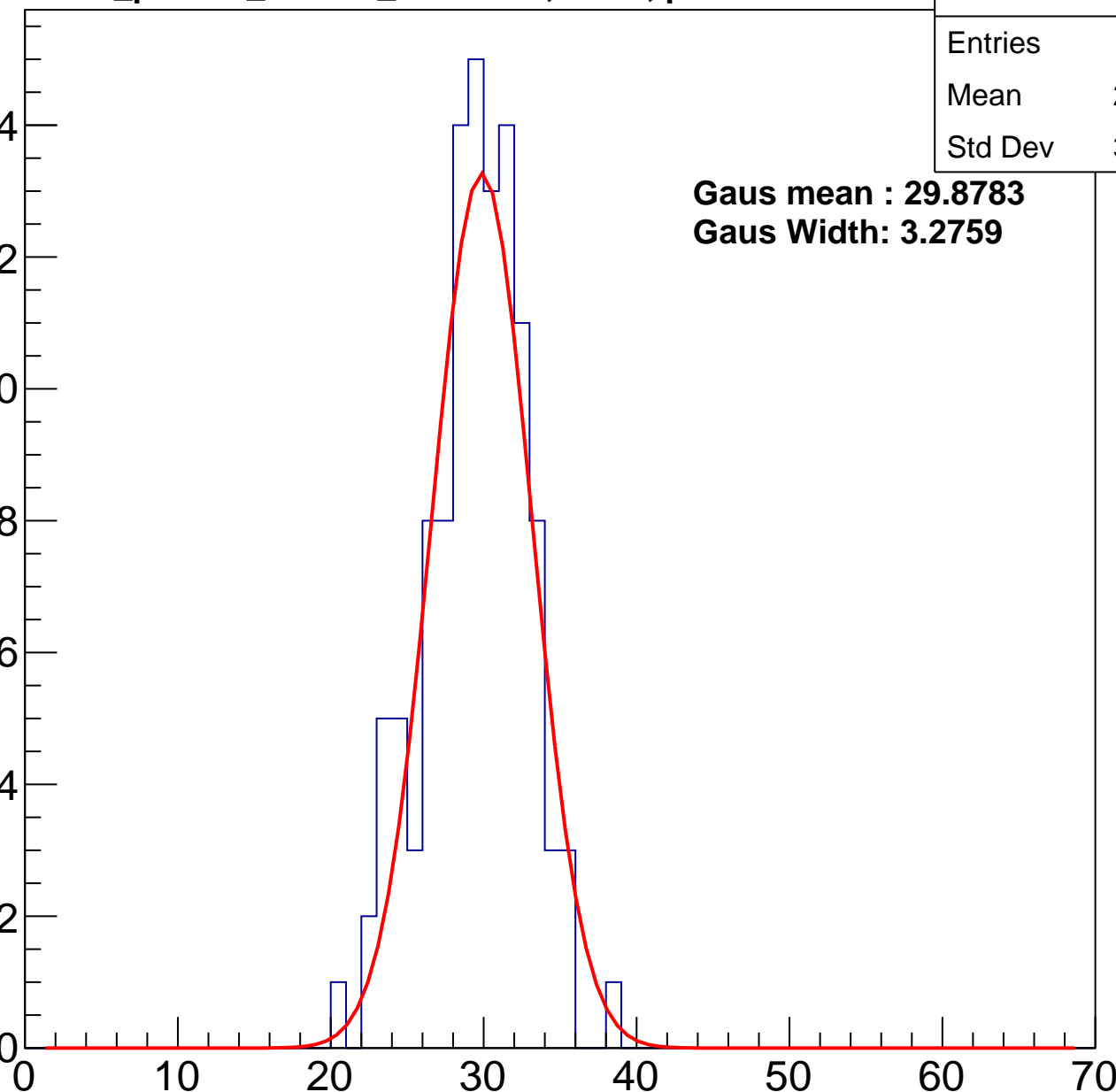
Entry

14
12
10
8
6
4
2
0

Entries	114
Mean	29.04
Std Dev	3.258

Gaus mean : 29.8783

Gaus Width: 3.2759



ampl

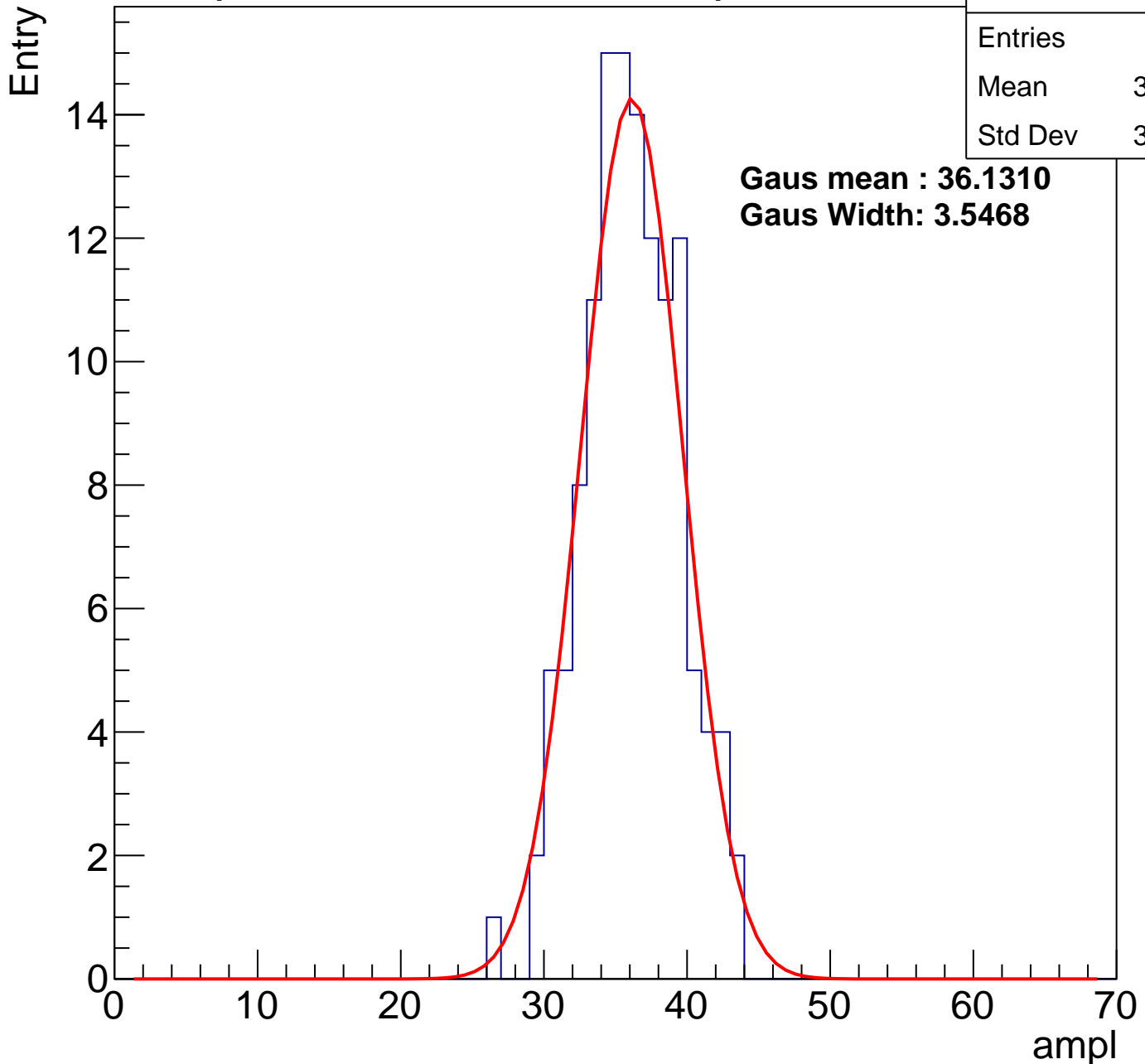
B1L001S, U14-ch62, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	35.67
Std Dev	3.307

Gaus mean : 36.1310

Gaus Width: 3.5468



B1L001S, U14-ch62, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

106

Mean

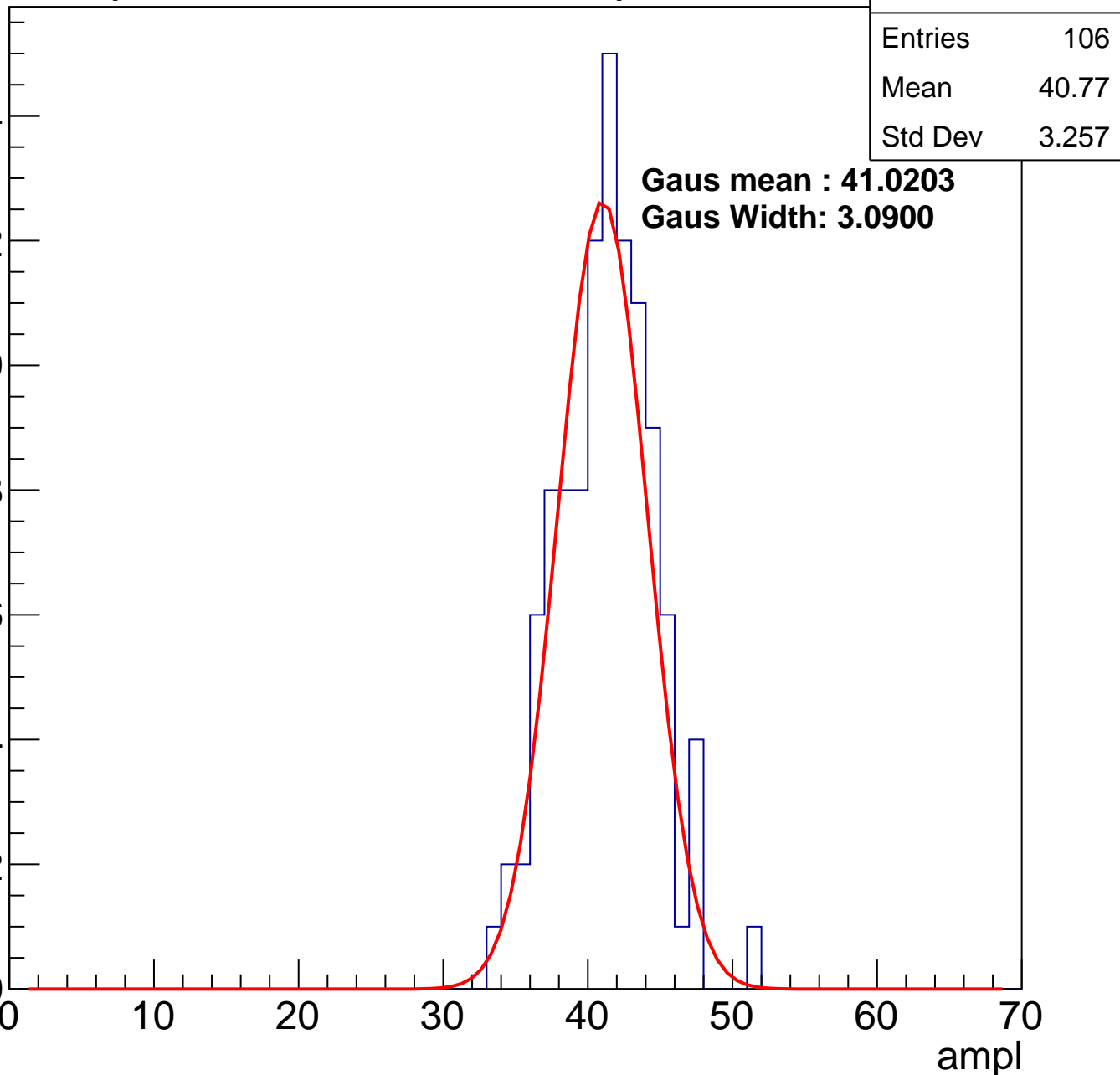
40.77

Std Dev

3.257

Gaus mean : 41.0203

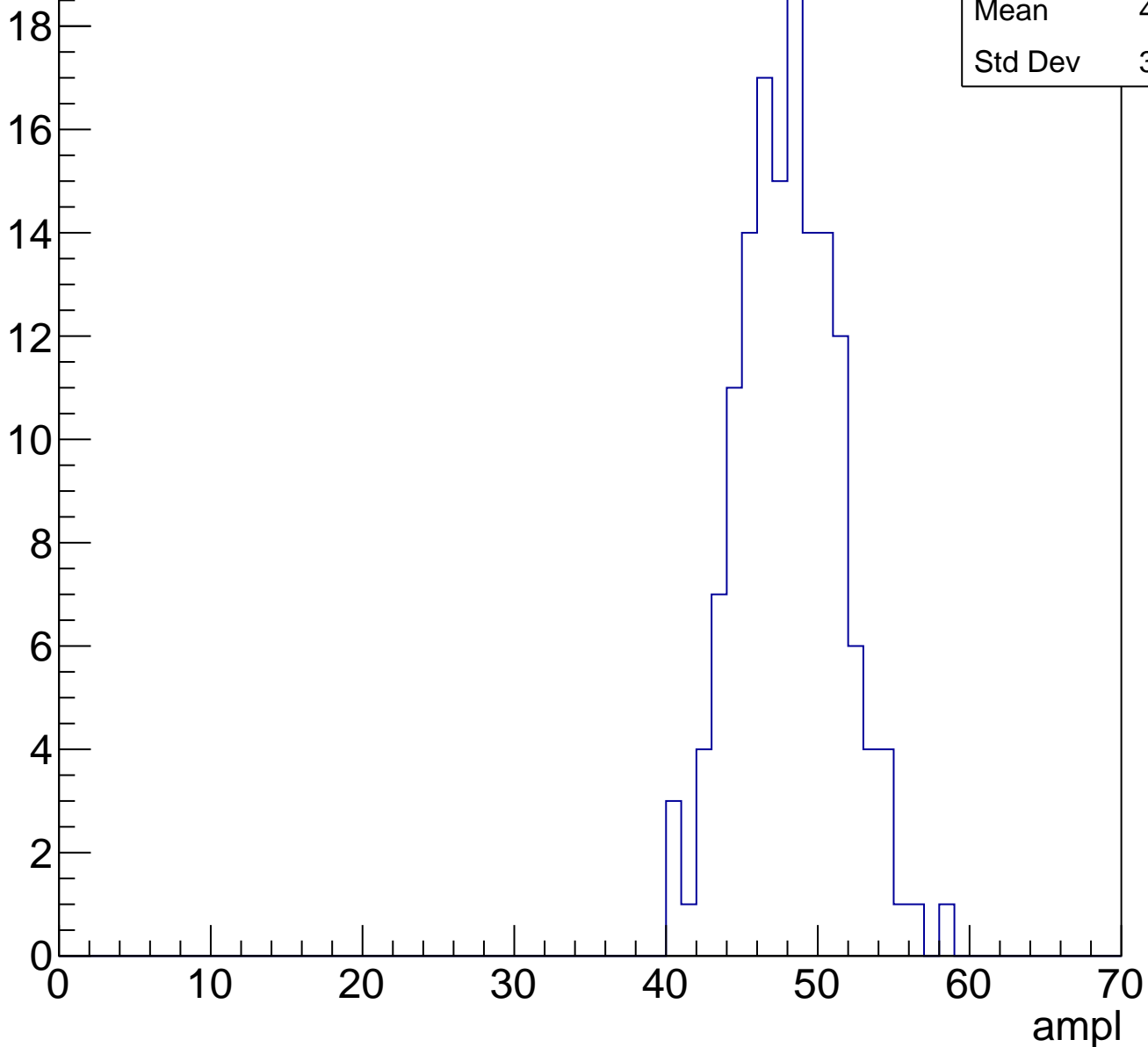
Gaus Width: 3.0900



B1L001S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch62, adc4

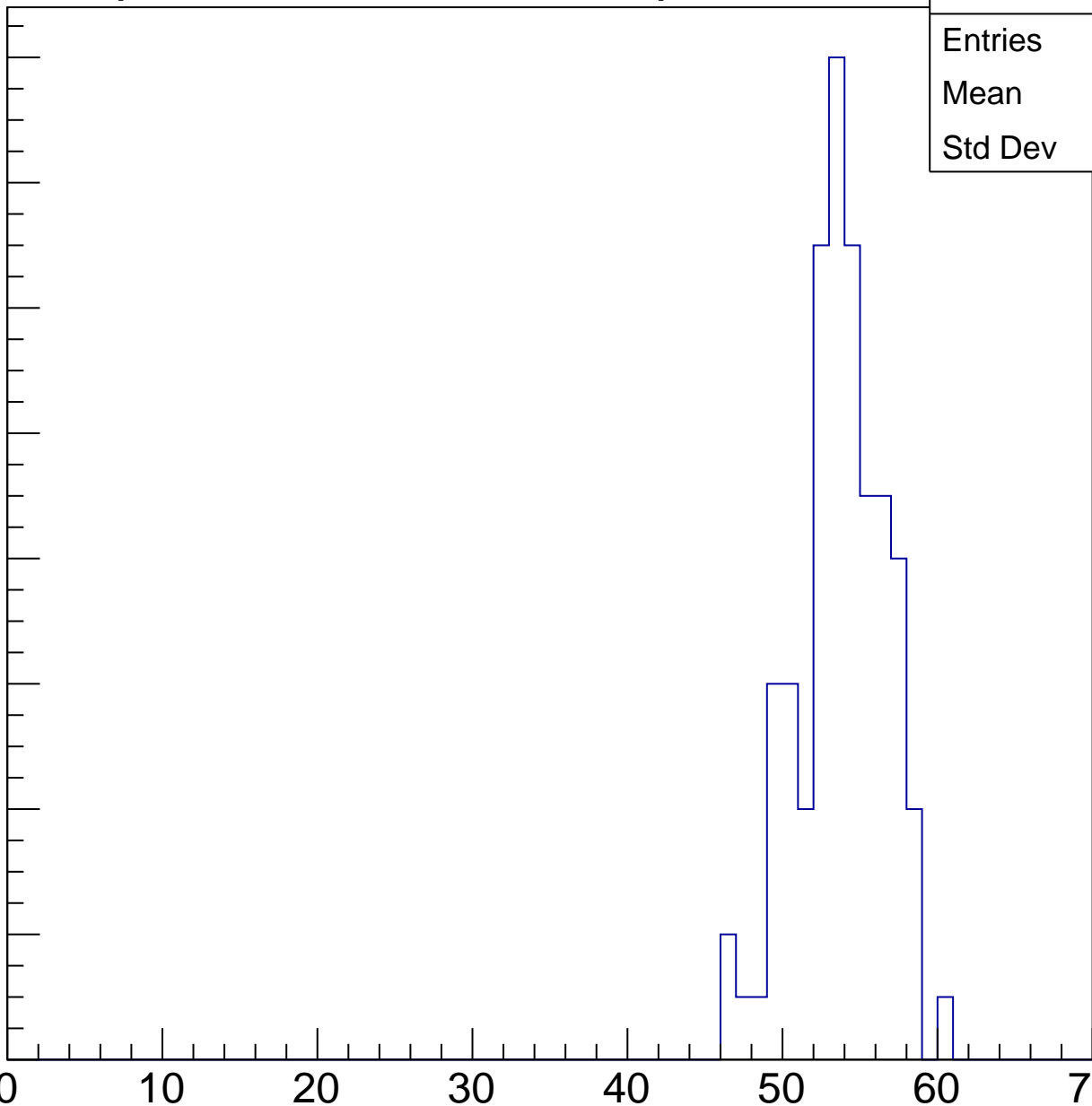
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	53.31
Std Dev	2.813

ampl



B1L001S, U14-ch62, adc5

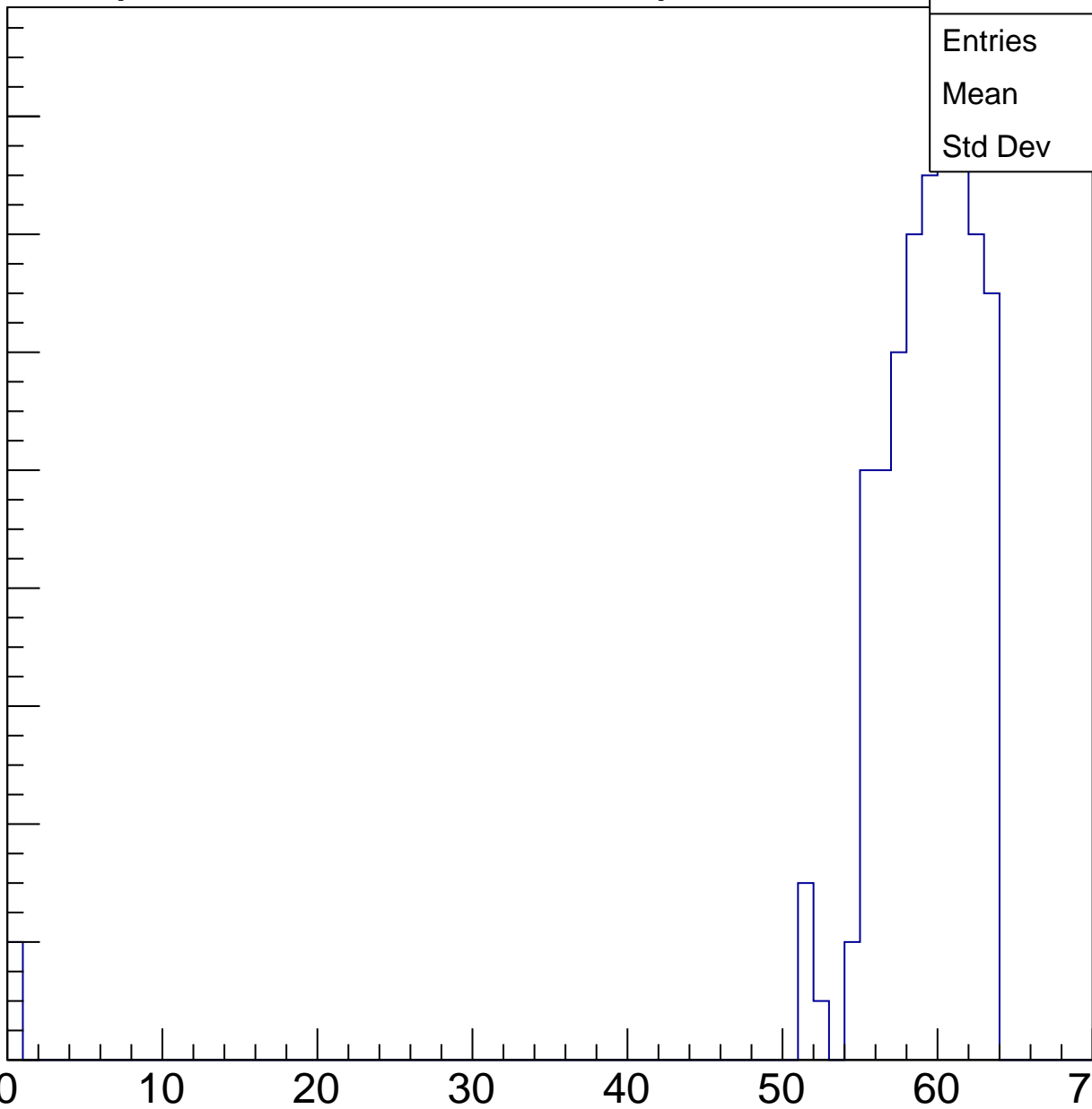
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	129
Mean	58.04
Std Dev	7.806

ampl

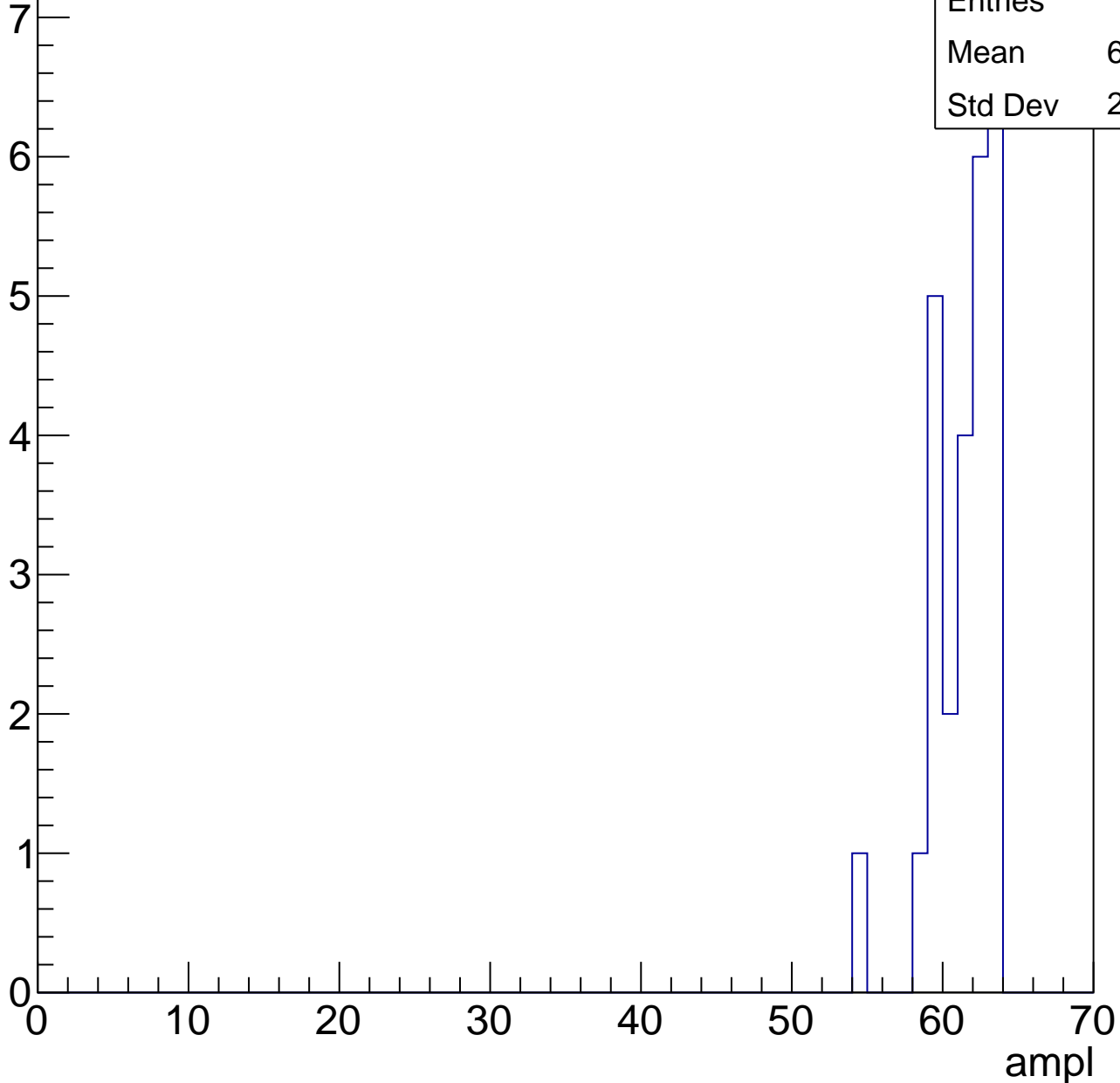


B1L001S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	60.92
Std Dev	2.093



B1L001S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch63, adc0

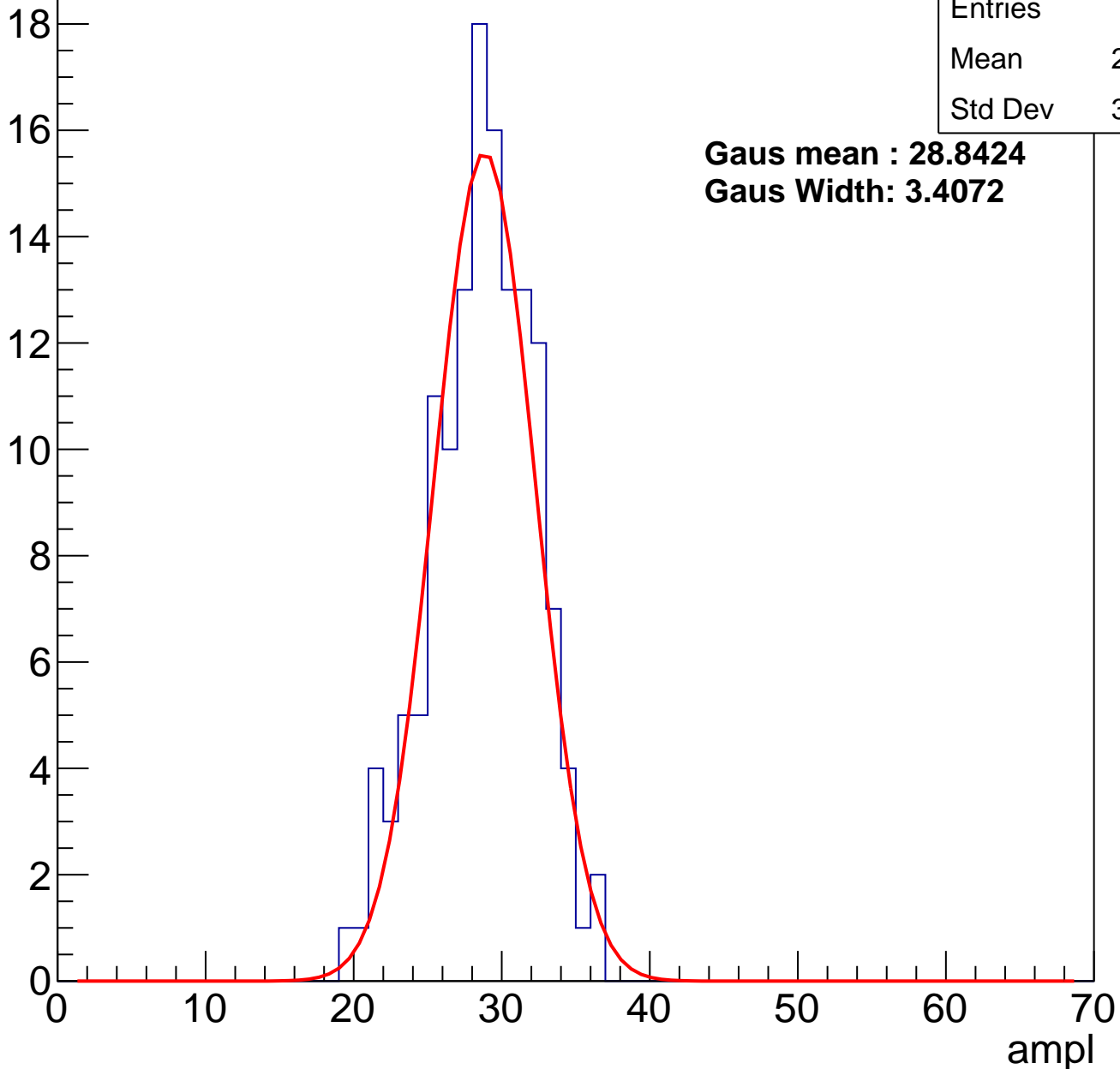
calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	28.27
Std Dev	3.465

Gaus mean : 28.8424

Gaus Width: 3.4072

Entry



B1L001S, U14-ch63, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.38
Std Dev	3.356

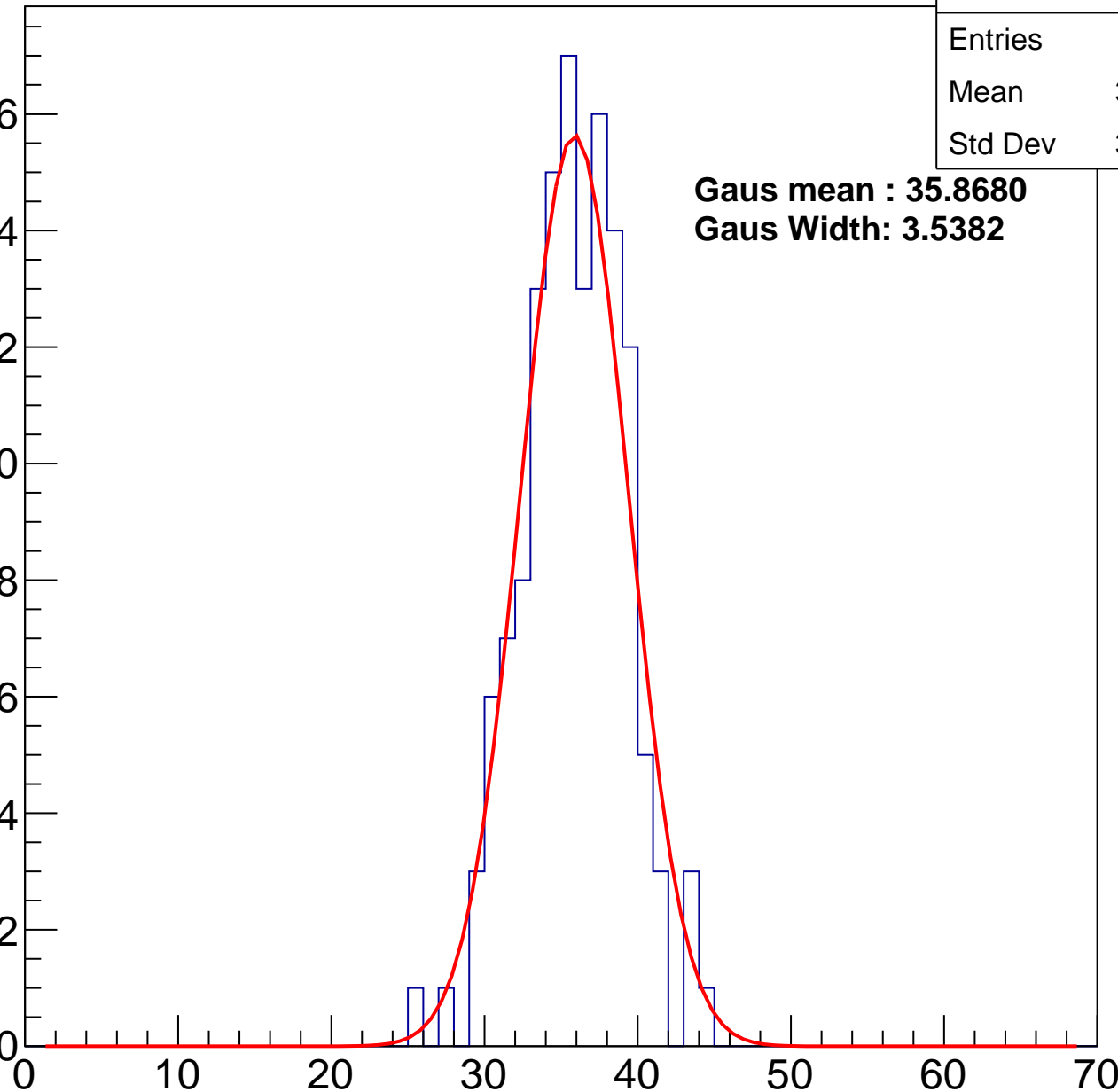
Gaus mean : 35.8680

Gaus Width: 3.5382

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch63, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	41.72
Std Dev	3.18

Gaus mean : 42.2596

Gaus Width: 3.1915

0

10

20

30

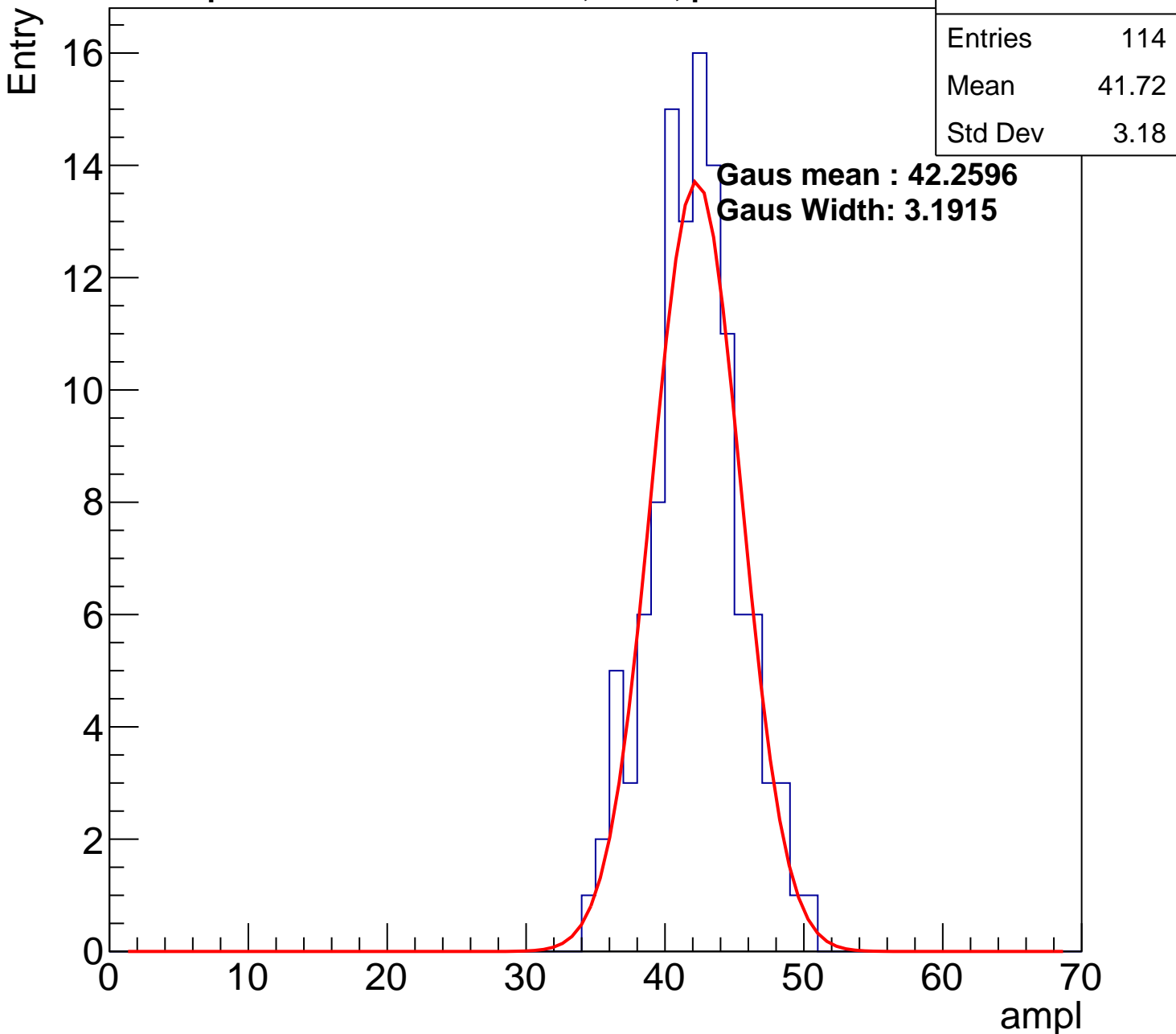
40

50

60

70

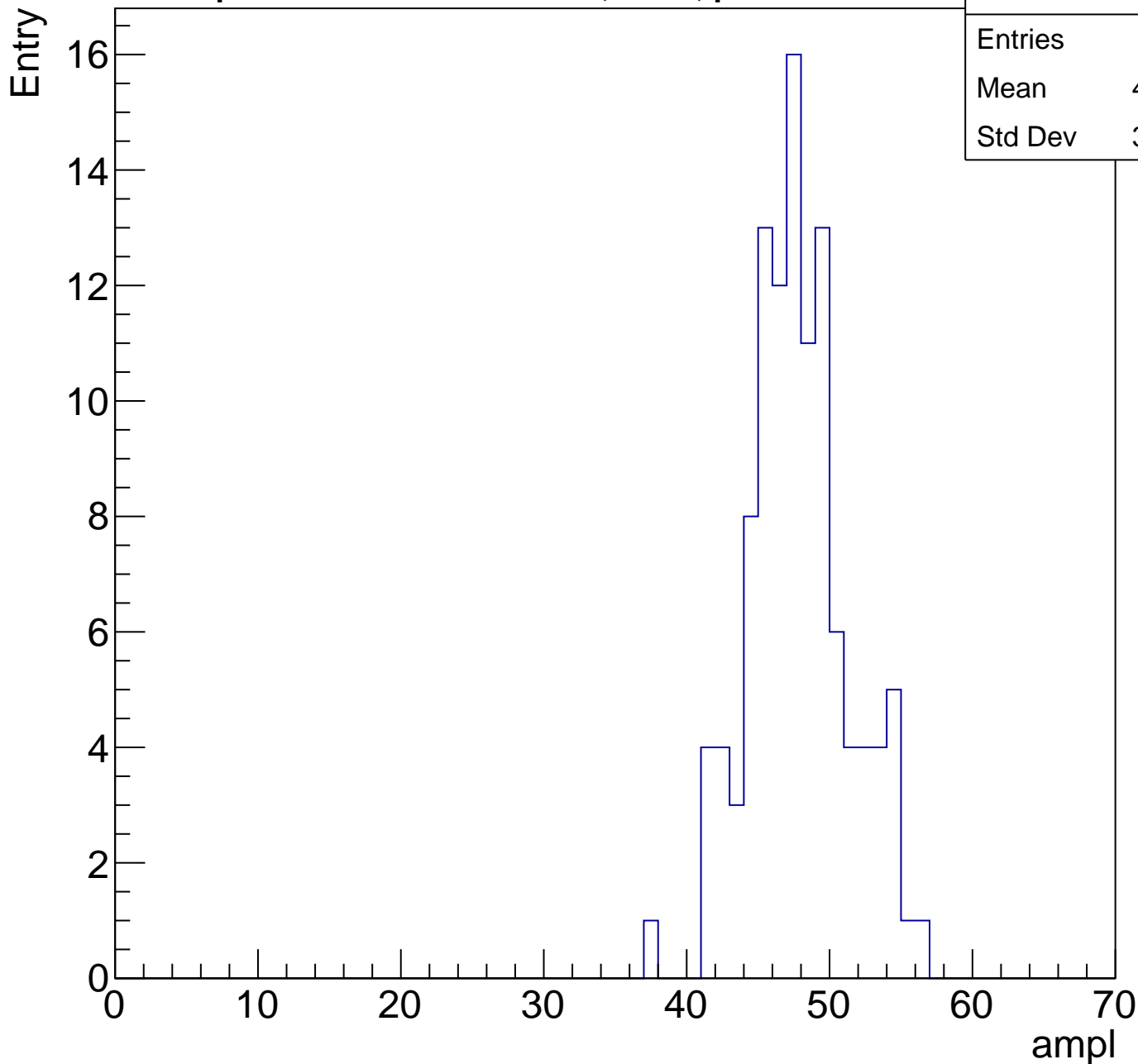
ampl



B1L001S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#2, port C2

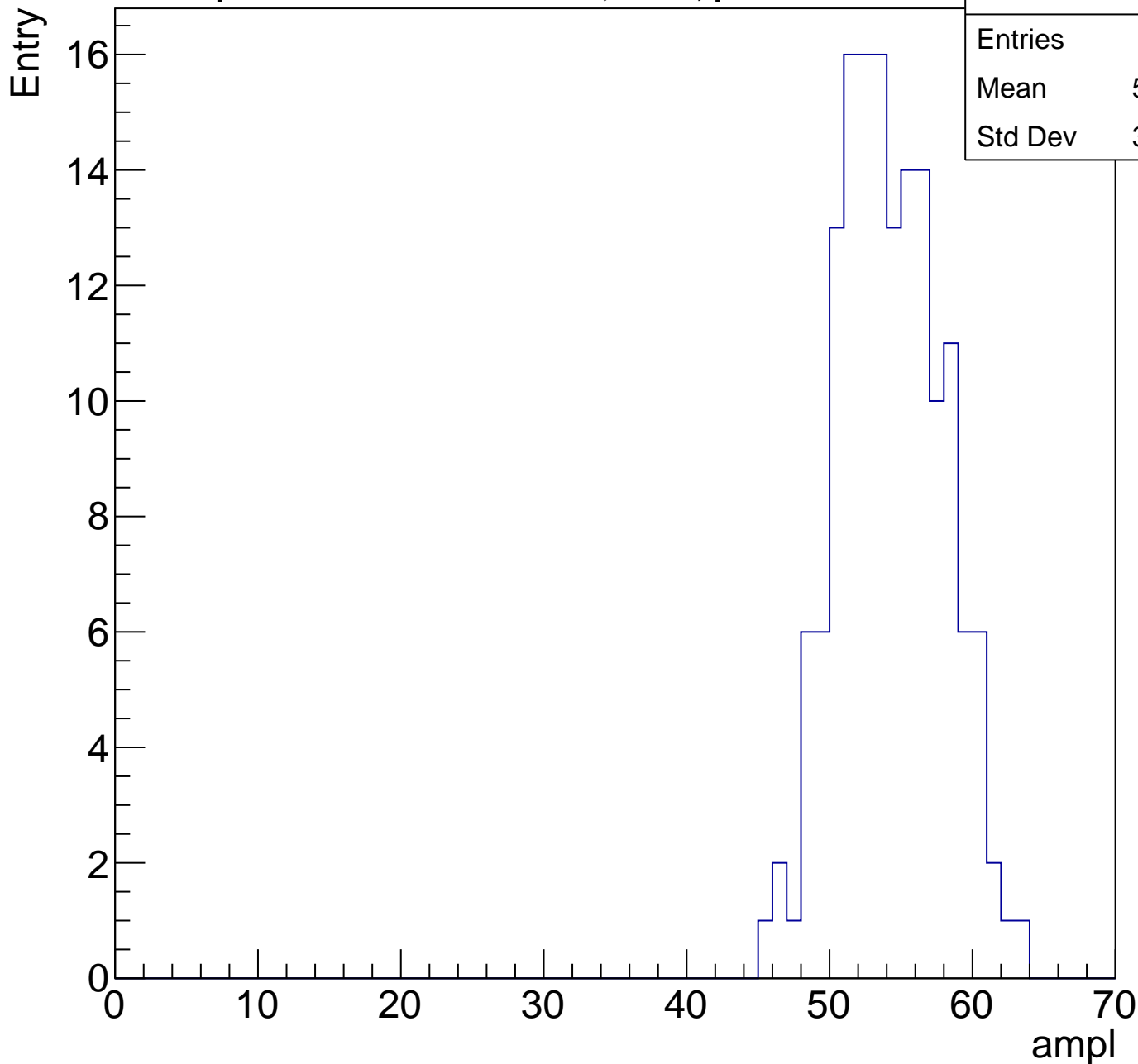
Entries	110
Mean	47.35
Std Dev	3.476



B1L001S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	53.79
Std Dev	3.587



B1L001S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	80
Mean	59.34
Std Dev	2.505

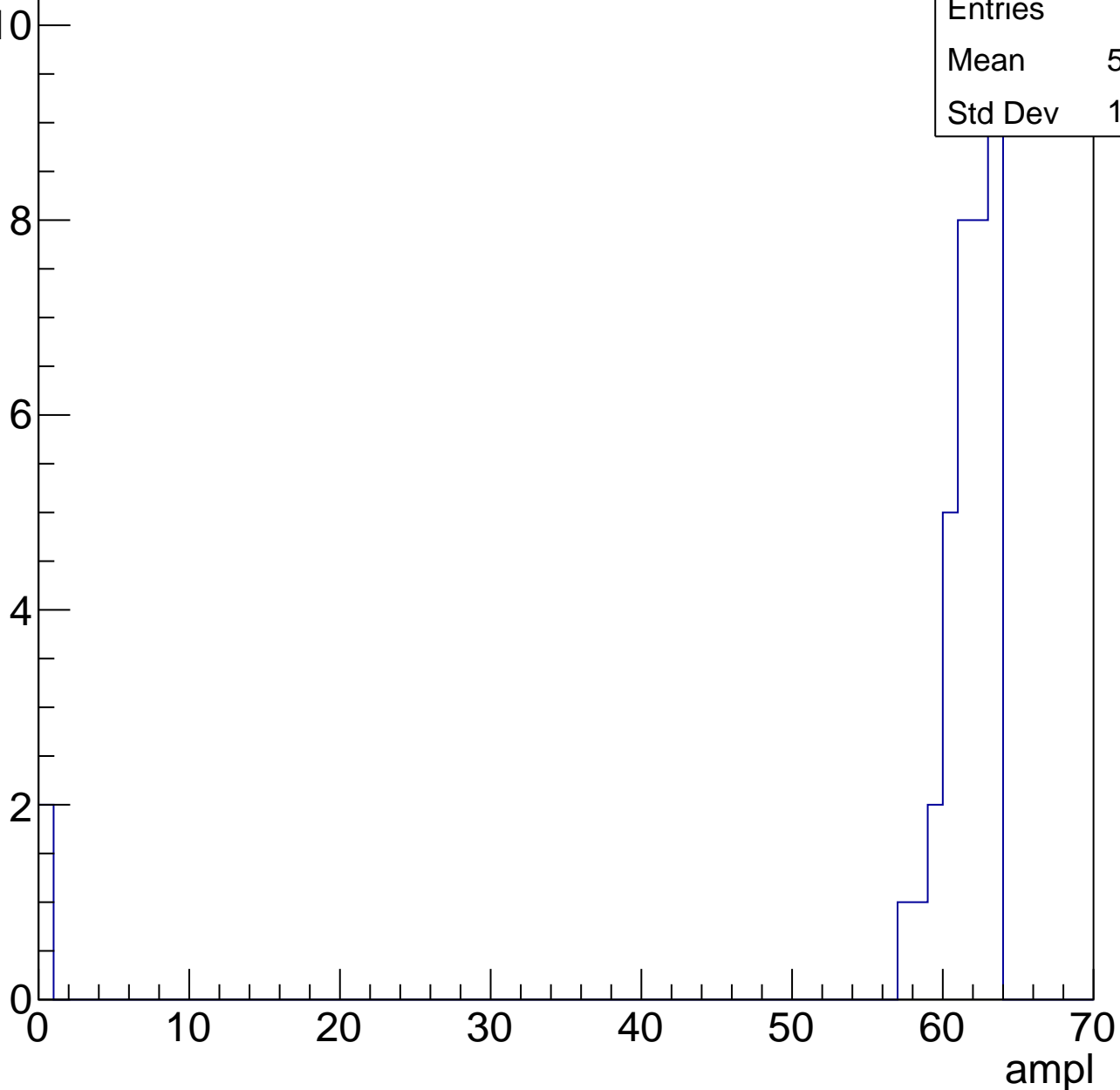


B1L001S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	37
Mean	58.03
Std Dev	13.95



B1L001S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch64, adc0

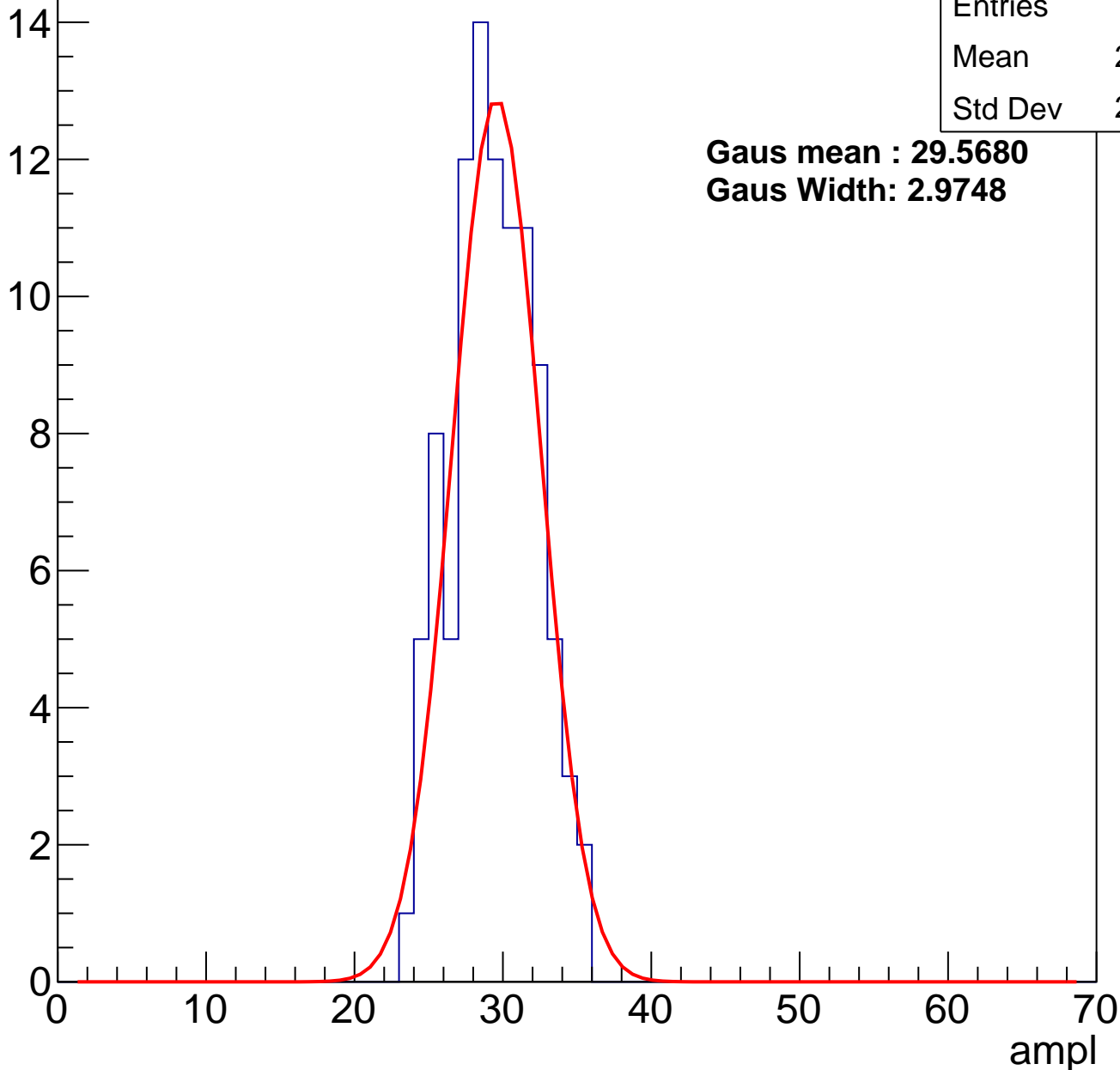
calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	28.91
Std Dev	2.781

Gaus mean : 29.5680

Gaus Width: 2.9748

Entry



B1L001S, U14-ch64, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	34.77
Std Dev	3.424

Gaus mean : 35.1849

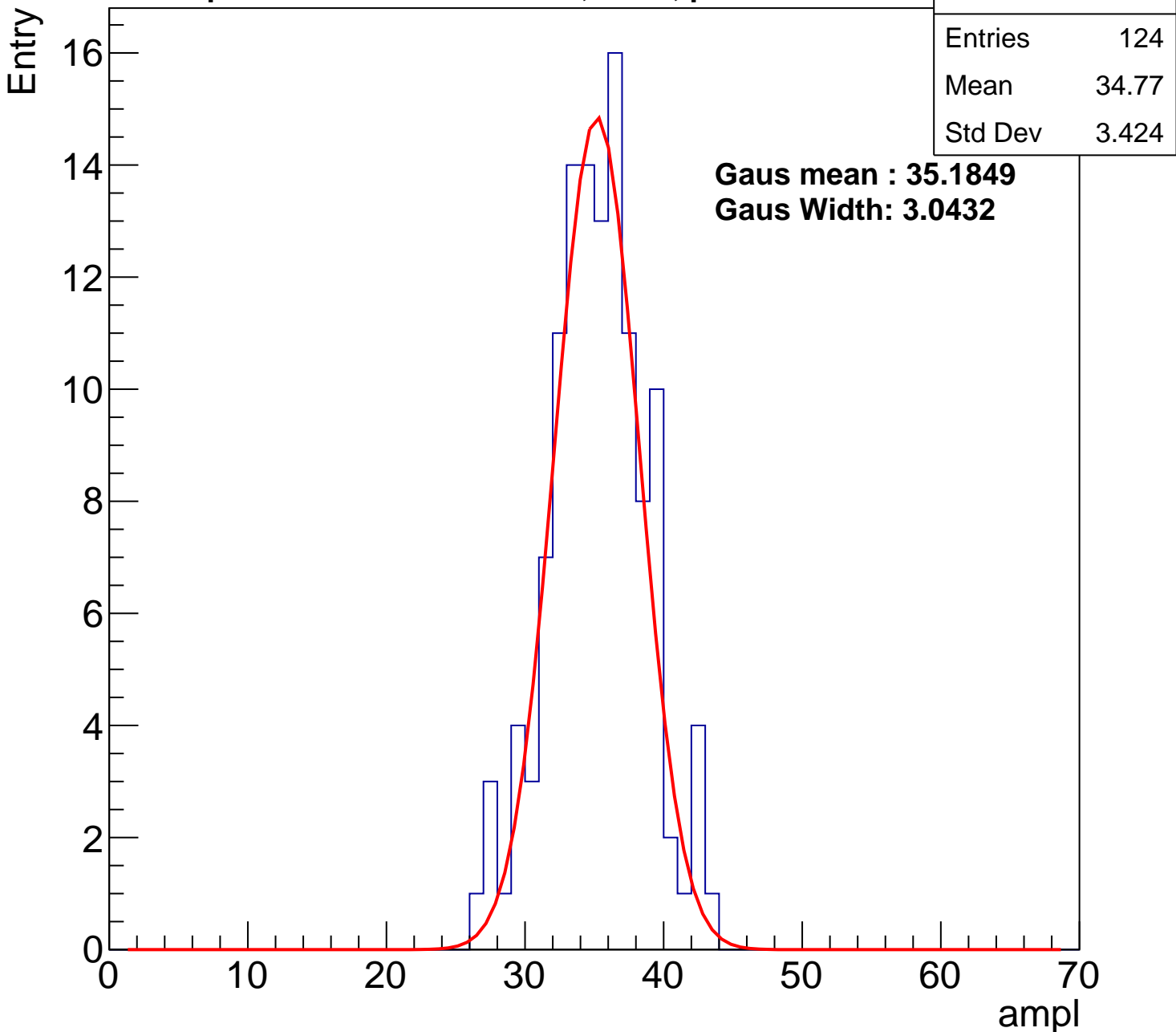
Gaus Width: 3.0432

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch64, adc2

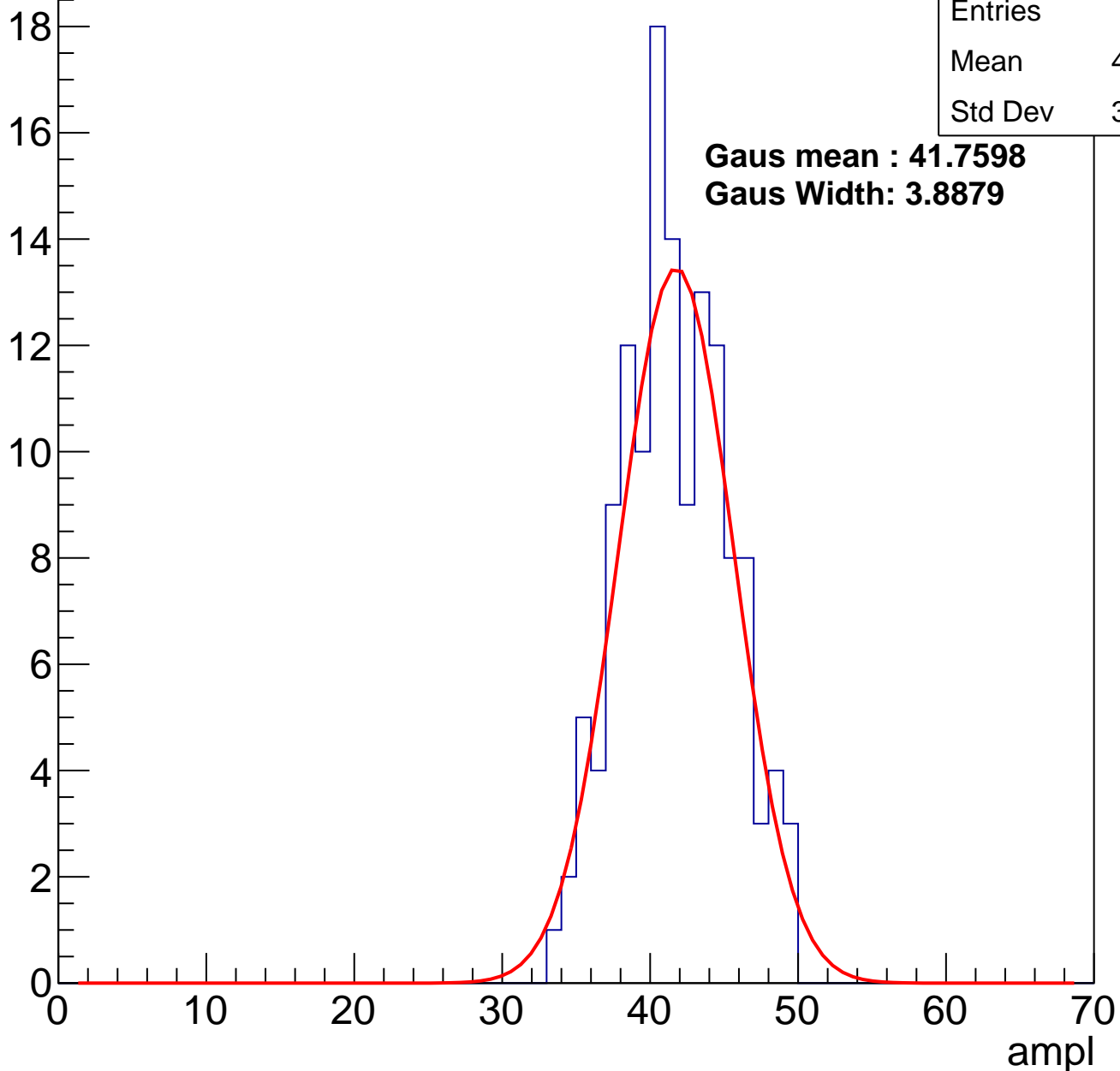
calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	41.23
Std Dev	3.588

Gaus mean : 41.7598

Gaus Width: 3.8879

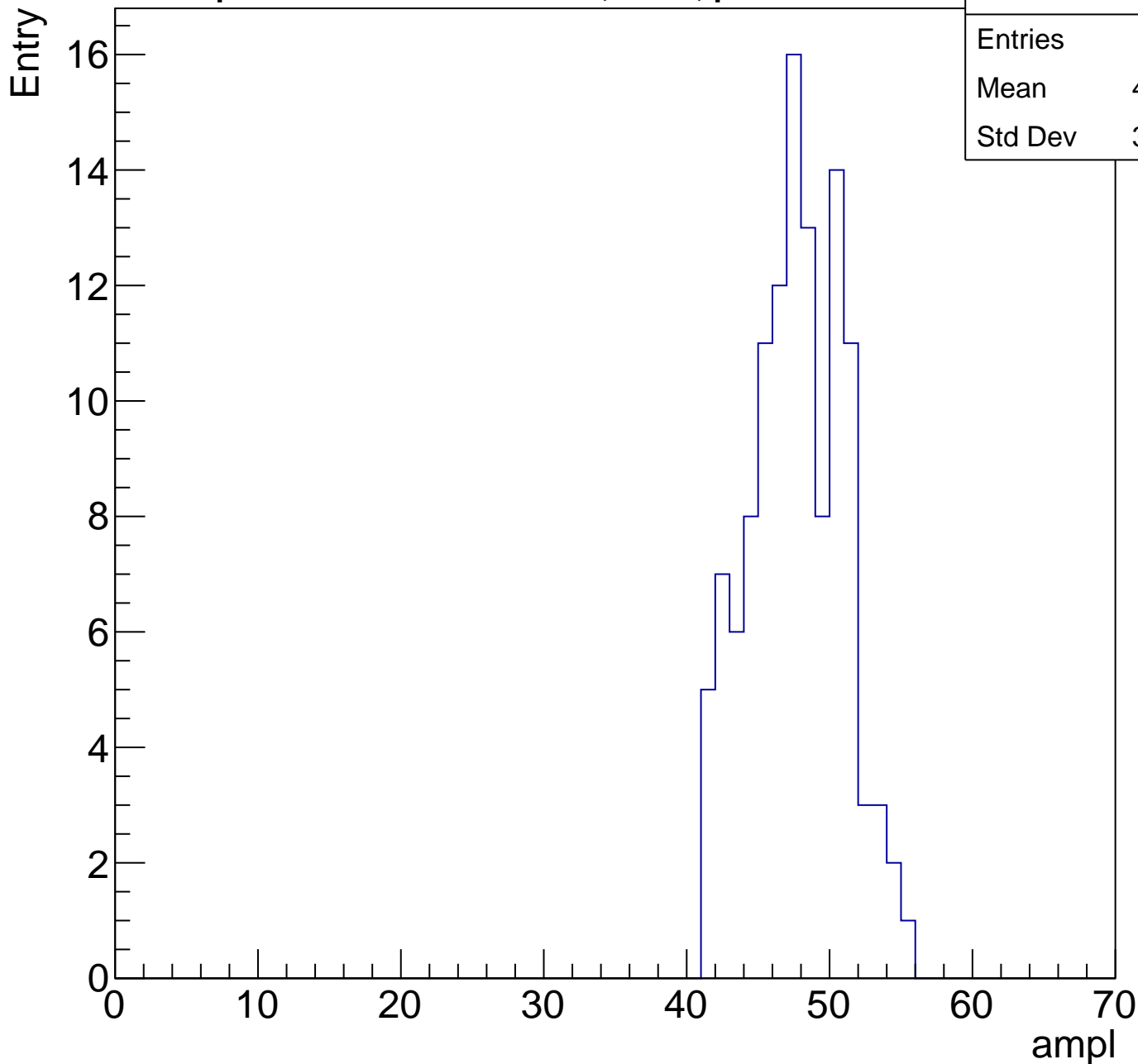
Entry



B1L001S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	47.19
Std Dev	3.249

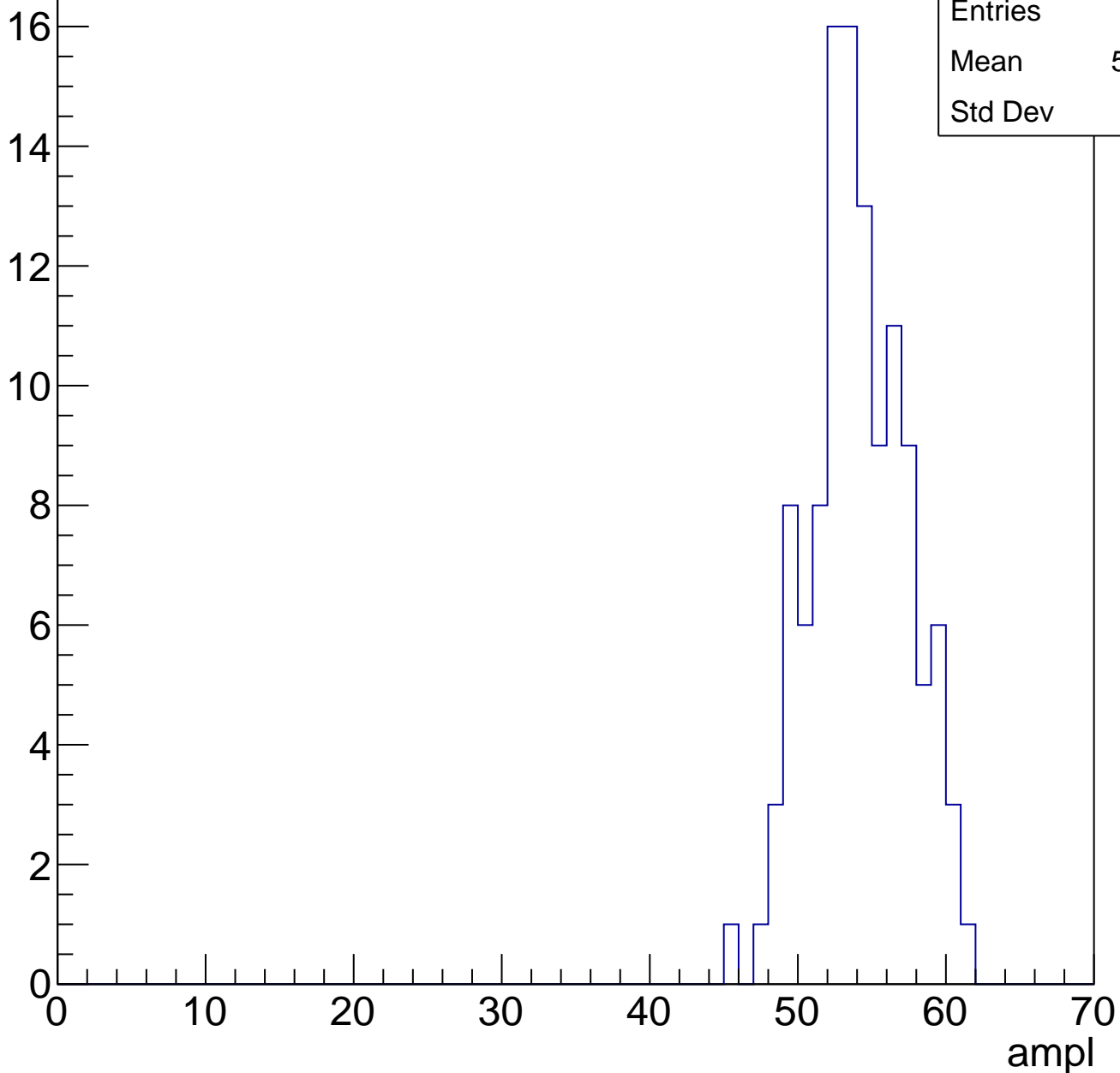


B1L001S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	53.68
Std Dev	3.21

Entry



B1L001S, U14-ch64, adc5

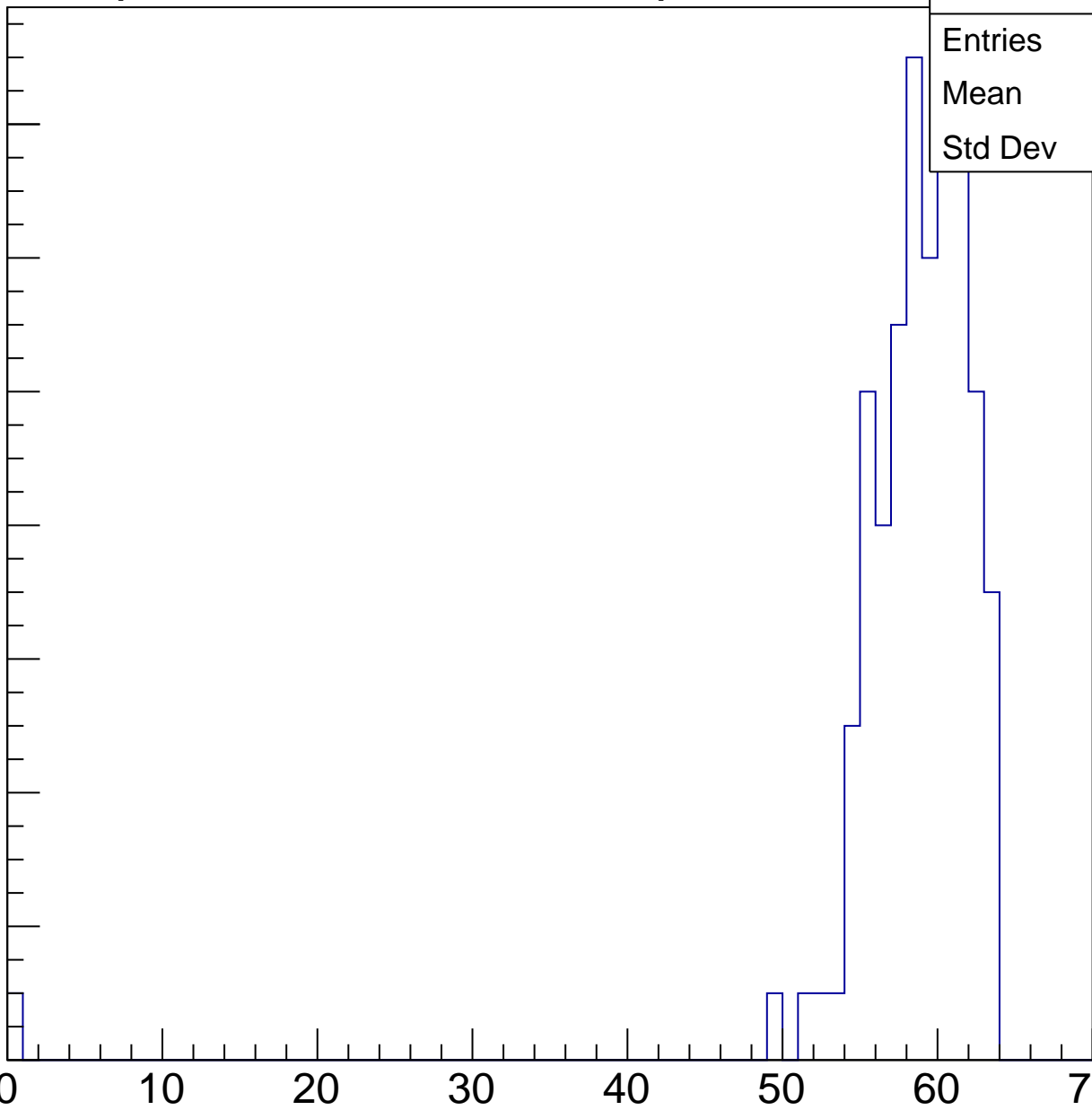
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	111
Mean	57.95
Std Dev	6.222

ampl



B1L001S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries	32
Mean	59.78
Std Dev	10.85

ampl

0

10

20

30

40

50

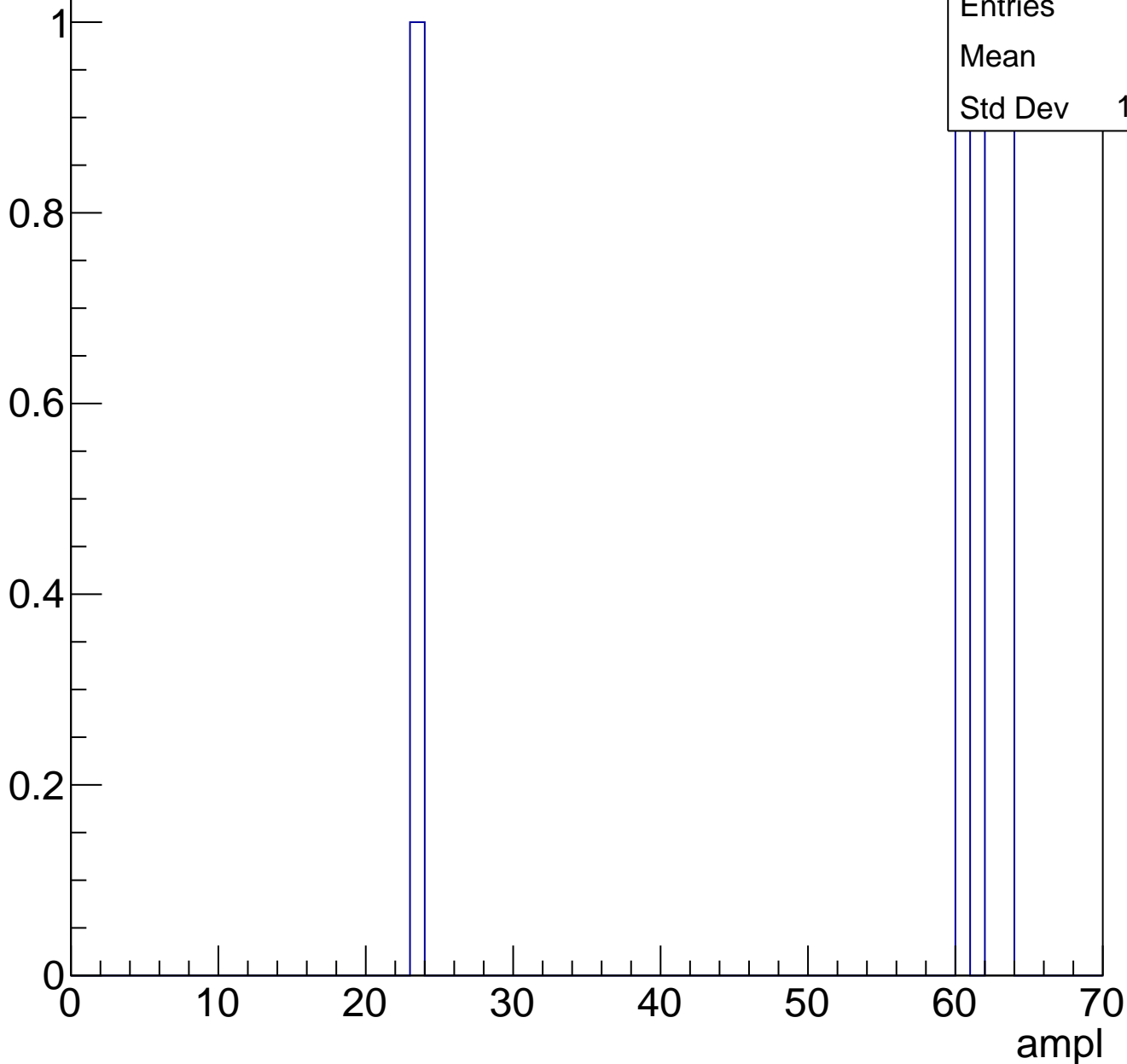
60

70

B1L001S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch65, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	29.71
Std Dev	3.401

Gaus mean : 30.2484

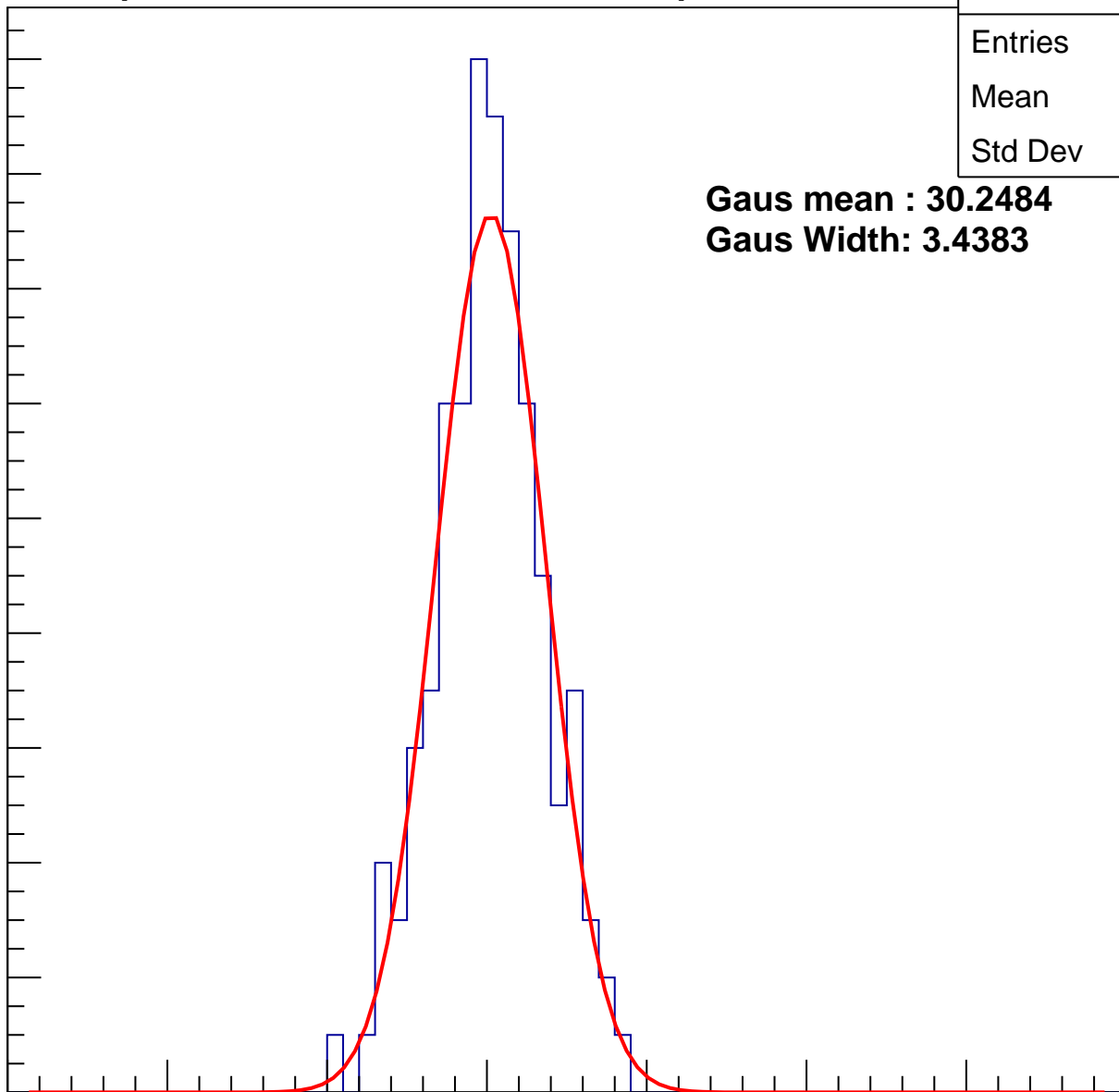
Gaus Width: 3.4383

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch65, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	36.25
Std Dev	3.363

Gaus mean : 36.5957

Gaus Width: 3.5553

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch65, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

Entries

125

Mean

41.85

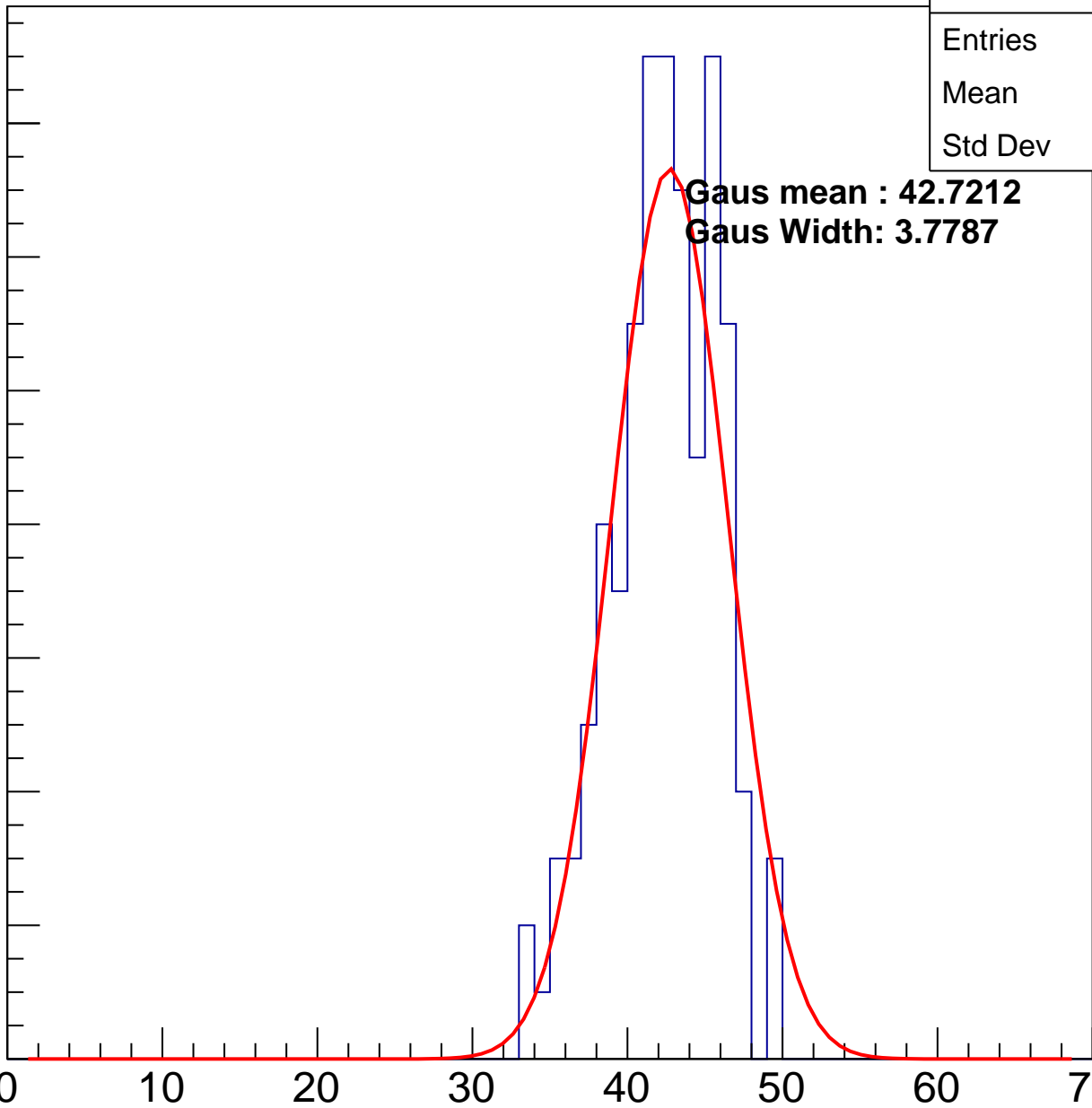
Std Dev

3.418

Gaus mean : 42.7212

Gaus Width: 3.7787

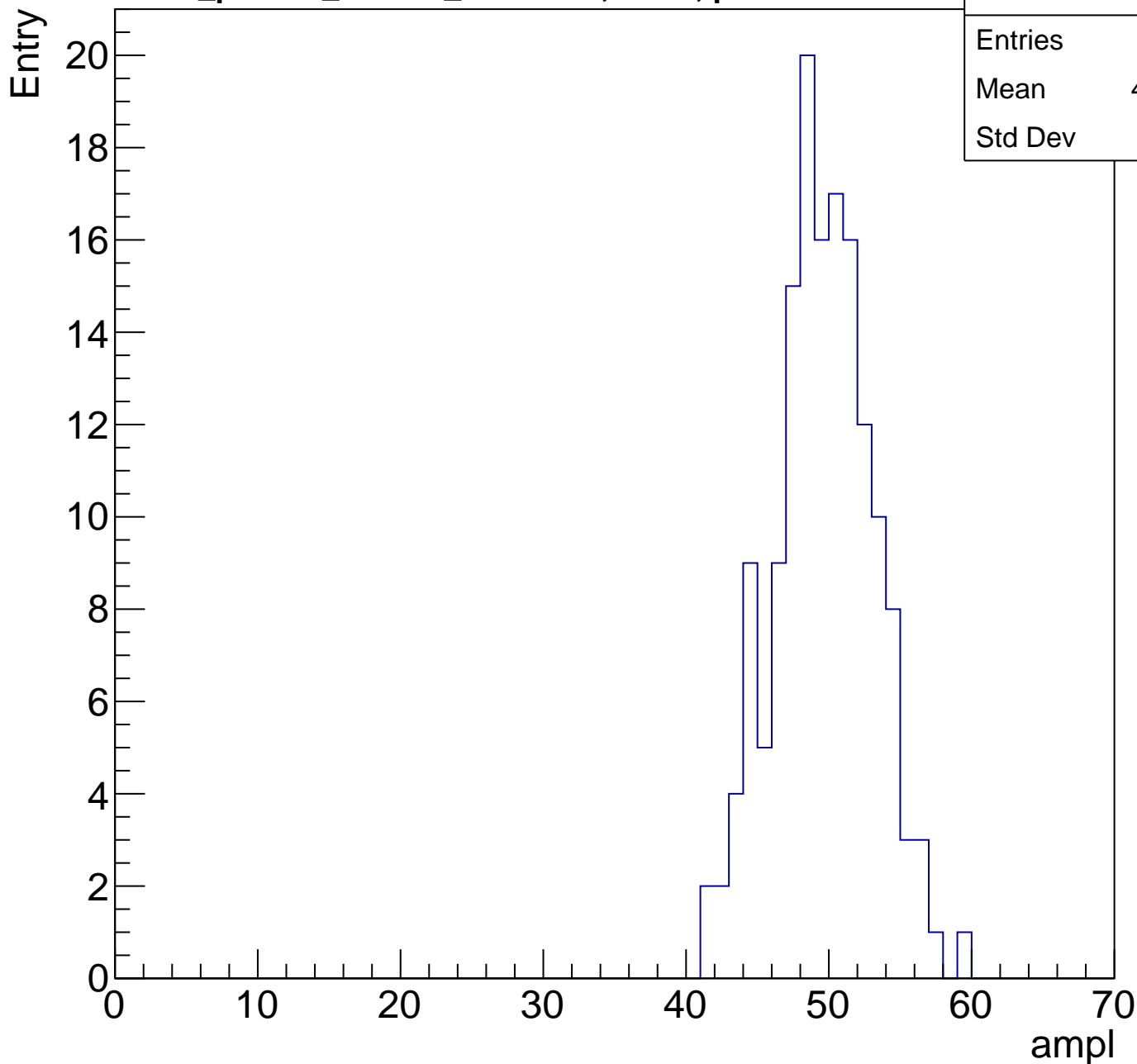
ampl



B1L001S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#2, port C2

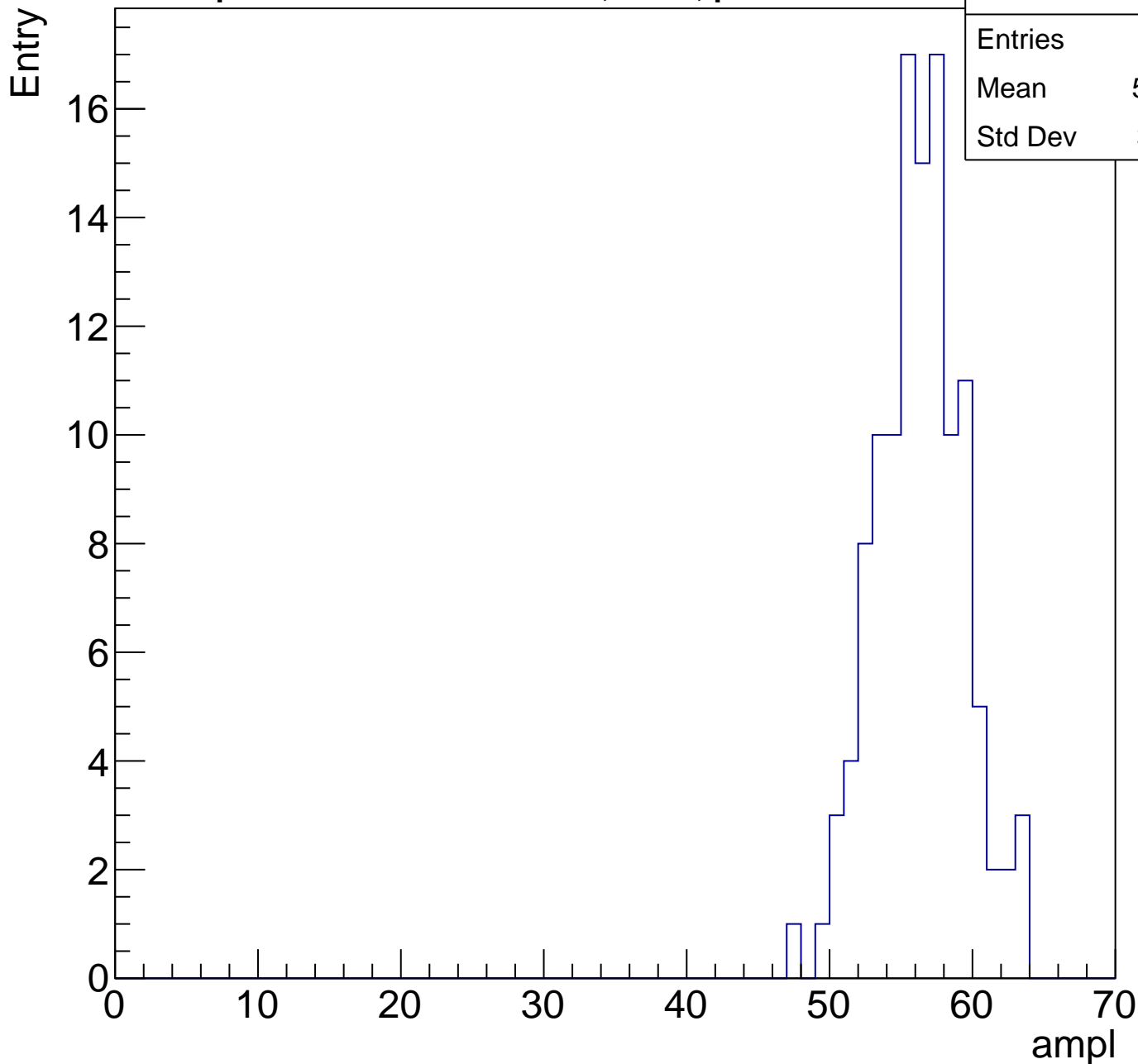
Entries	153
Mean	49.17
Std Dev	3.43



B1L001S, U14-ch65, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	55.83
Std Dev	3.061



B1L001S, U14-ch65, adc5

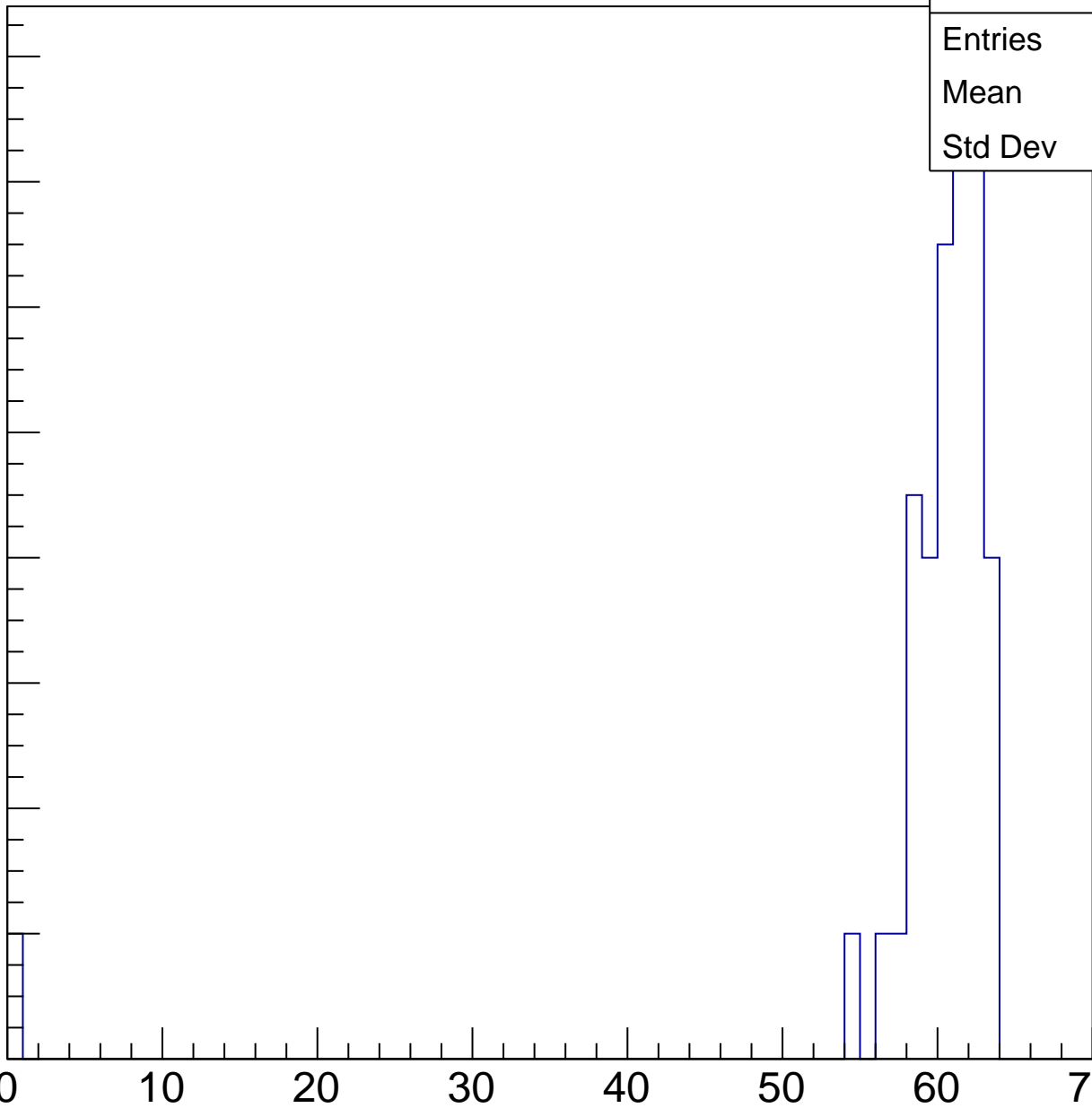
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	78
Mean	58.72
Std Dev	9.733

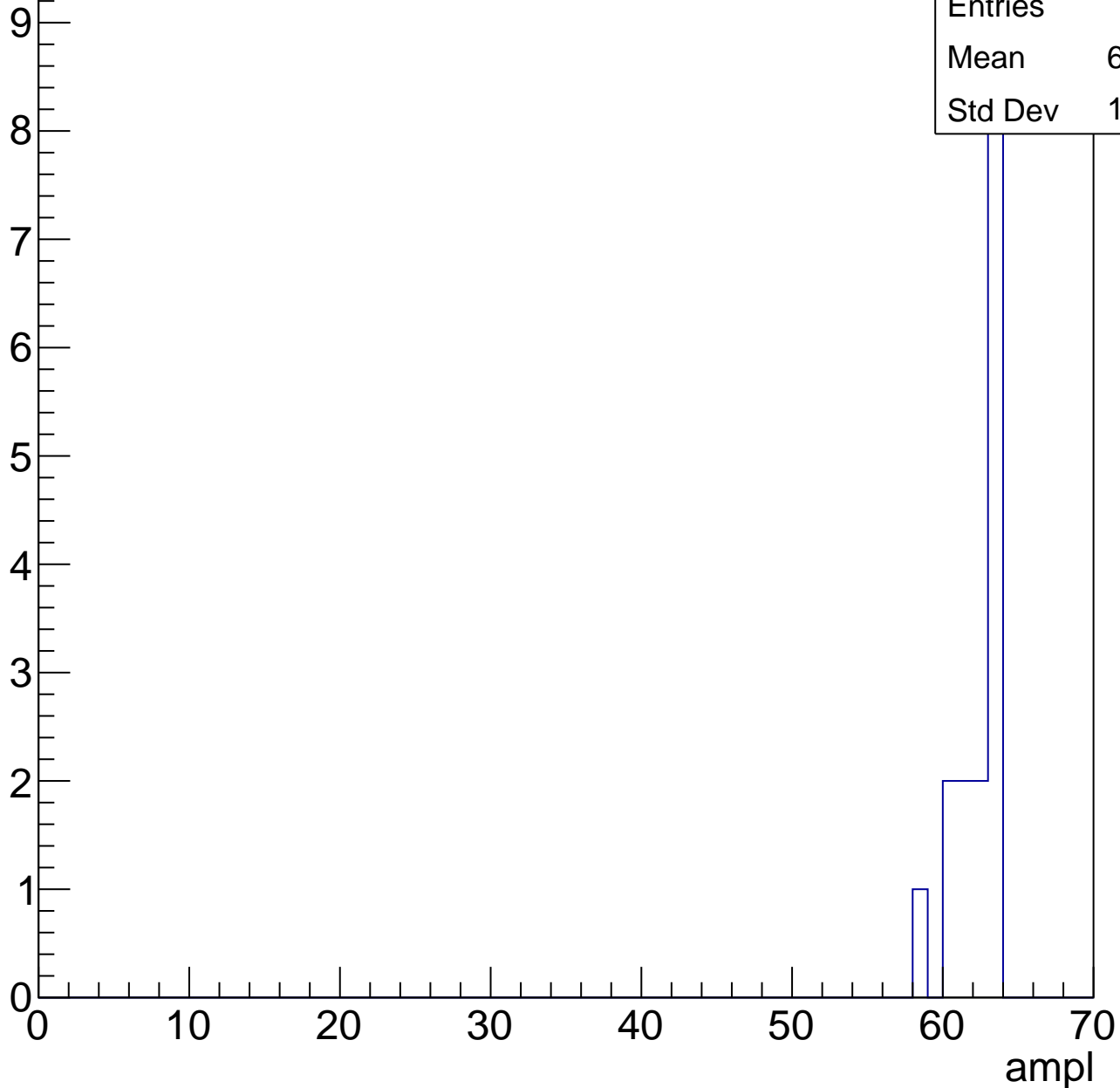
ampl



B1L001S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch66, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	27.9
Std Dev	3.164

Gaus mean : 28.3562

Gaus Width: 3.2820

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

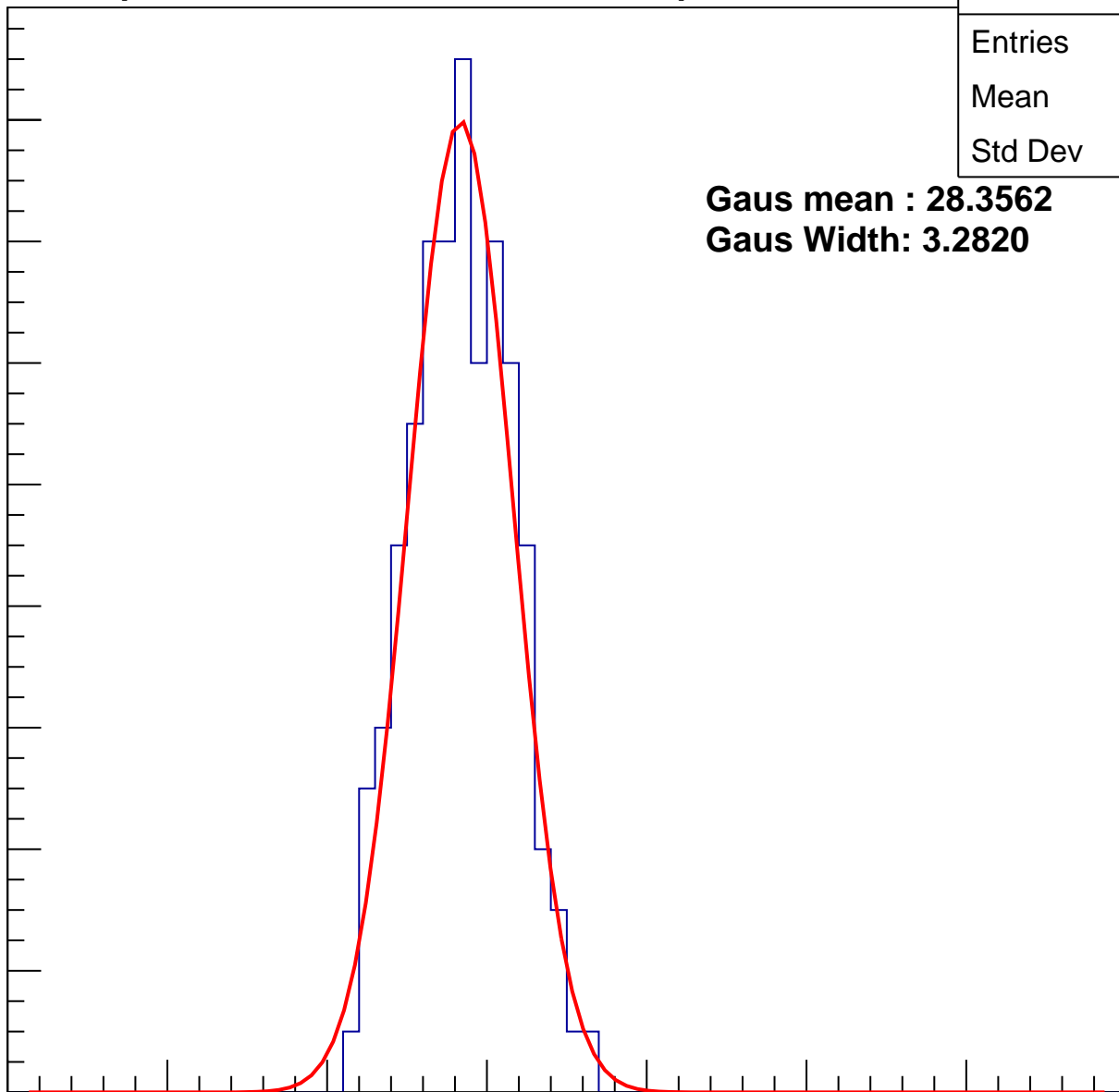
40

50

60

70

ampl



B1L001S, U14-ch66, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	159
Mean	35.24
Std Dev	3.53

Gaus mean : 35.6549

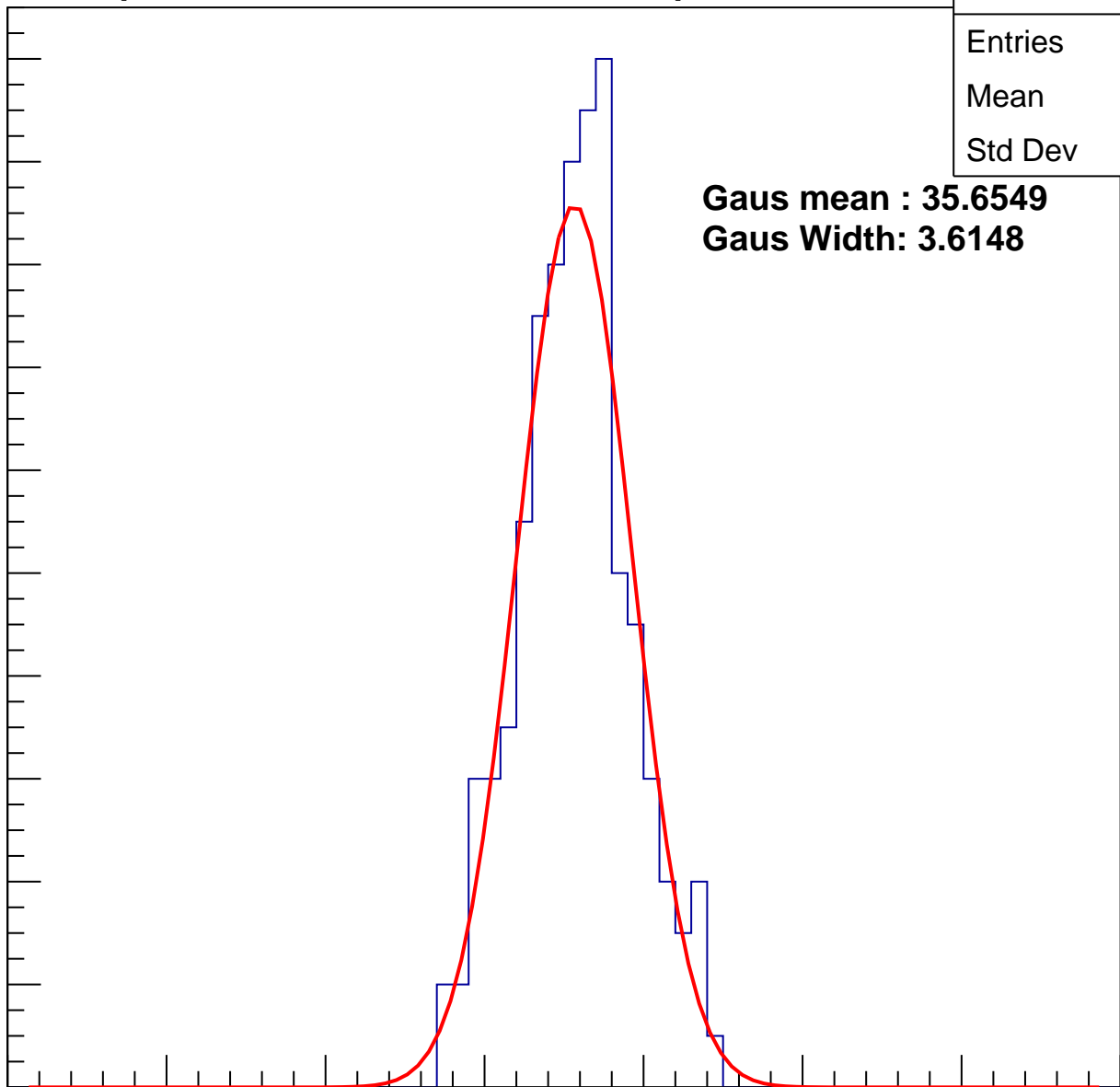
Gaus Width: 3.6148

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch66, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	41.97
Std Dev	3.052

Gaus mean : 42.0149

Gaus Width: 3.1690

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

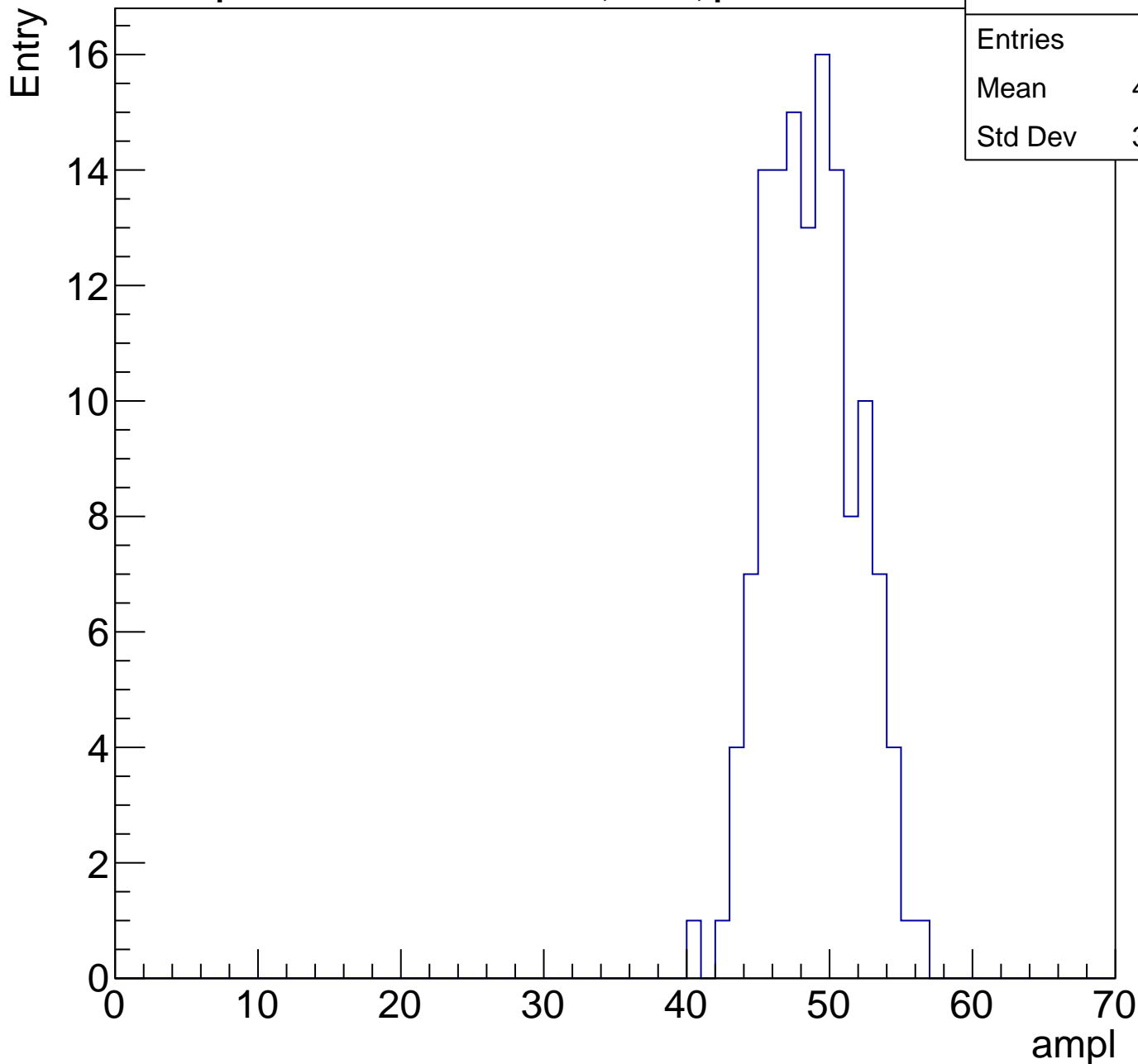
60

70

B1L001S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#2, port C2

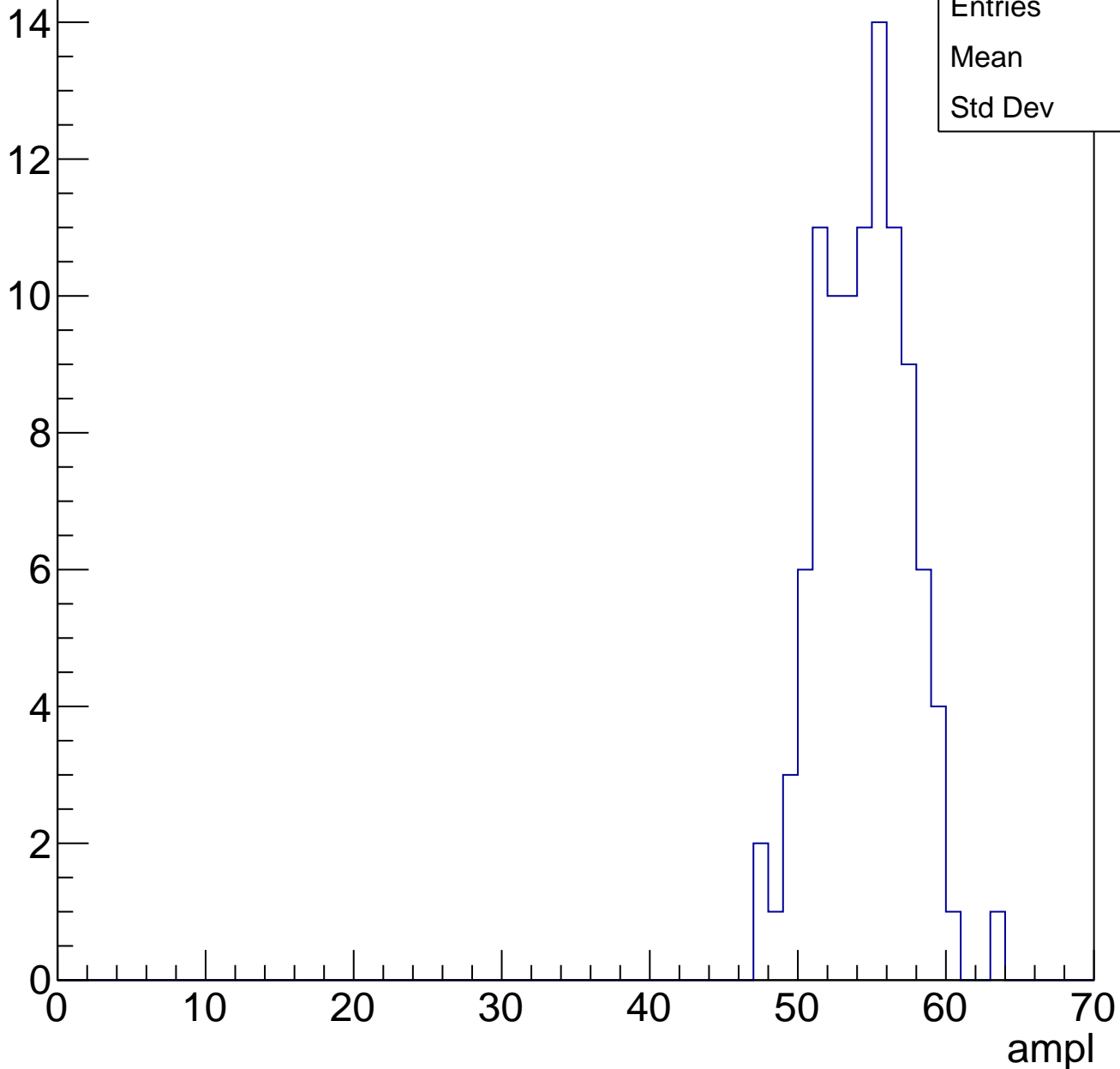
Entries	130
Mean	48.27
Std Dev	3.083



B1L001S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch66, adc5

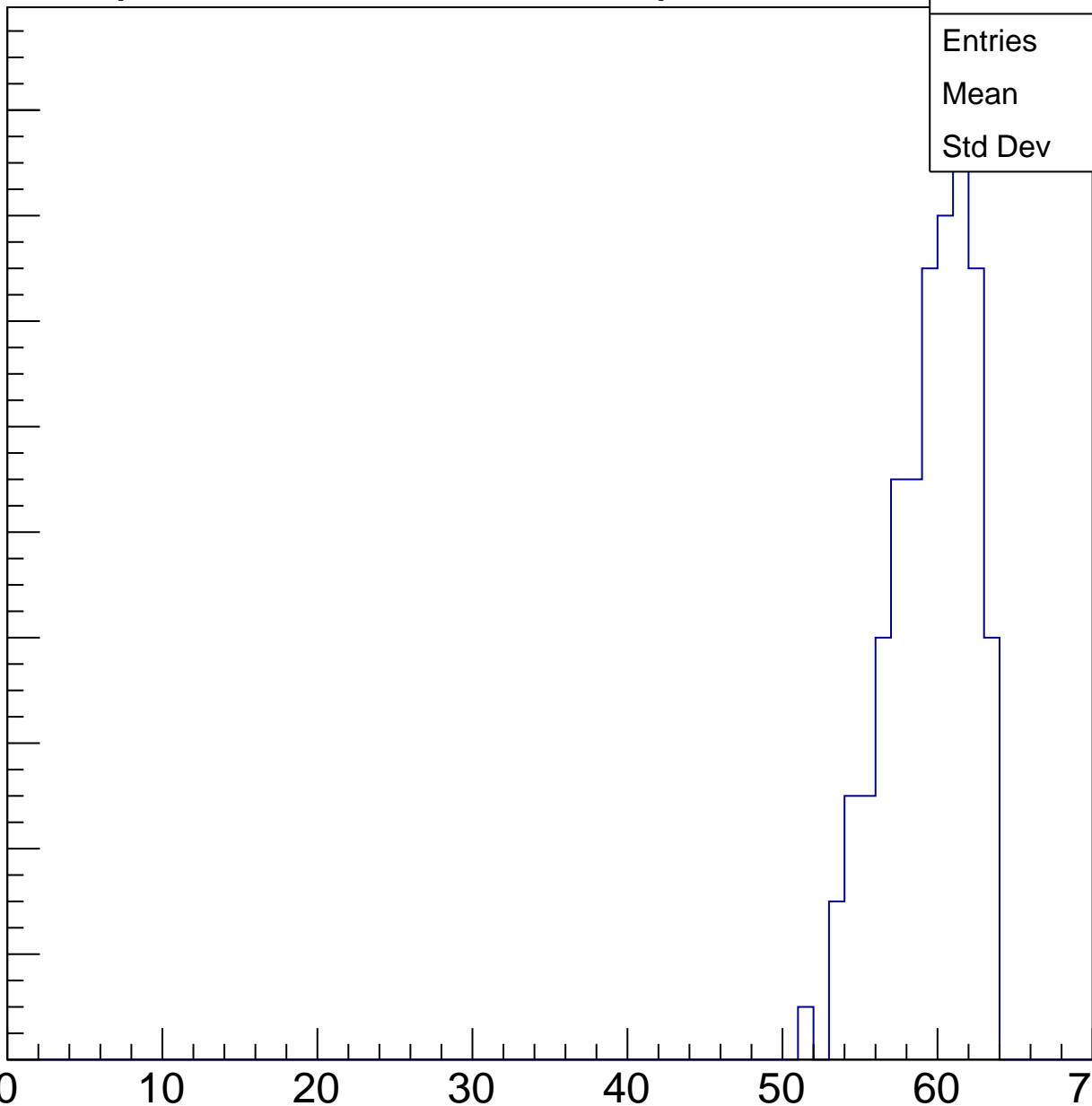
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	117
Mean	59.03
Std Dev	2.709

ampl



B1L001S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	26
Mean	56.81
Std Dev	16.49

B1L001S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch67, adc0

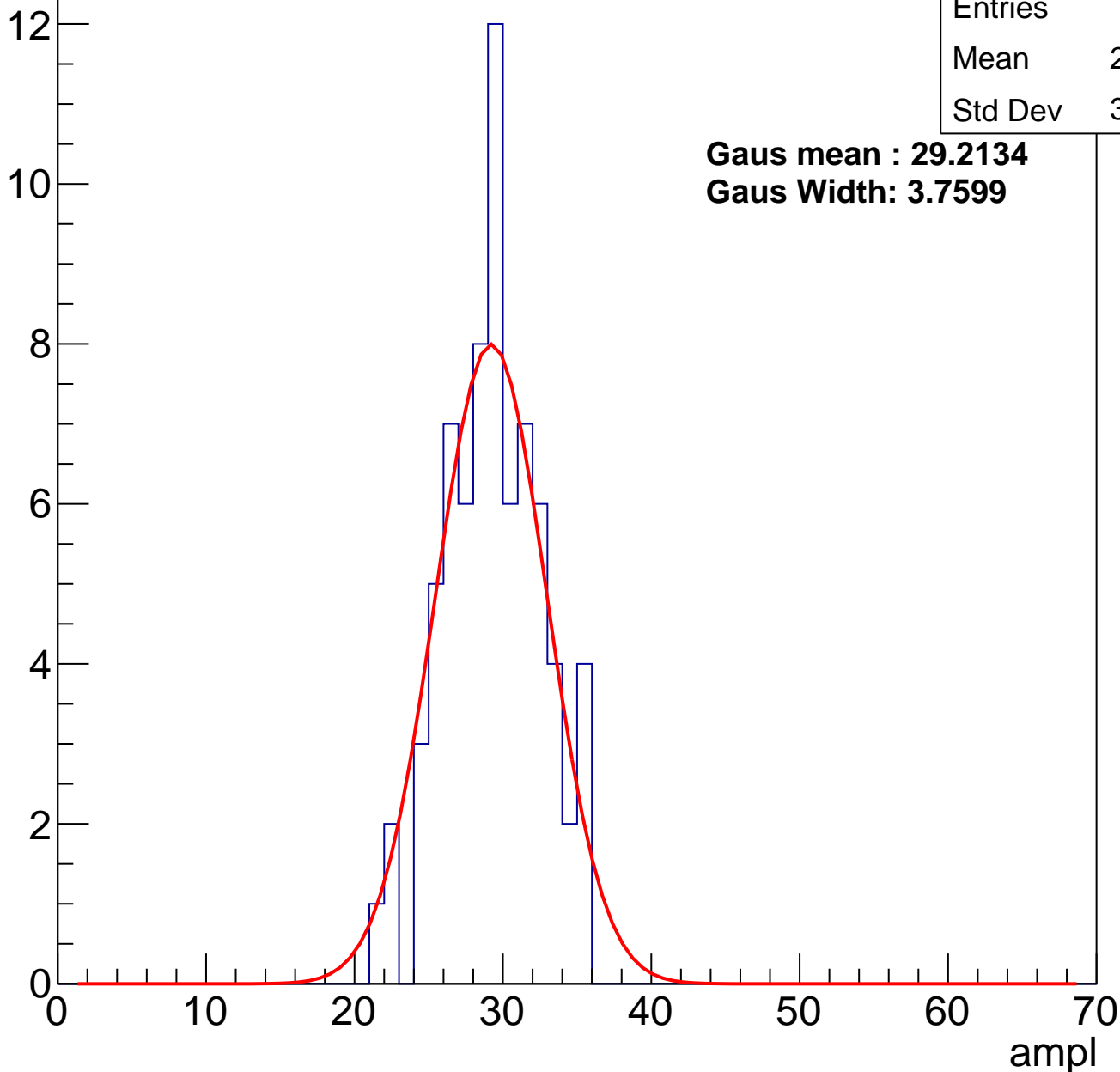
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	73
Mean	28.86
Std Dev	3.219

Gaus mean : 29.2134

Gaus Width: 3.7599



B1L001S, U14-ch67, adc1

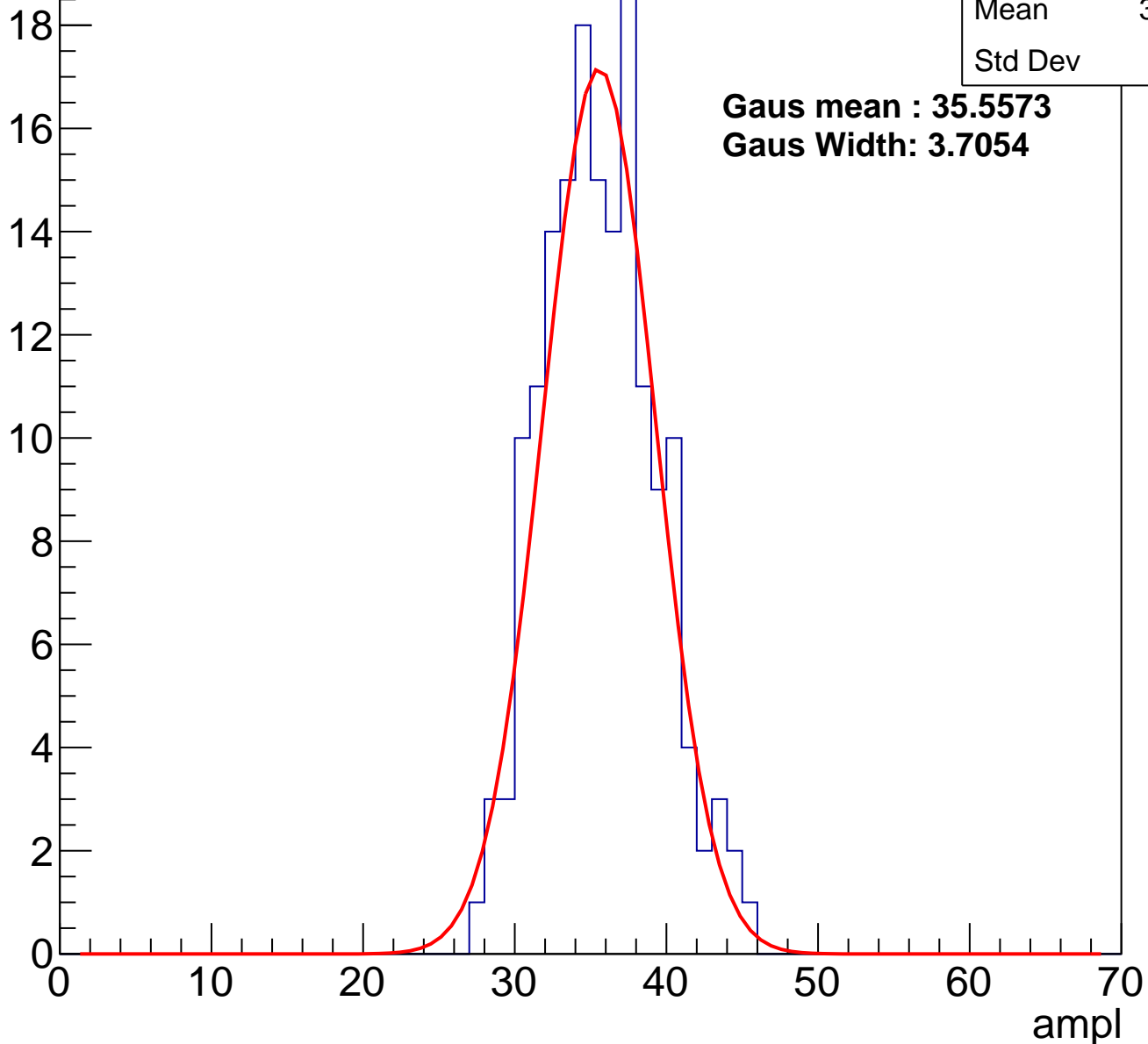
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	35.18
Std Dev	3.67

Gaus mean : 35.5573

Gaus Width: 3.7054

Entry



B1L001S, U14-ch67, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	94
Mean	41.53
Std Dev	3.28

Gaus mean : 42.3098

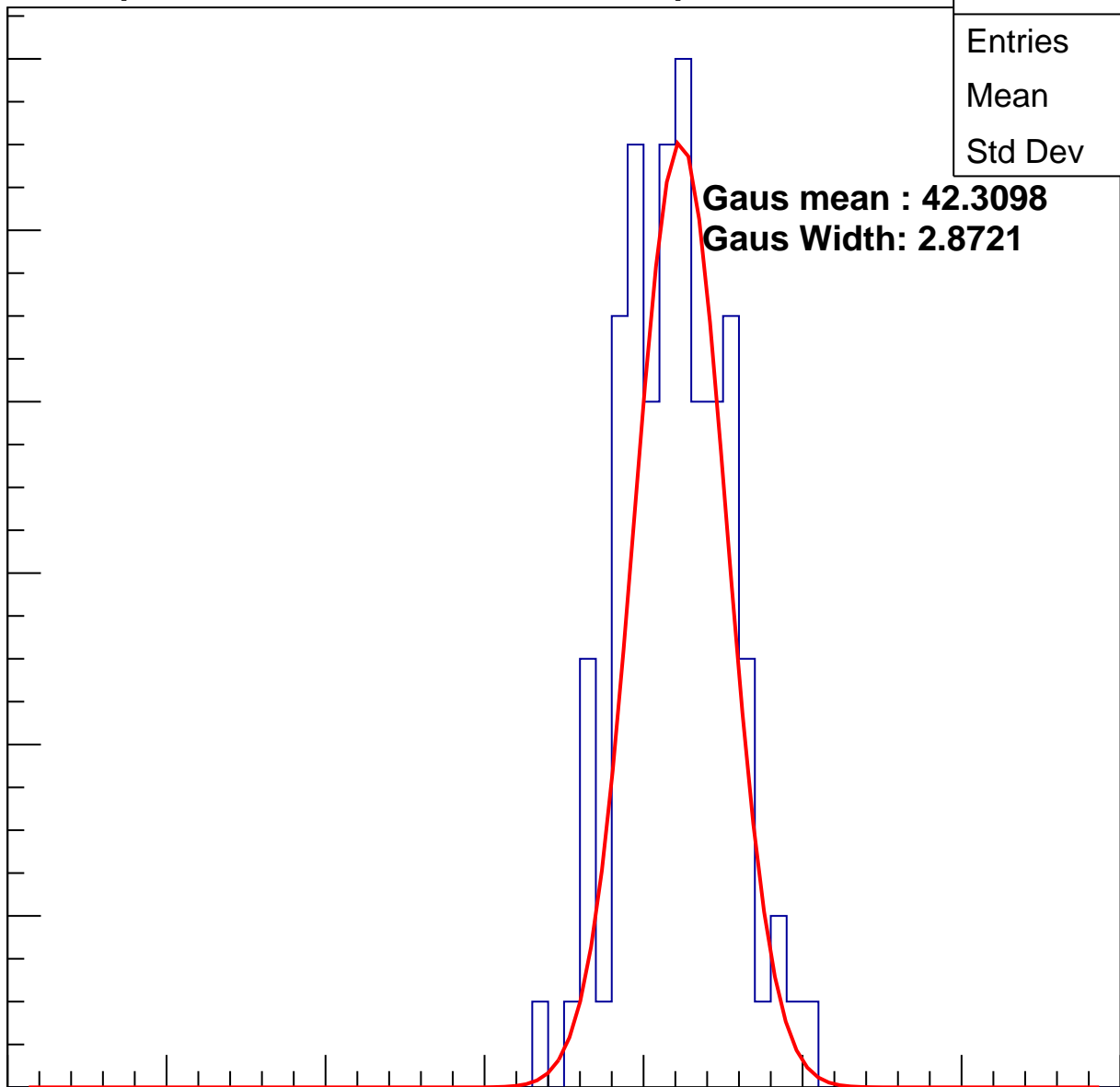
Gaus Width: 2.8721

Entry

12
10
8
6
4
2
0

ampl

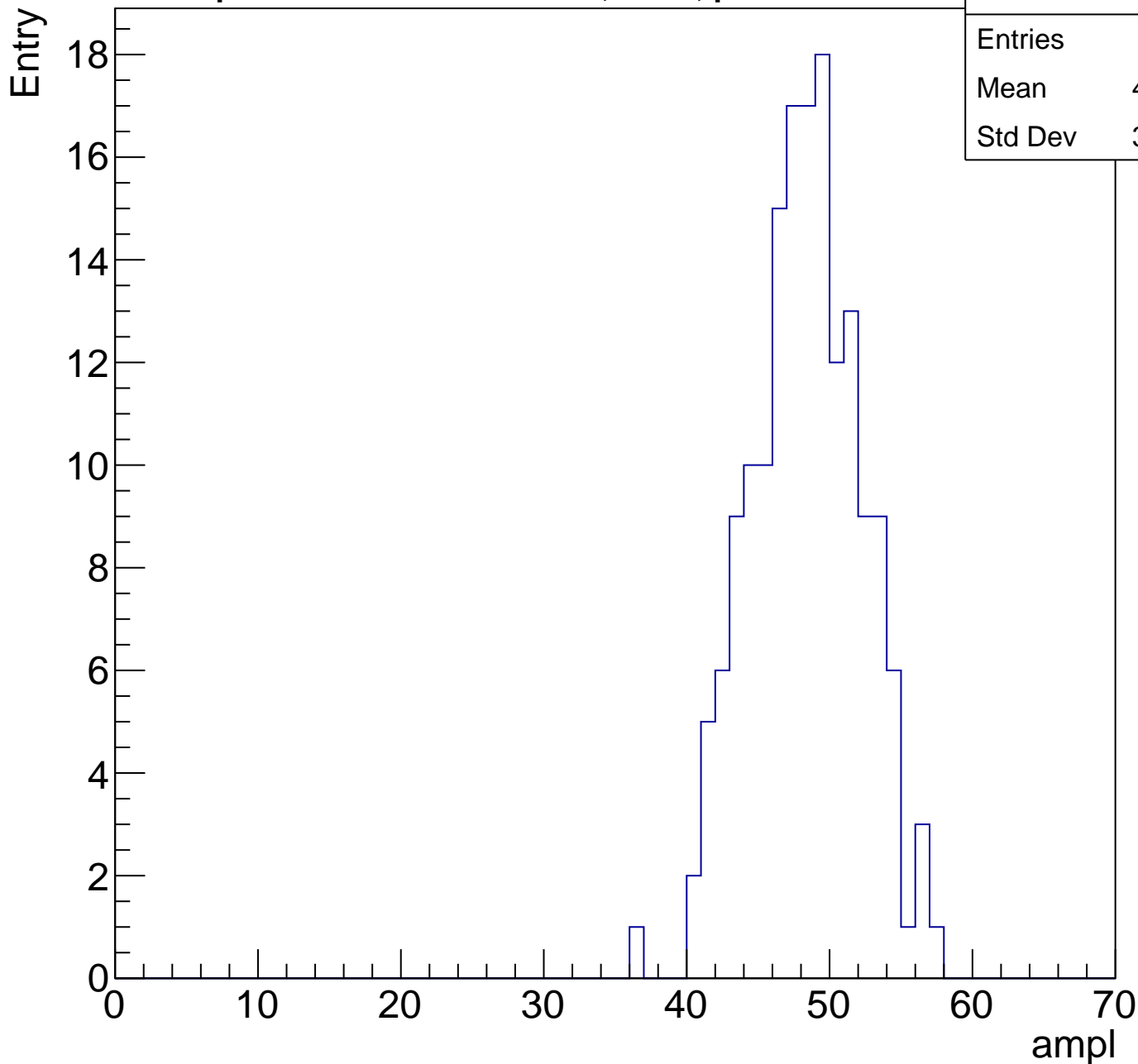
0 10 20 30 40 50 60 70



B1L001S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	164
Mean	47.86
Std Dev	3.803



B1L001S, U14-ch67, adc4

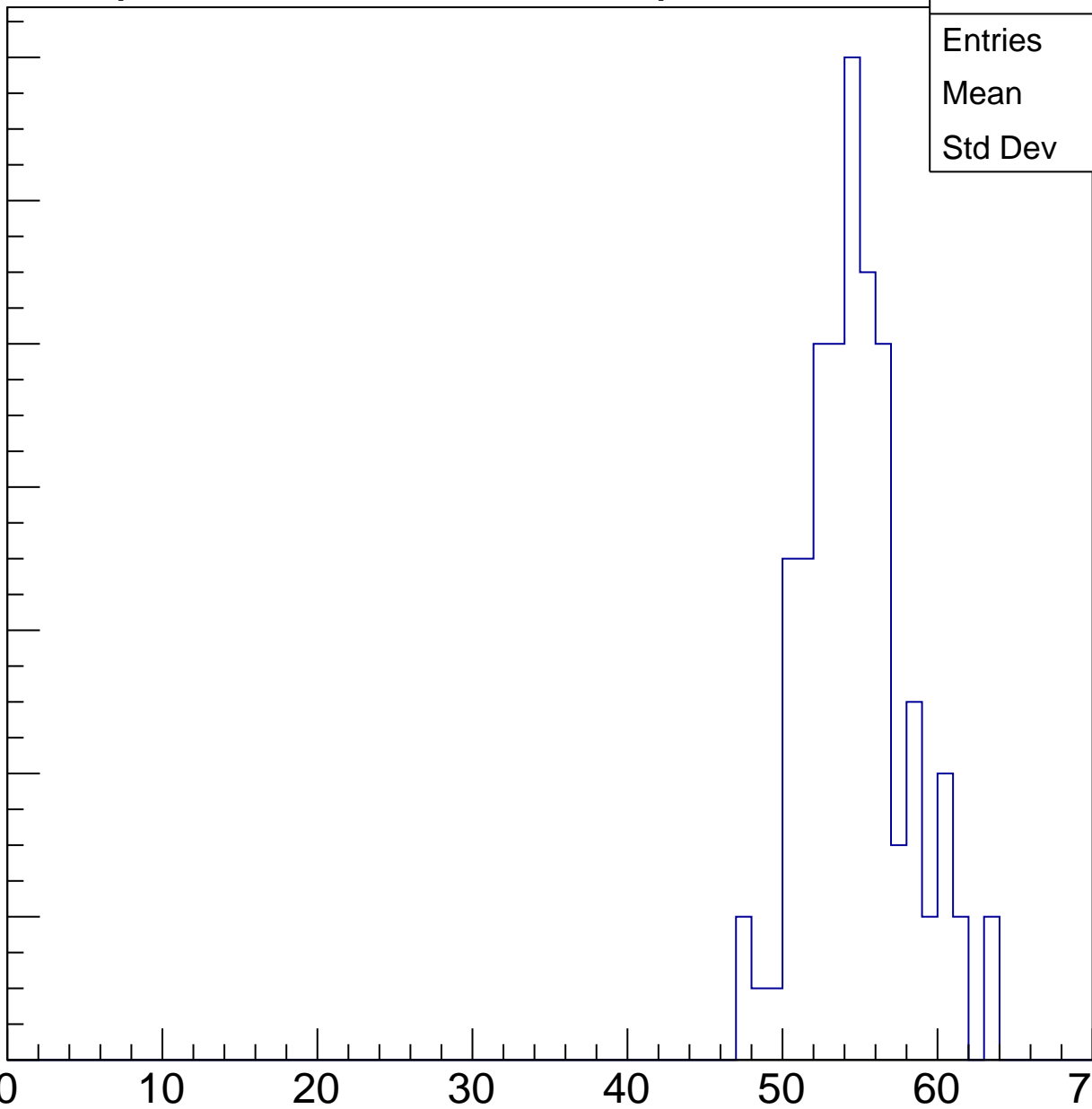
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	91
Mean	54.24
Std Dev	3.299

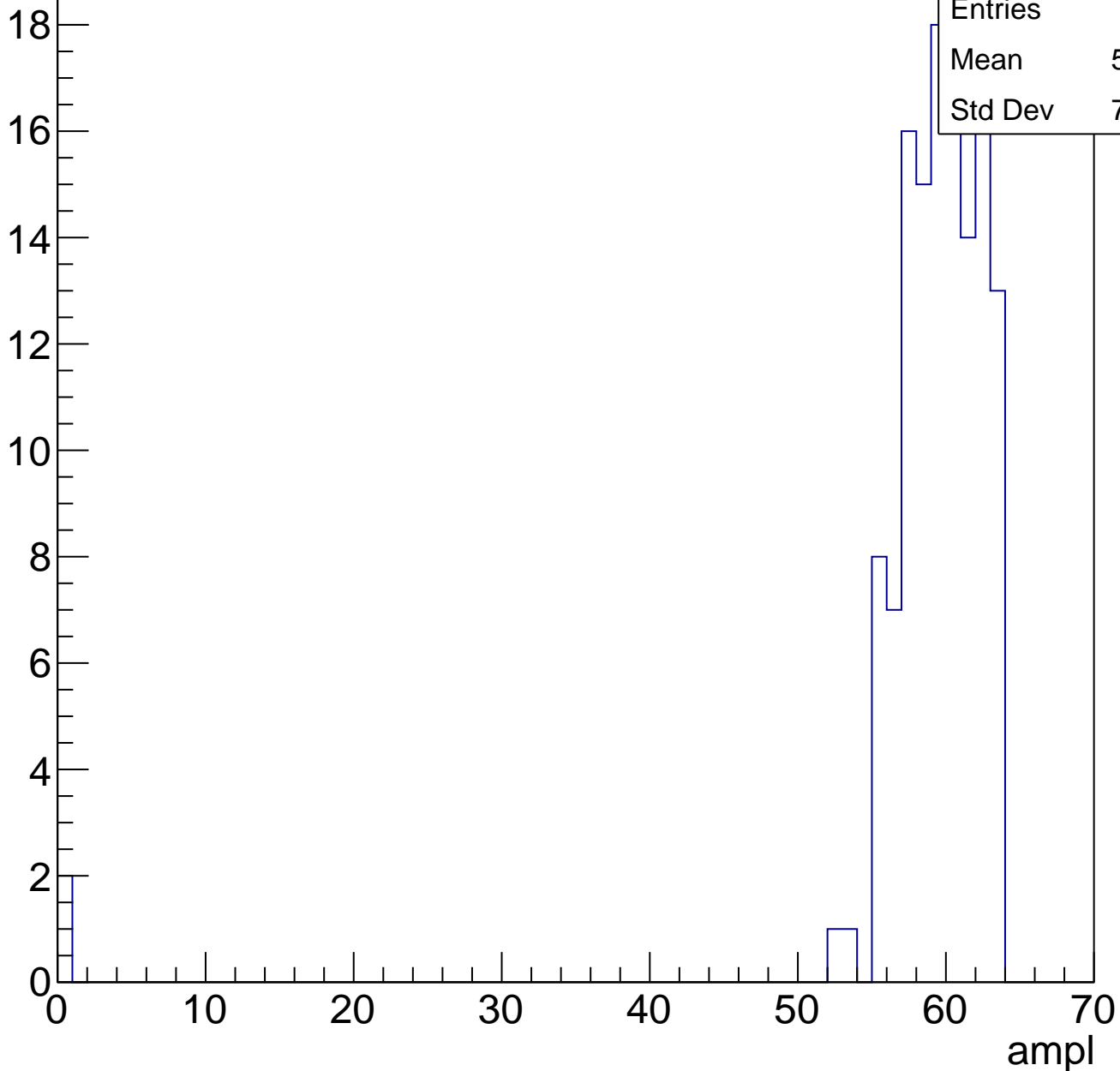
ampl



B1L001S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

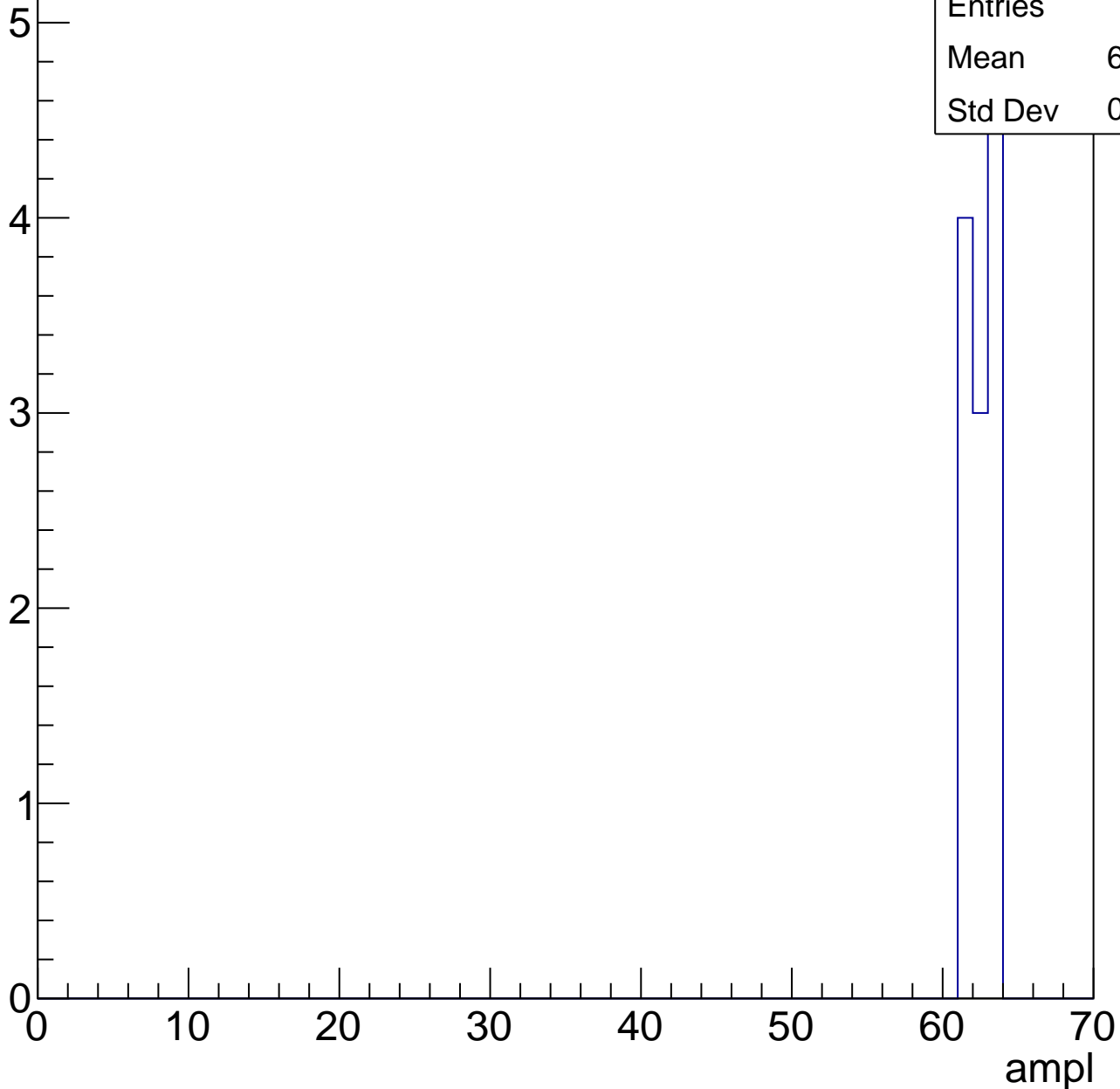


B1L001S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	12
Mean	62.08
Std Dev	0.862



B1L001S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

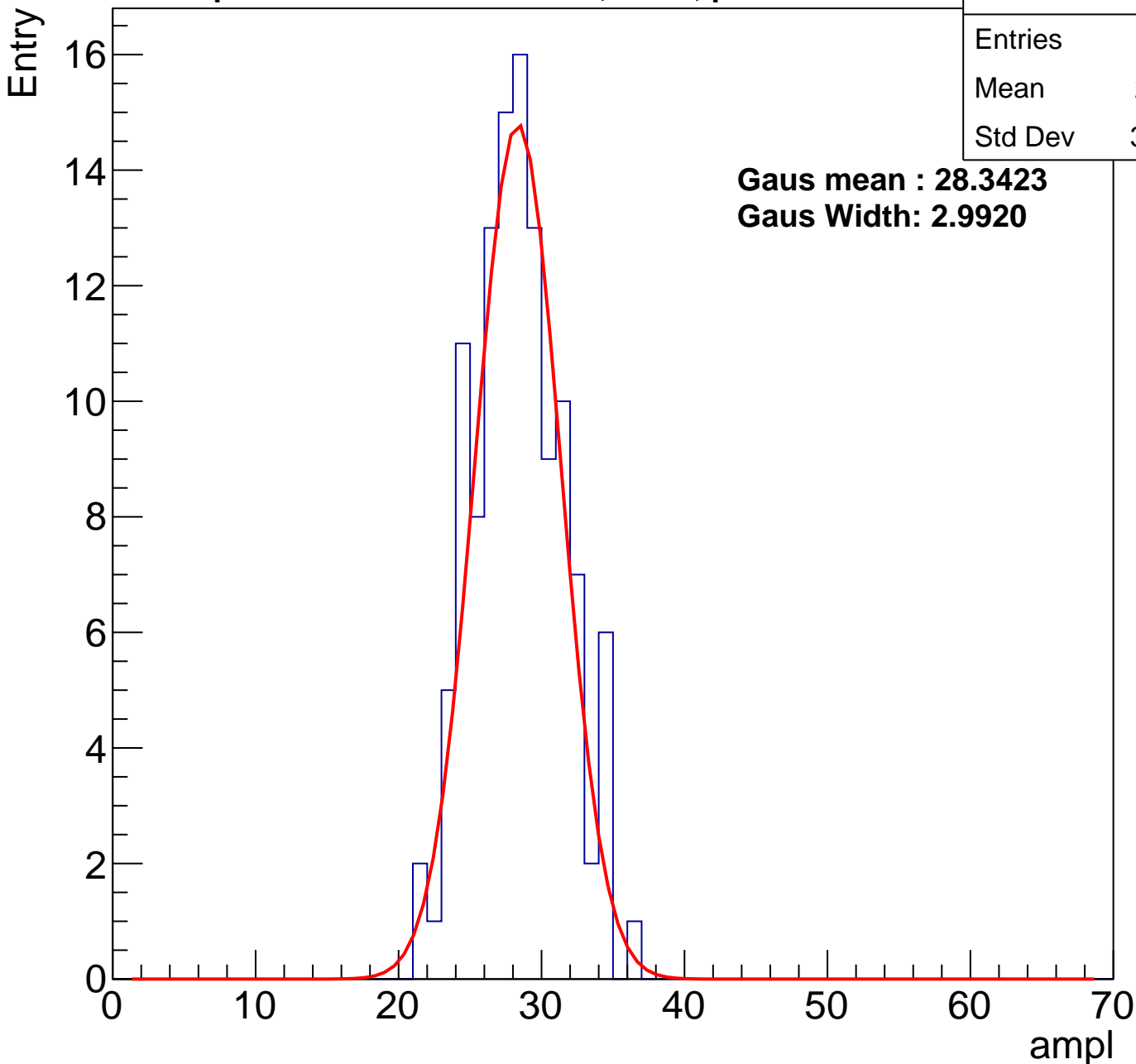
B1L001S, U14-ch68, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	27.91
Std Dev	3.114

Gaus mean : 28.3423

Gaus Width: 2.9920



B1L001S, U14-ch68, adc1

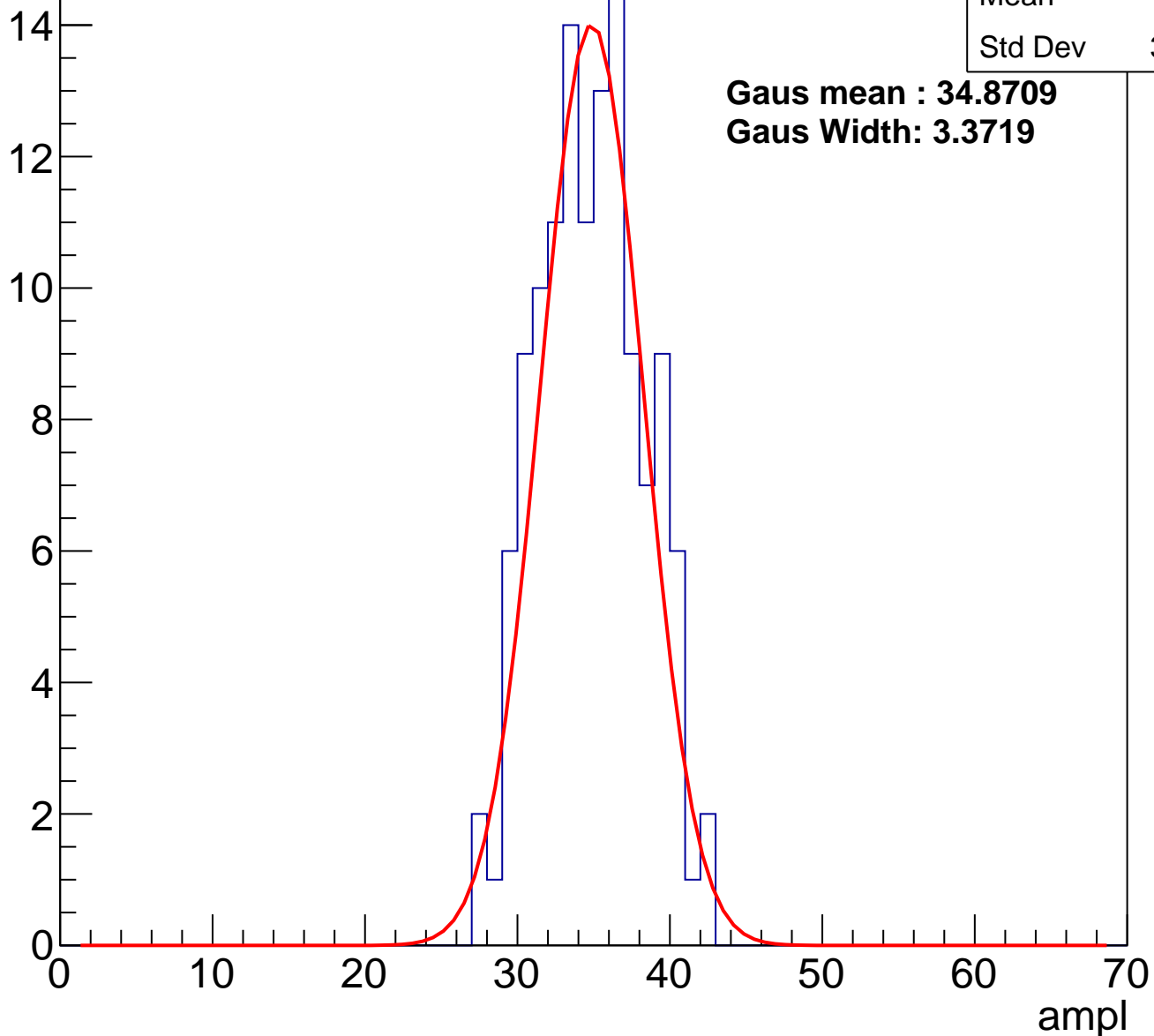
calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	34.4
Std Dev	3.381

Gaus mean : 34.8709

Gaus Width: 3.3719

Entry



B1L001S, U14-ch68, adc2

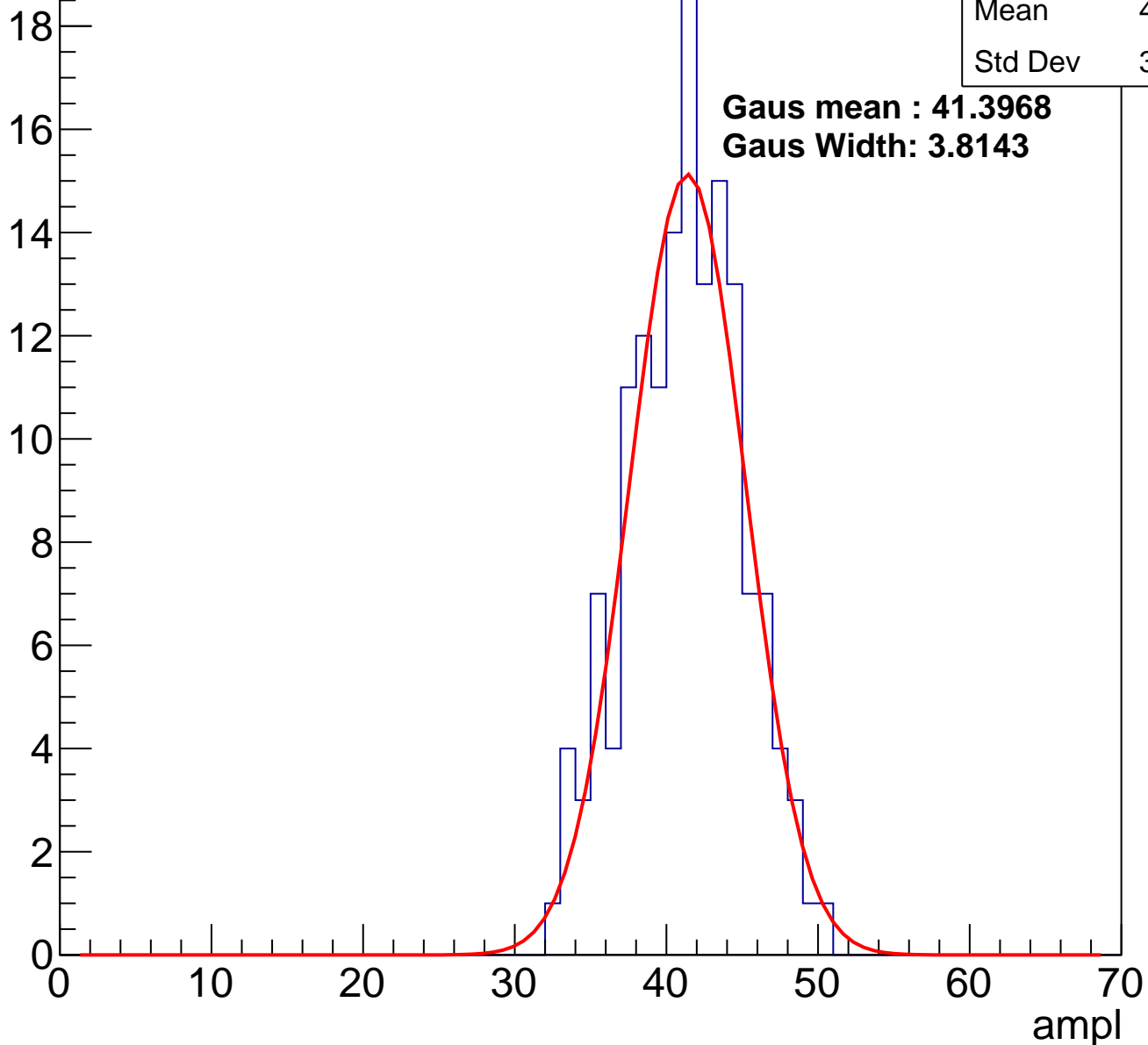
calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	40.78
Std Dev	3.727

Gaus mean : 41.3968

Gaus Width: 3.8143

Entry



B1L001S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#2, port C2

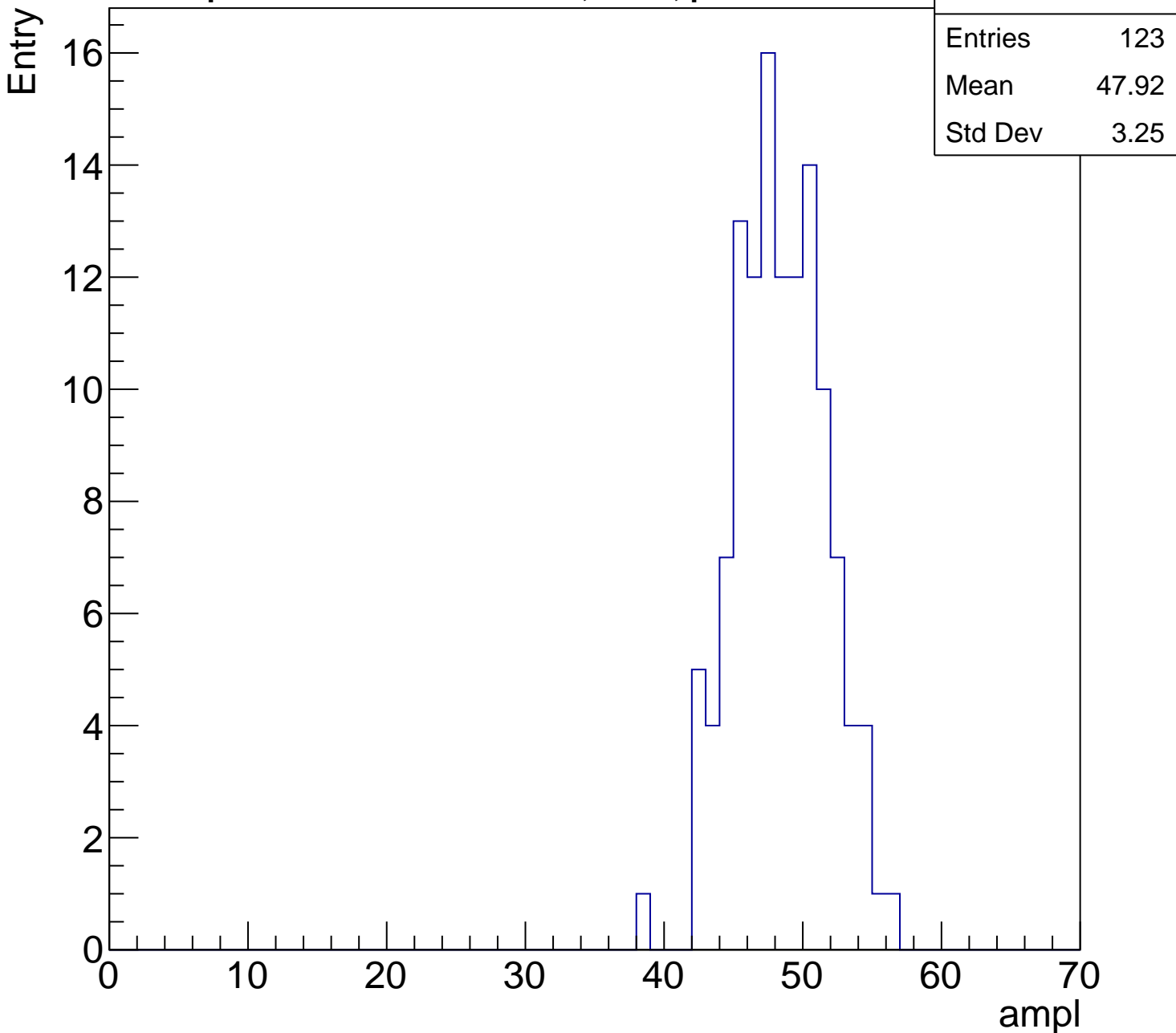
Entries	123
Mean	47.92
Std Dev	3.25

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

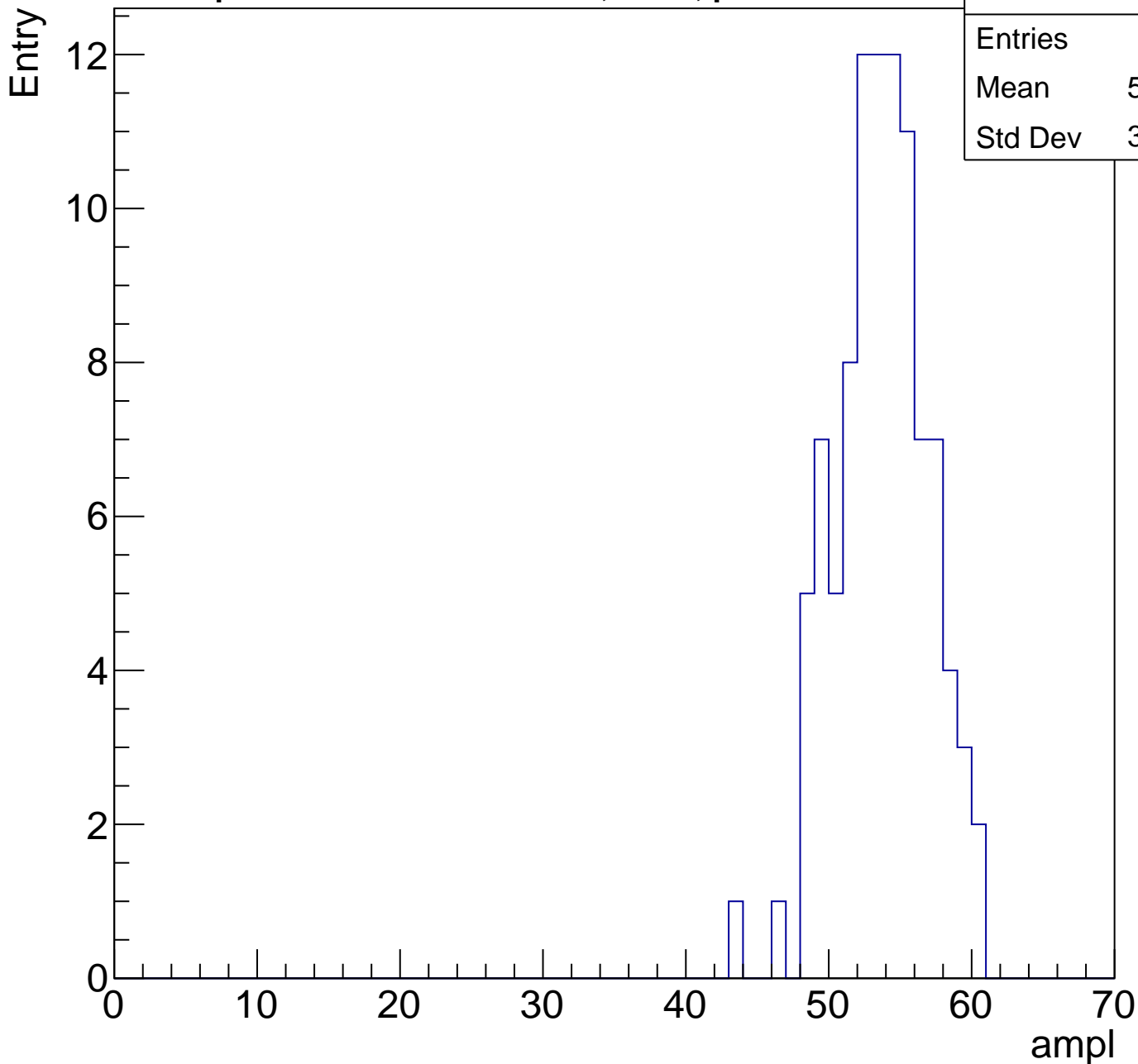
ampl



B1L001S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	97
Mean	53.23
Std Dev	3.222



B1L001S, U14-ch68, adc5

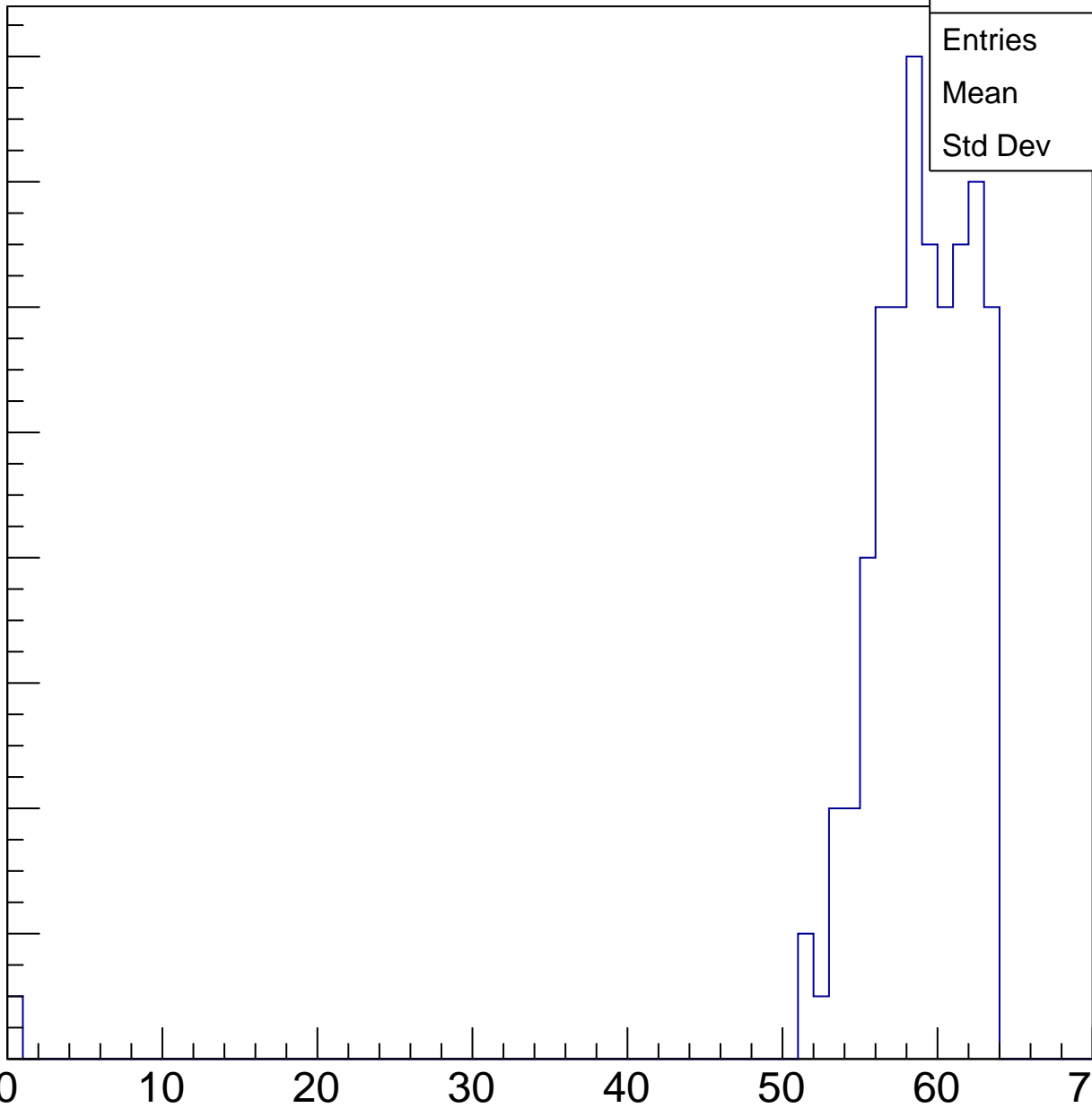
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.15
Std Dev	6.02

ampl

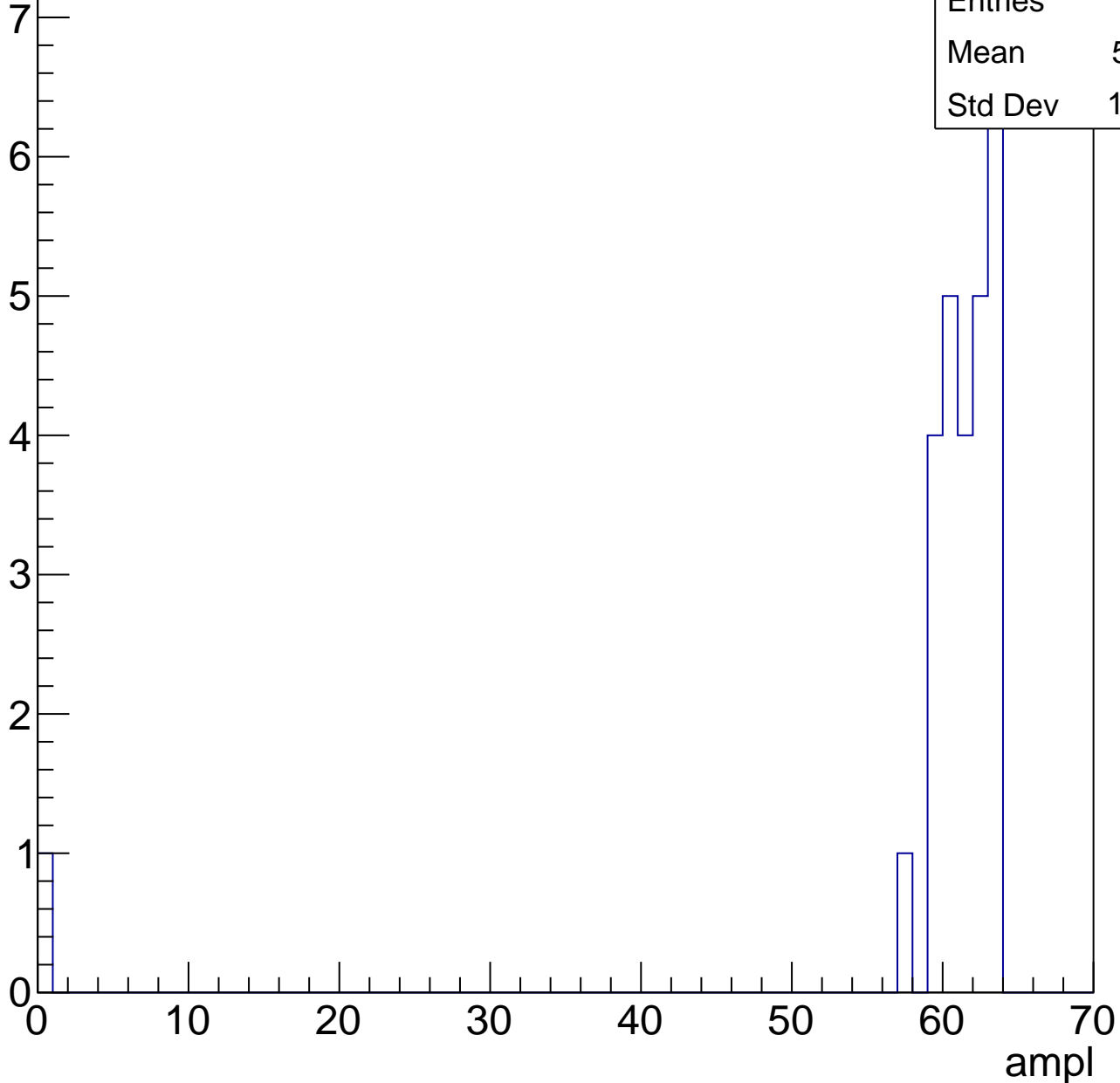


B1L001S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

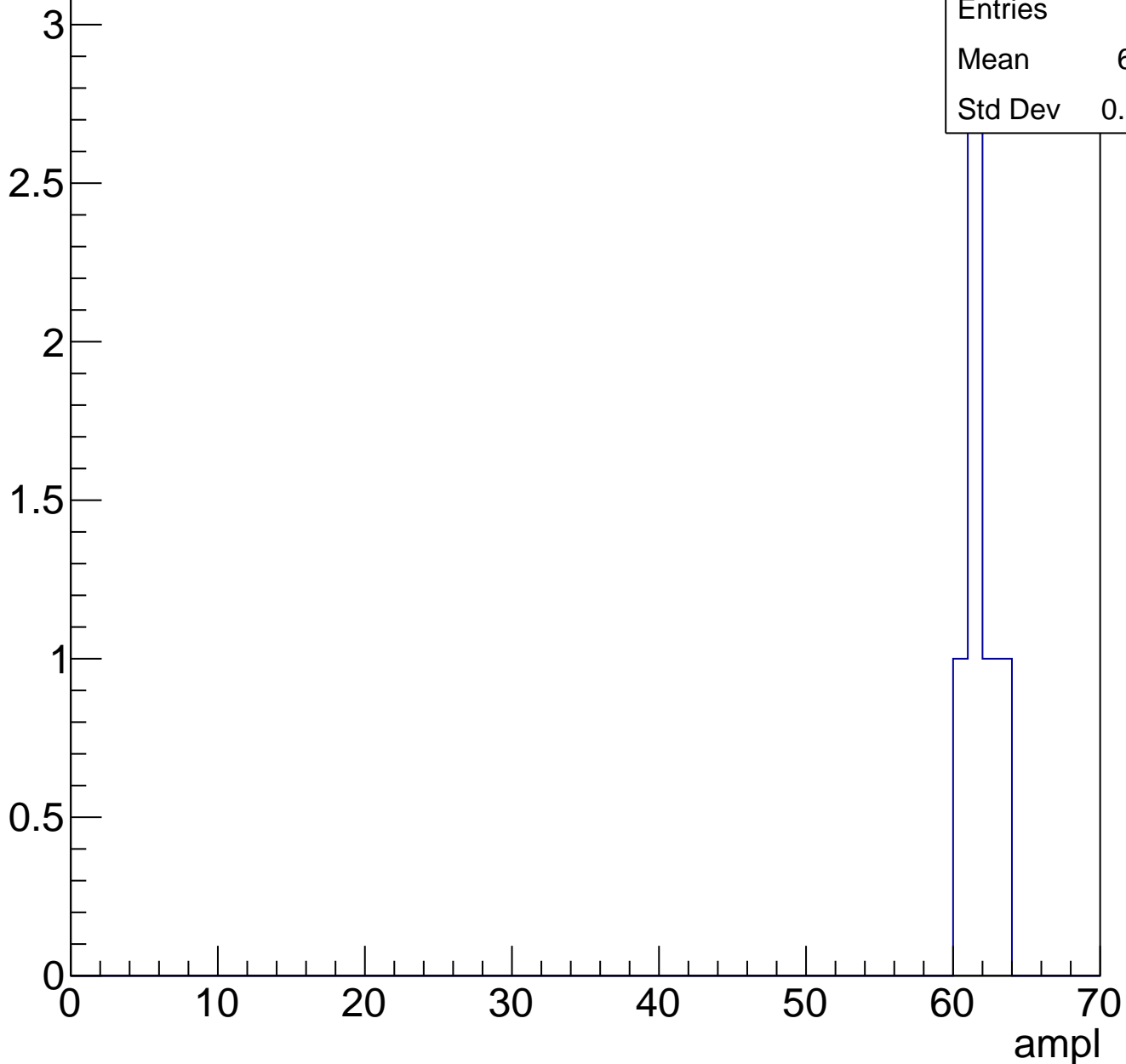
Entries	27
Mean	58.81
Std Dev	11.65



B1L001S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch69, adc0

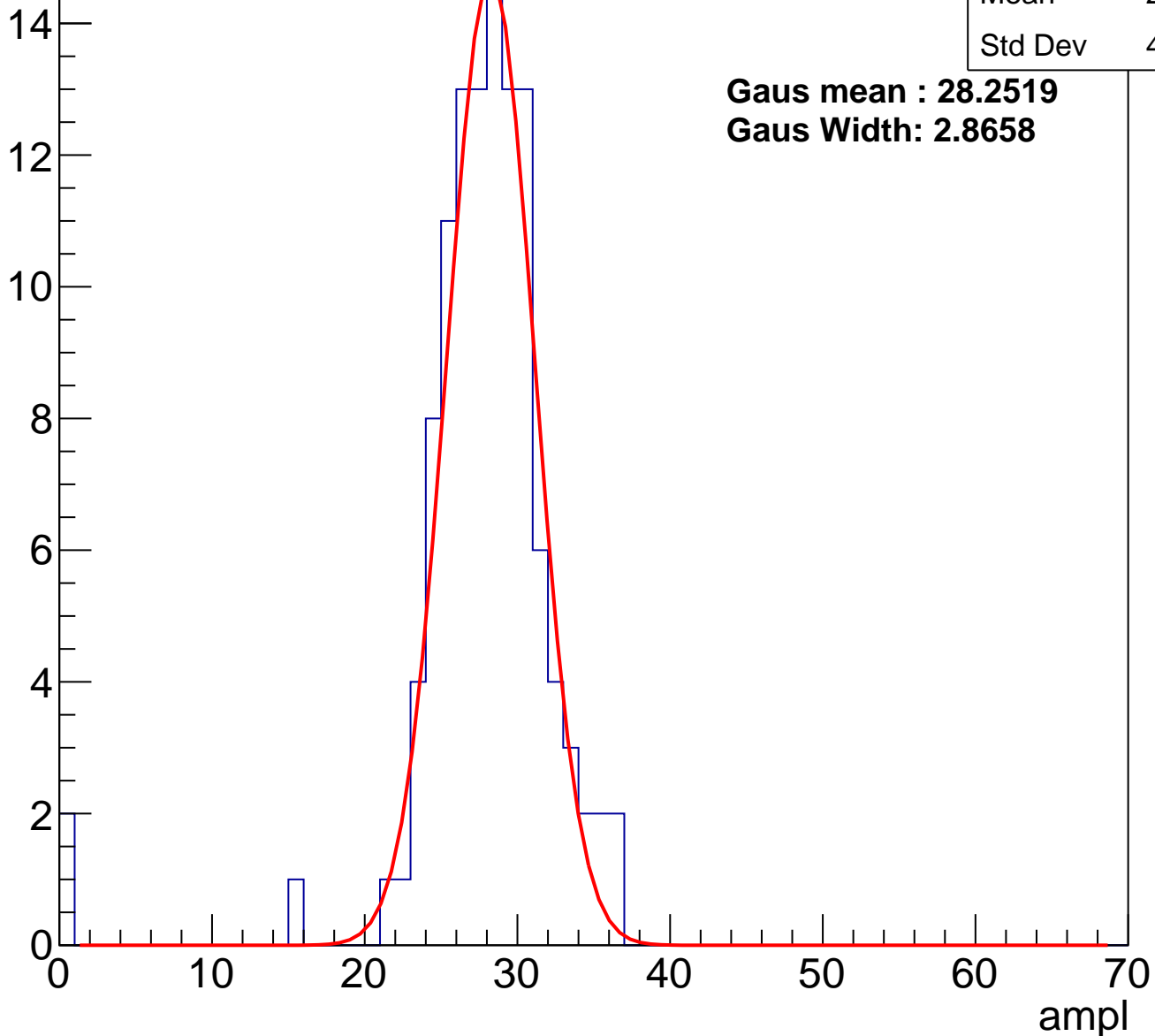
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	27.33
Std Dev	4.884

Gaus mean : 28.2519

Gaus Width: 2.8658

Entry



B1L001S, U14-ch69, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	34.18
Std Dev	3.564

Gaus mean : 34.6292

Gaus Width: 3.2766

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

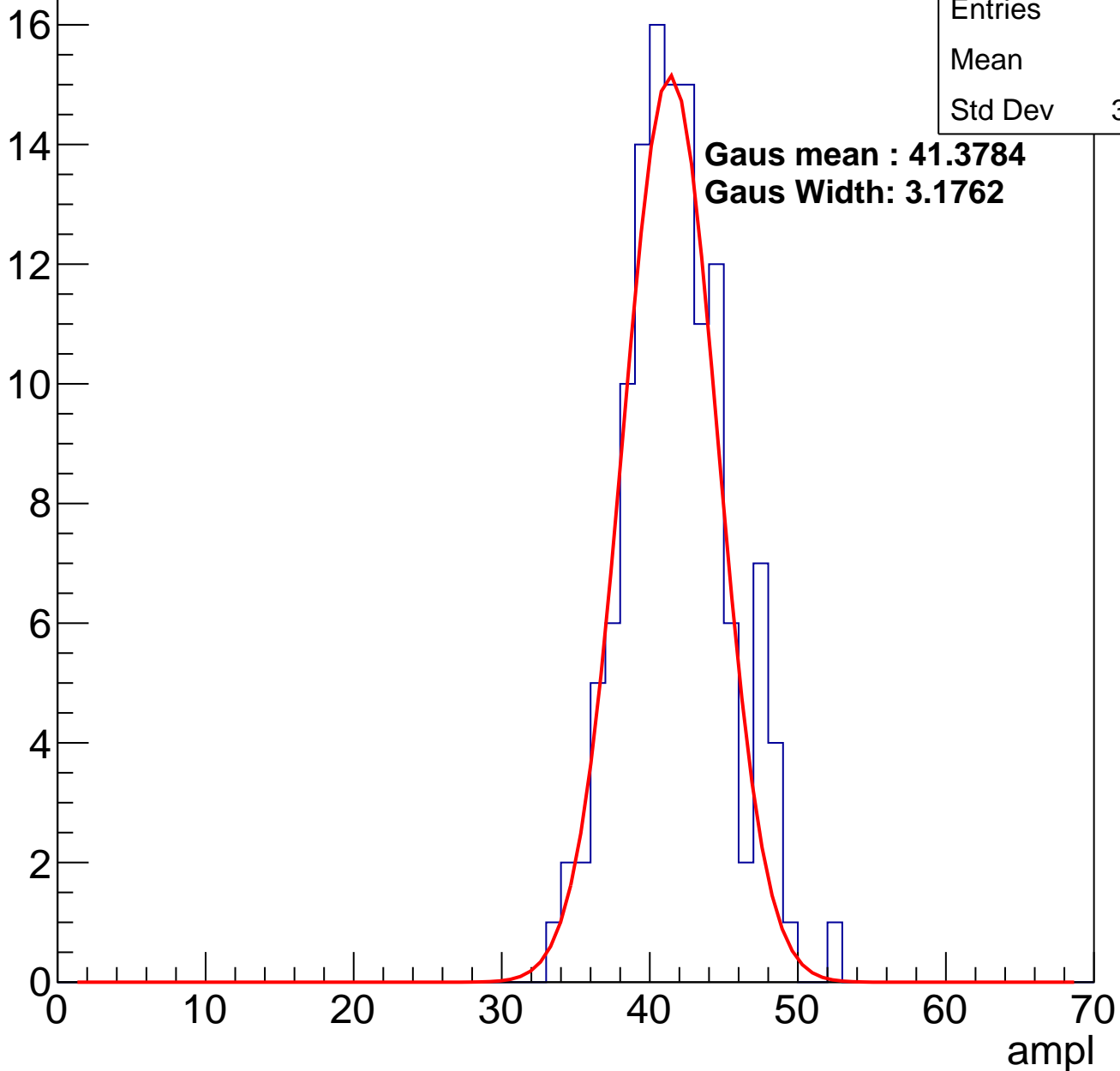
60

70

B1L001S, U14-ch69, adc2

calib_packv5_042523_0143.root, FC#2, port C2

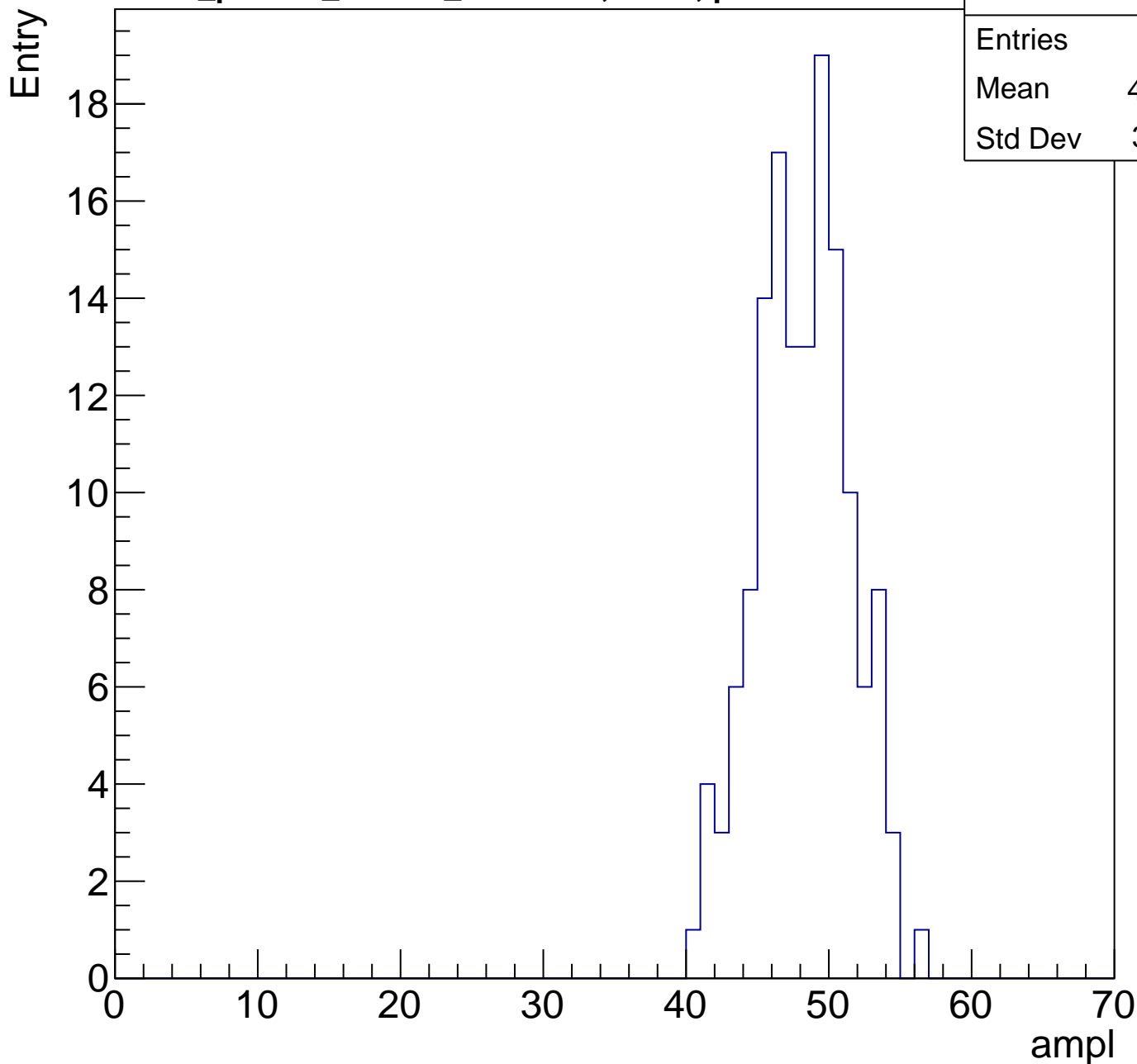
Entry



B1L001S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	47.74
Std Dev	3.241

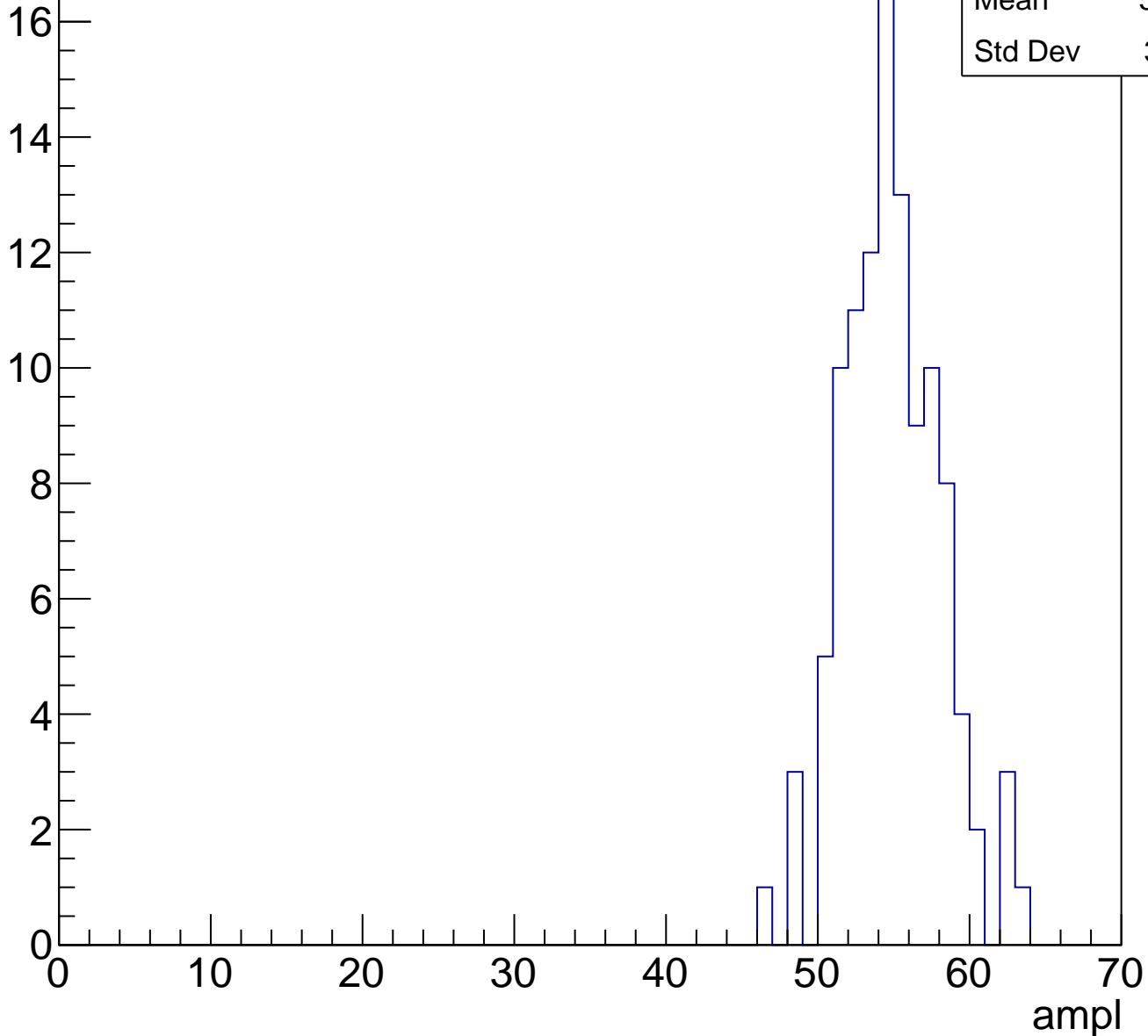


B1L001S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	109
Mean	54.44
Std Dev	3.161



B1L001S, U14-ch69, adc5

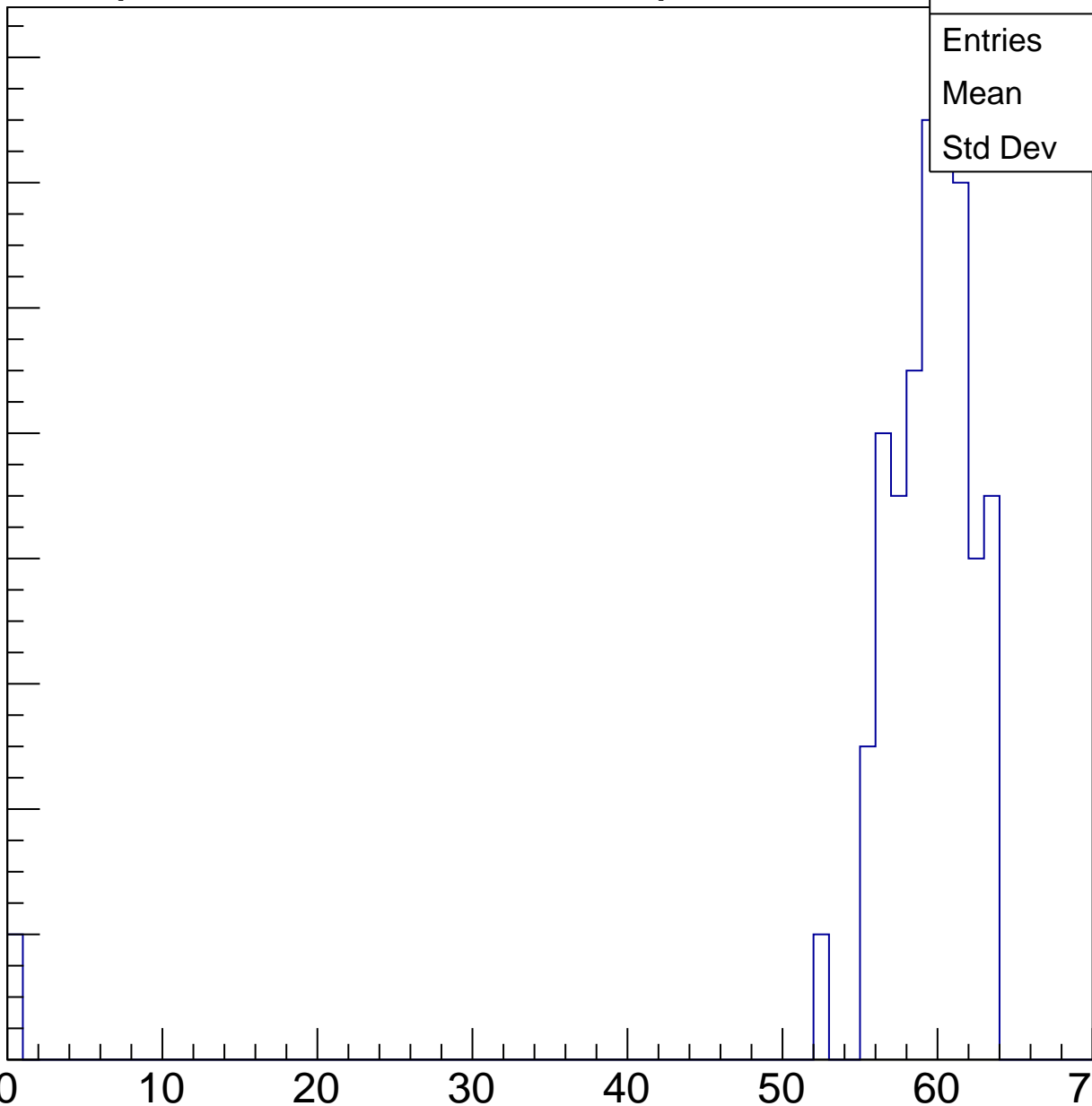
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	101
Mean	57.94
Std Dev	8.589

ampl

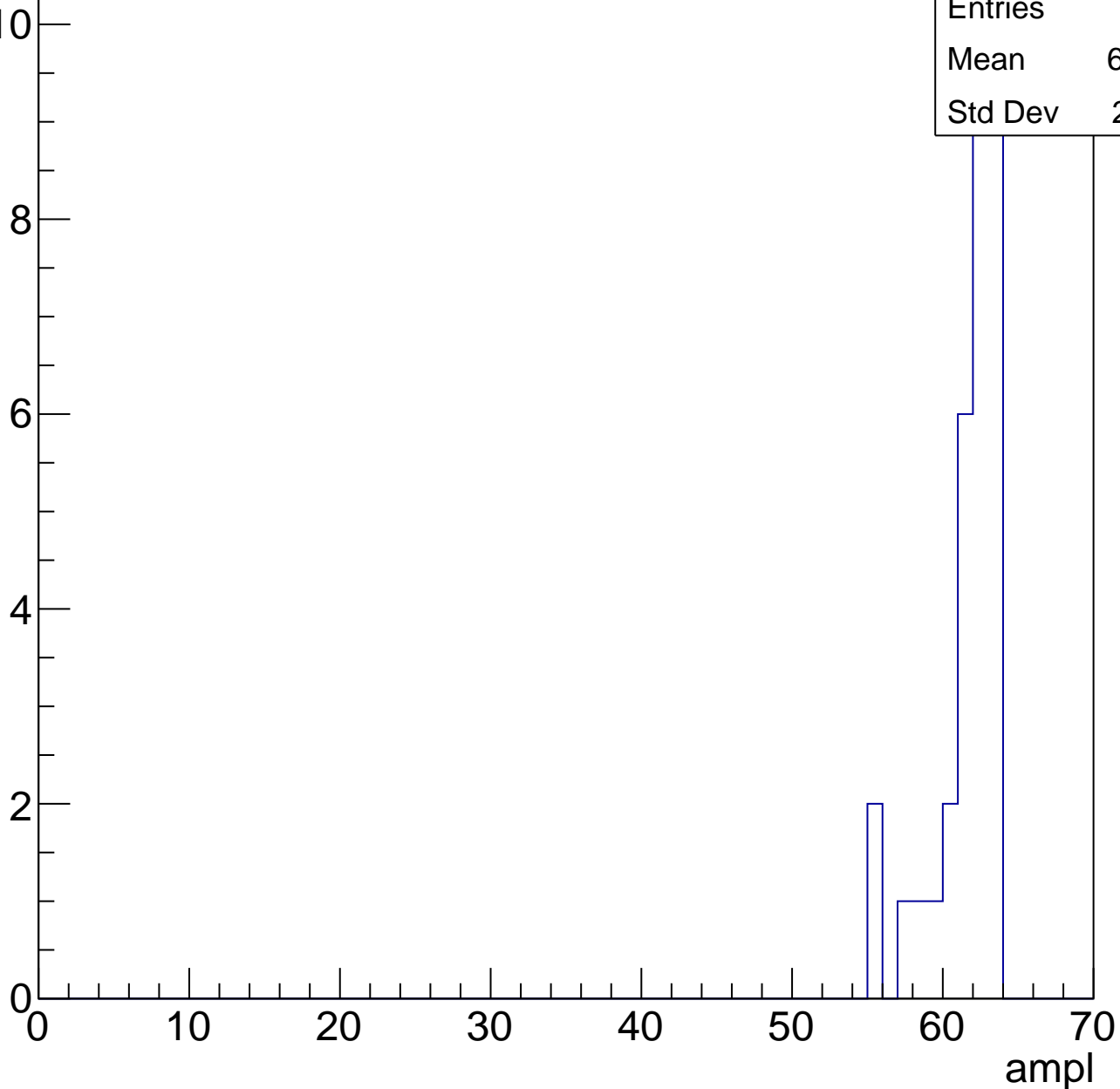


B1L001S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	32
Mean	61.19
Std Dev	2.171



B1L001S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

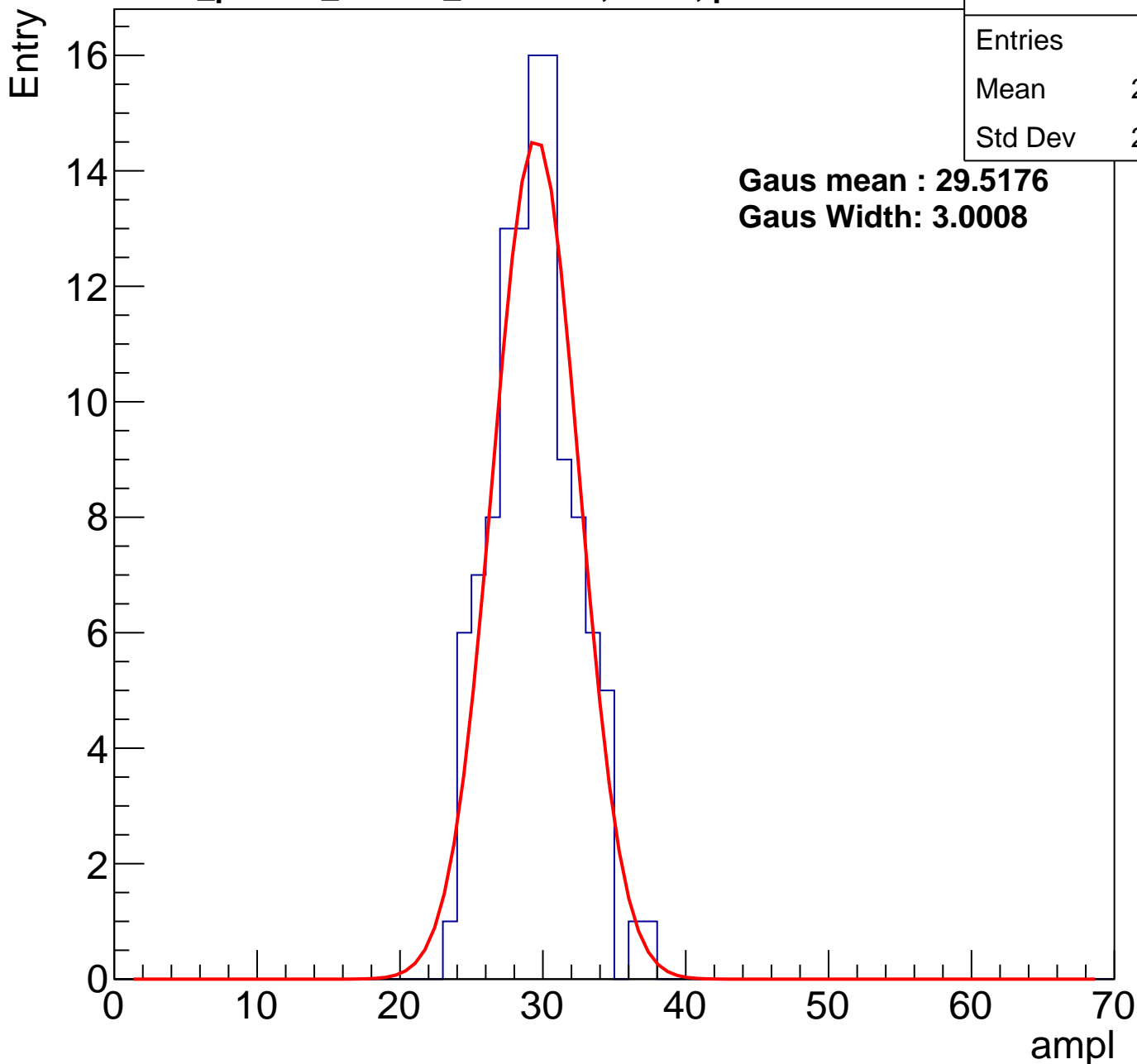
B1L001S, U14-ch70, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	28.95
Std Dev	2.849

Gaus mean : 29.5176

Gaus Width: 3.0008



B1L001S, U14-ch70, adc1

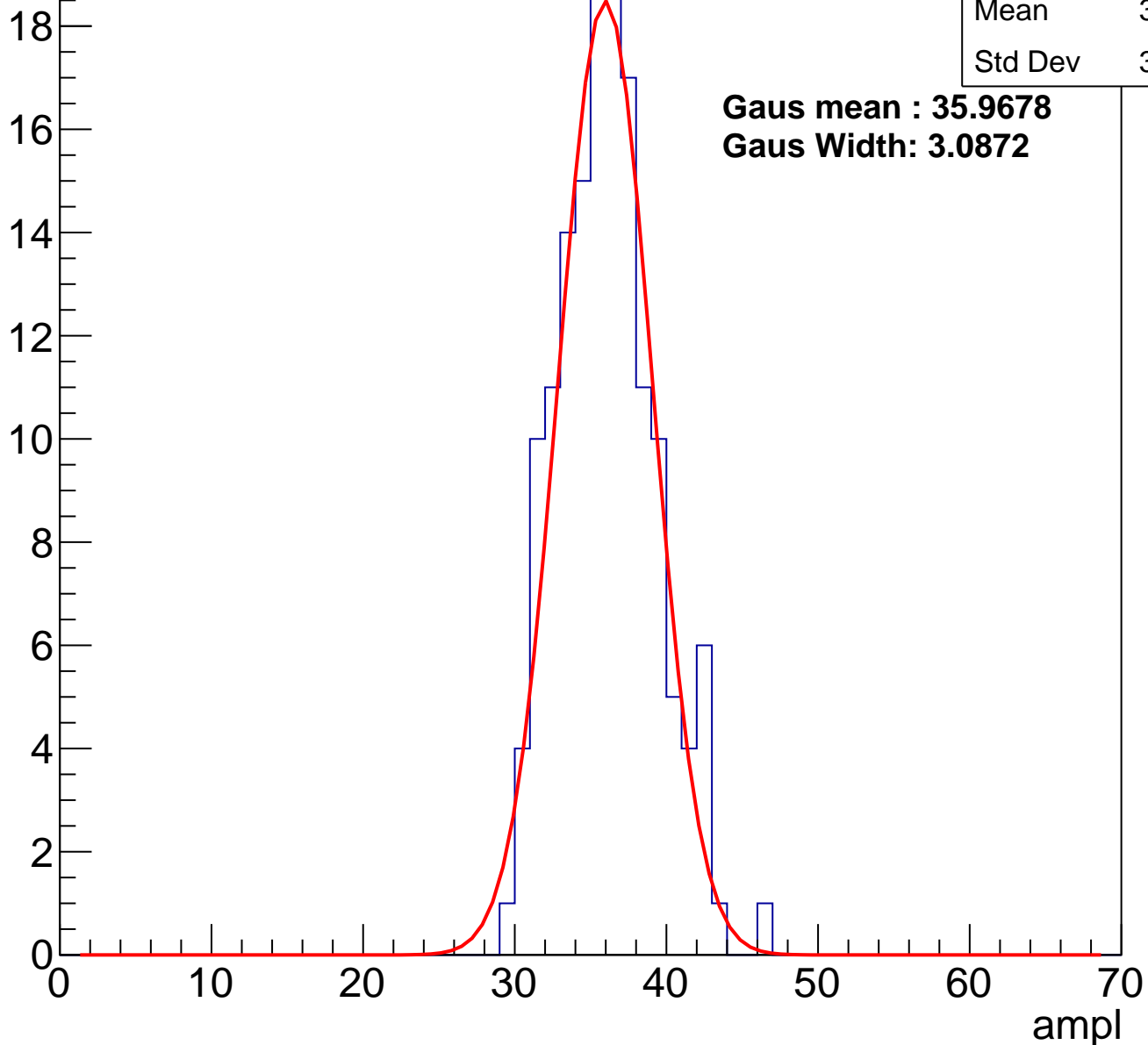
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.64
Std Dev	3.184

Gaus mean : 35.9678

Gaus Width: 3.0872

Entry



B1L001S, U14-ch70, adc2

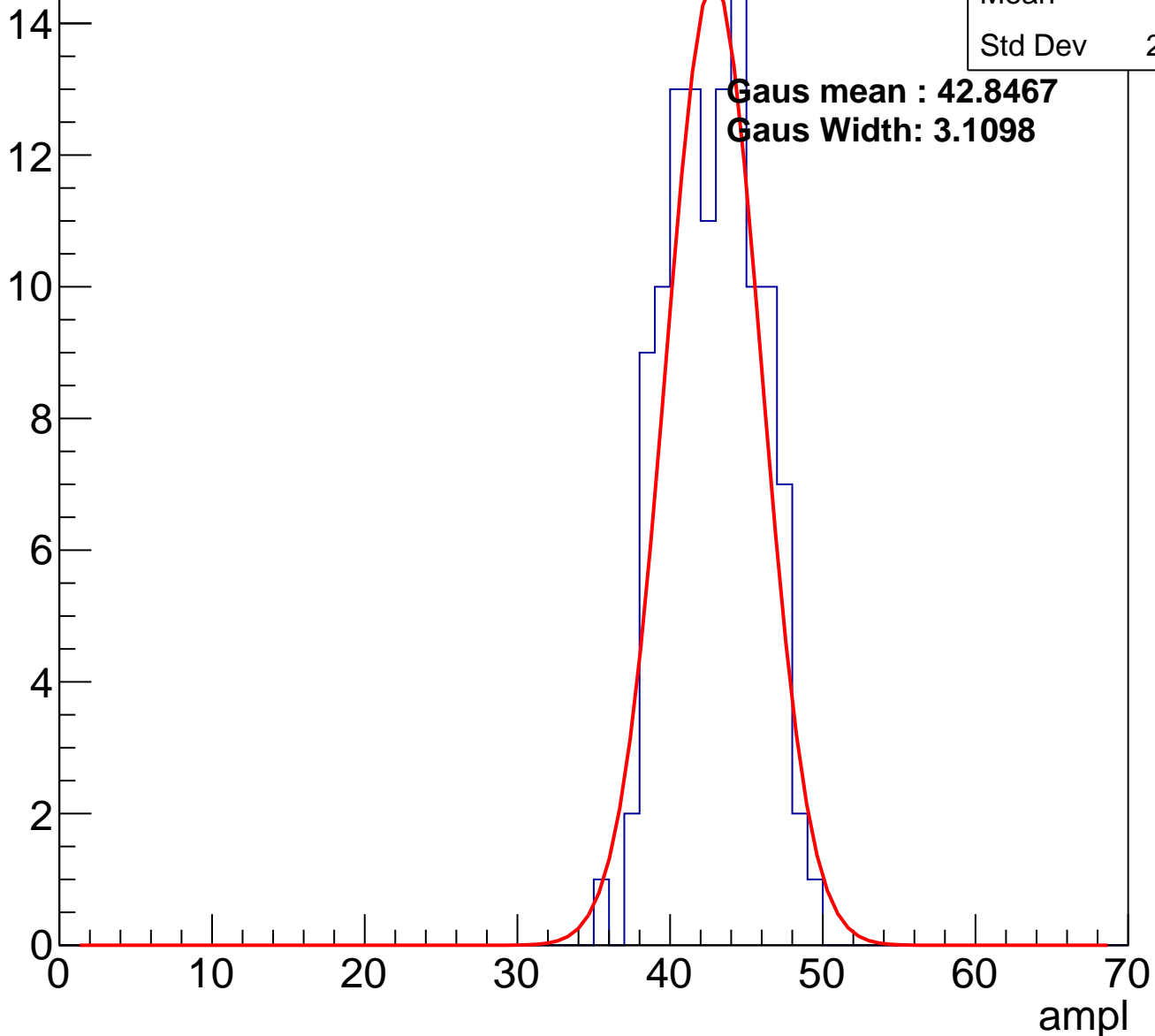
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	42.38
Std Dev	2.923

Gaus mean : 42.8467

Gaus Width: 3.1098

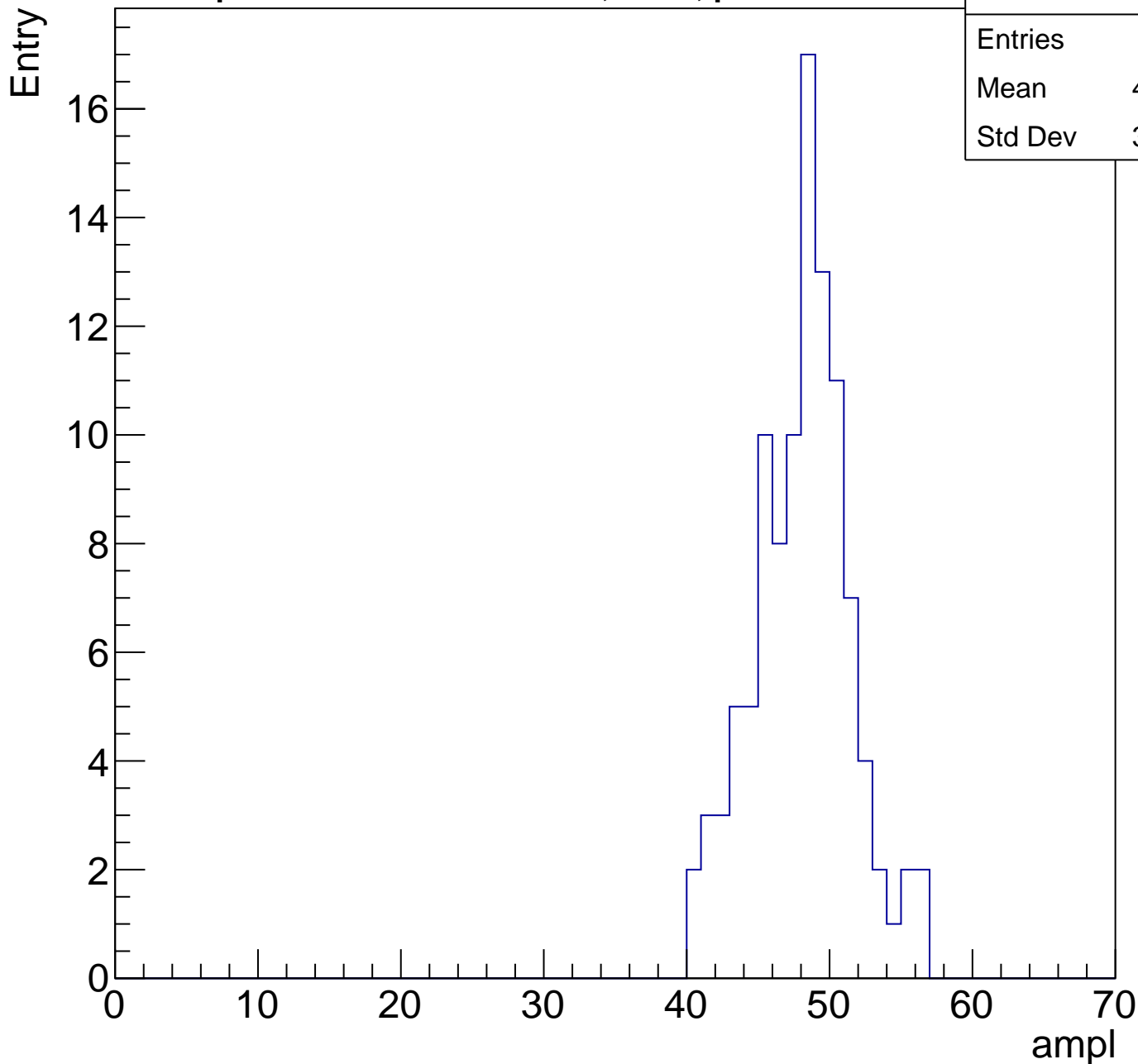
Entry



B1L001S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	47.64
Std Dev	3.384

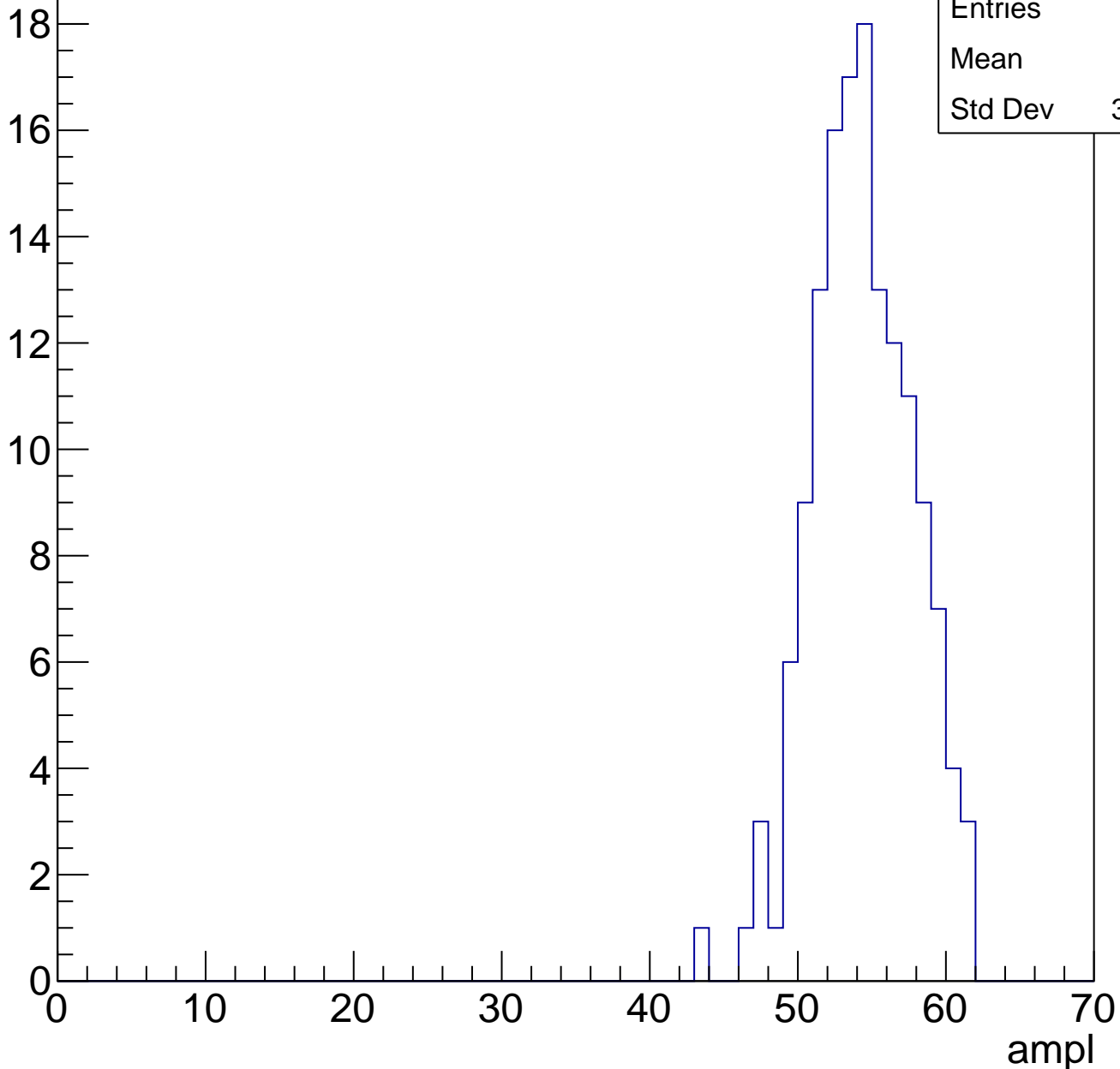


B1L001S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	53.9
Std Dev	3.363

Entry



B1L001S, U14-ch70, adc5

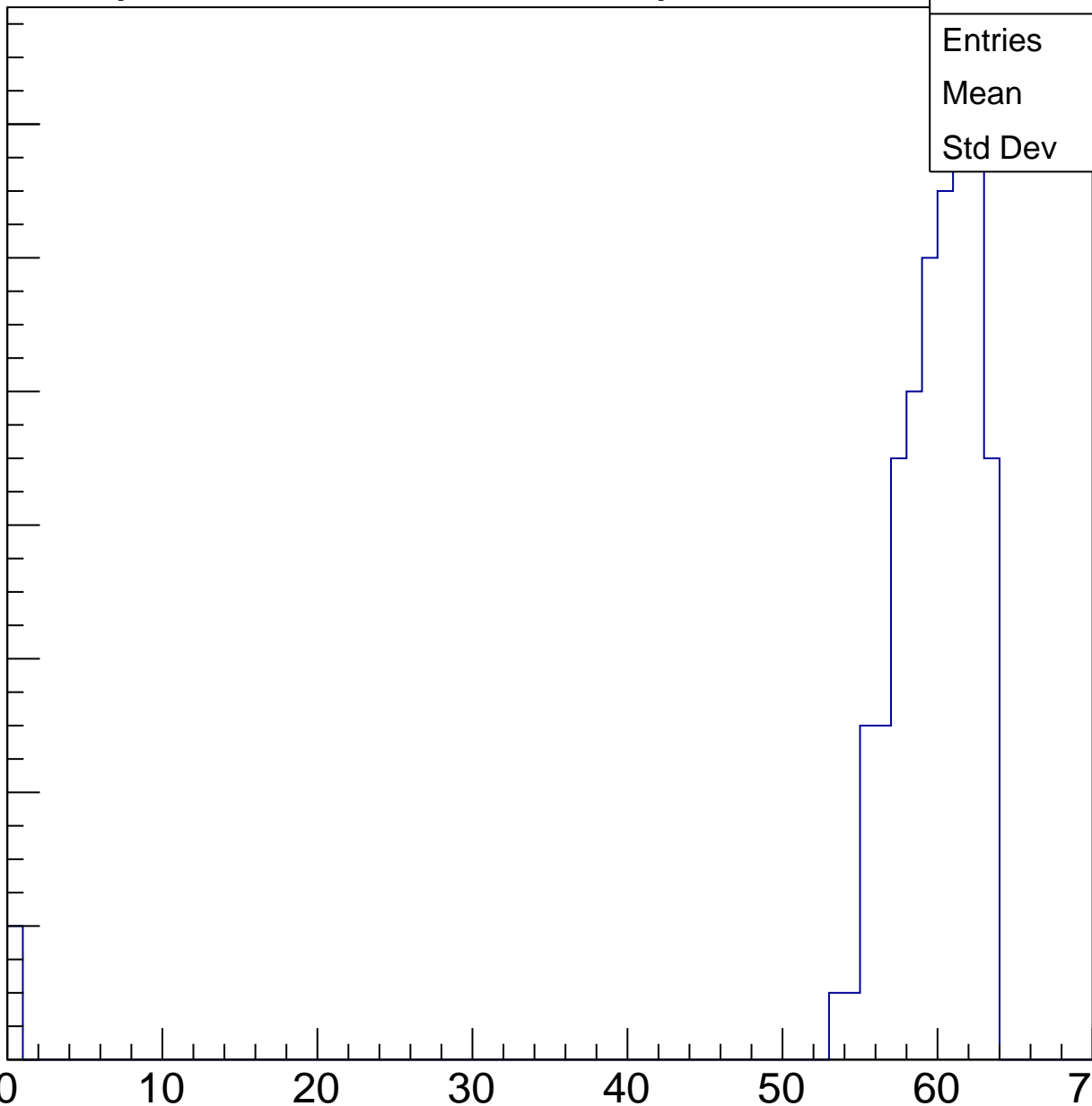
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	58.3
Std Dev	8.792

ampl



B1L001S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

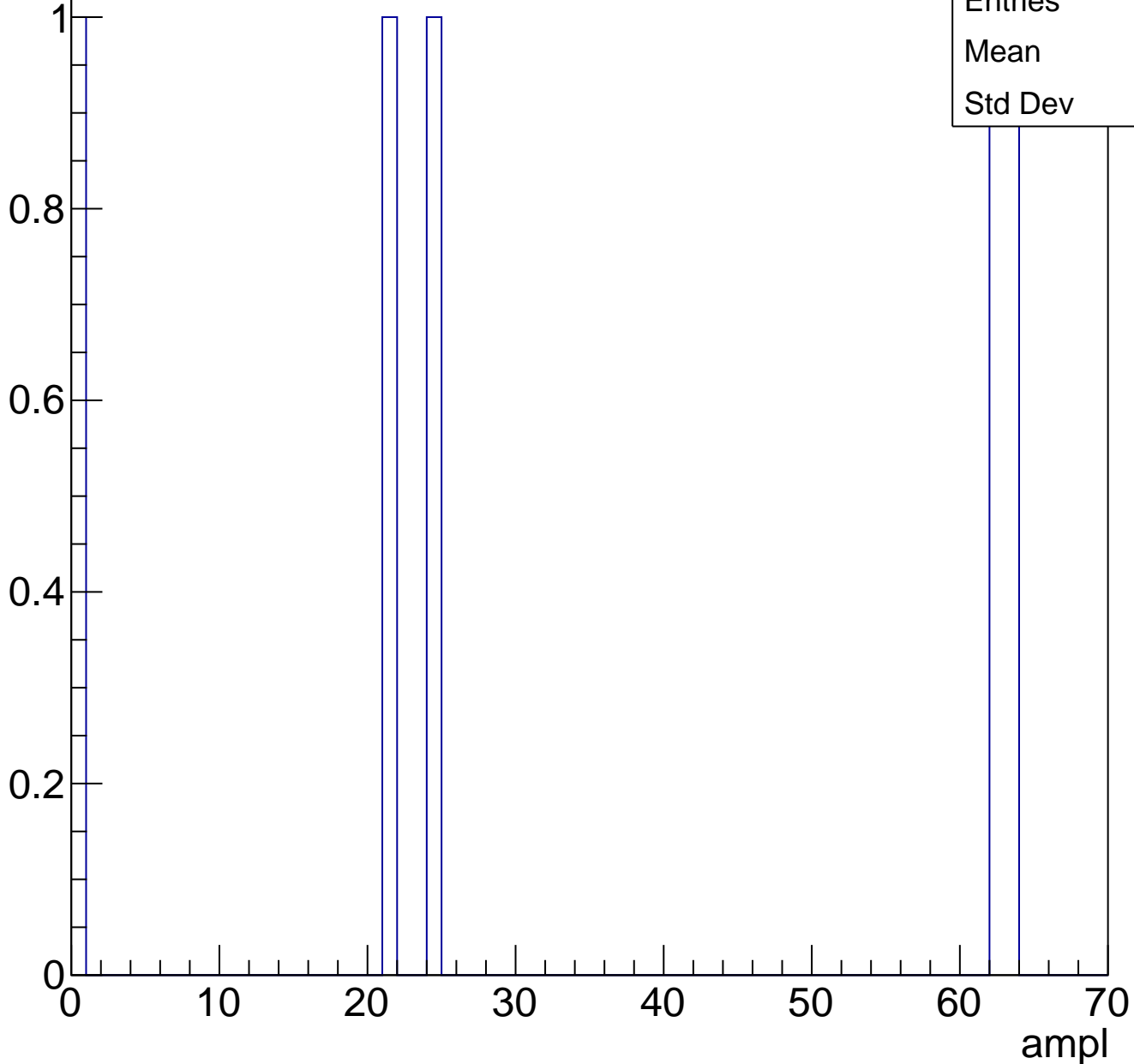
ampl

Entries	22
Mean	61.5
Std Dev	1.901

B1L001S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch71, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	150
Mean	28.11
Std Dev	3.647

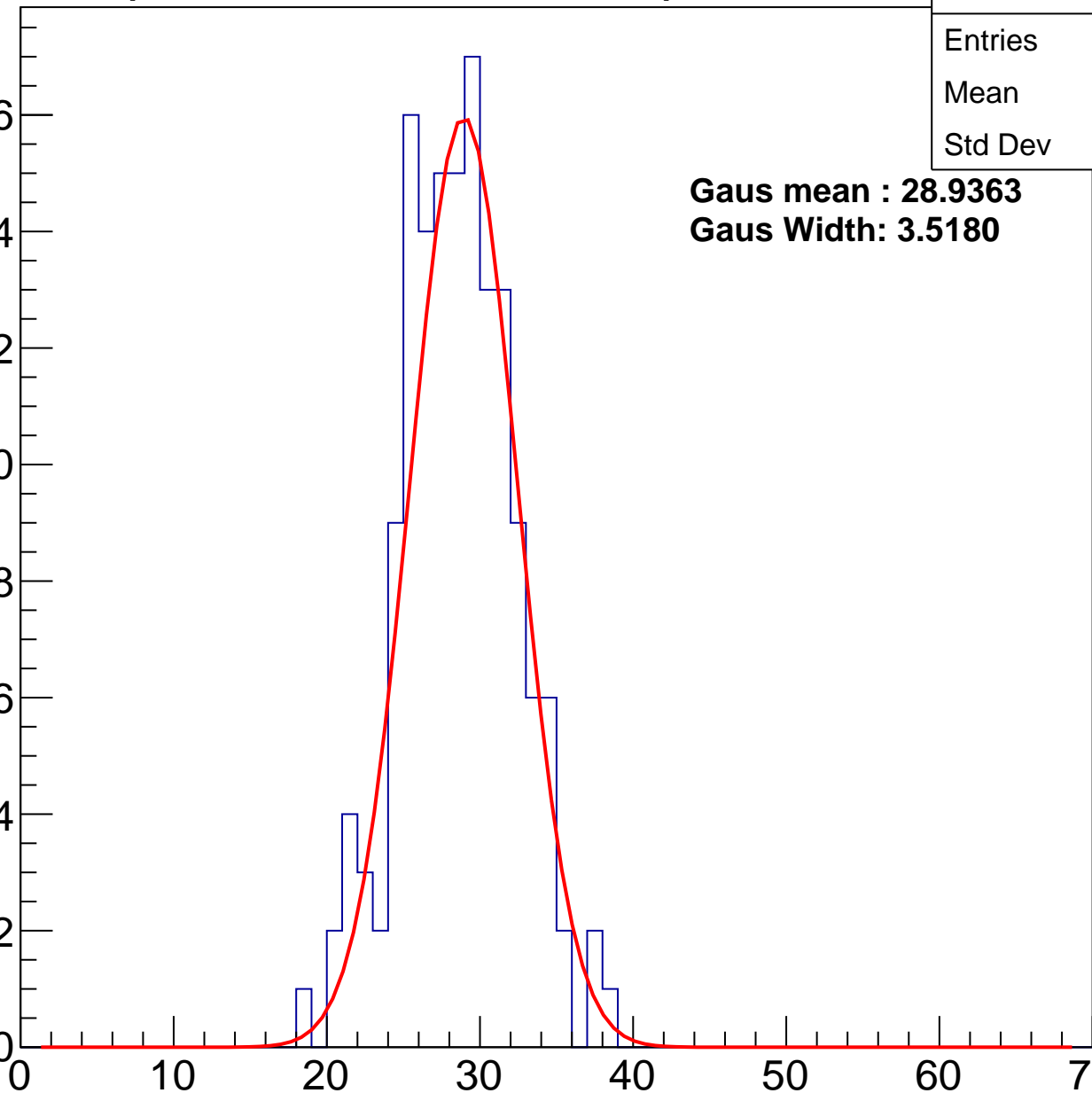
Gaus mean : 28.9363

Gaus Width: 3.5180

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch71, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	35.53
Std Dev	3.576

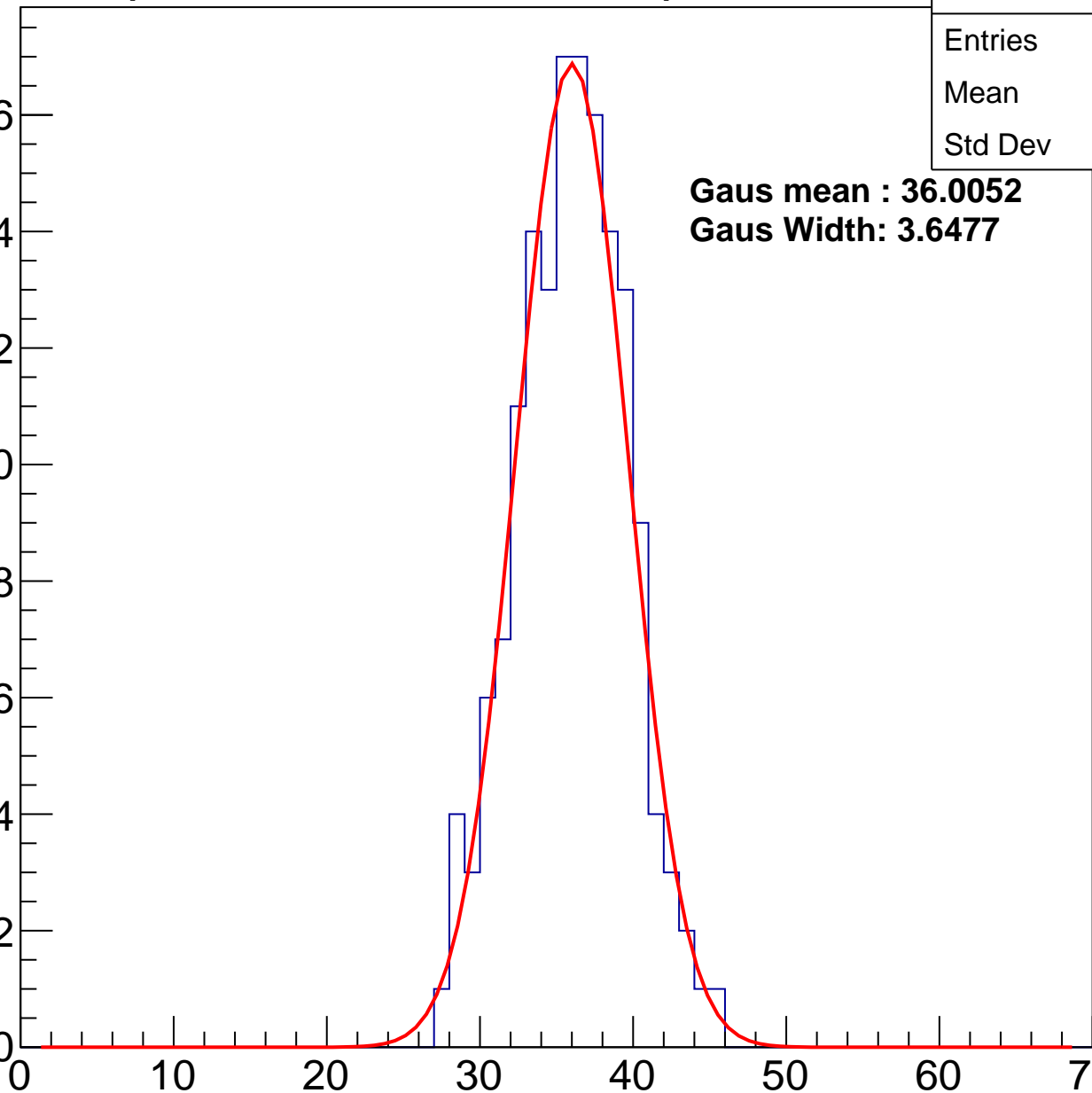
Gaus mean : 36.0052

Gaus Width: 3.6477

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch71, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	42.72
Std Dev	3.17

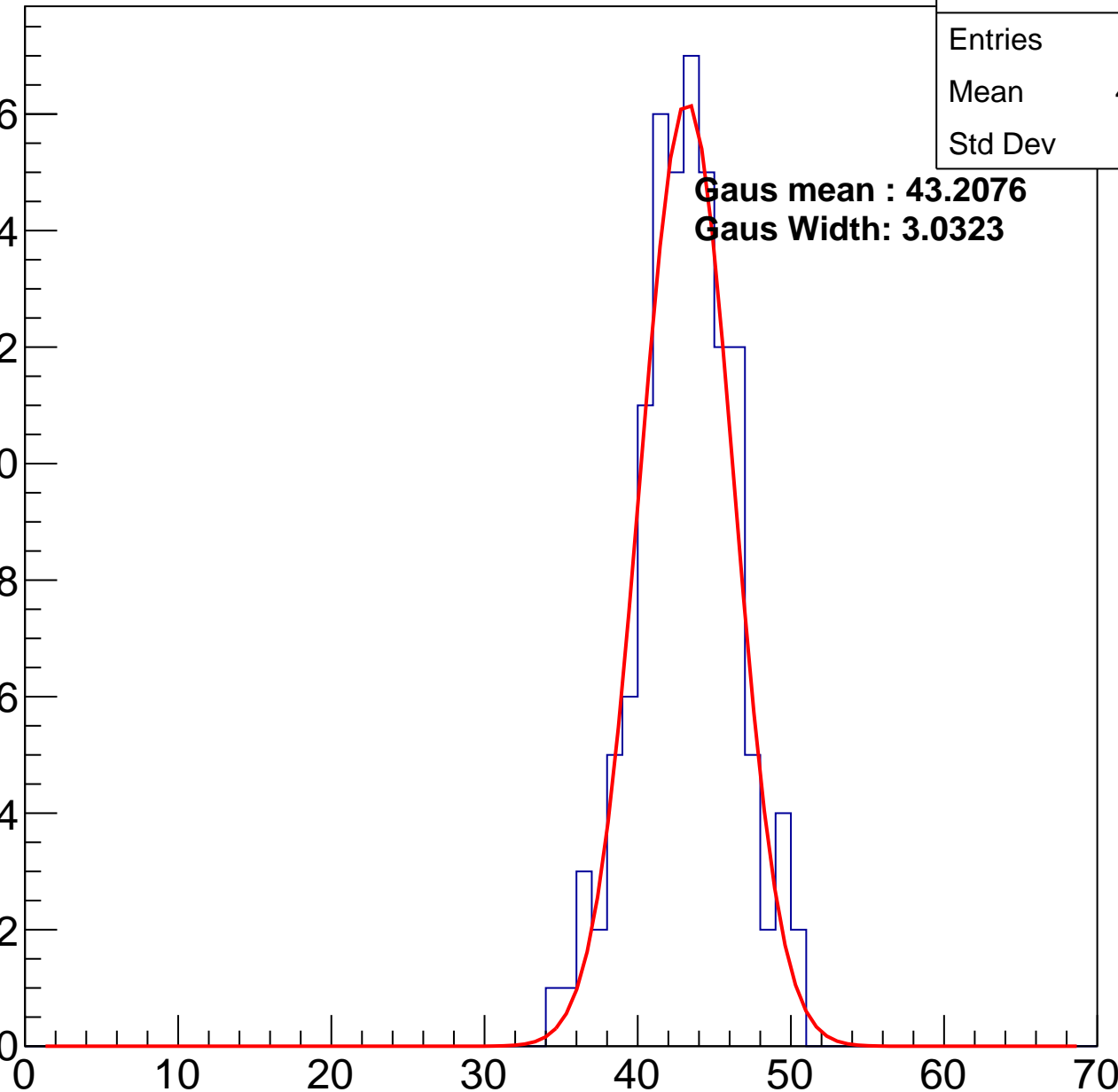
Gaus mean : 43.2076

Gaus Width: 3.0323

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#2, port C2

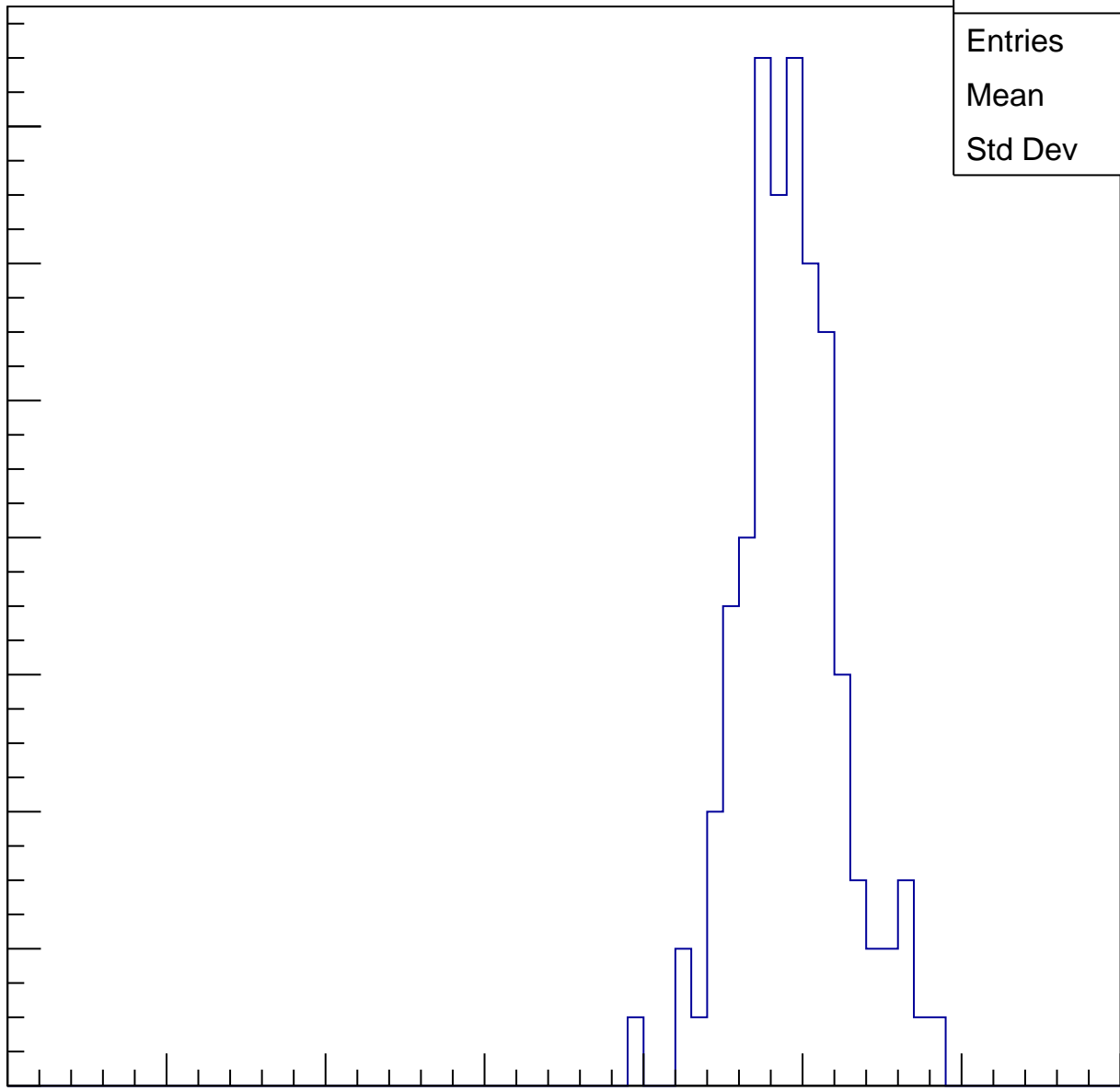
Entries	107
Mean	48.8
Std Dev	3.291

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

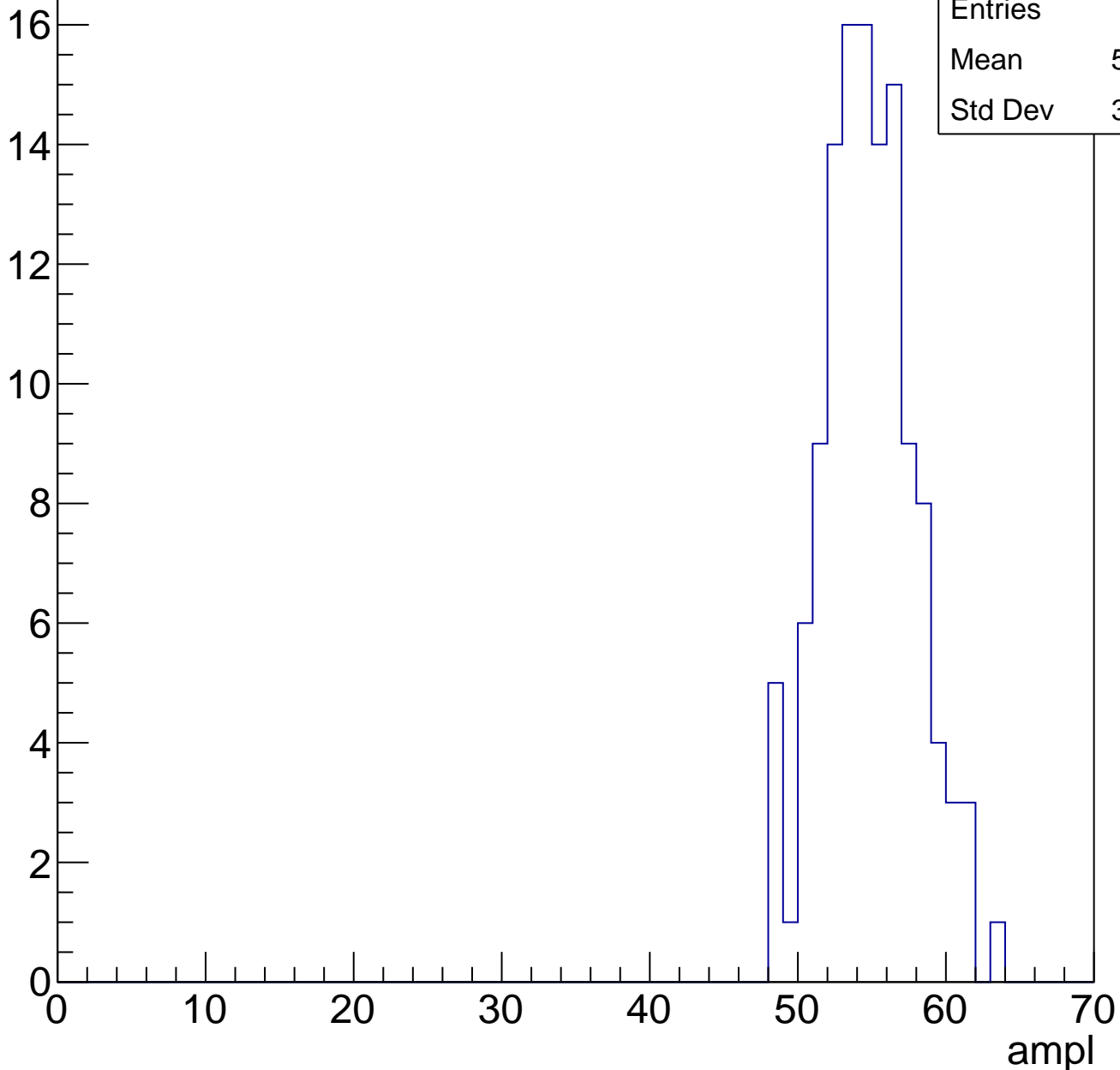
ampl



B1L001S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	124
Mean	54.33
Std Dev	3.058

B1L001S, U14-ch71, adc5

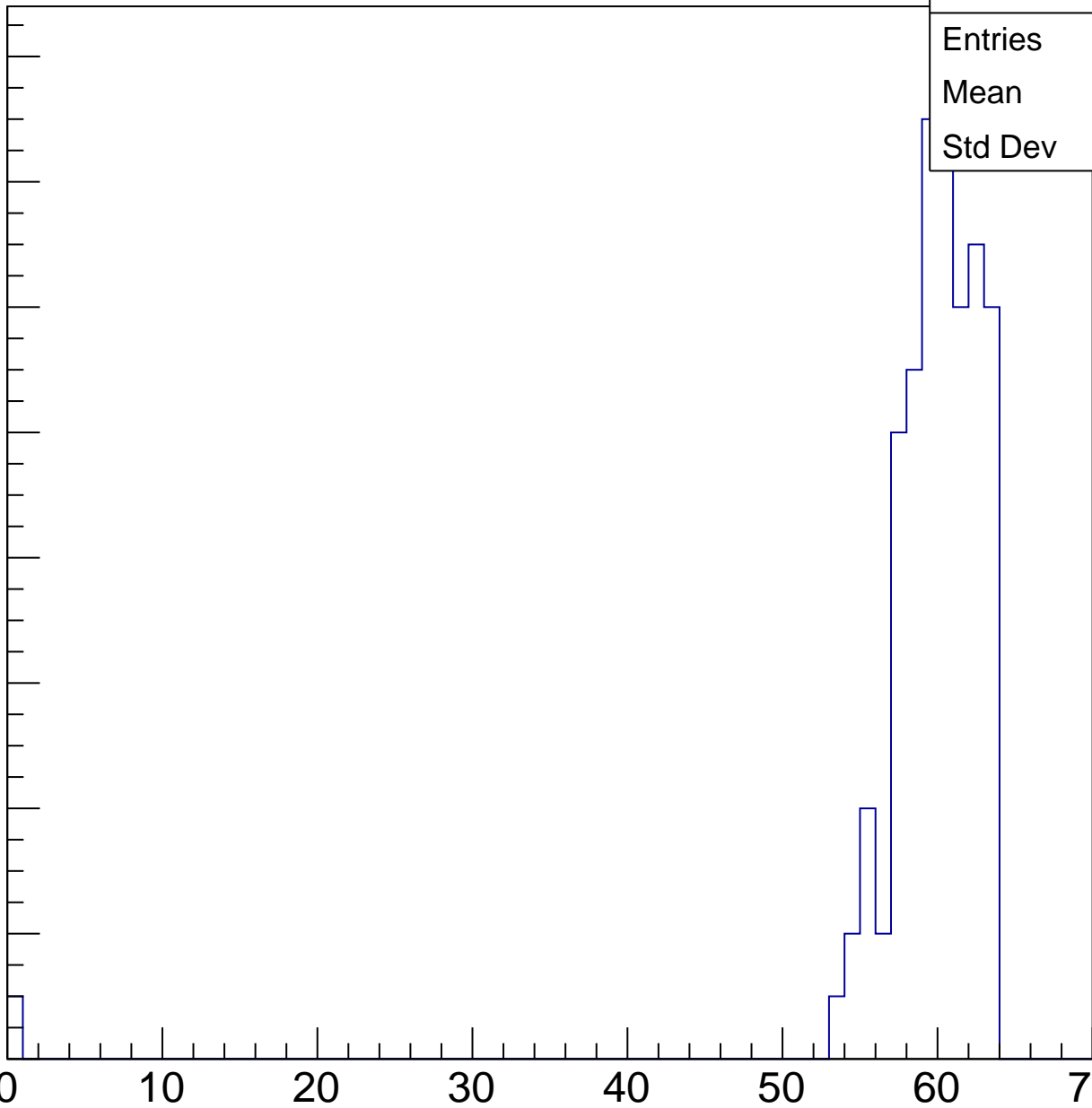
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	99
Mean	58.99
Std Dev	6.414

ampl

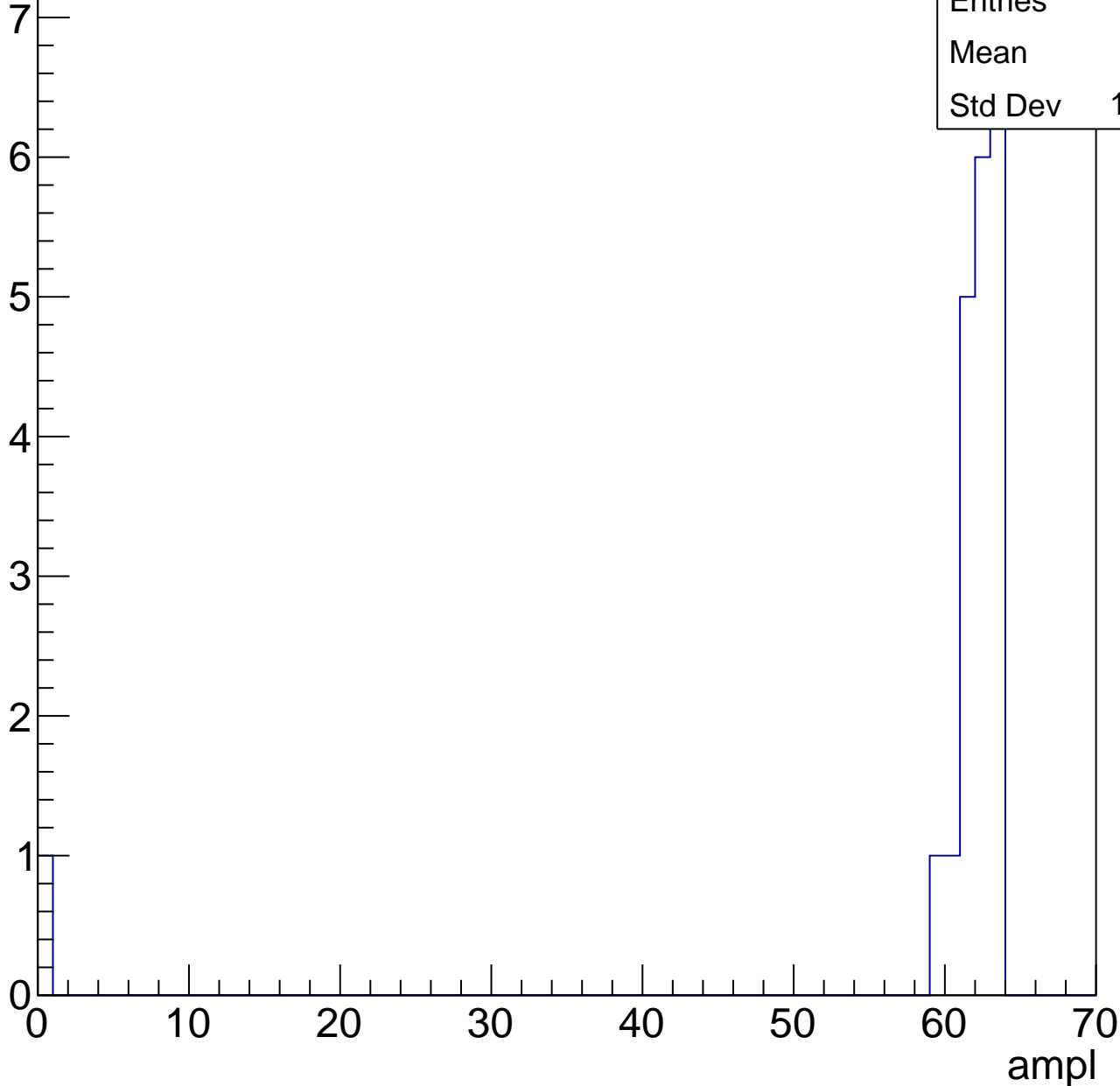


B1L001S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

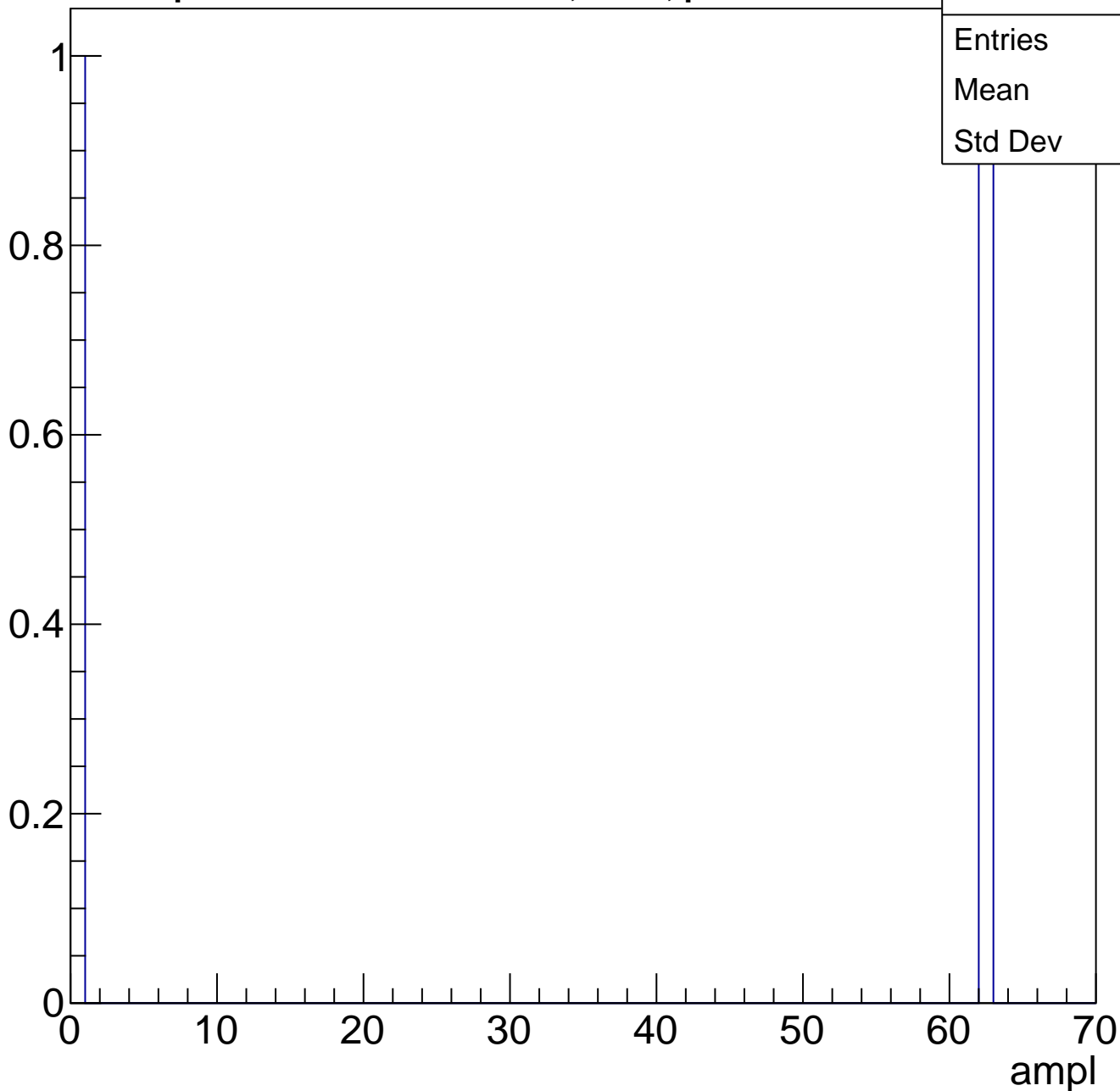
Entries	21
Mean	58.9
Std Dev	13.22



B1L001S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch72, adc0

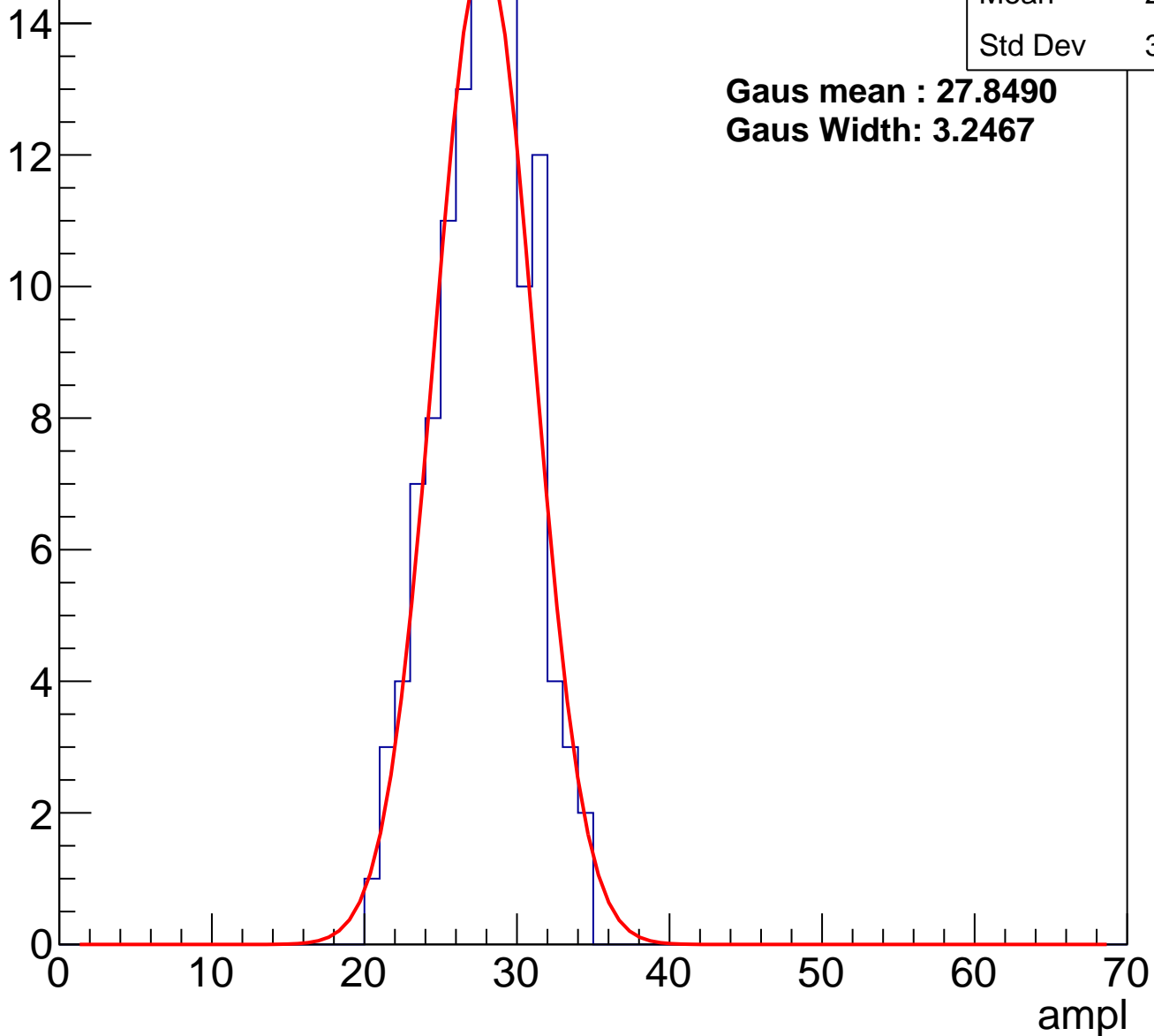
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	27.35
Std Dev	3.052

Gaus mean : 27.8490

Gaus Width: 3.2467

Entry



B1L001S, U14-ch72, adc1

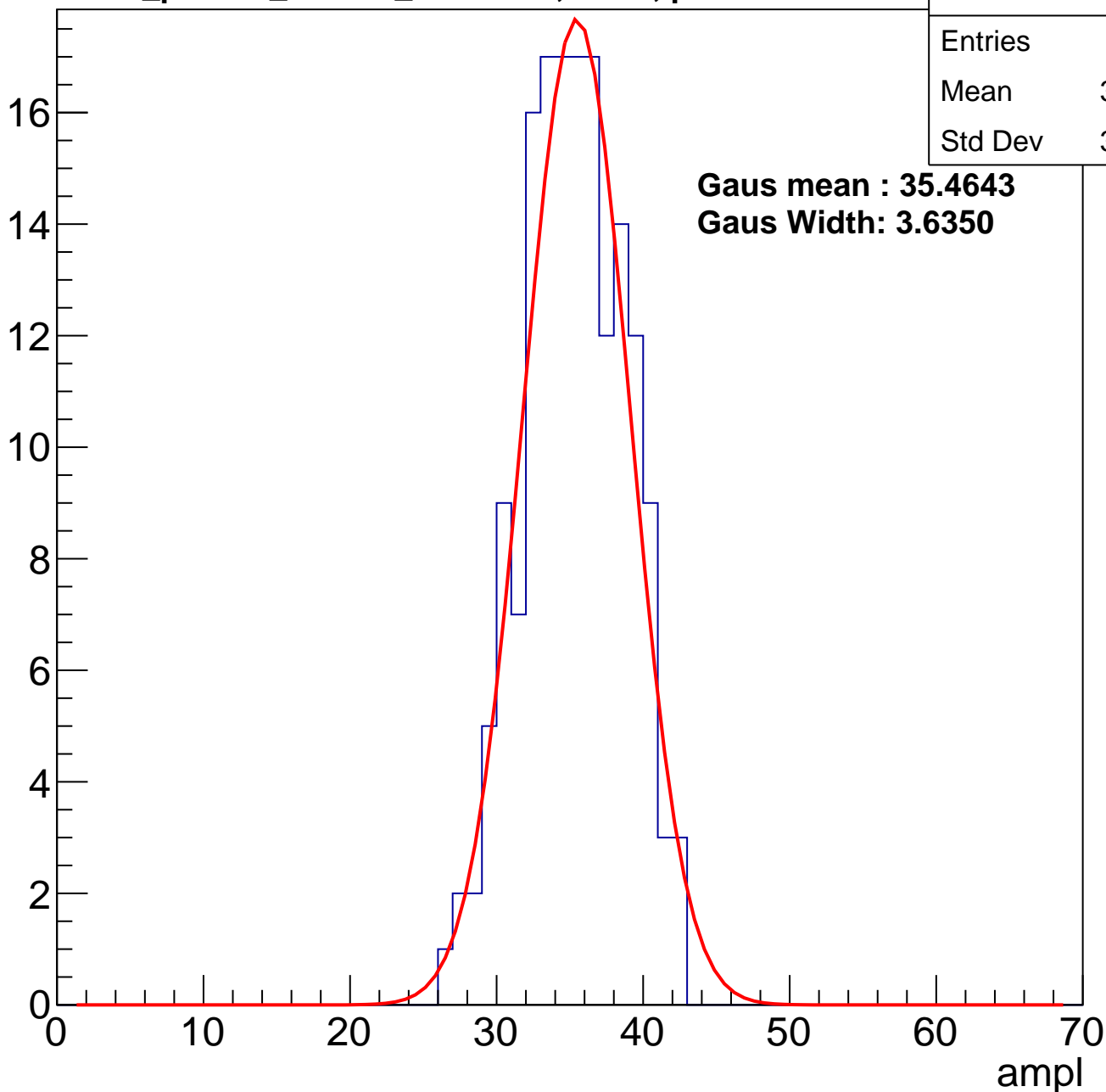
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	34.84
Std Dev	3.425

Gaus mean : 35.4643

Gaus Width: 3.6350

Entry



B1L001S, U14-ch72, adc2

calib_packv5_042523_0143.root, FC#2, port C2

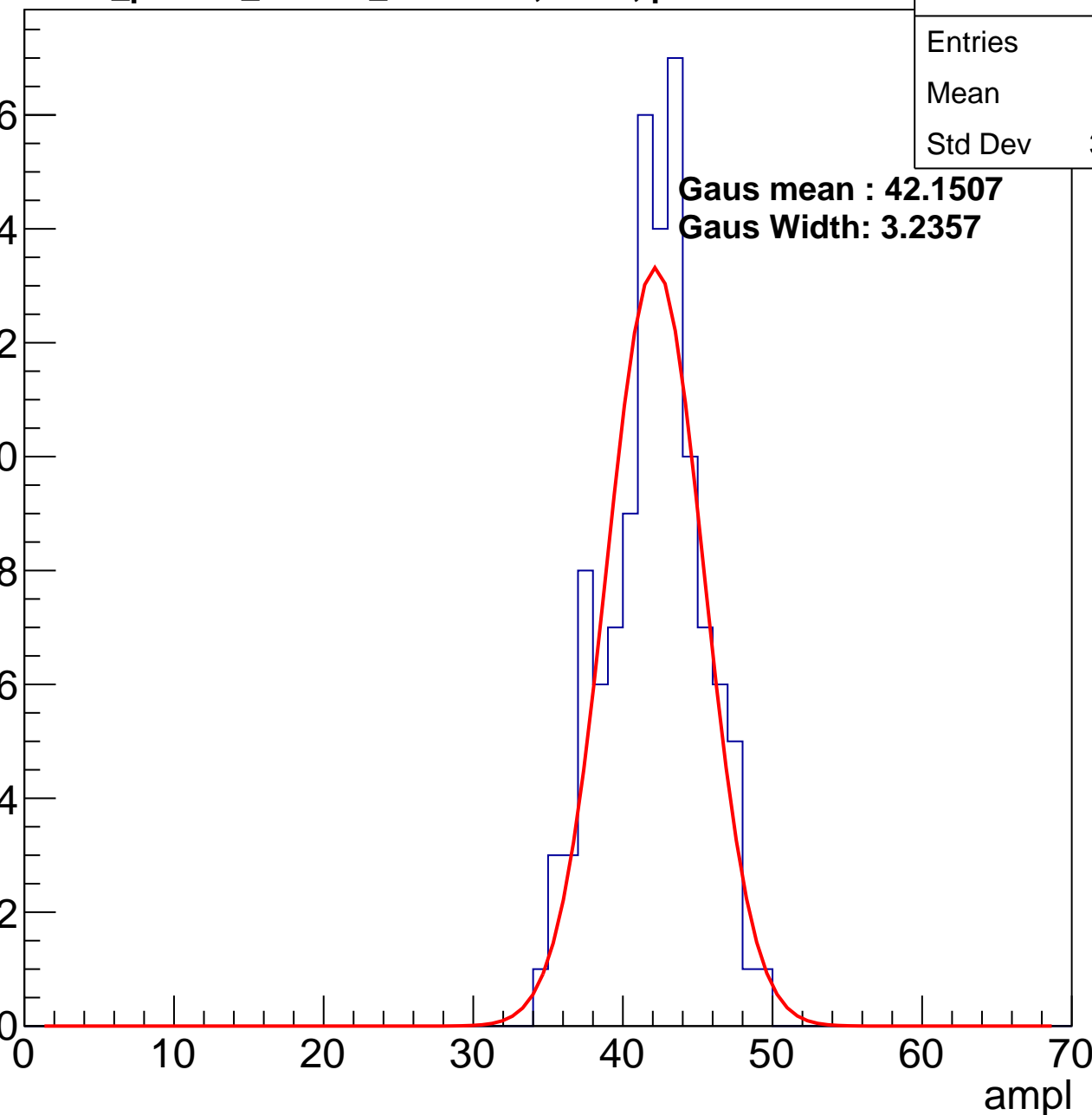
Entry

16
14
12
10
8
6
4
2
0

Entries	114
Mean	41.6
Std Dev	3.159

Gaus mean : 42.1507

Gaus Width: 3.2357



B1L001S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	48.2
Std Dev	3.514

Entry

18
16
14
12
10
8
6
4
2
0

0

10

20

30

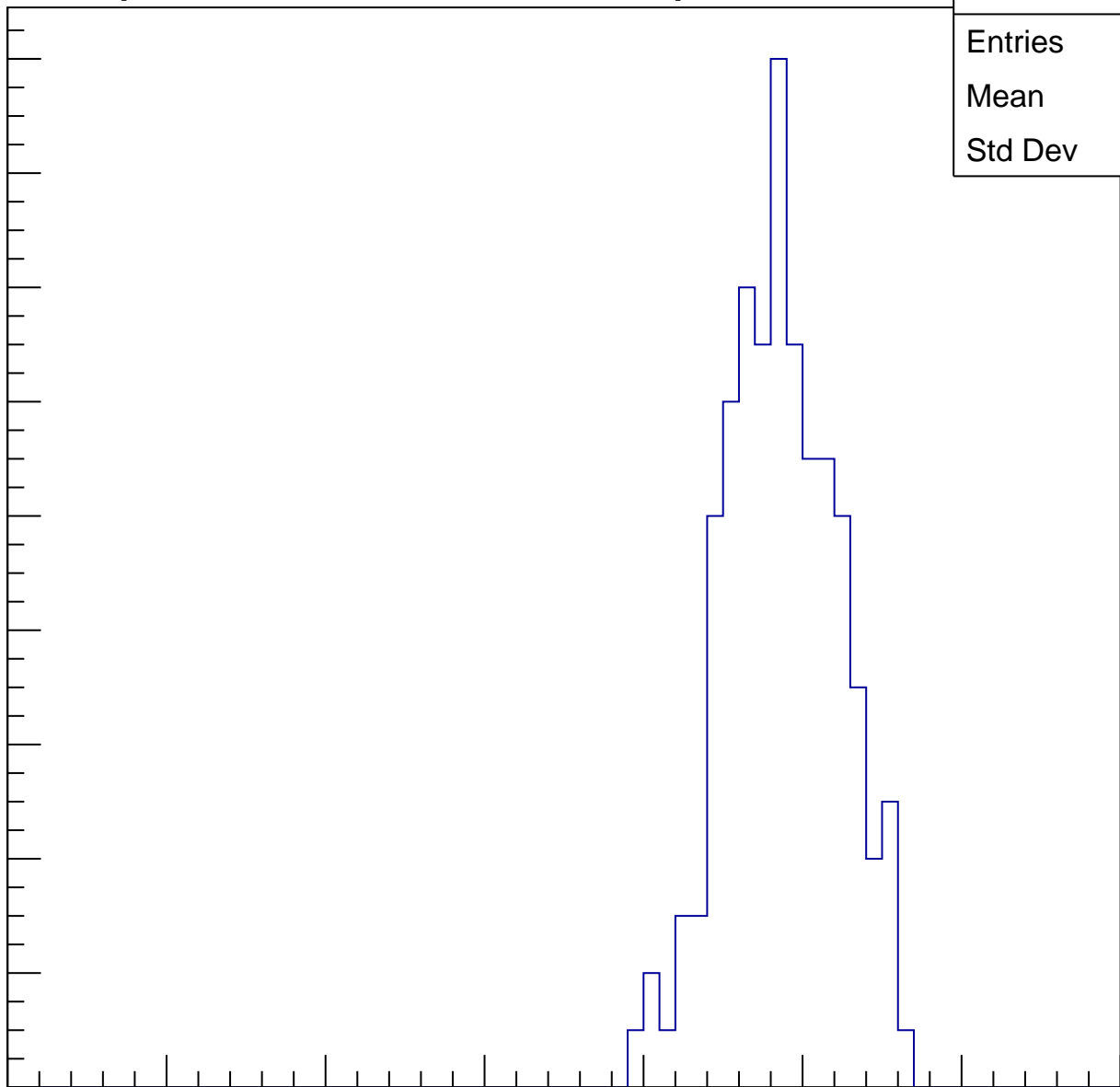
40

50

60

70

ampl



B1L001S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

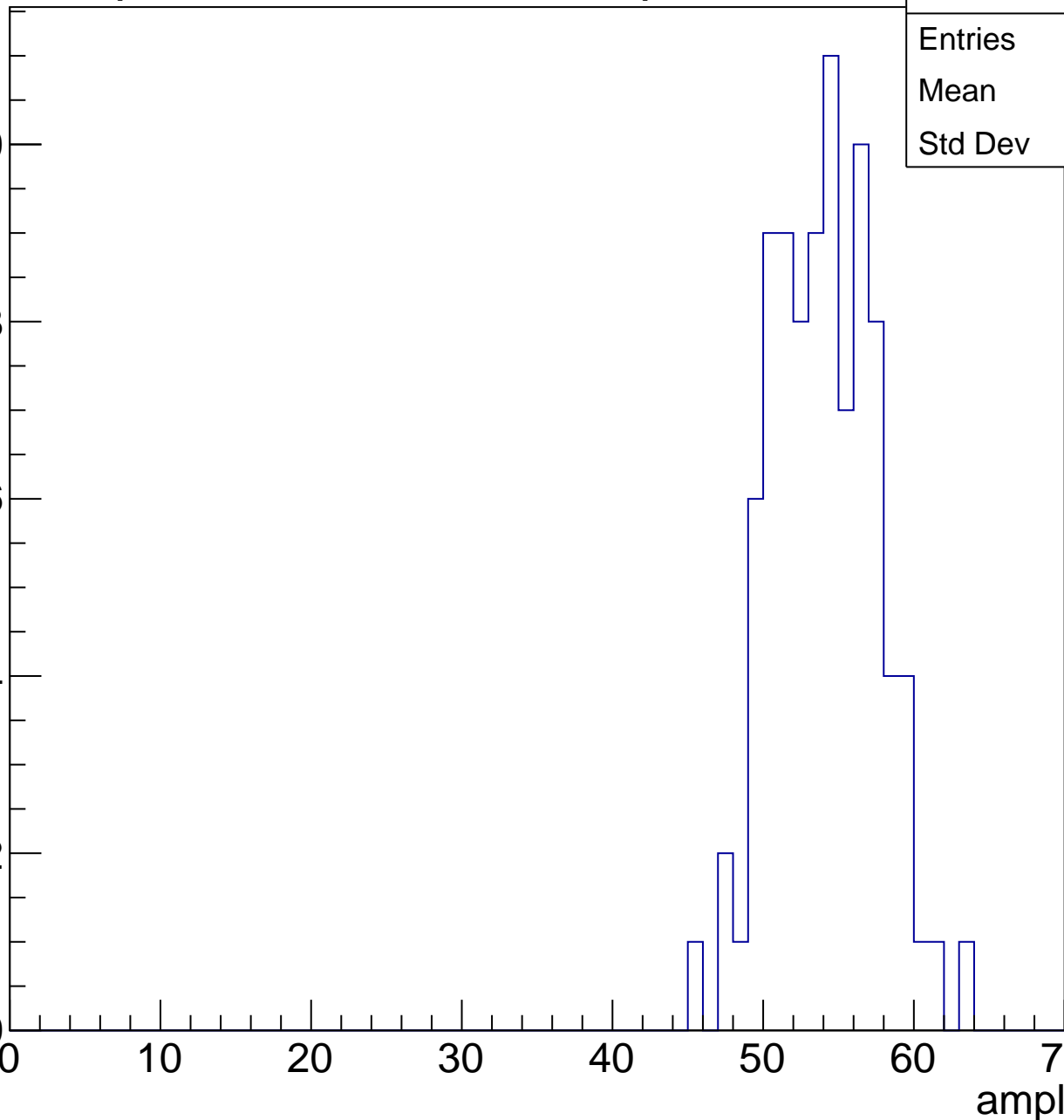
50

60

70

ampl

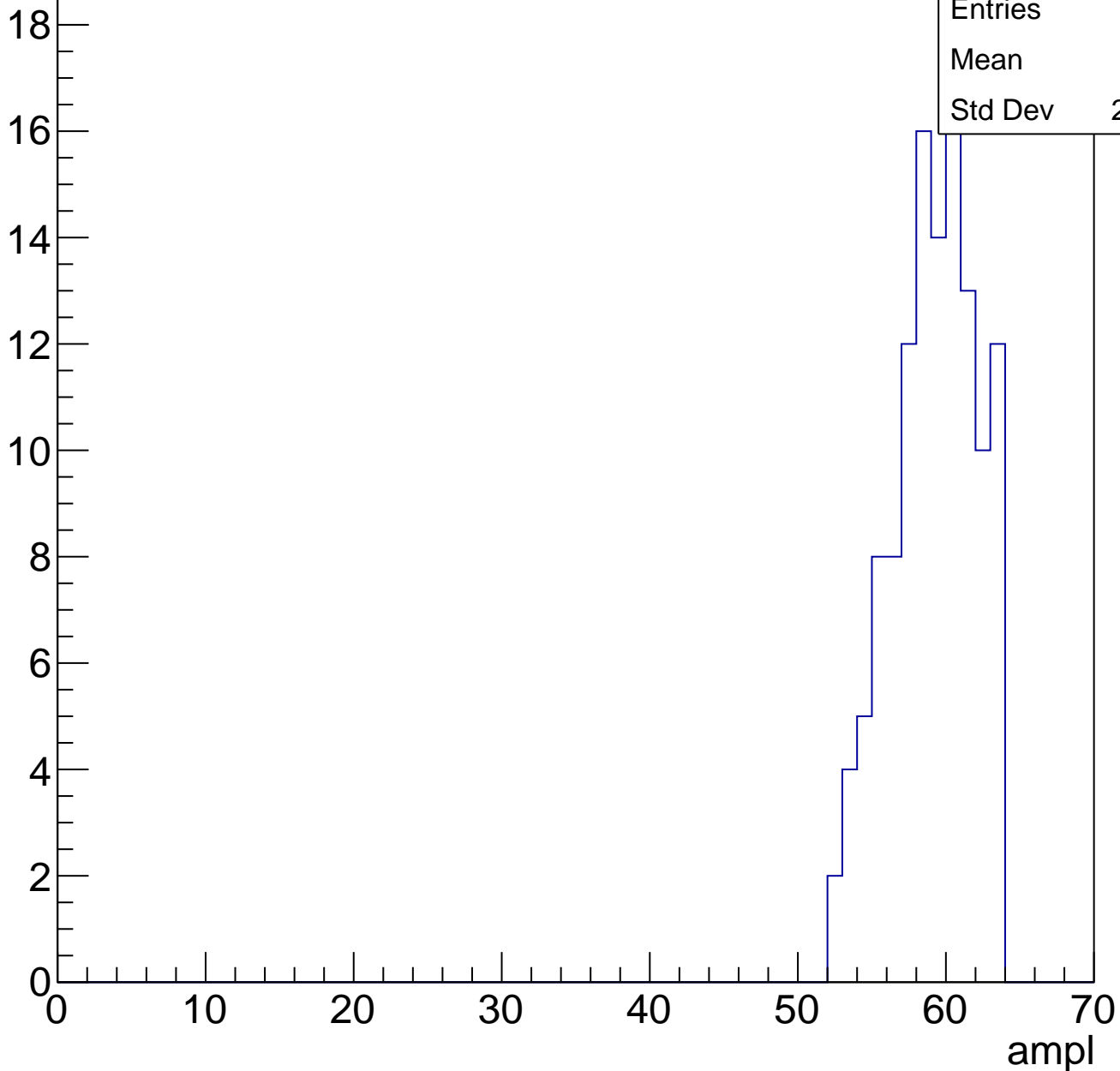
Entries	92
Mean	53.59
Std Dev	3.401



B1L001S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

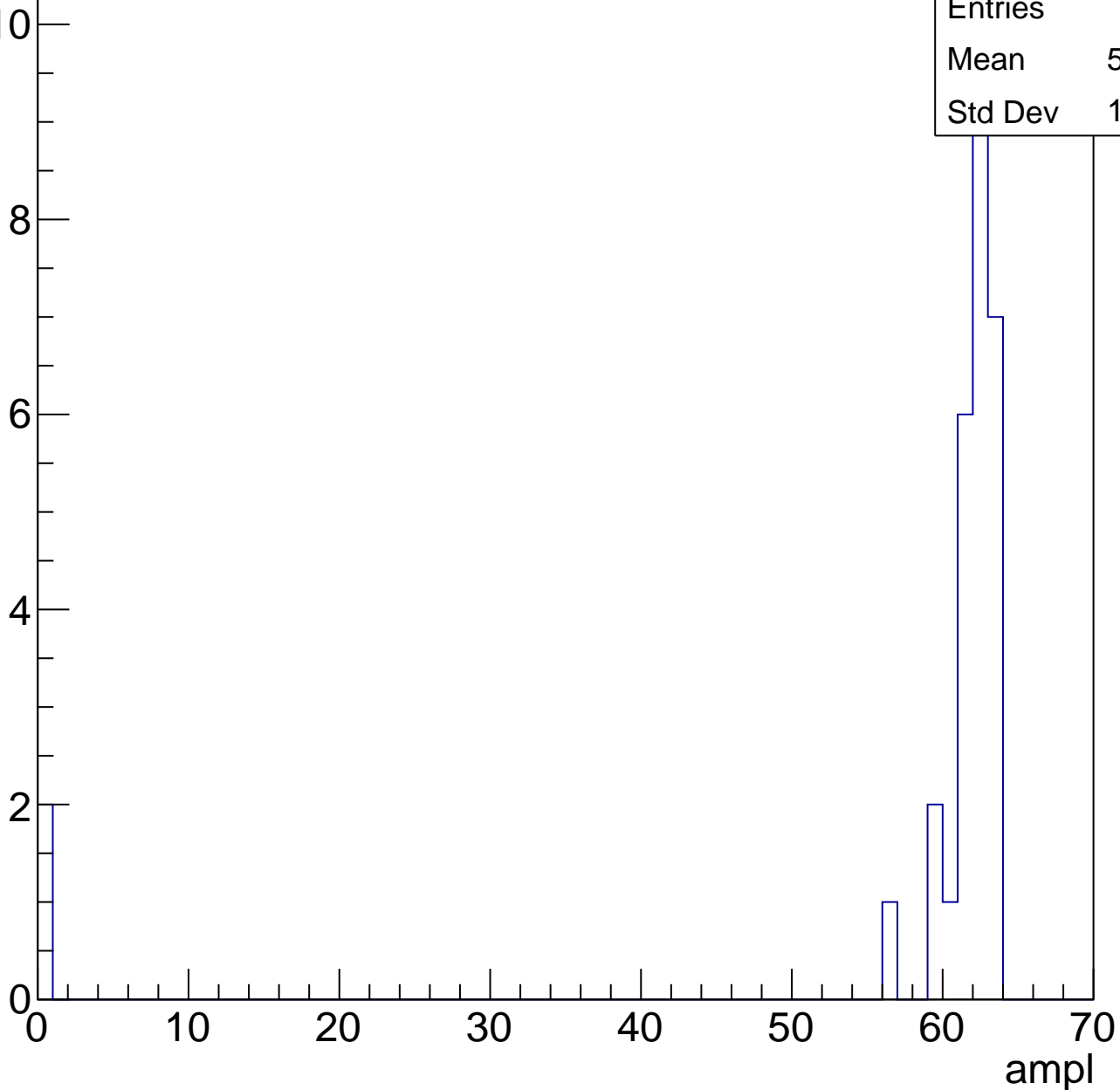


B1L001S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	29
Mean	57.28
Std Dev	15.66



B1L001S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

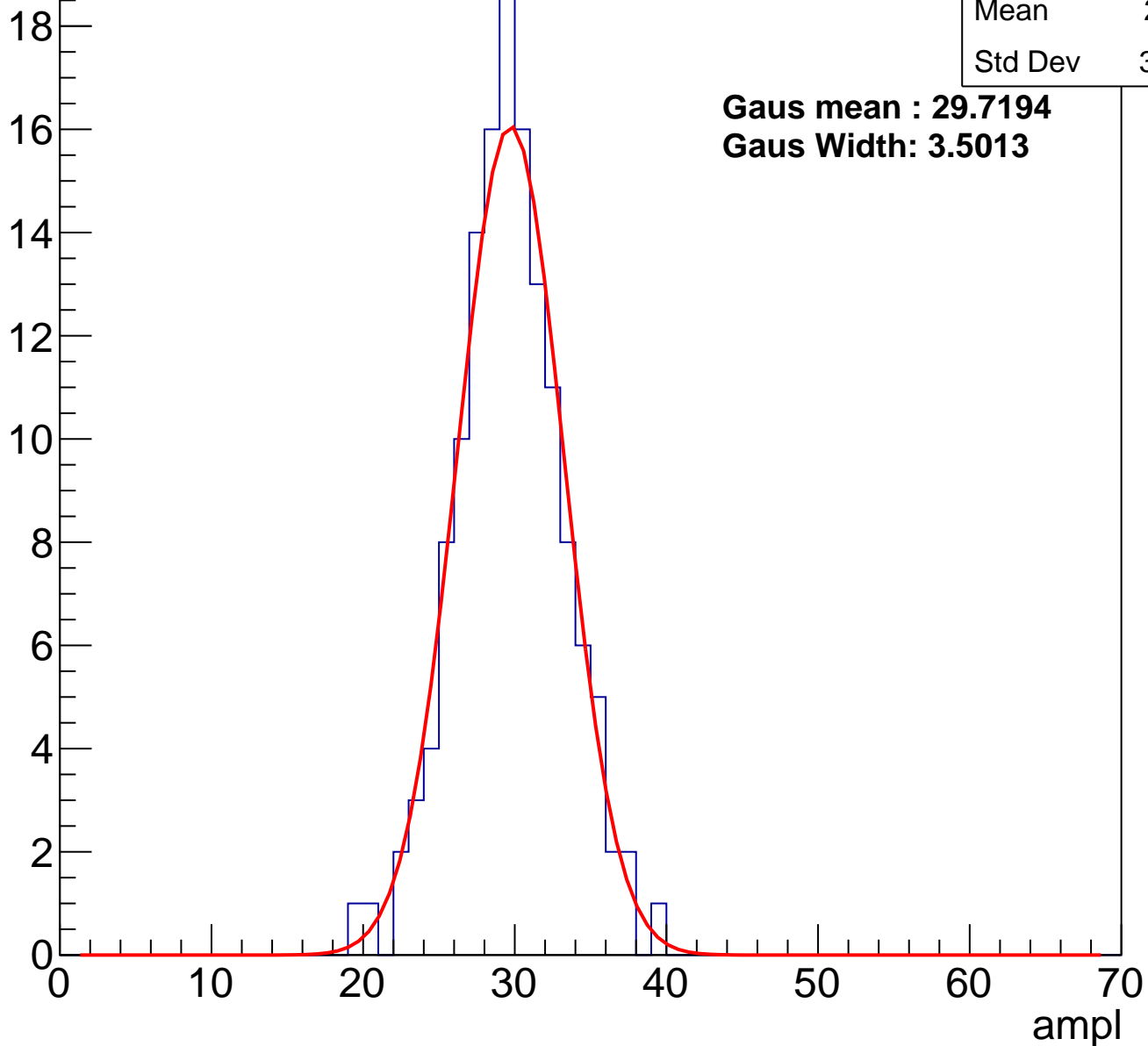


Entries	2
Mean	0
Std Dev	0

B1L001S, U14-ch73, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch73, adc1

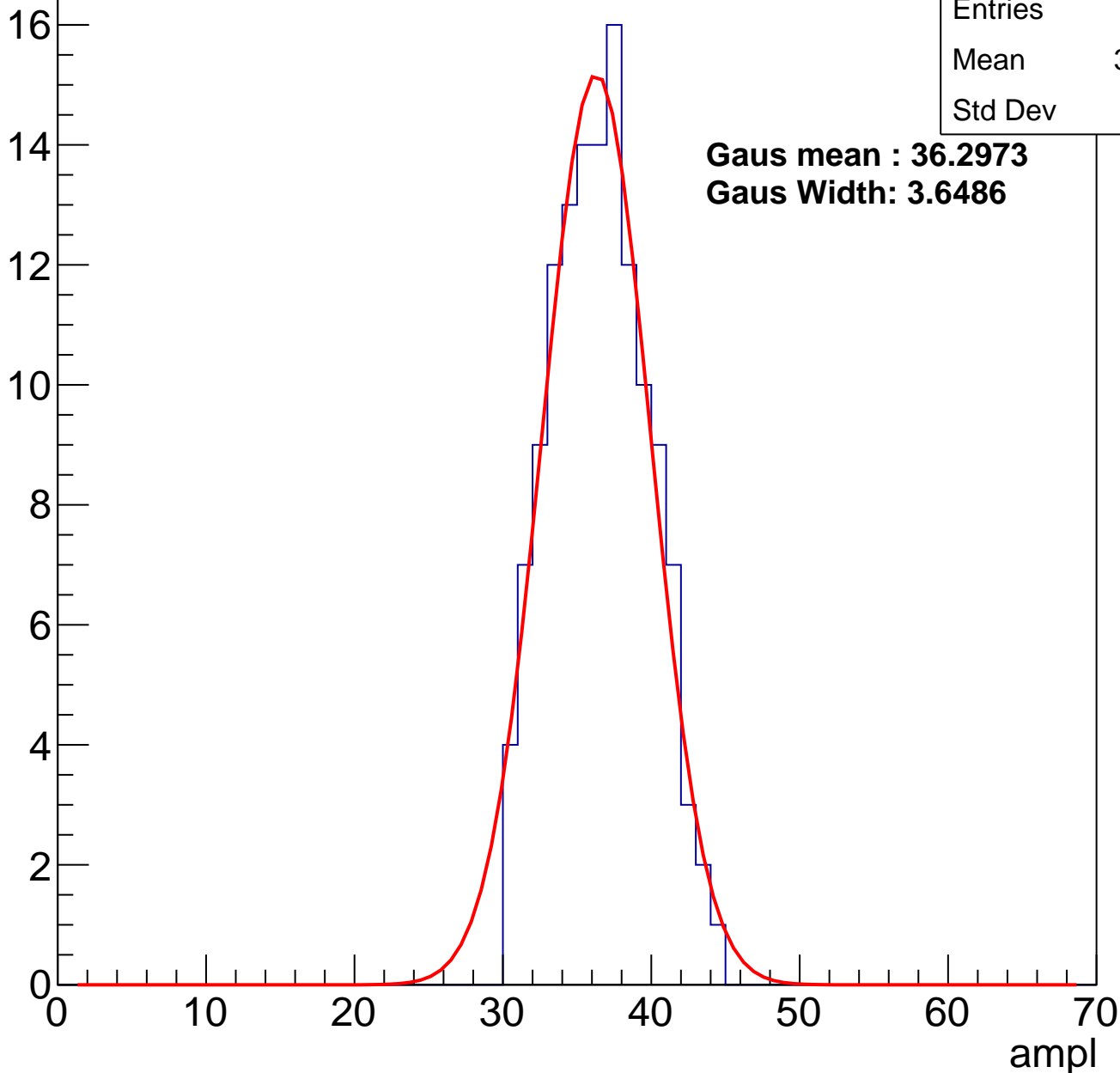
calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.08
Std Dev	3.22

Gaus mean : 36.2973

Gaus Width: 3.6486

Entry



B1L001S, U14-ch73, adc2

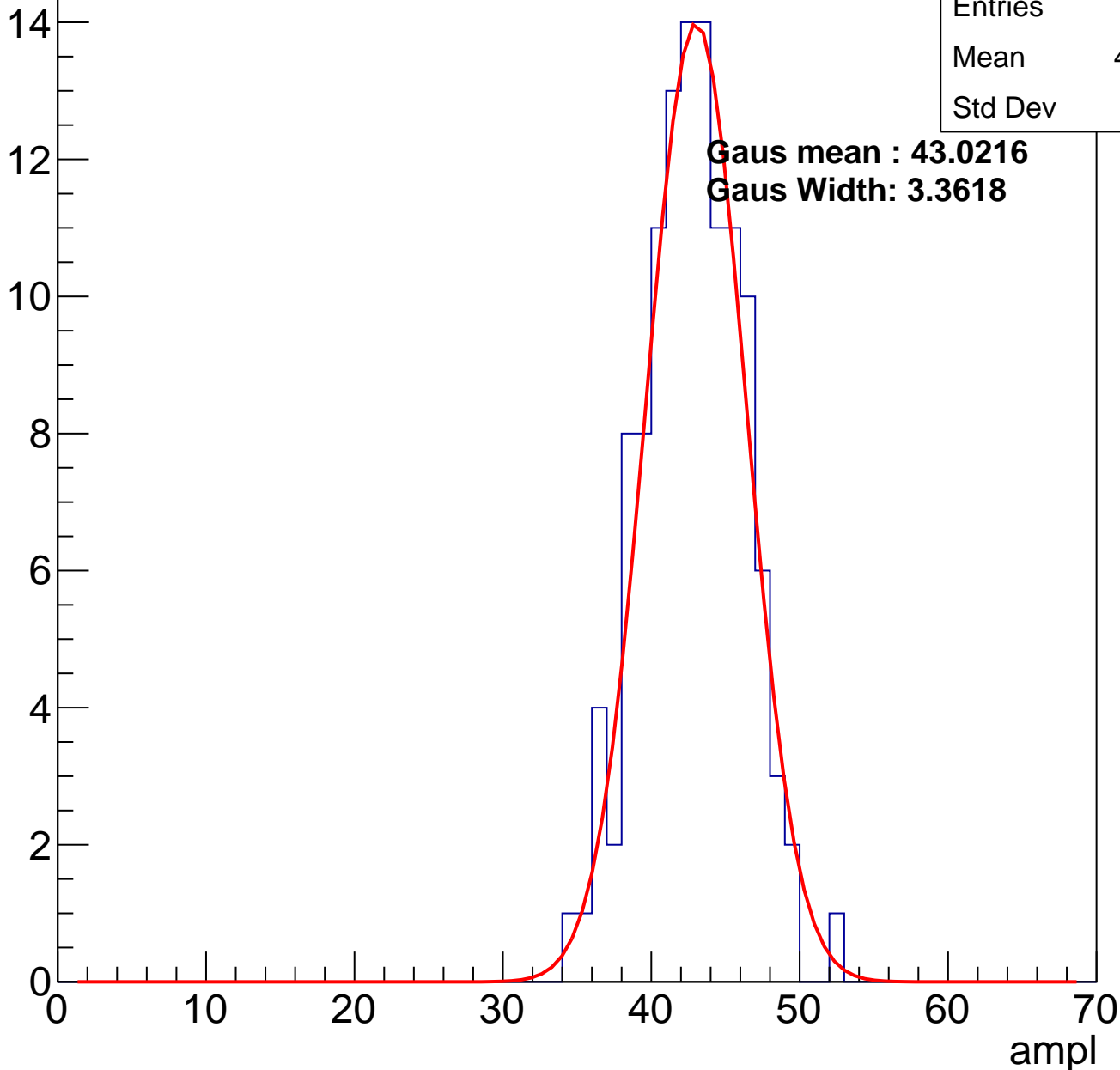
calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	42.34
Std Dev	3.32

Gaus mean : 43.0216

Gaus Width: 3.3618

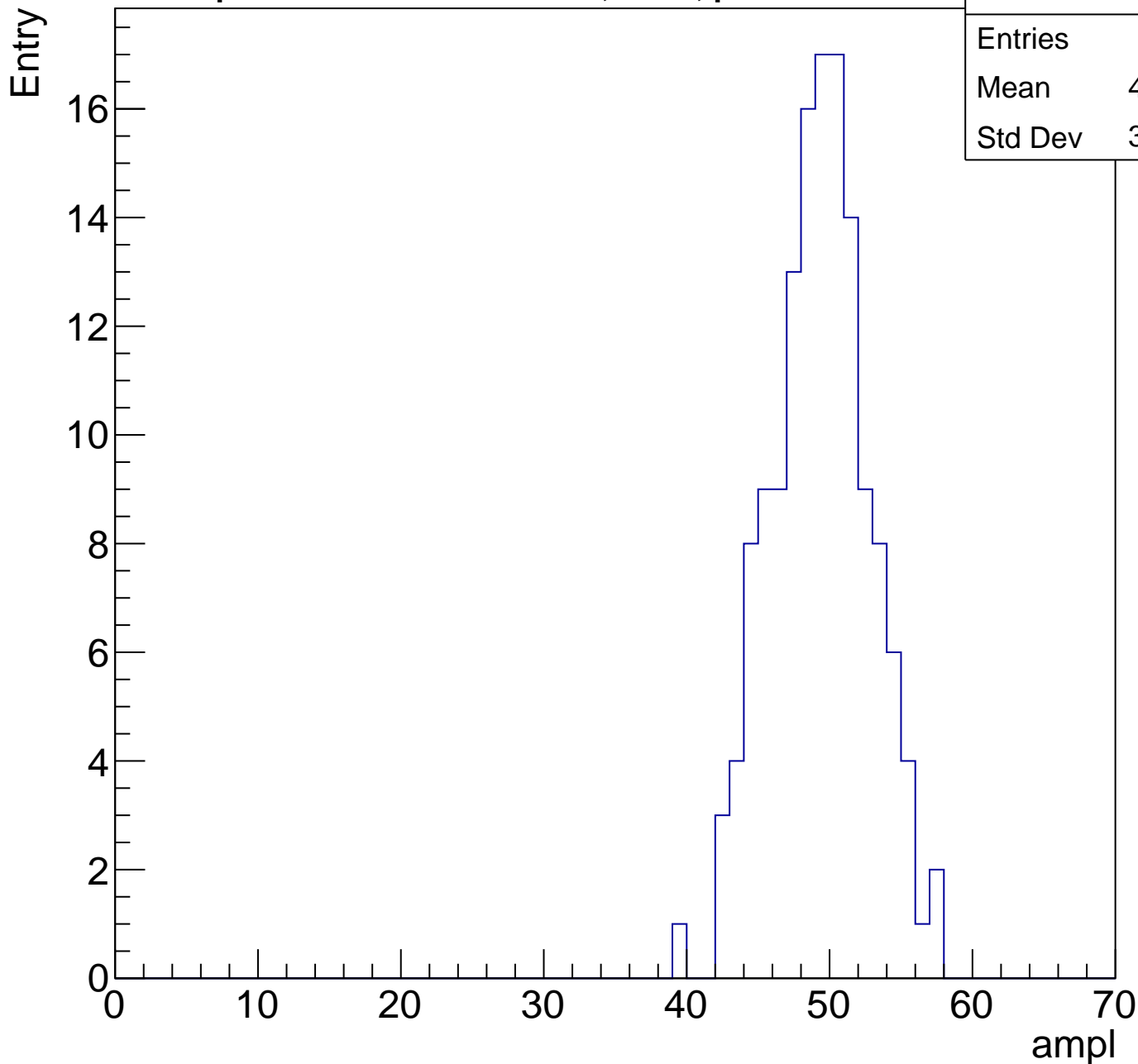
Entry



B1L001S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#2, port C2

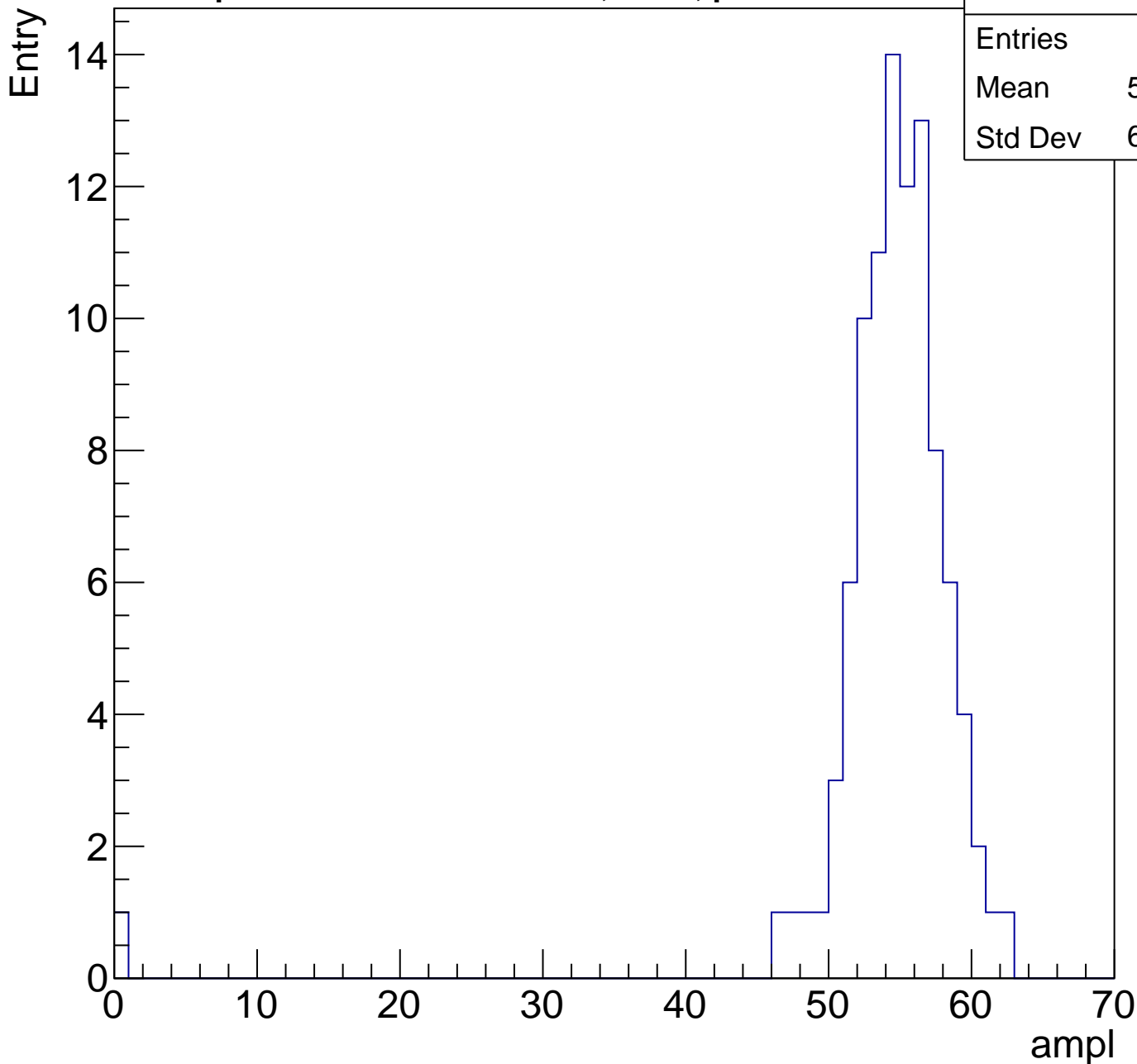
Entries	141
Mean	48.87
Std Dev	3.405



B1L001S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	53.92
Std Dev	6.258



B1L001S, U14-ch73, adc5

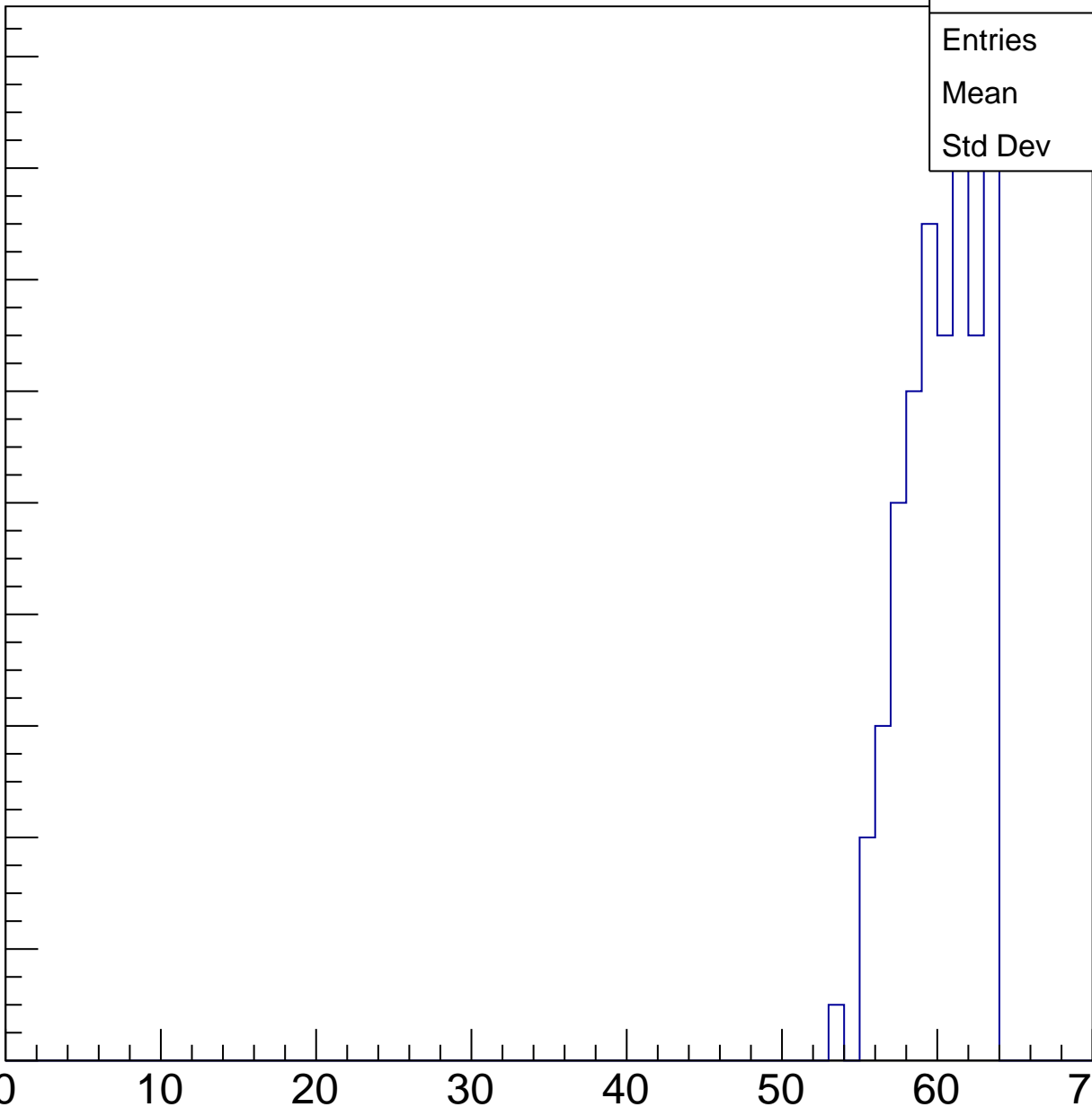
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	108
Mean	59.74
Std Dev	2.366

ampl

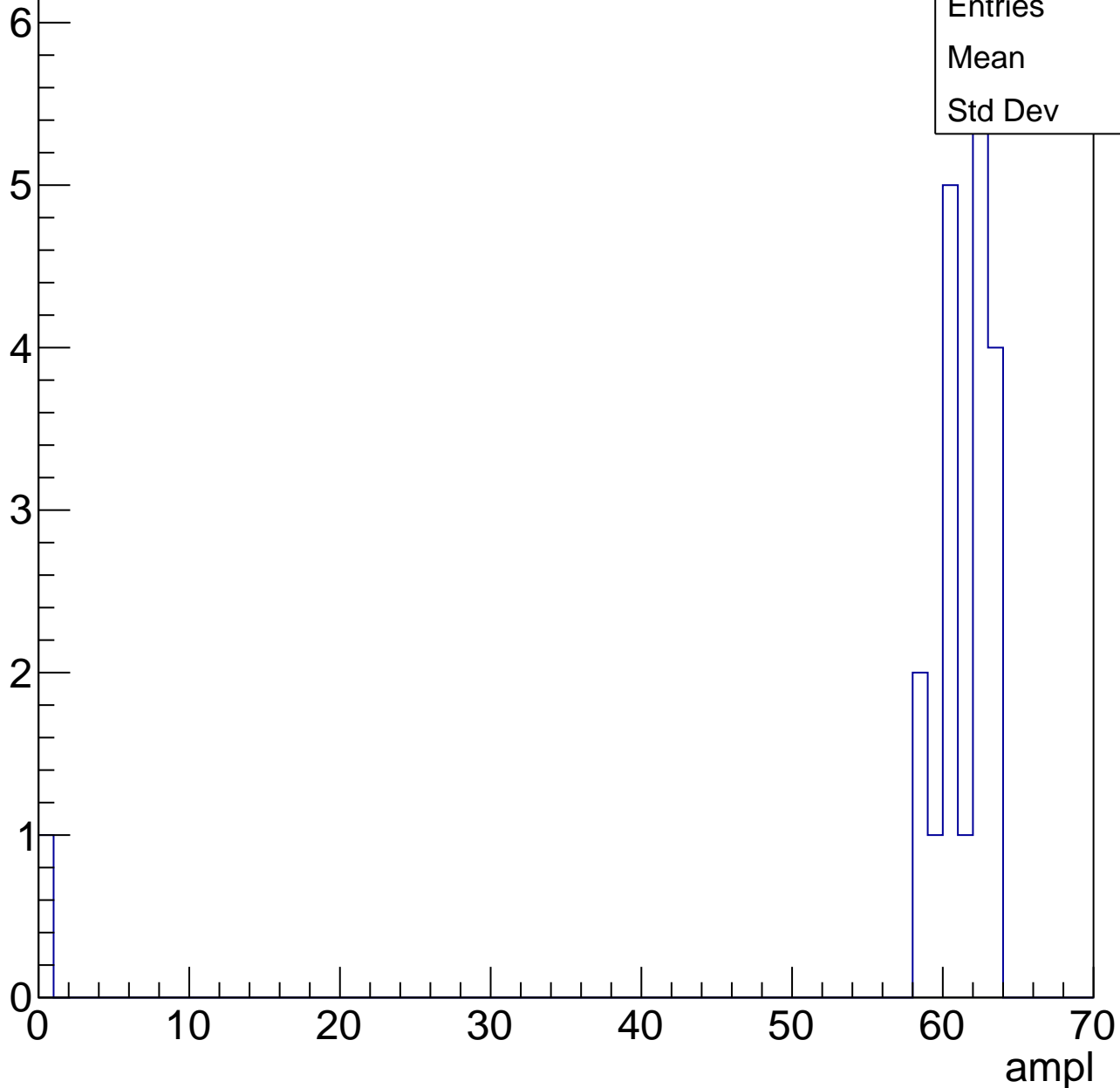


B1L001S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	20
Mean	58
Std Dev	13.4



B1L001S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch74, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	29.46
Std Dev	3.362

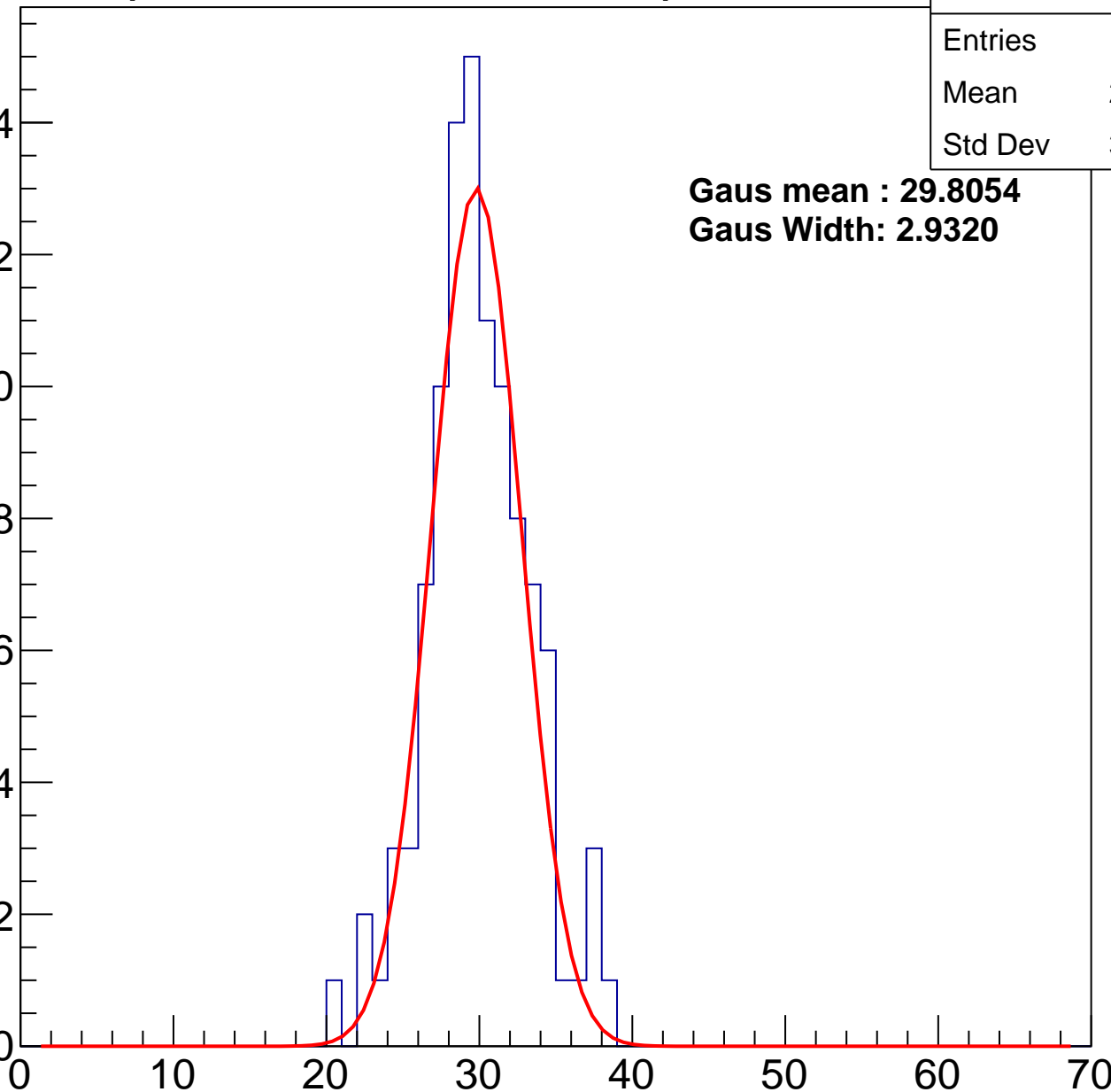
Gaus mean : 29.8054

Gaus Width: 2.9320

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch74, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	34.77
Std Dev	3.113

Gaus mean : 35.3008

Gaus Width: 3.0810

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

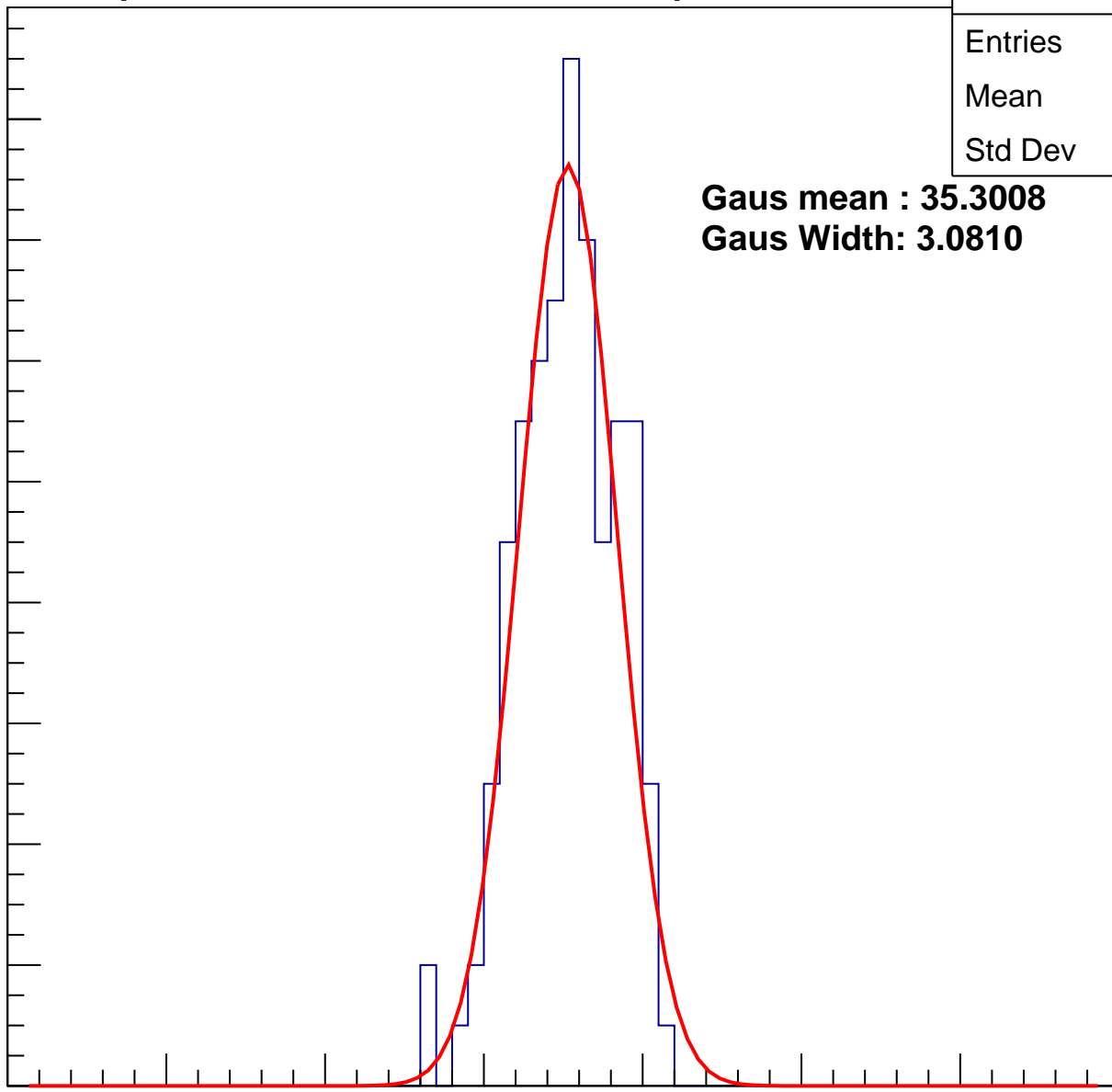
40

50

60

70

ampl



B1L001S, U14-ch74, adc2

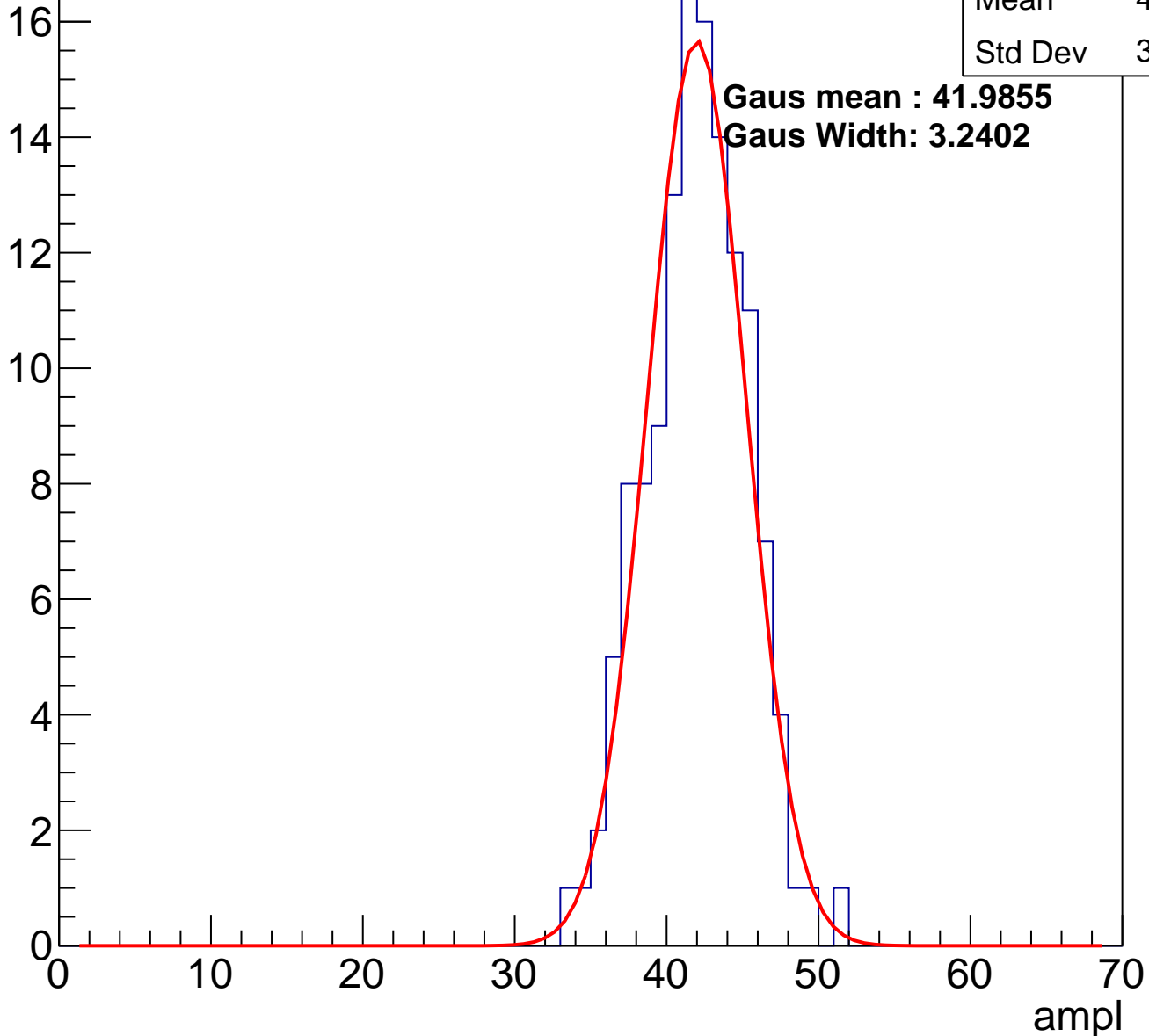
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	131
Mean	41.53
Std Dev	3.282

Gaus mean : 41.9855

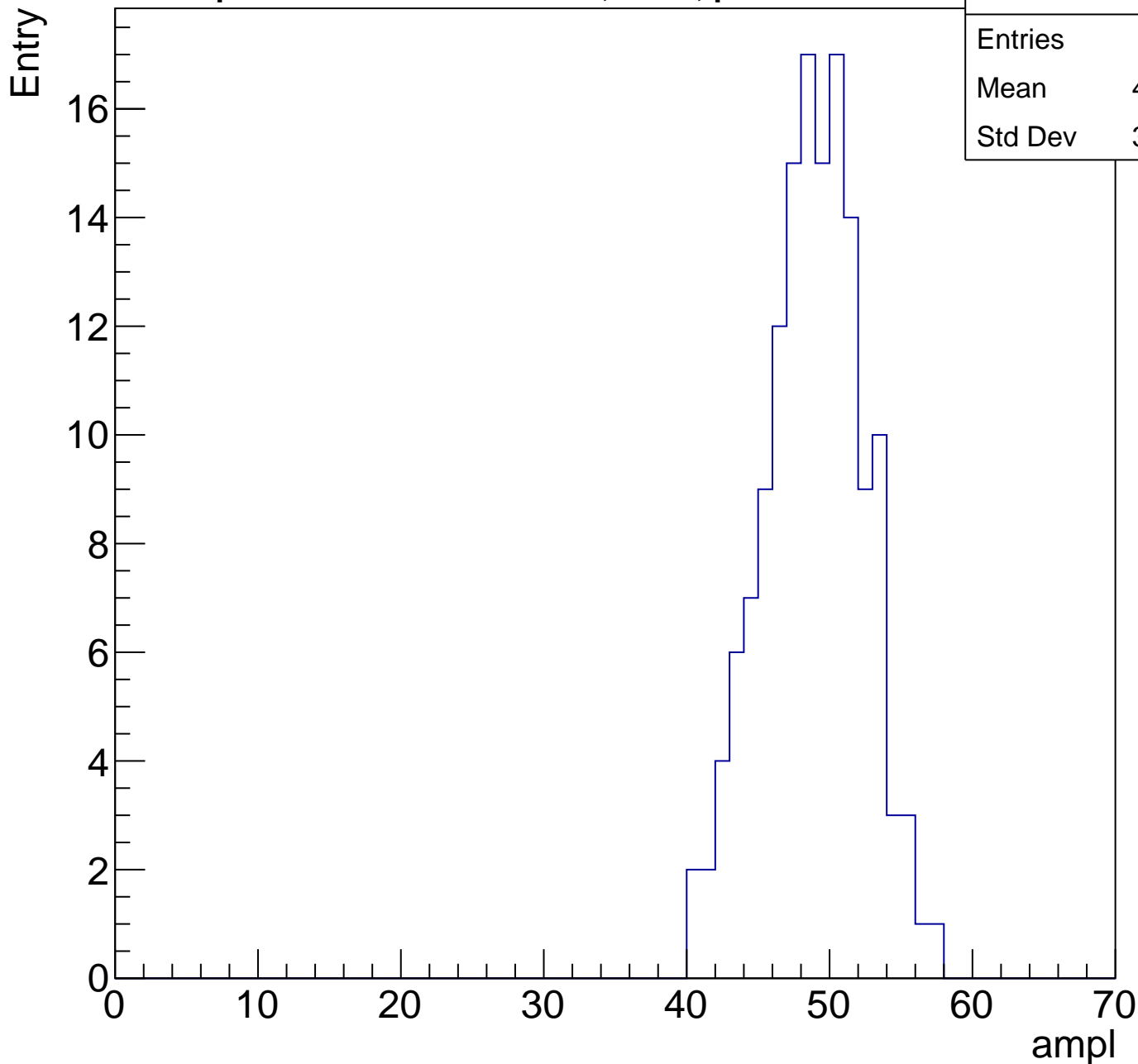
Gaus Width: 3.2402



B1L001S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	48.37
Std Dev	3.459



B1L001S, U14-ch74, adc4

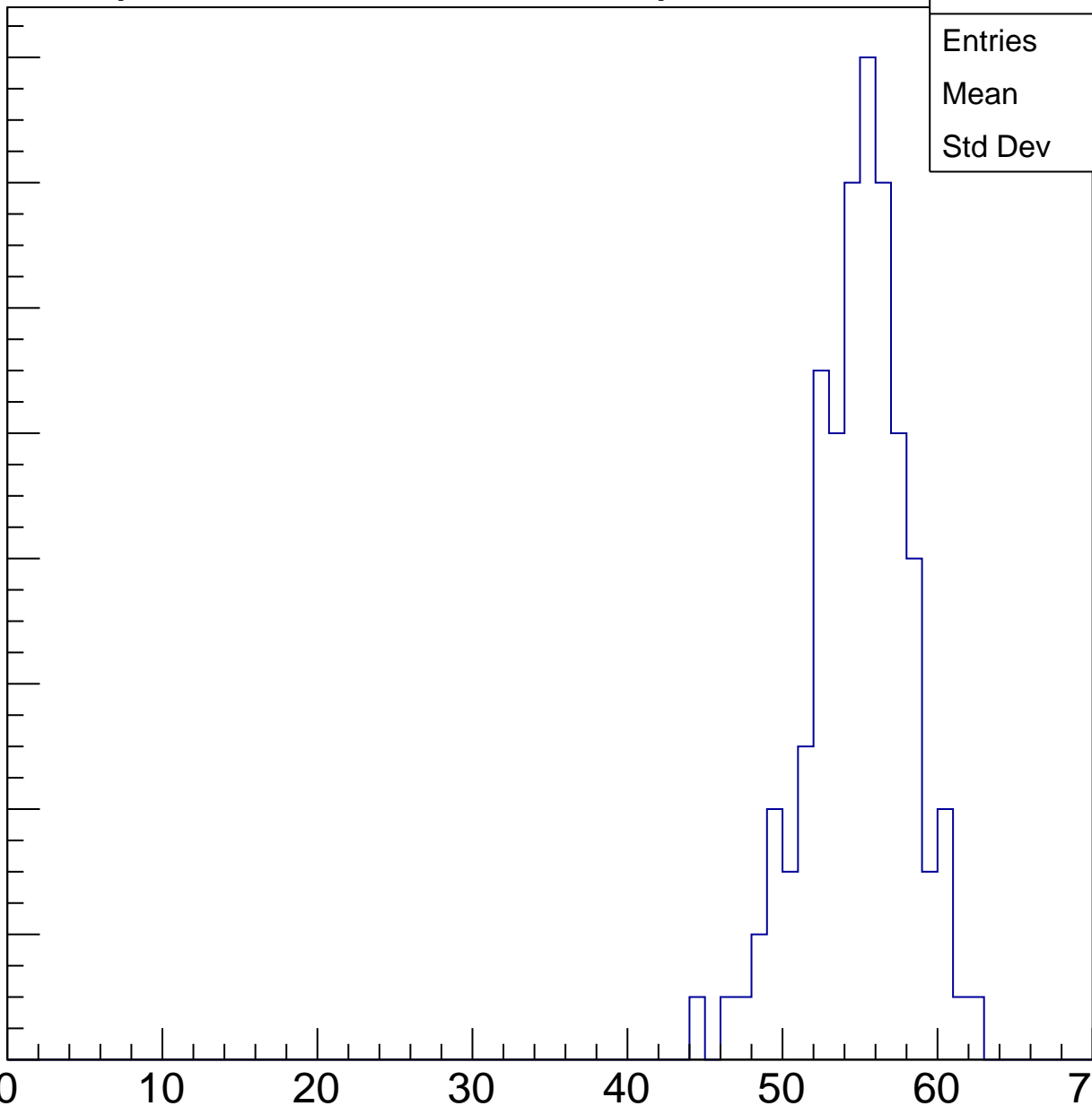
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	109
Mean	54.4
Std Dev	3.229

ampl



B1L001S, U14-ch74, adc5

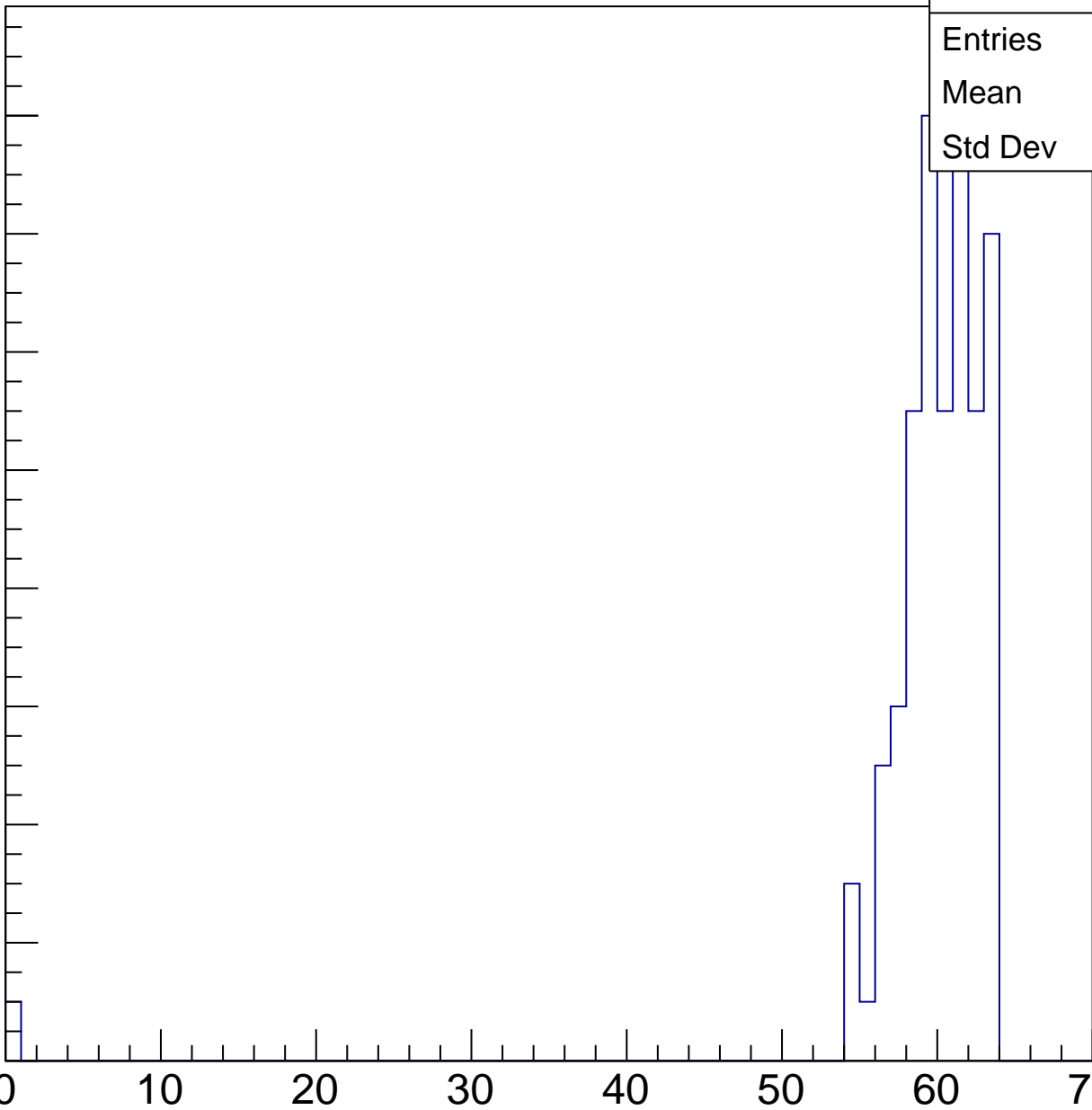
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	96
Mean	59.19
Std Dev	6.496

ampl

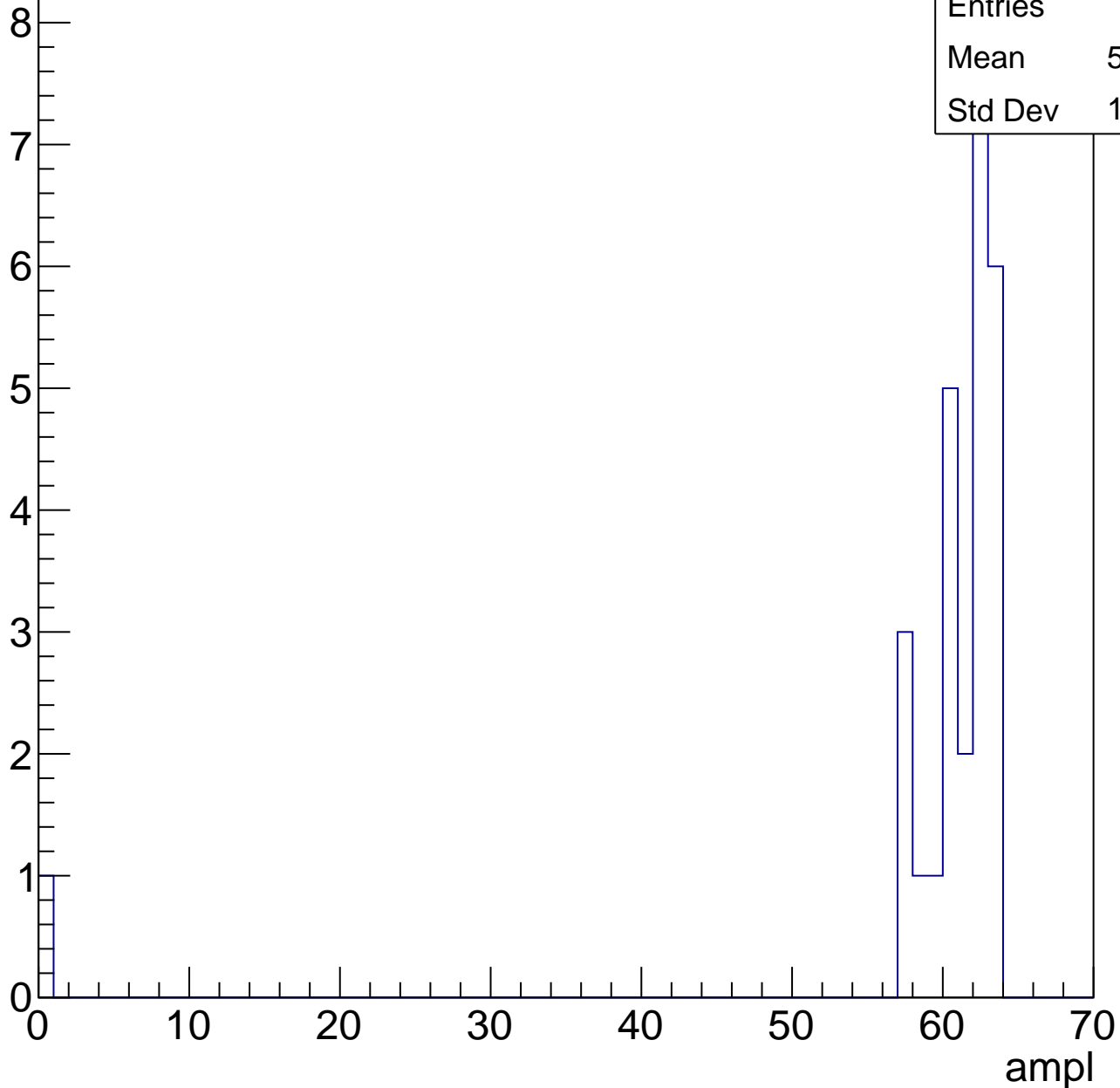


B1L001S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	27
Mean	58.67
Std Dev	11.66



B1L001S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L001S, U14-ch75, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	29.54
Std Dev	3.56

Gaus mean : 30.0228

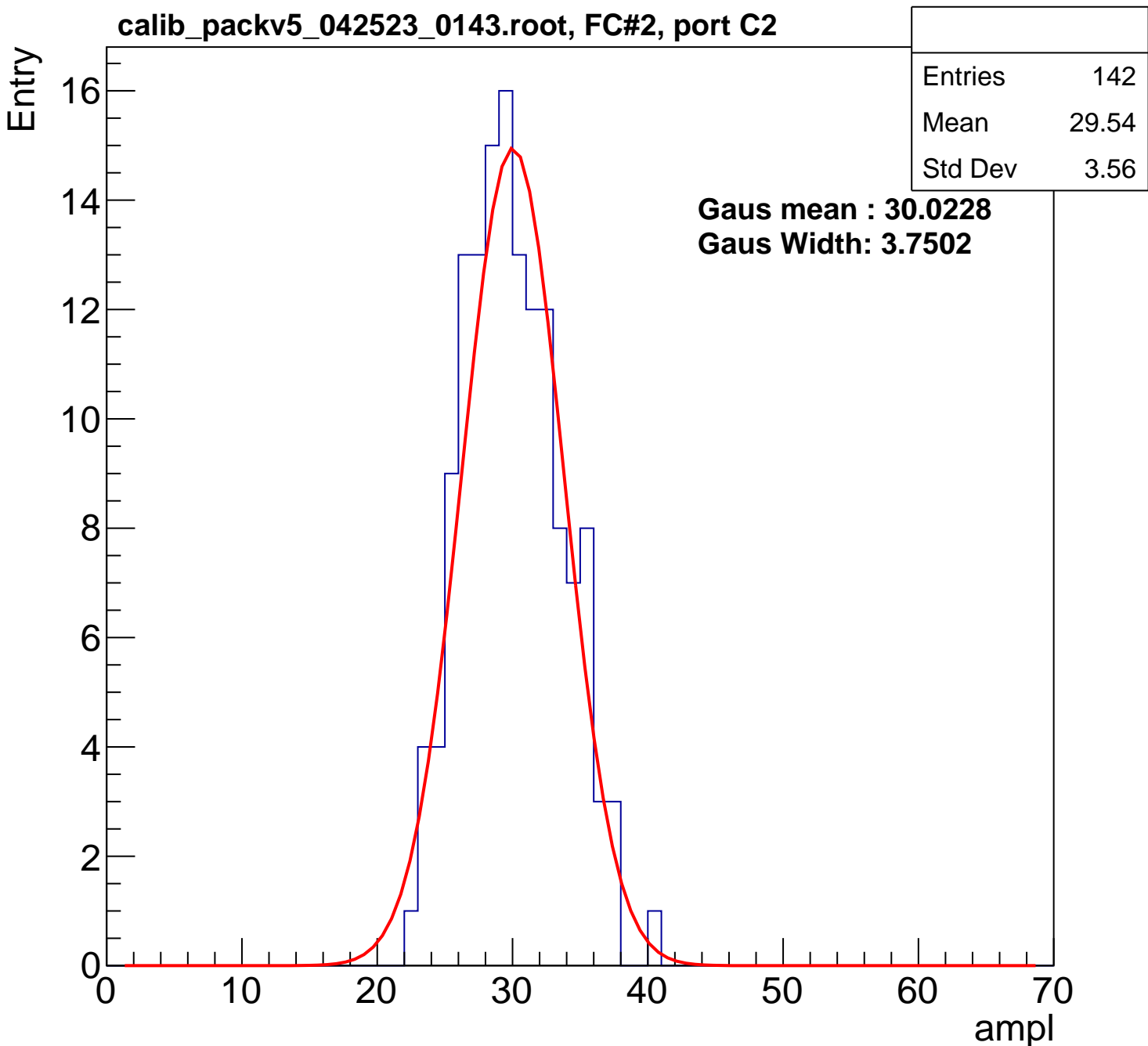
Gaus Width: 3.7502

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

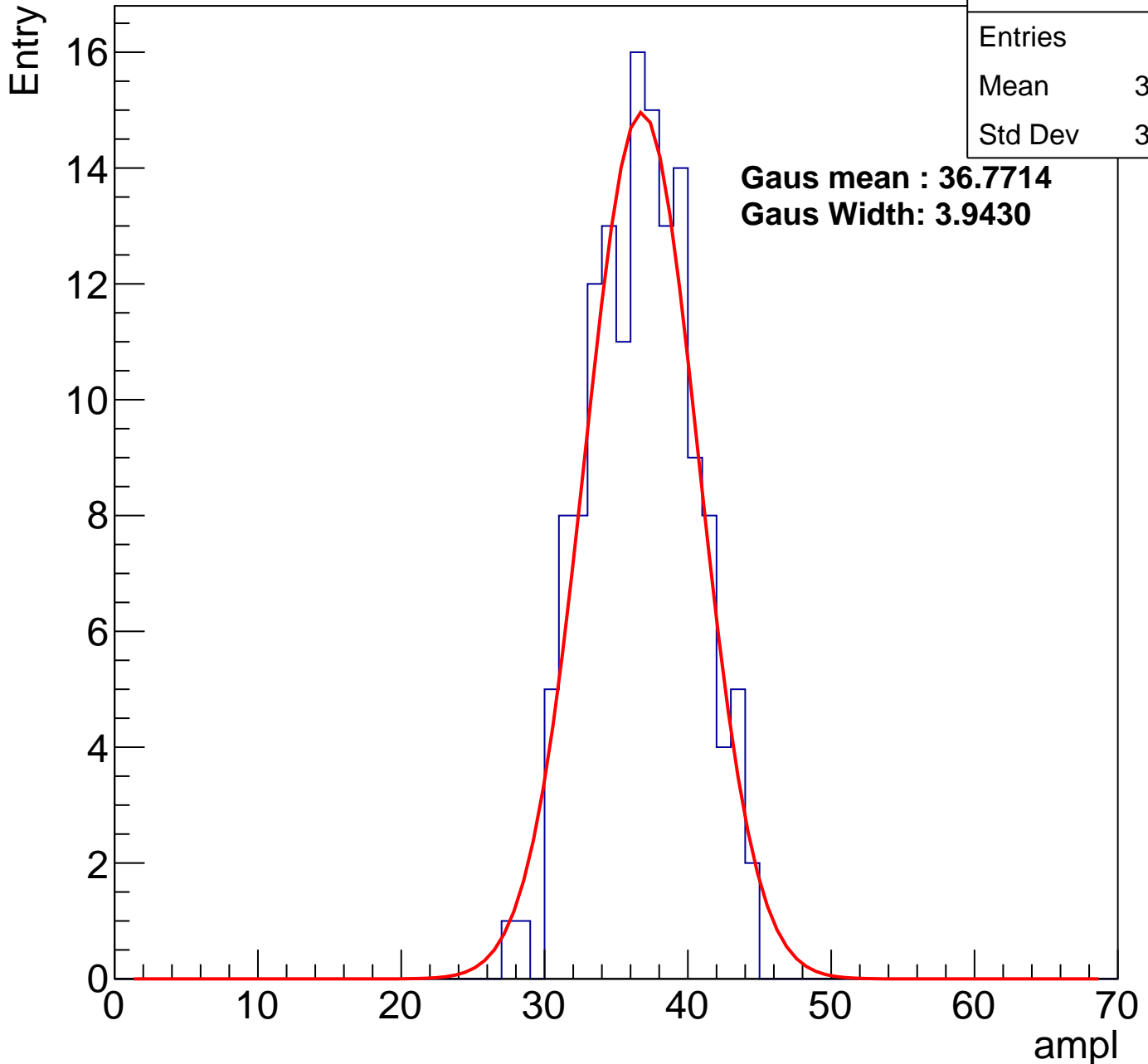


B1L001S, U14-ch75, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	36.29
Std Dev	3.587

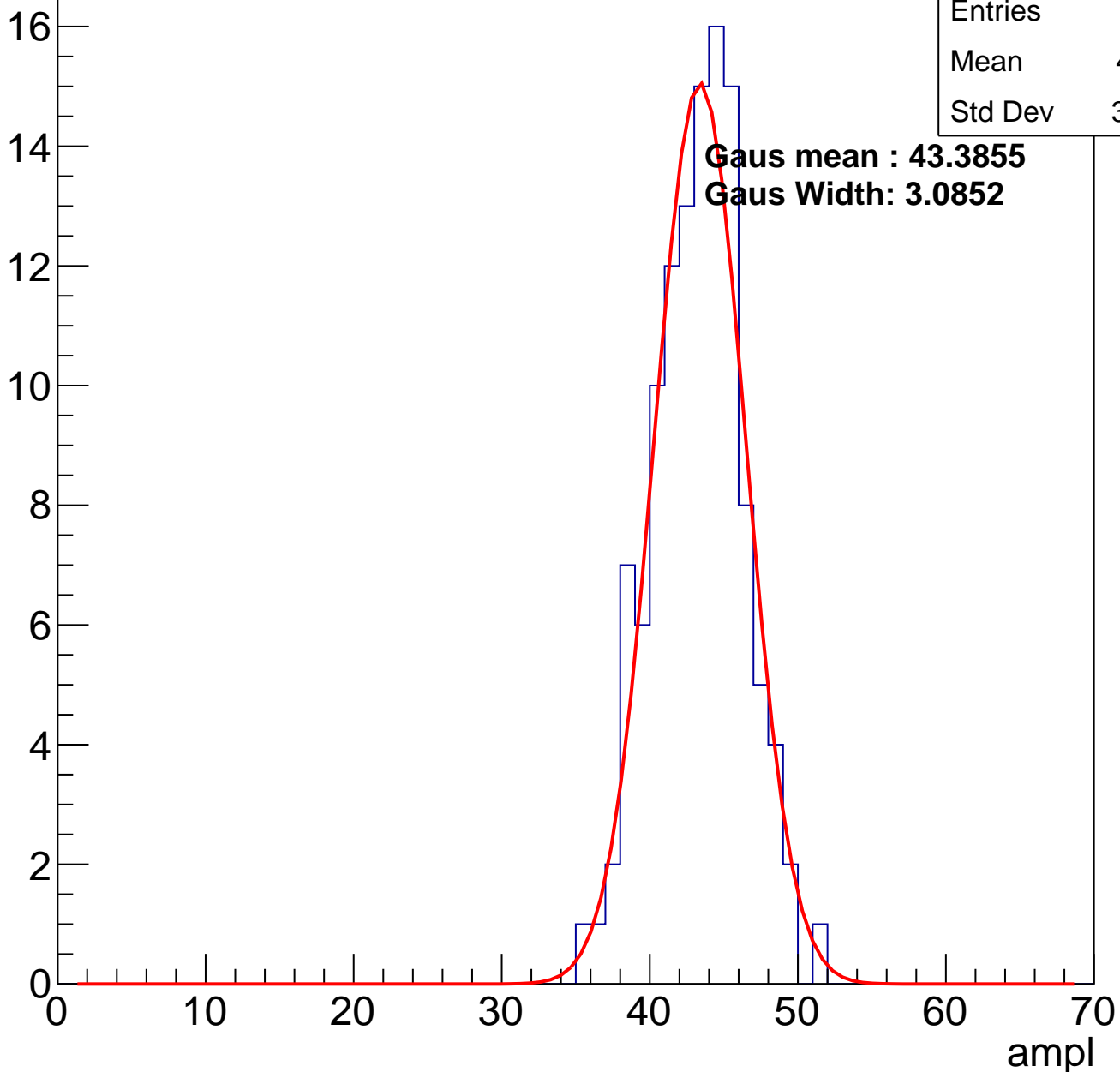
Gaus mean : 36.7714
Gaus Width: 3.9430



B1L001S, U14-ch75, adc2

calib_packv5_042523_0143.root, FC#2, port C2

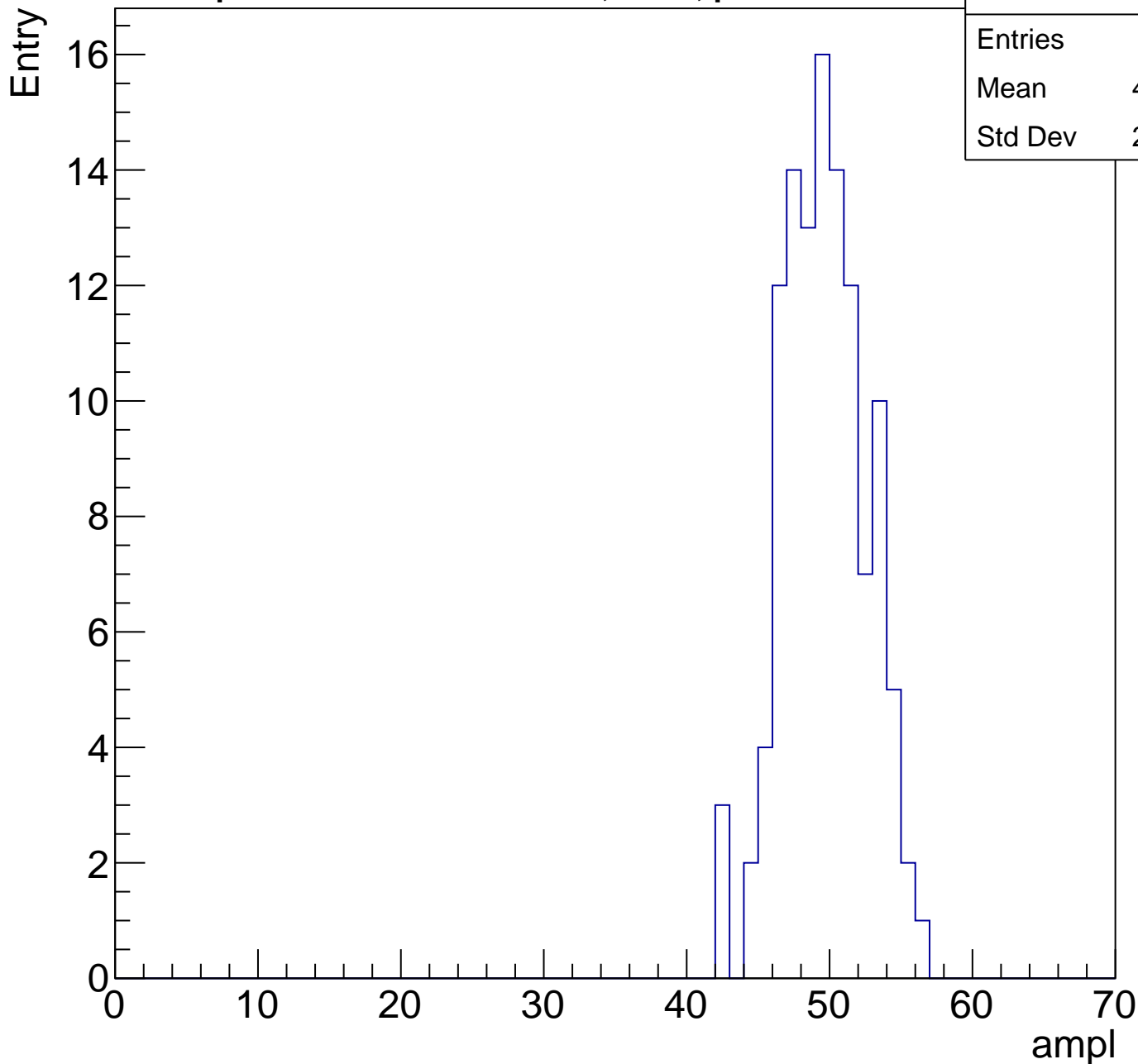
Entry



B1L001S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	49.17
Std Dev	2.898



B1L001S, U14-ch75, adc4

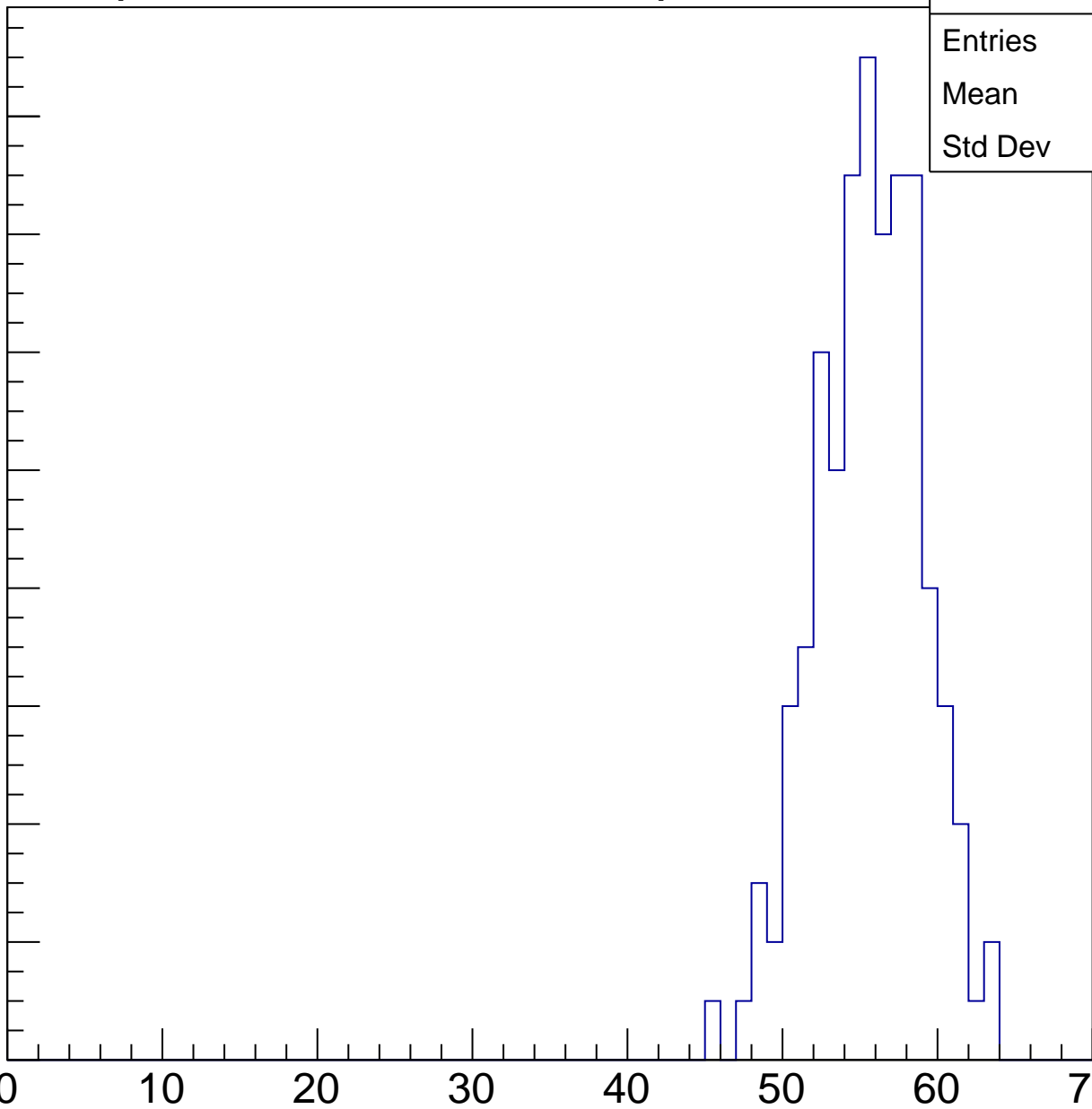
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	139
Mean	55.13
Std Dev	3.407

ampl



B1L001S, U14-ch75, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

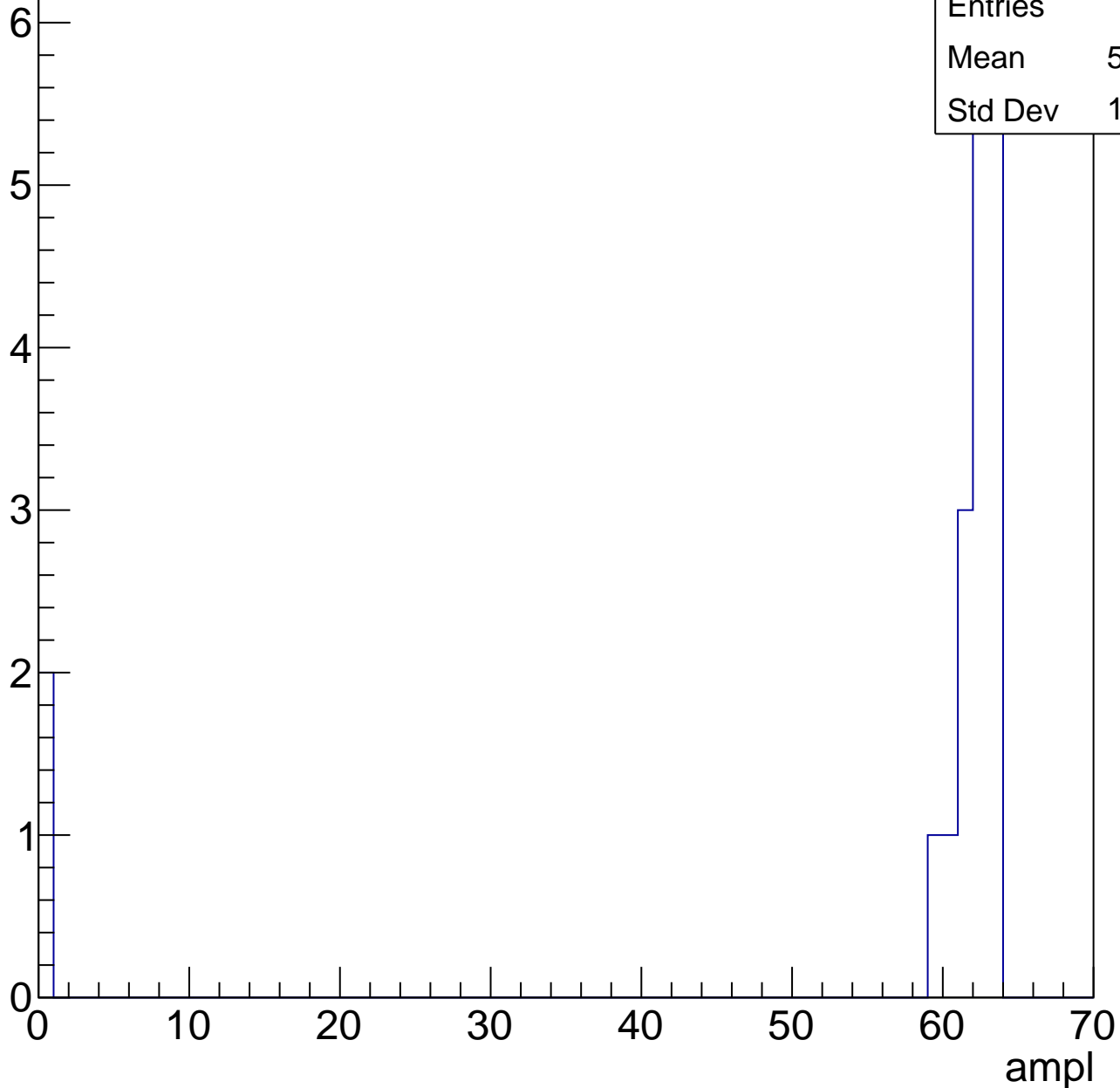
Entries	79
Mean	60.08
Std Dev	2.299

B1L001S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	55.37
Std Dev	19.02



B1L001S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B1L001S, U14-ch76, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	27.16
Std Dev	3.321

Gaus mean : 27.7185

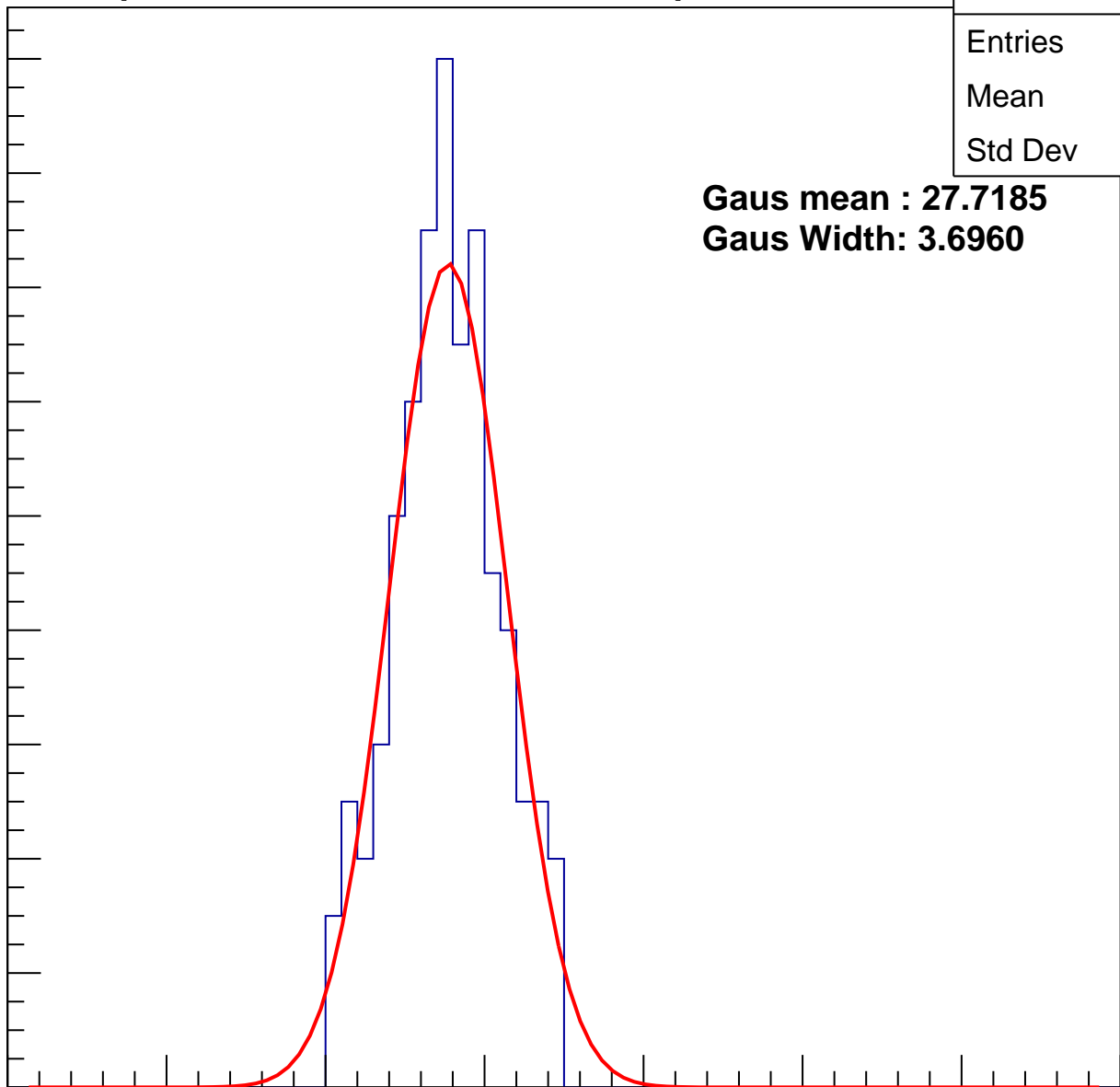
Gaus Width: 3.6960

Entry

18
16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



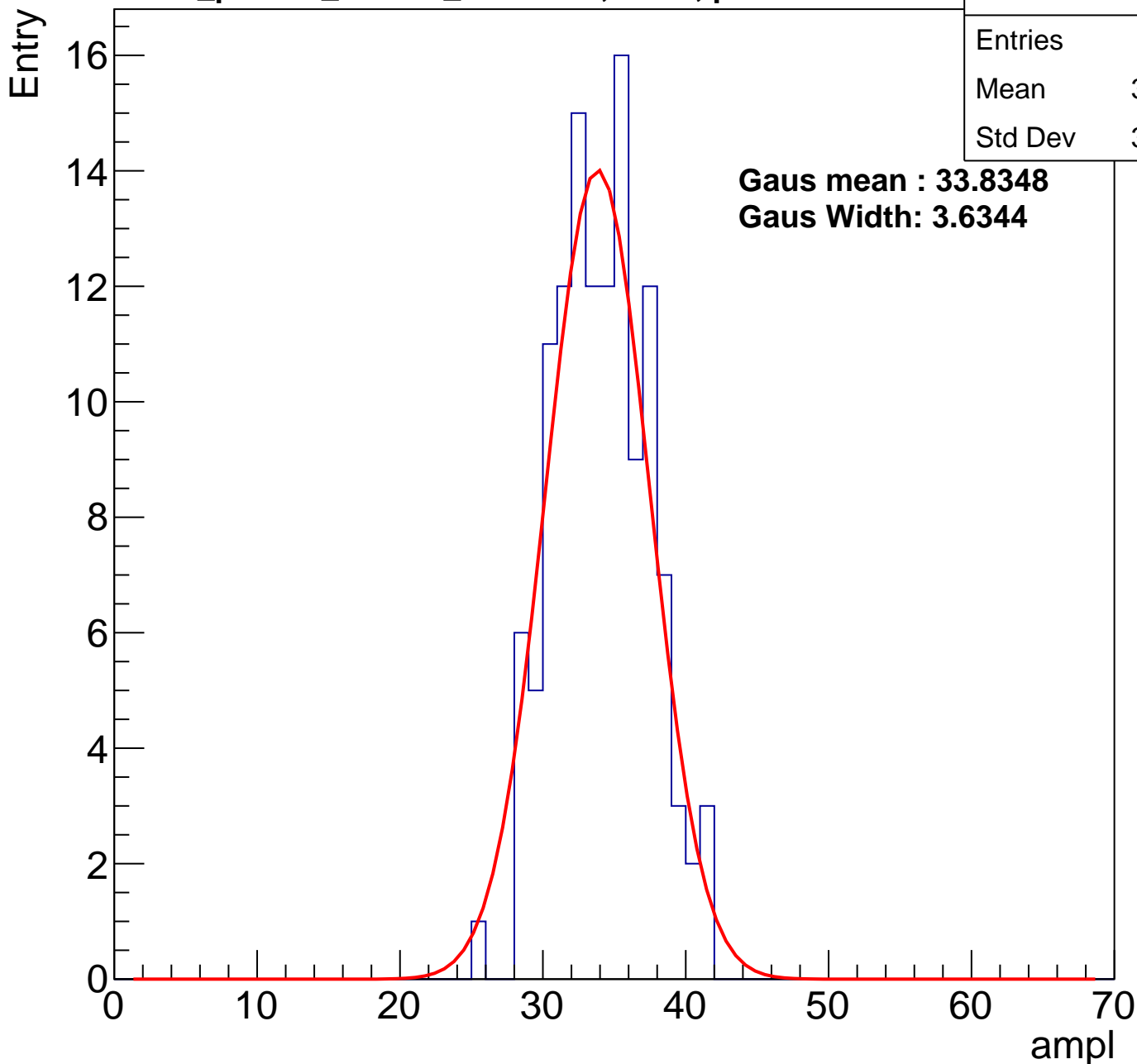
B1L001S, U14-ch76, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	33.63
Std Dev	3.246

Gaus mean : 33.8348

Gaus Width: 3.6344



B1L001S, U14-ch76, adc2

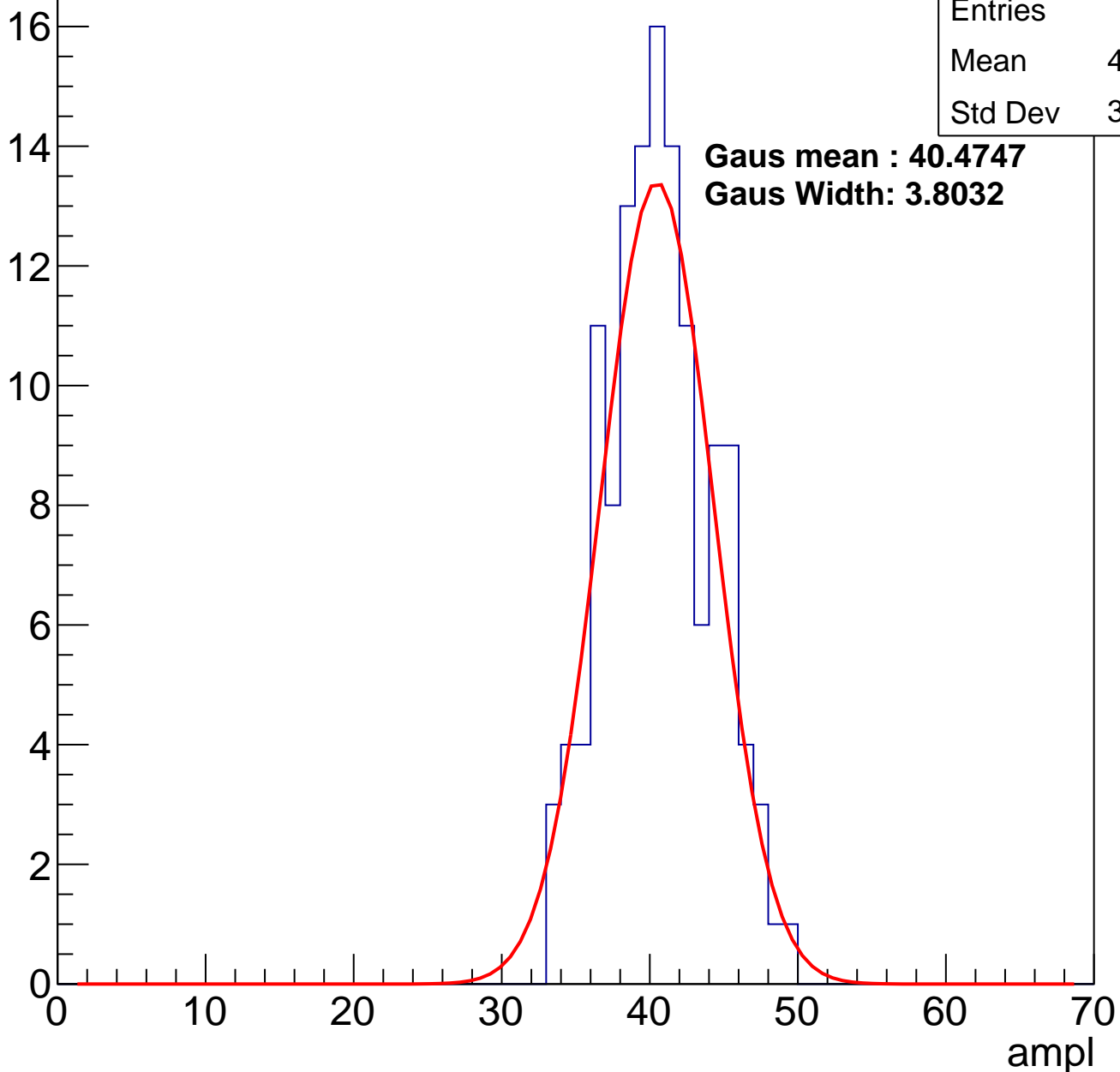
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	40.18
Std Dev	3.514

Gaus mean : 40.4747

Gaus Width: 3.8032

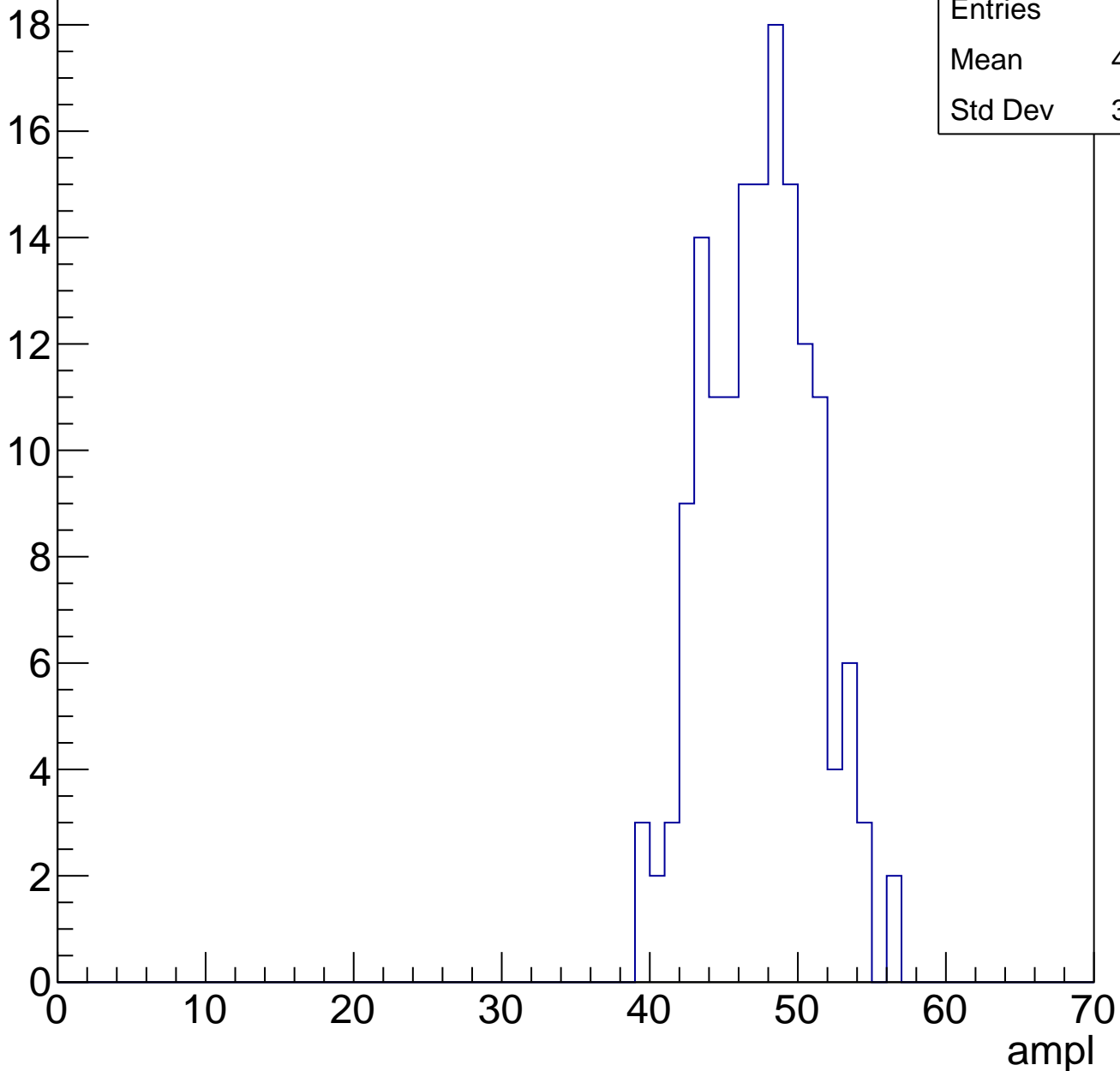
Entry



B1L001S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch76, adc4

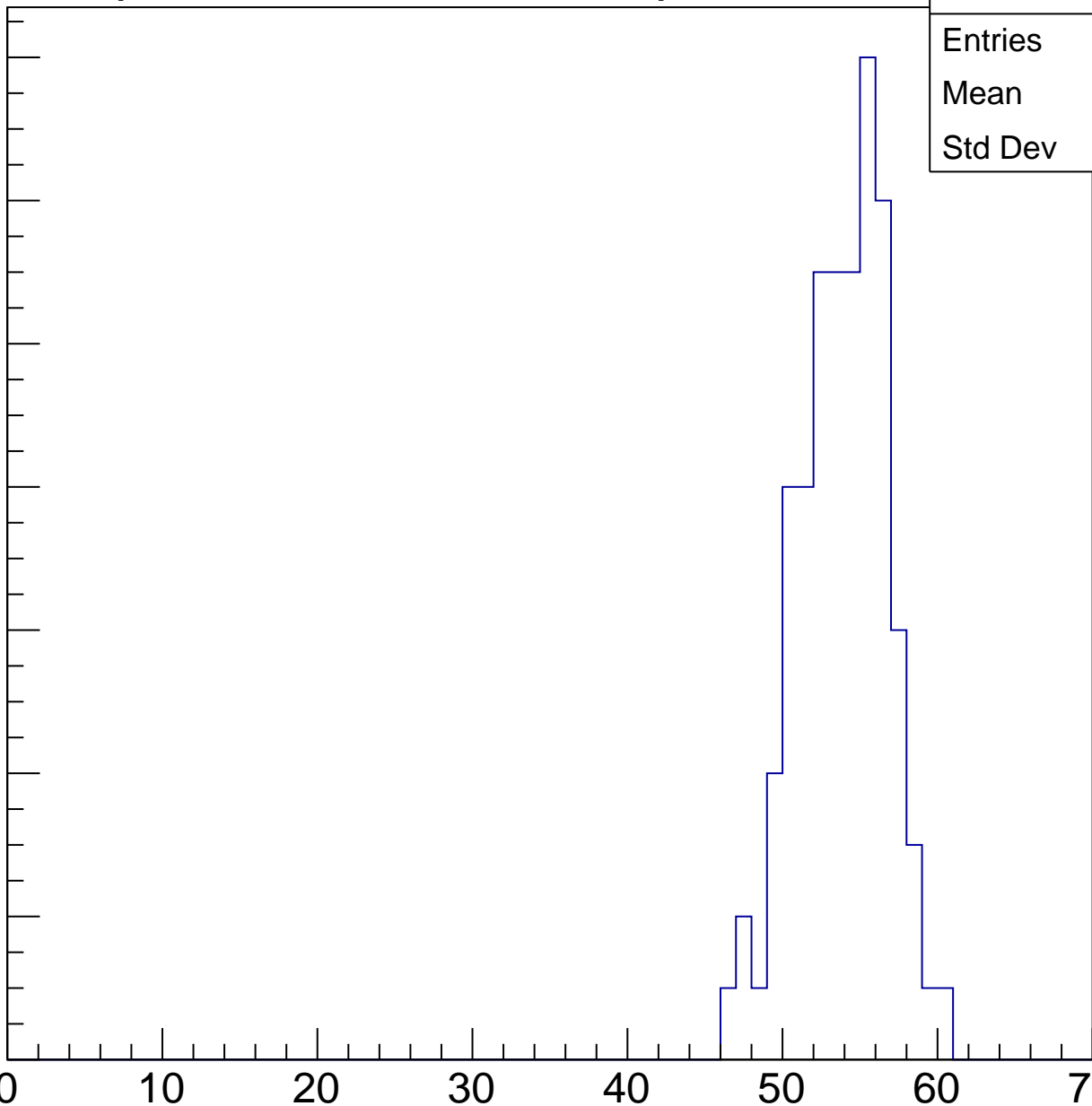
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	94
Mean	53.38
Std Dev	2.81

ampl



B1L001S, U14-ch76, adc5

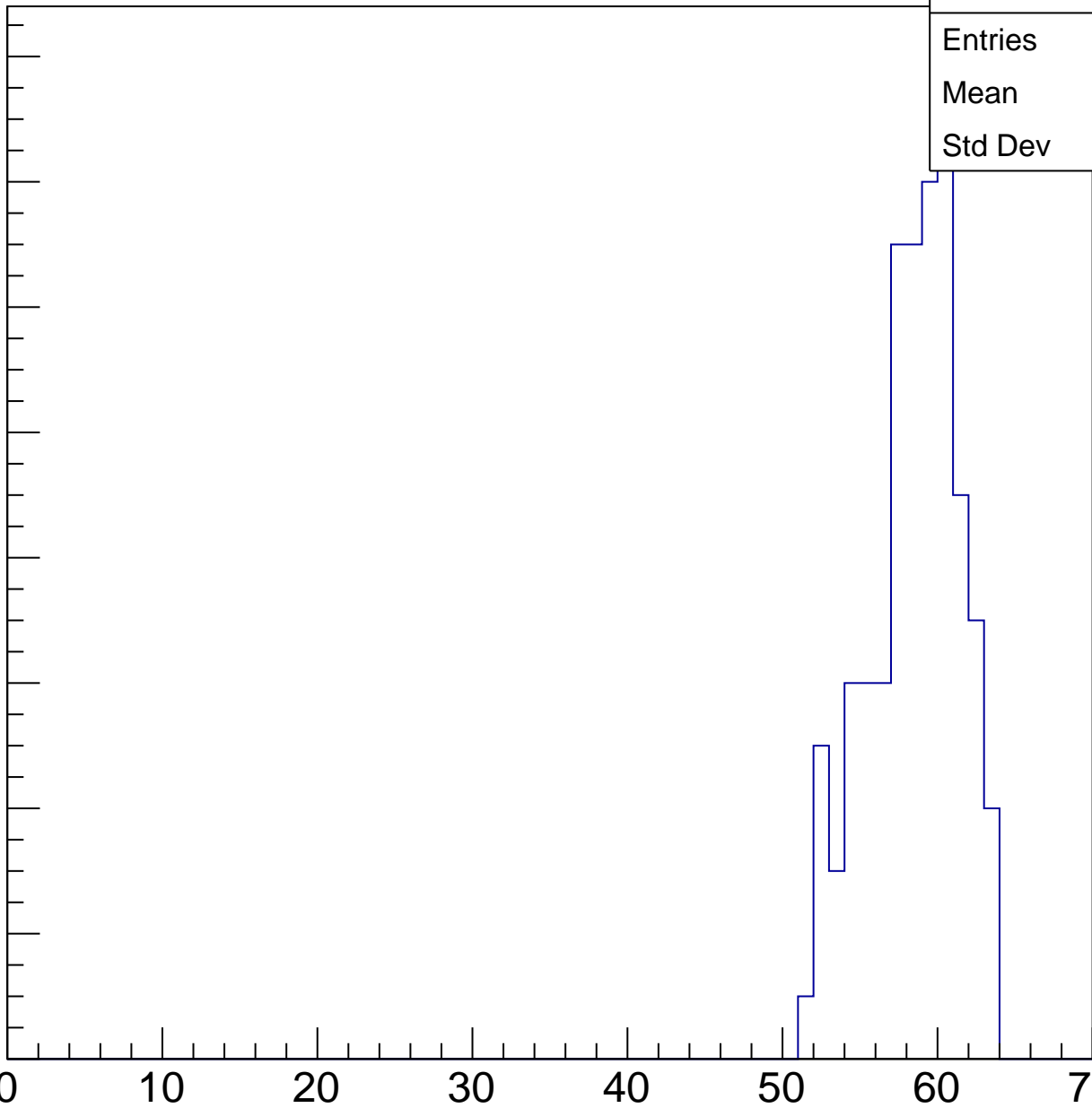
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	103
Mean	58.02
Std Dev	2.893

ampl



B1L001S, U14-ch76, adc6

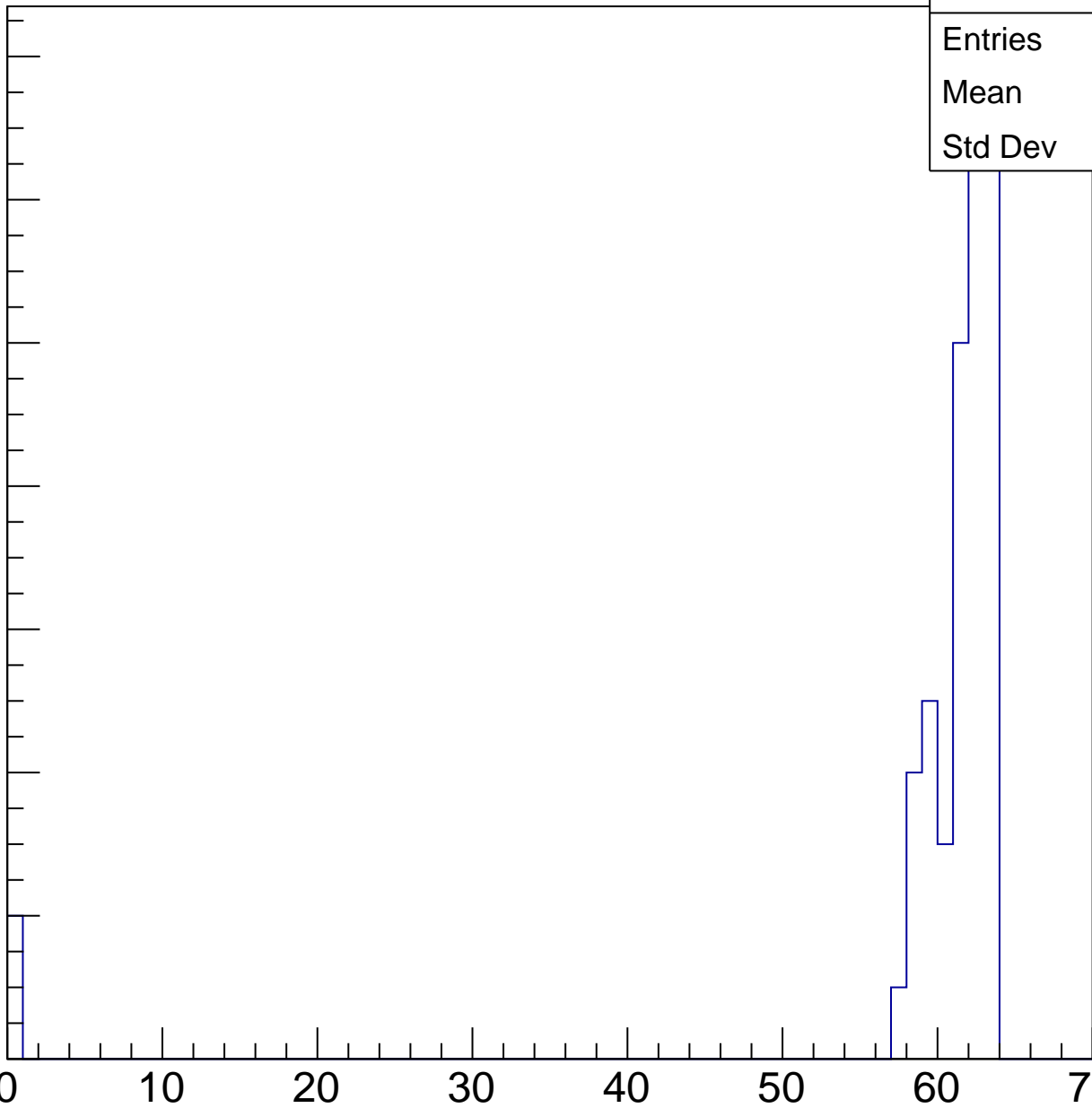
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	52
Mean	58.88
Std Dev	11.89

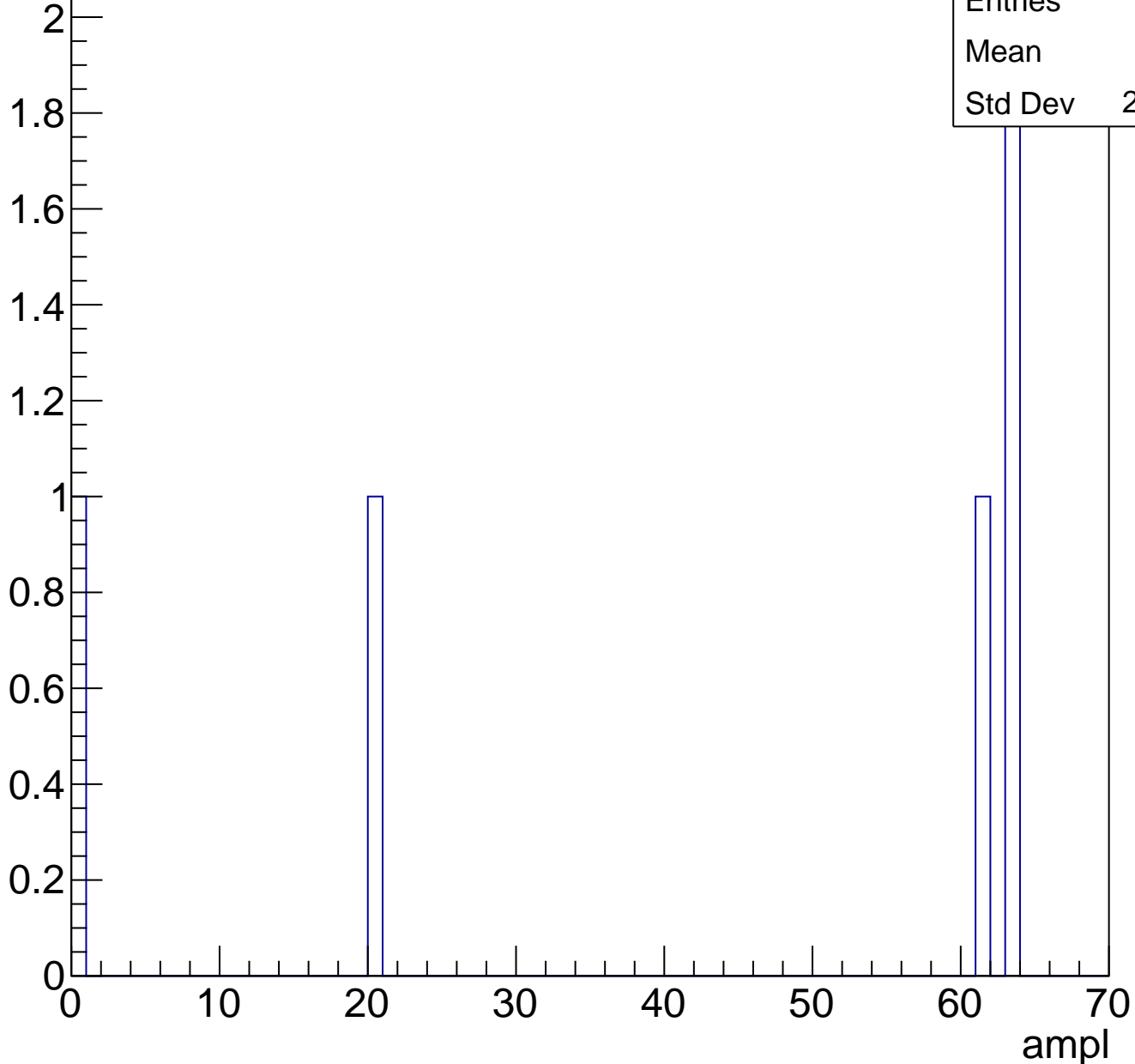
ampl



B1L001S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	41.4
Std Dev	26.42

B1L001S, U14-ch77, adc0

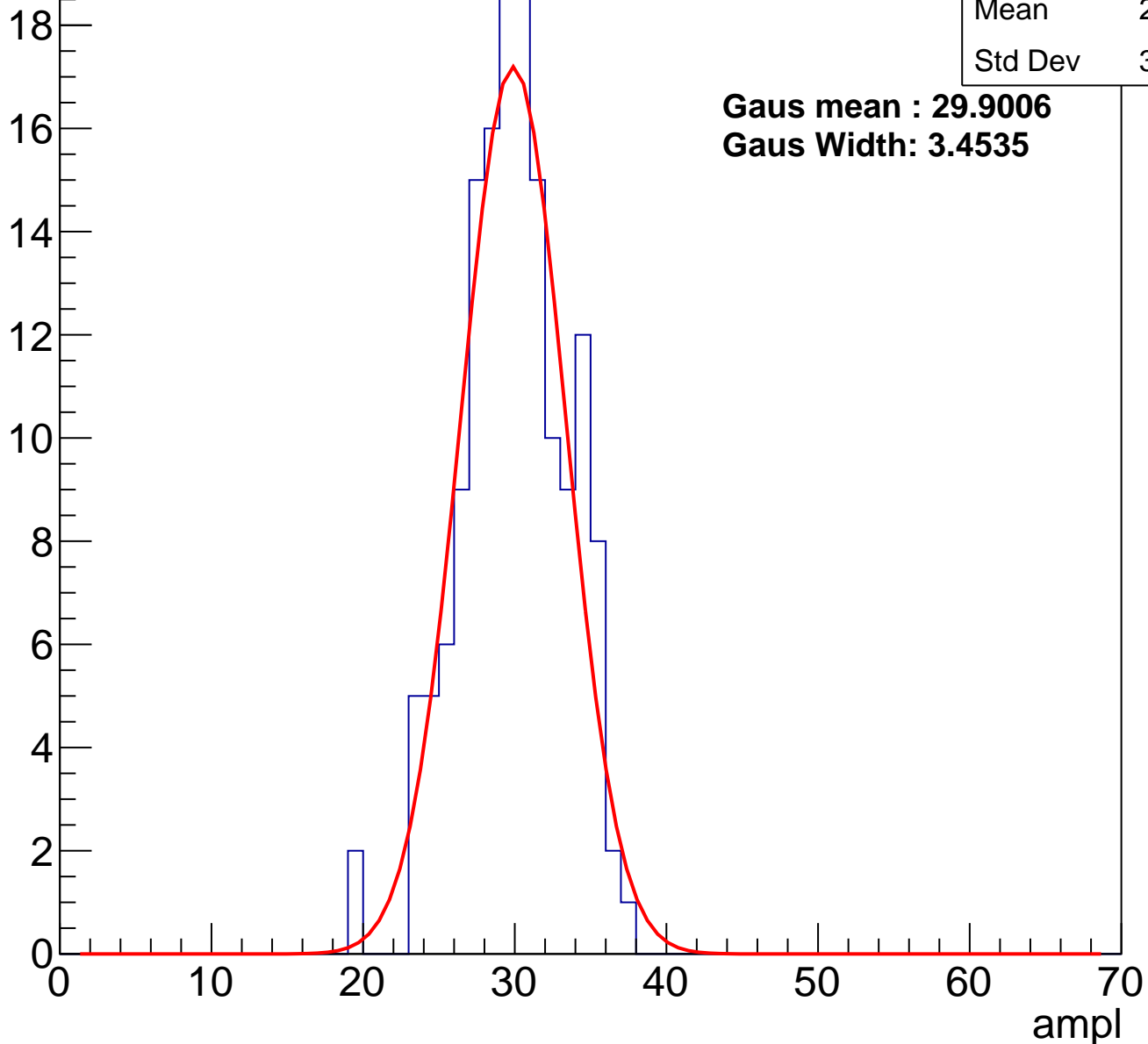
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.48
Std Dev	3.417

Gaus mean : 29.9006

Gaus Width: 3.4535

Entry



B1L001S, U14-ch77, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	37.11
Std Dev	3.342

Gaus mean : 37.2542

Gaus Width: 3.6219

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

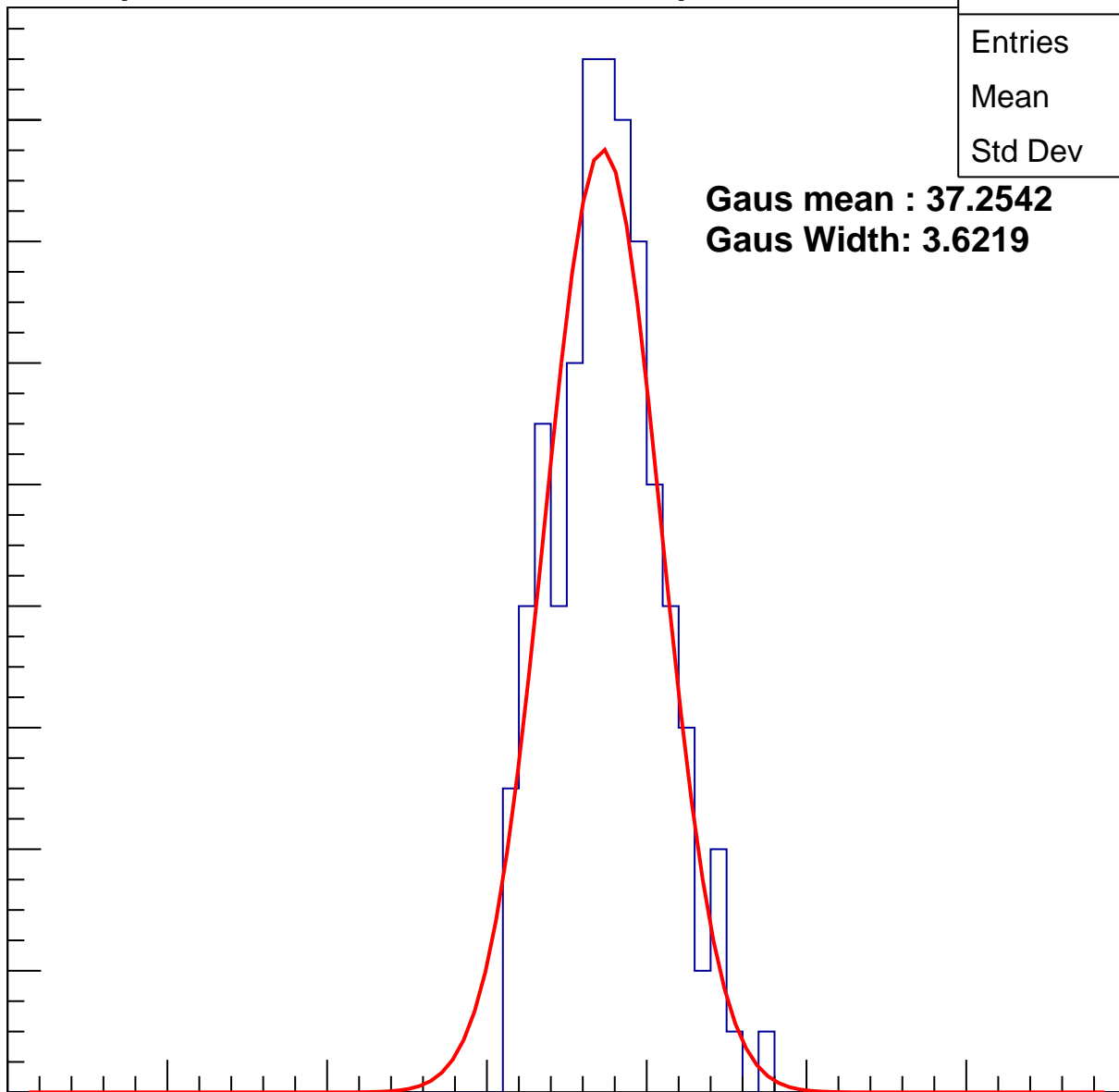
40

50

60

70

ampl



B1L001S, U14-ch77, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

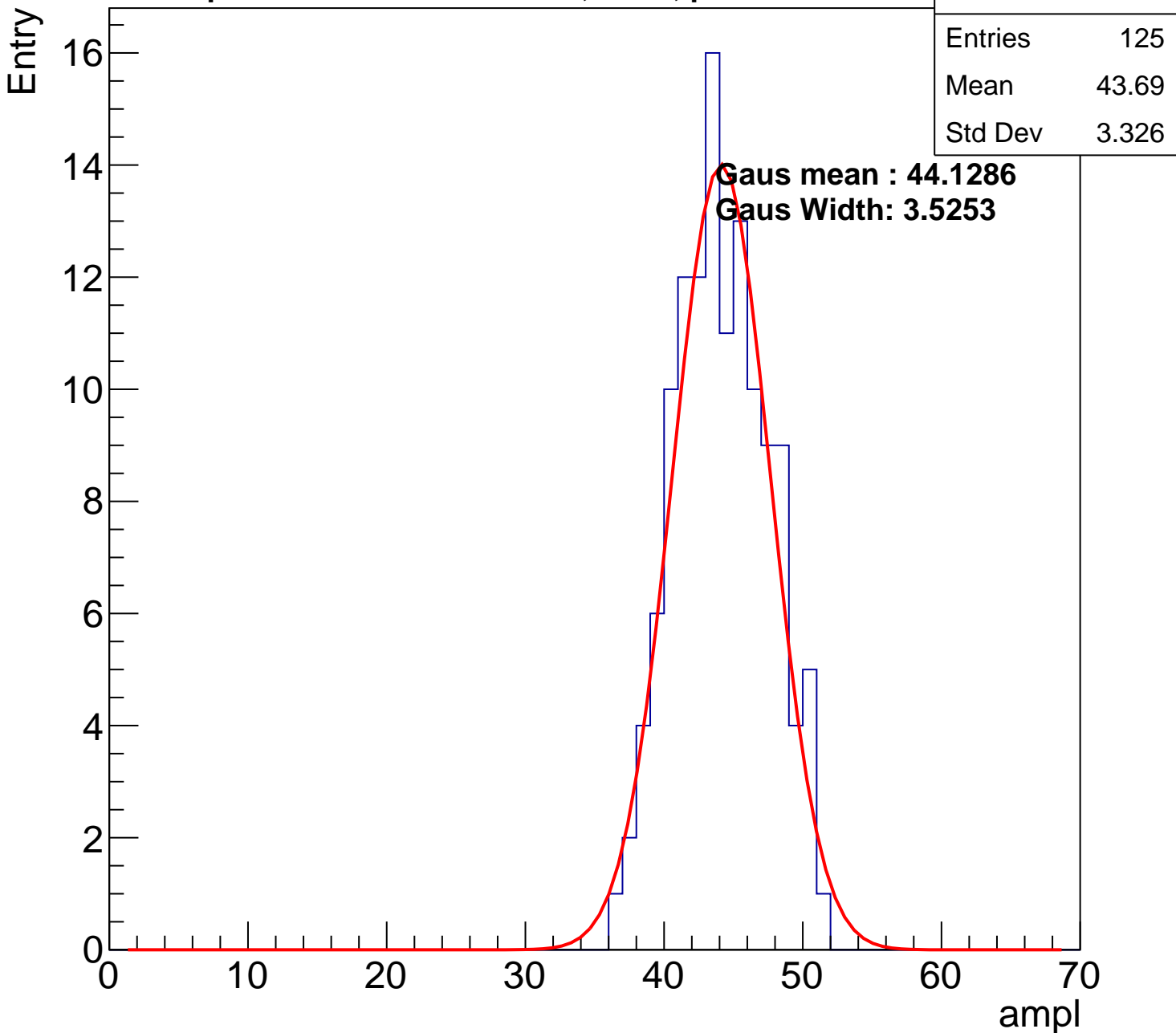
16
14
12
10
8
6
4
2
0

Entries	125
Mean	43.69
Std Dev	3.326

Gaus mean : 44.1286
Gaus Width: 3.5253

ampl

0 10 20 30 40 50 60 70

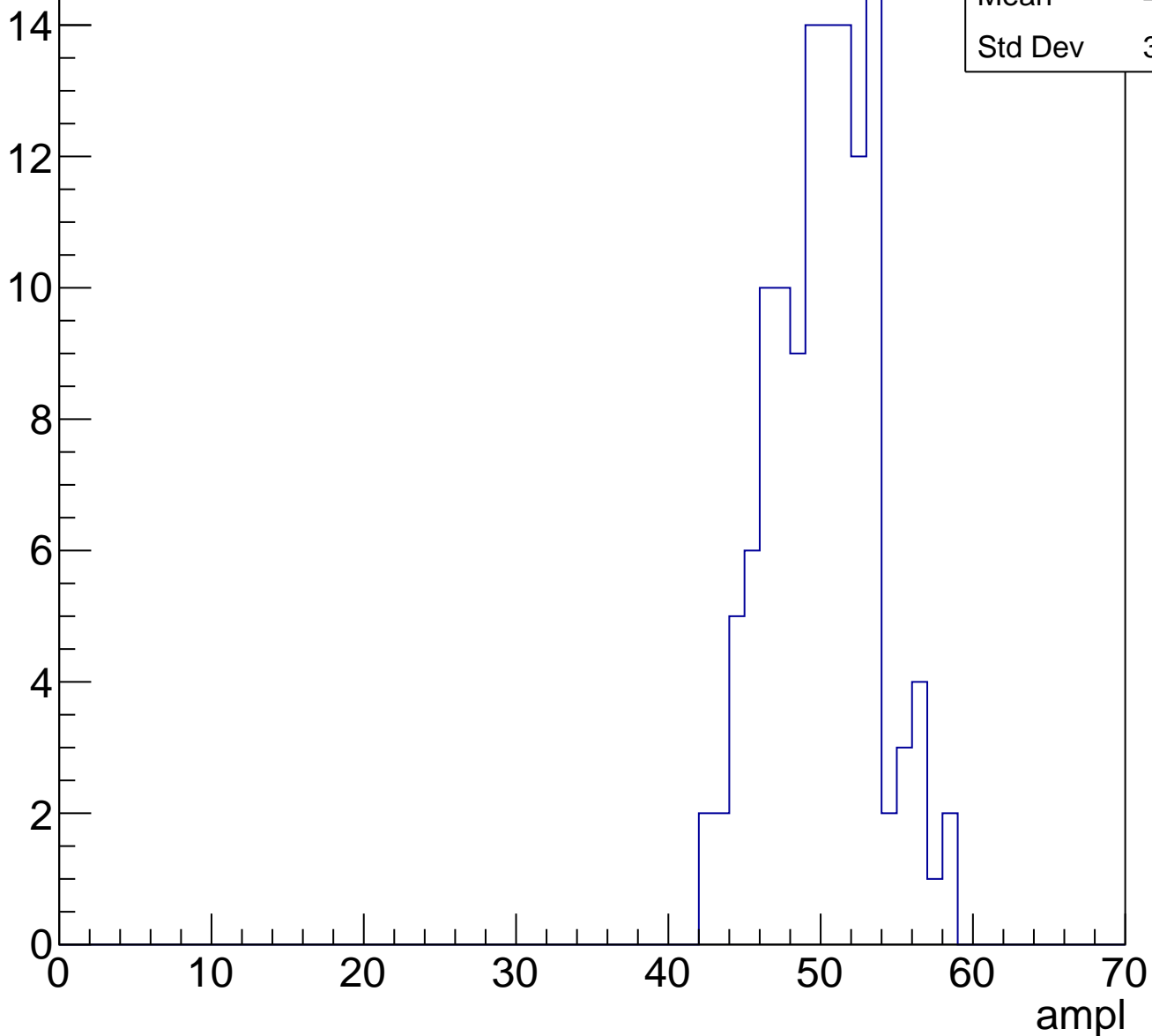


B1L001S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	125
Mean	49.69
Std Dev	3.428

Entry



B1L001S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#2, port C2

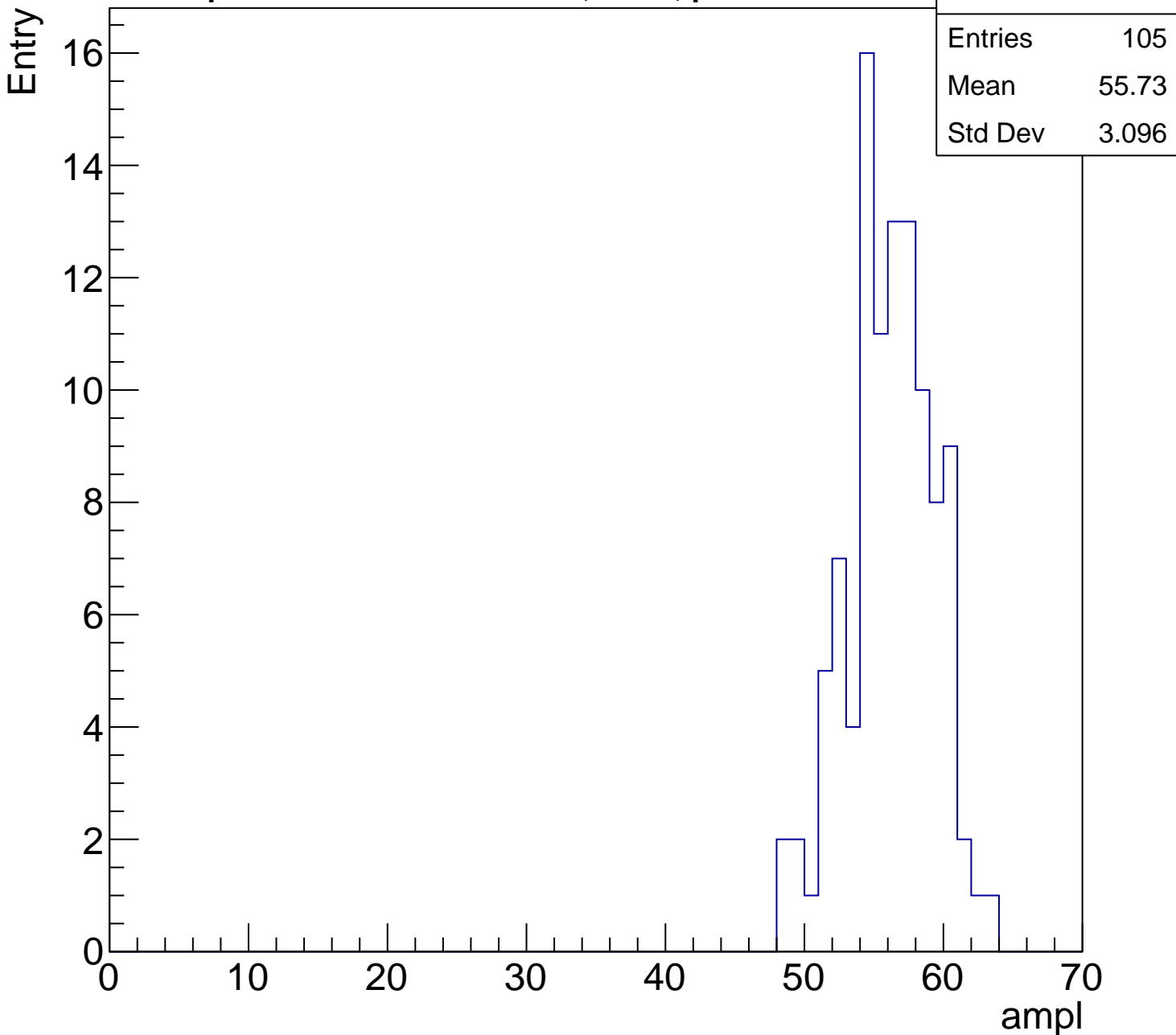
Entries	105
Mean	55.73
Std Dev	3.096

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch77, adc5

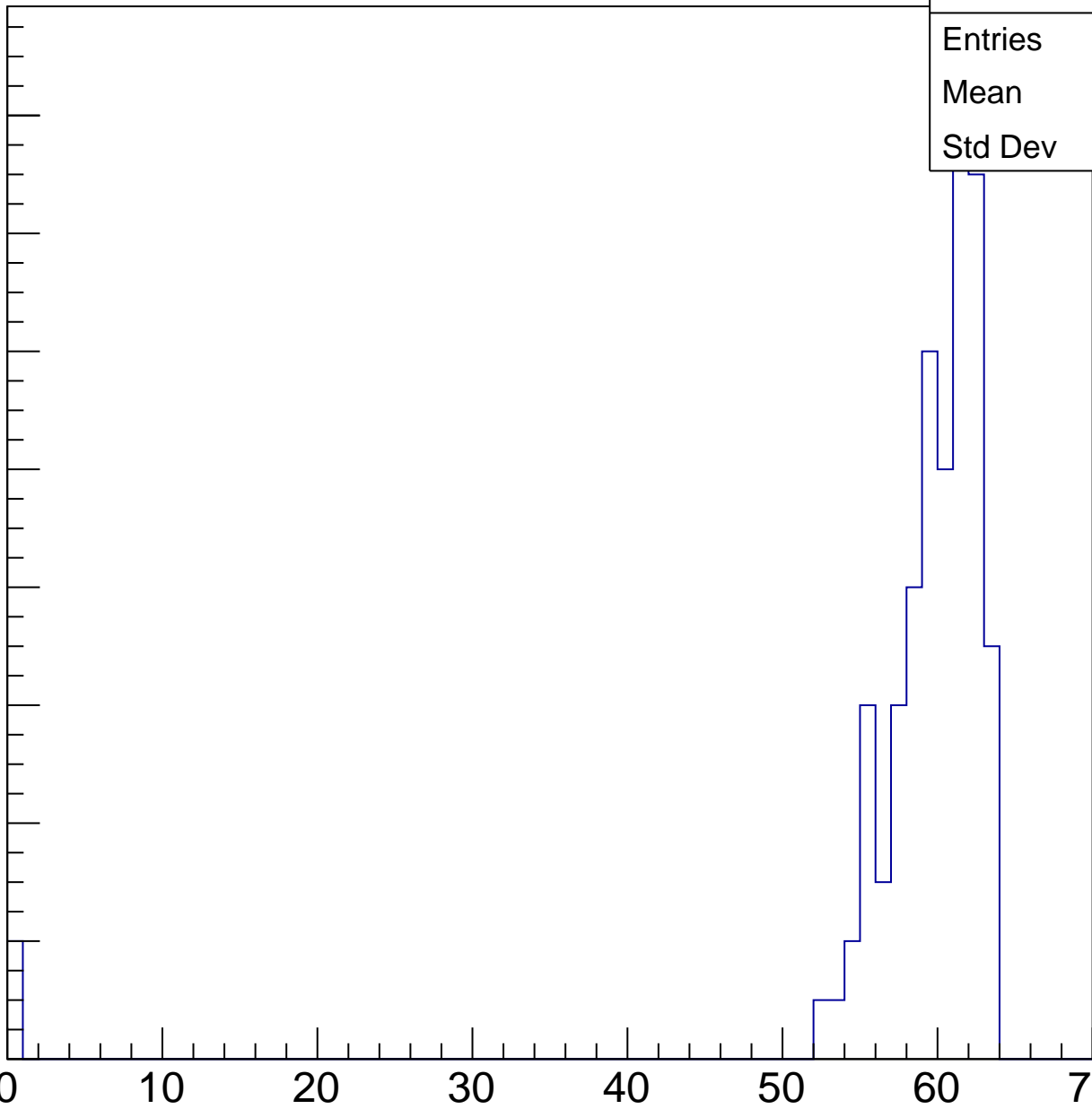
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	90
Mean	58.14
Std Dev	9.135

ampl



B1L001S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

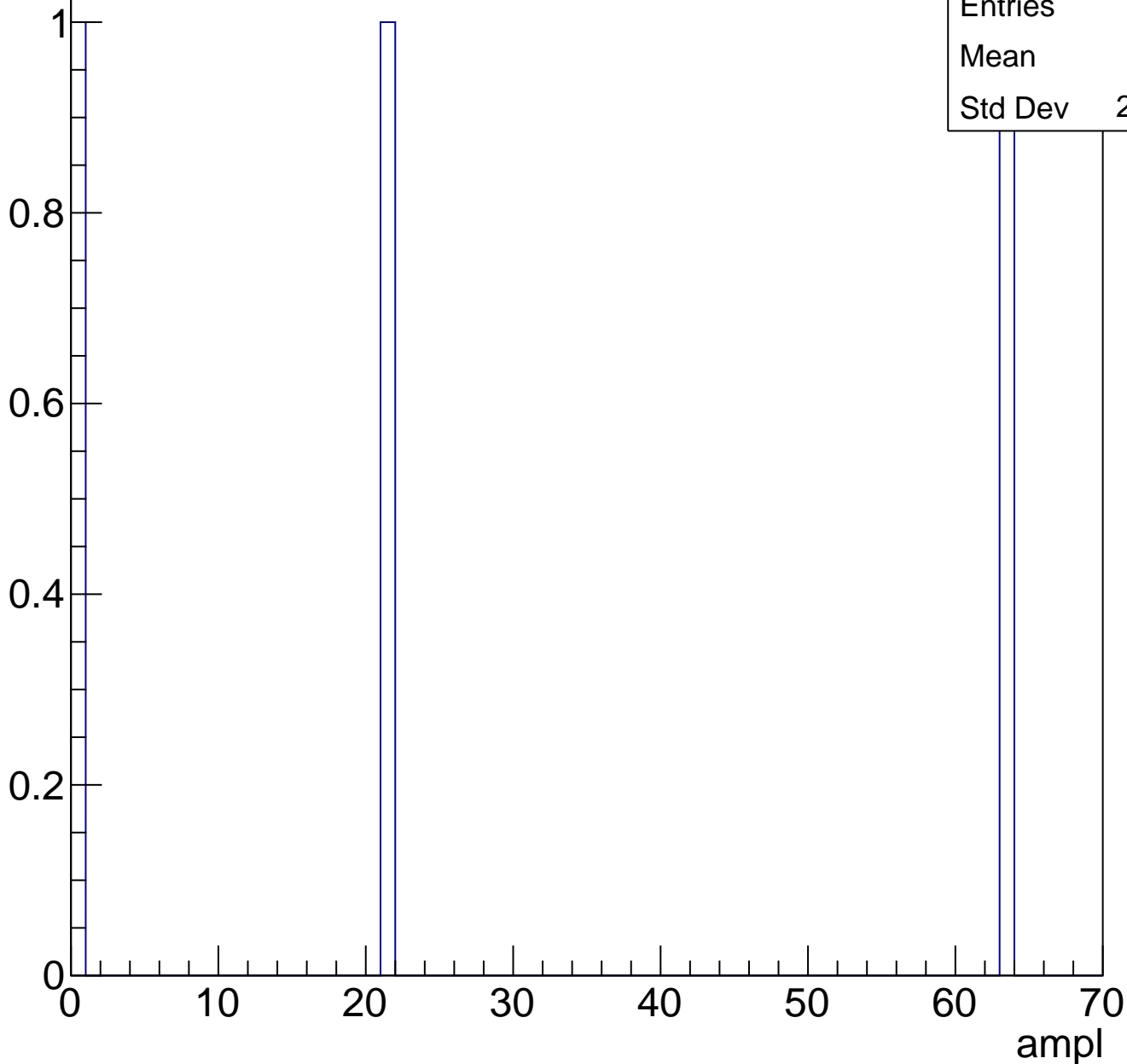
ampl

Entries	17
Mean	62.47
Std Dev	0.8484

B1L001S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch78, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	28.28
Std Dev	4.072

Gaus mean : 29.1596

Gaus Width: 2.9578

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

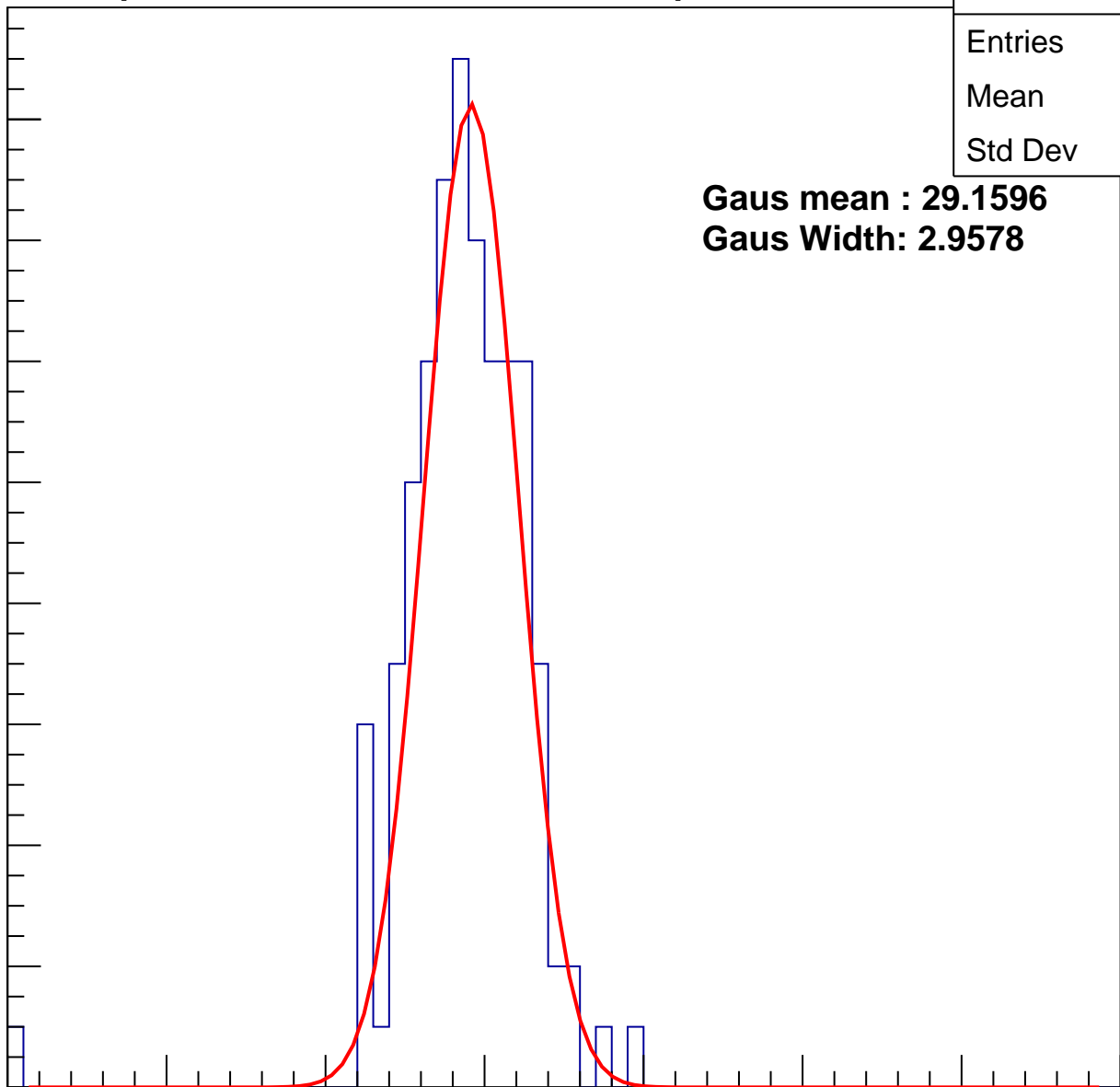
40

50

60

70

ampl



B1L001S, U14-ch78, adc1

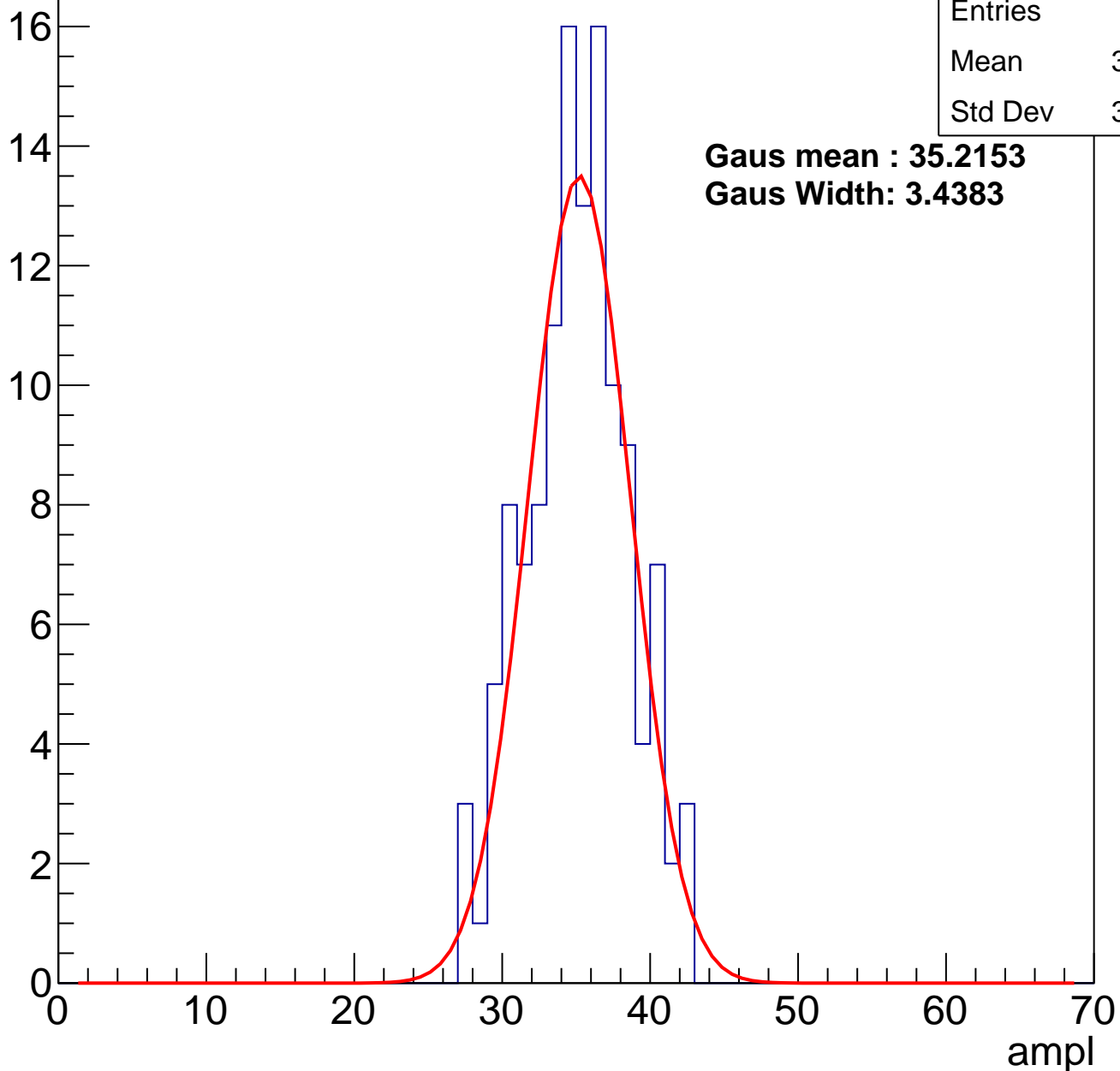
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	34.64
Std Dev	3.427

Gaus mean : 35.2153

Gaus Width: 3.4383

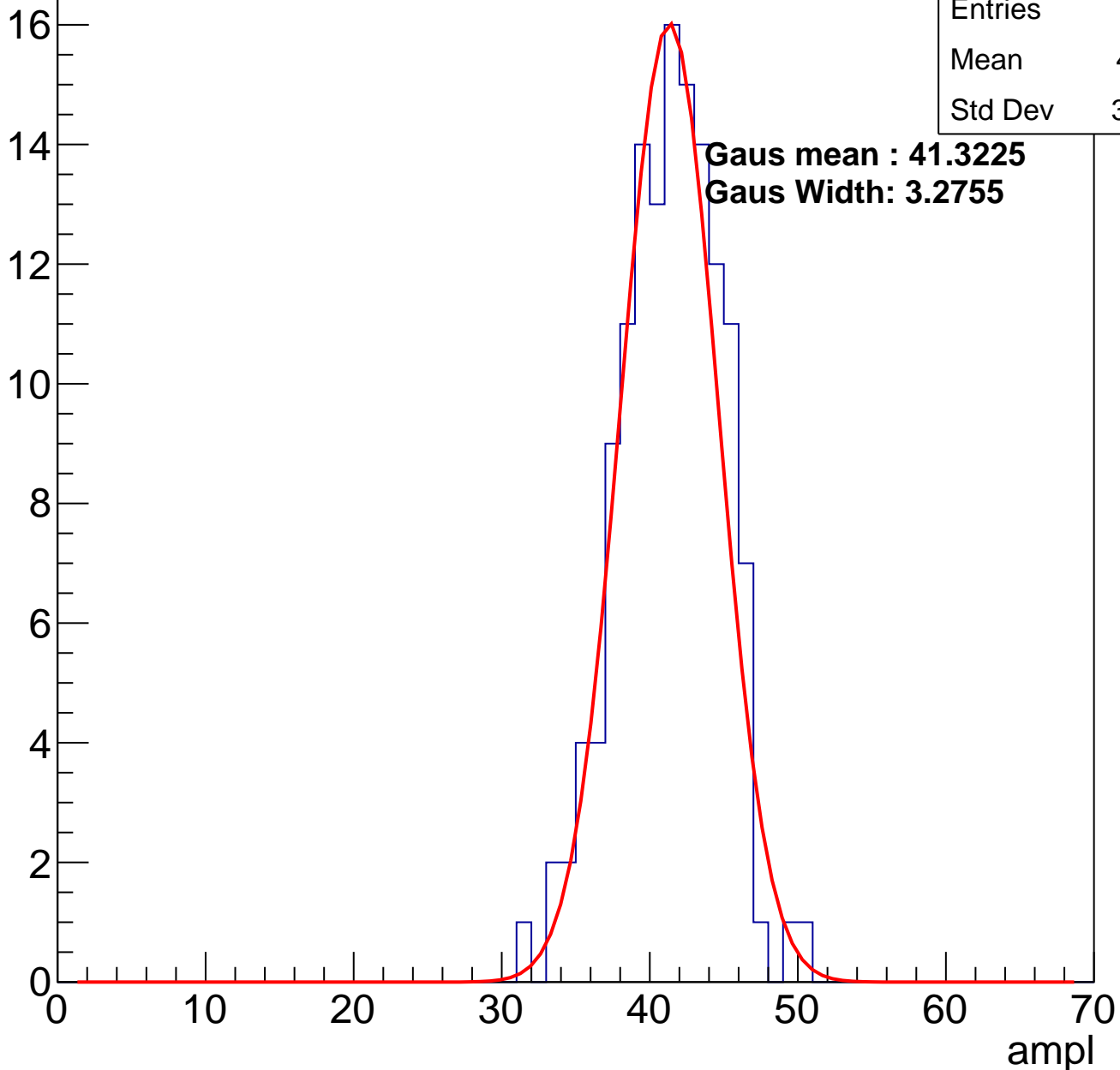
Entry



B1L001S, U14-ch78, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

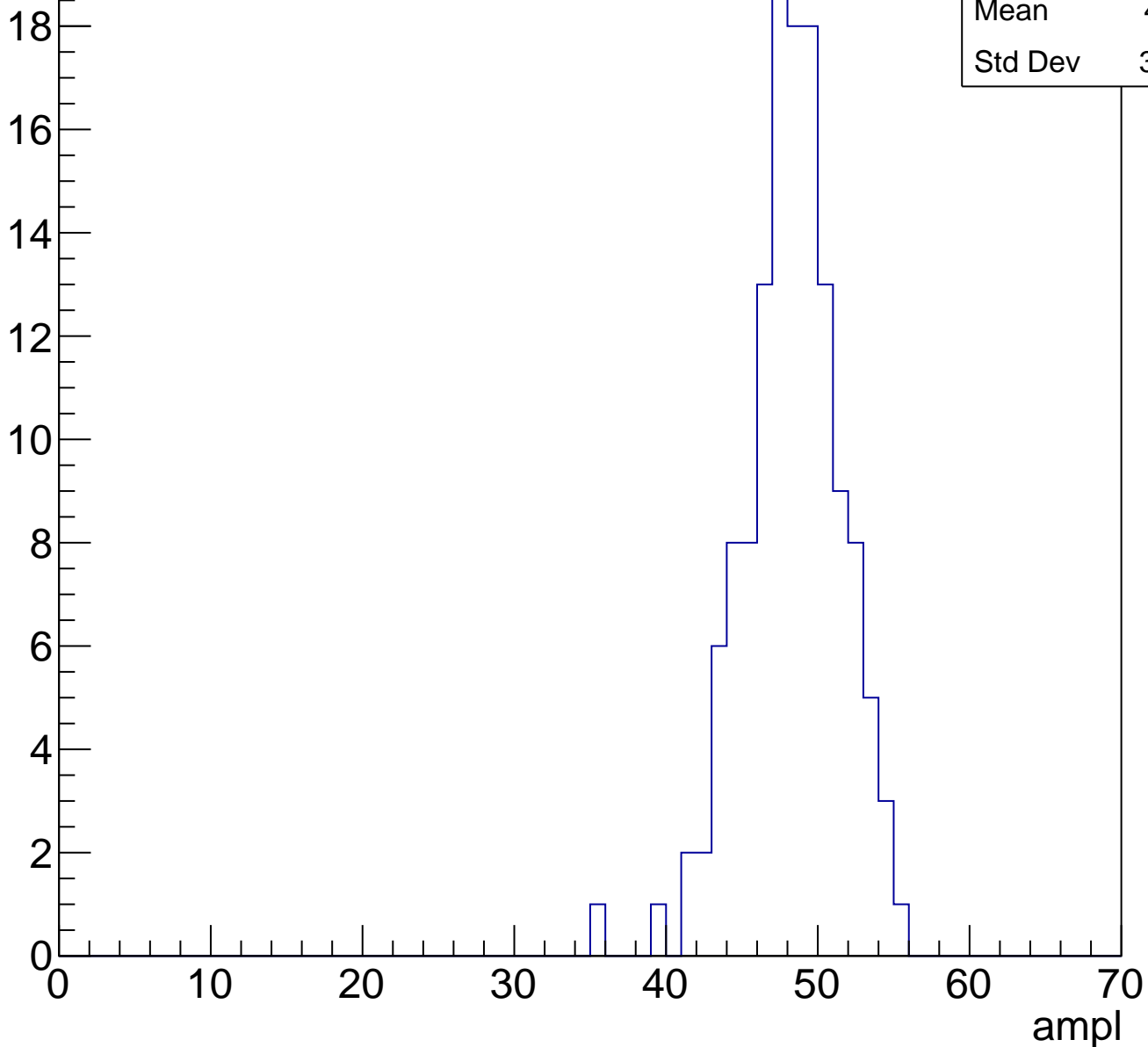


B1L001S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	47.81
Std Dev	3.217

Entry

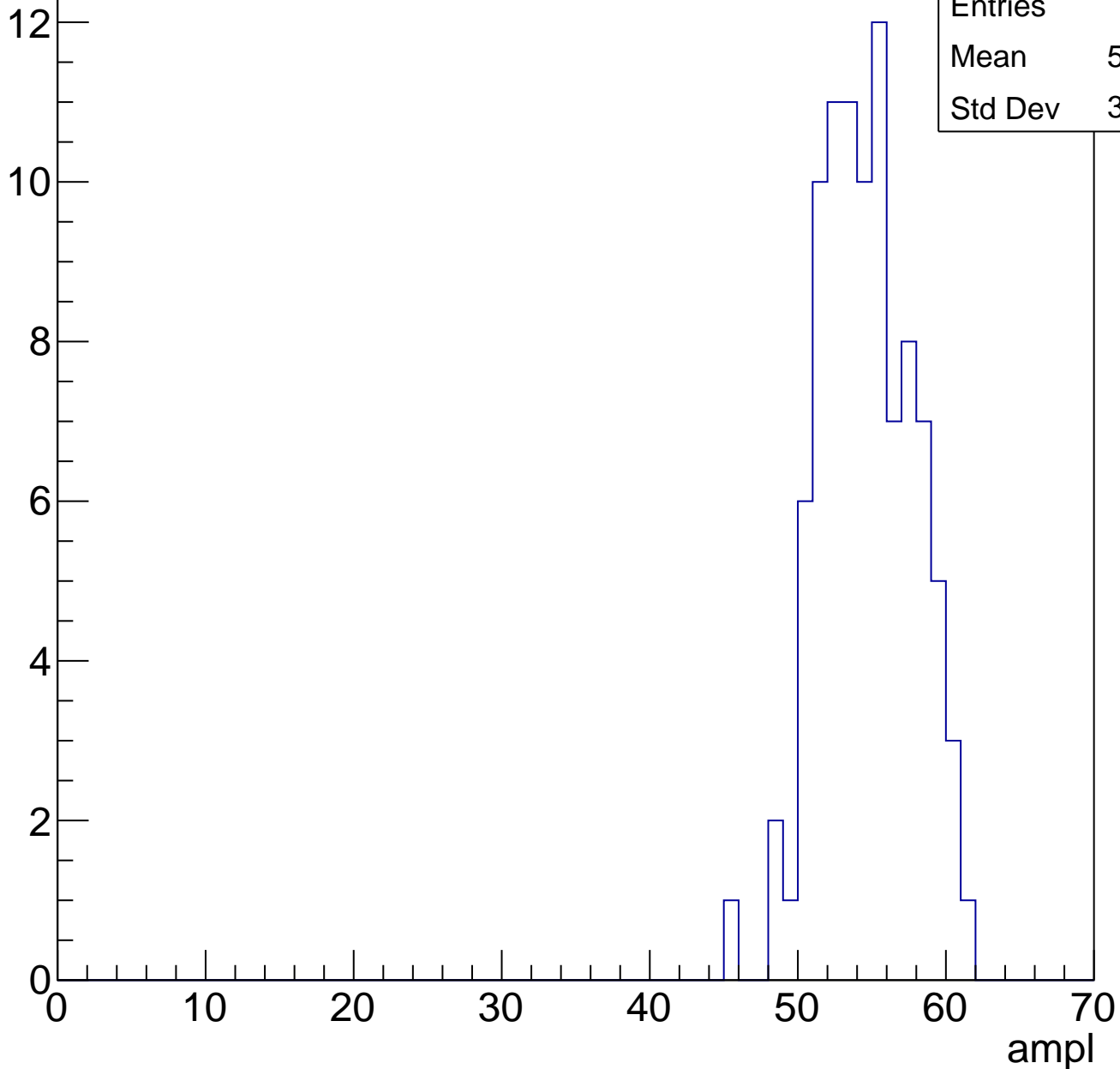


B1L001S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	95
Mean	54.16
Std Dev	3.116

Entry



B1L001S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	58.28
Std Dev	2.931

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70

B1L001S, U14-ch78, adc6

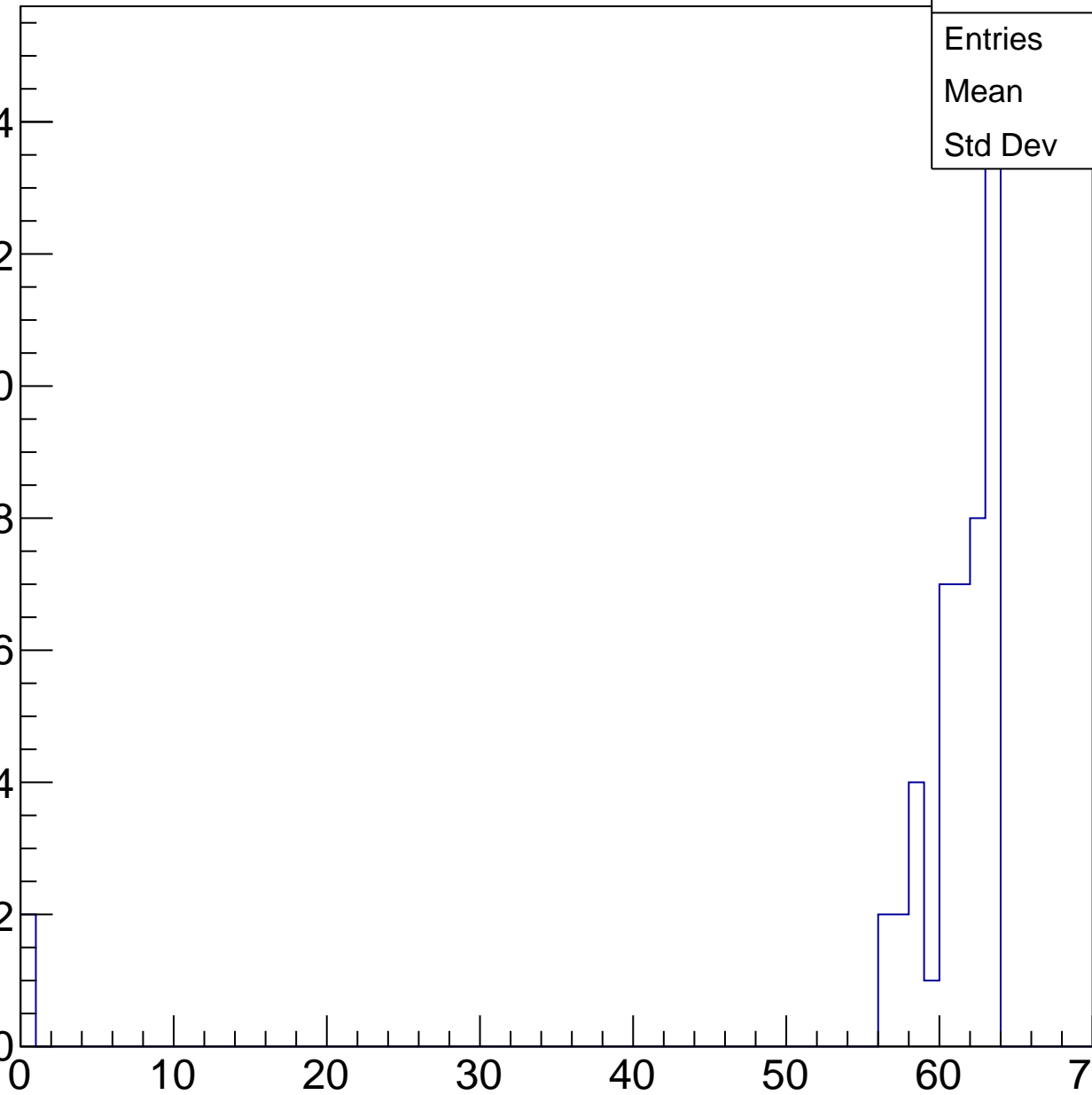
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	48
Mean	58.44
Std Dev	12.35

ampl



B1L001S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U14-ch79, adc0

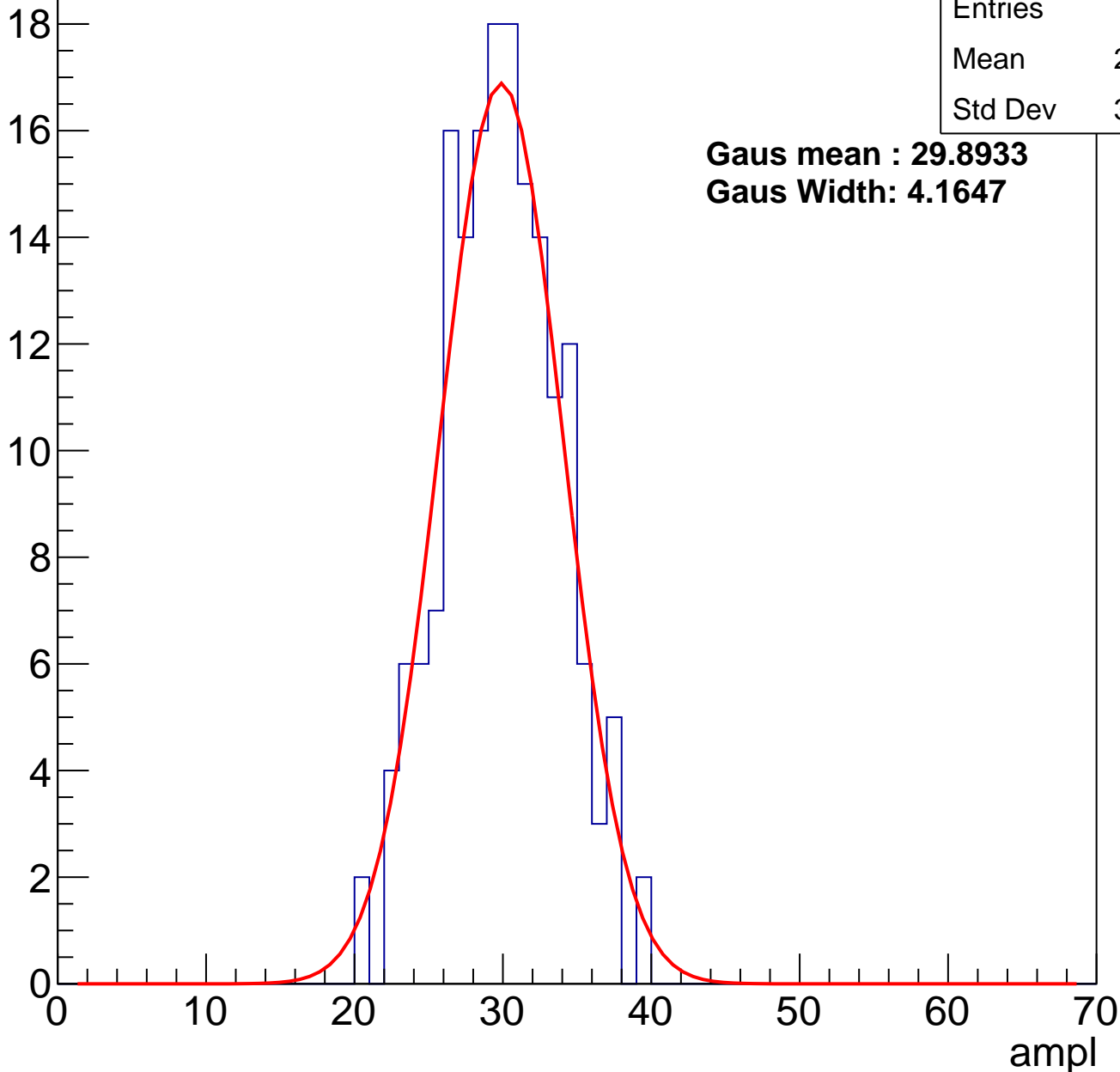
calib_packv5_042523_0143.root, FC#2, port C2

Entries	175
Mean	29.45
Std Dev	3.839

Gaus mean : 29.8933

Gaus Width: 4.1647

Entry



B1L001S, U14-ch79, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	36.38
Std Dev	3.312

Gaus mean : 37.0068

Gaus Width: 3.3971

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch79, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries

134

Mean

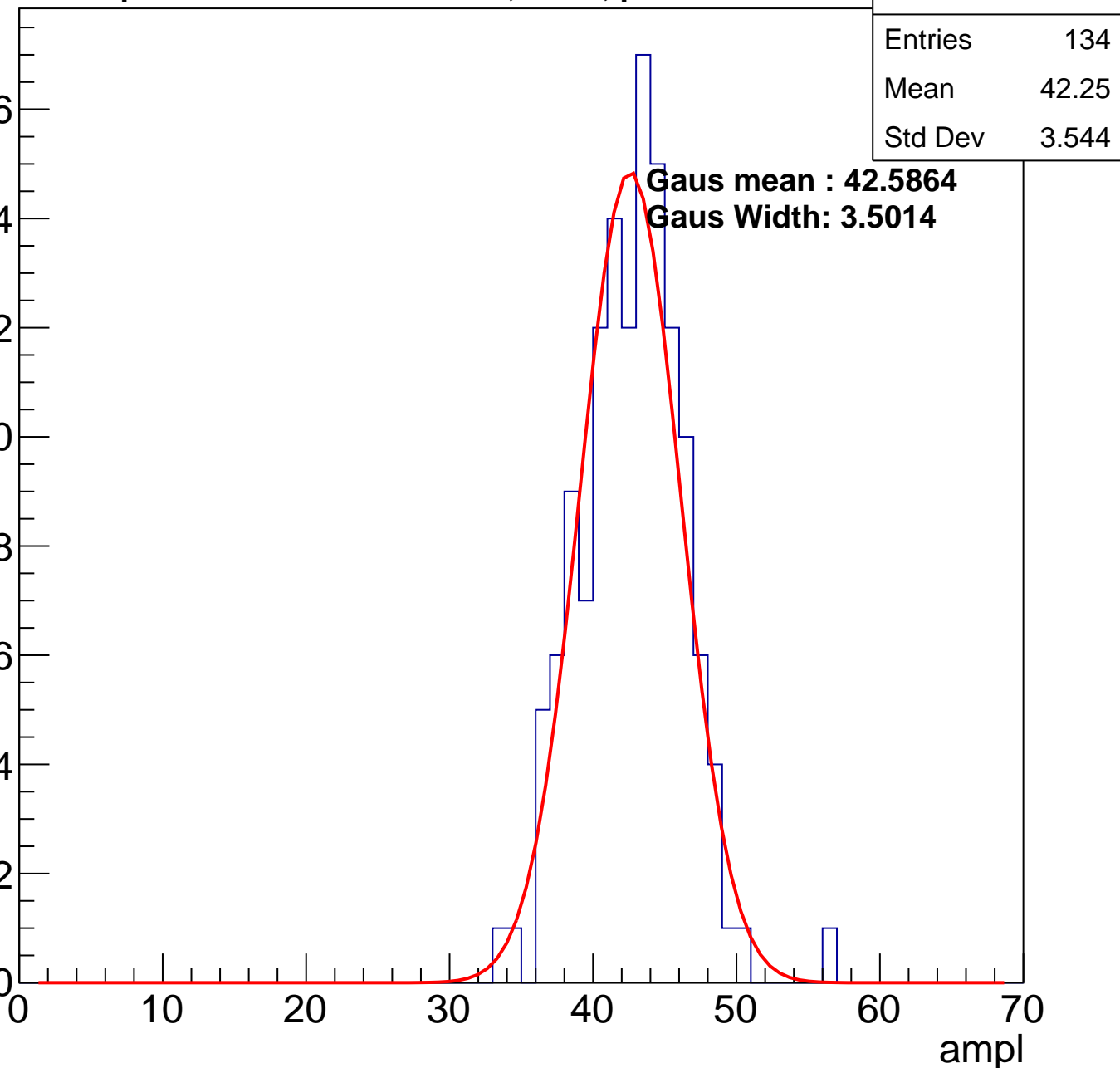
42.25

Std Dev

3.544

Gaus mean : 42.5864

Gaus Width: 3.5014

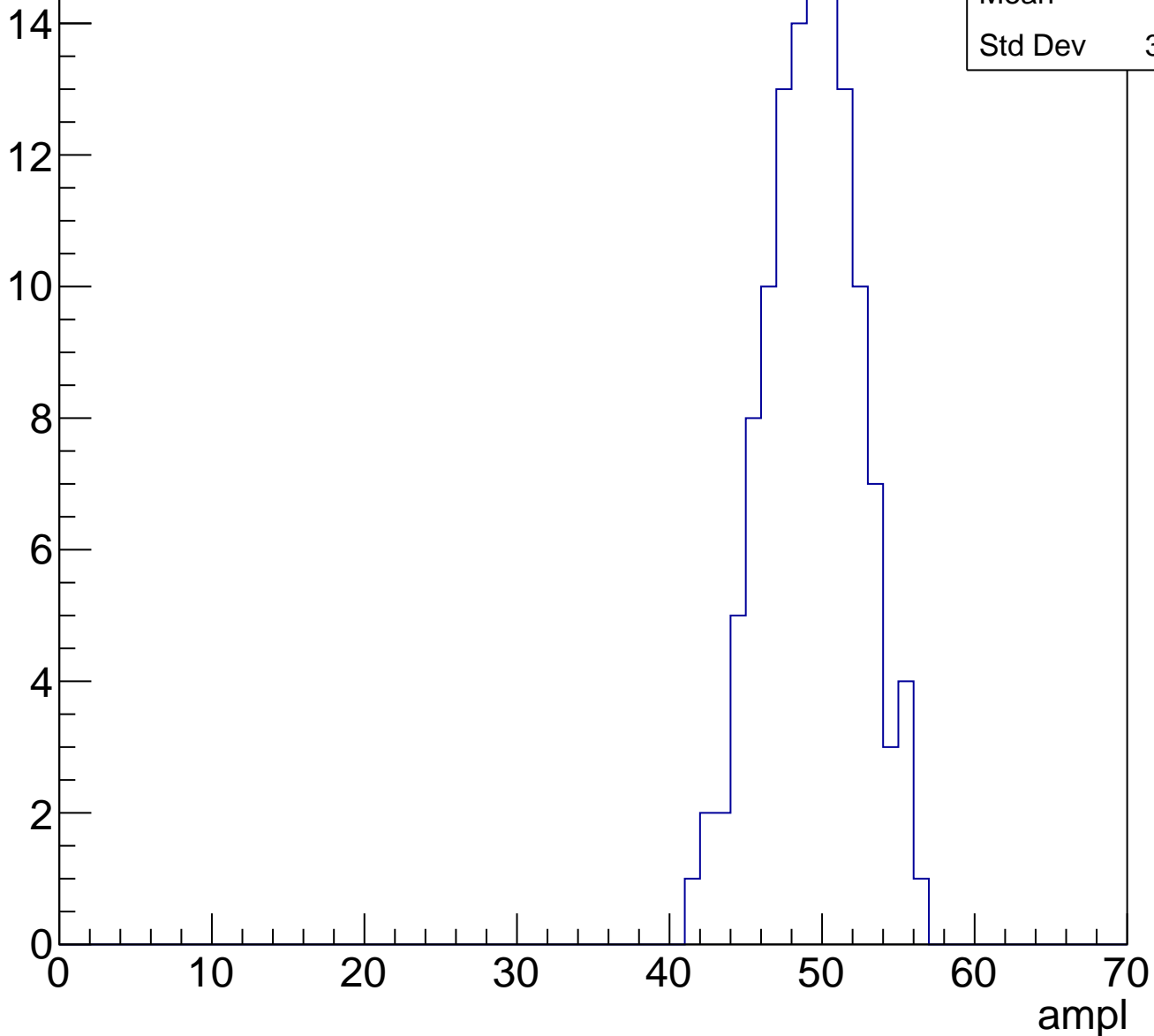


B1L001S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	48.87
Std Dev	3.095

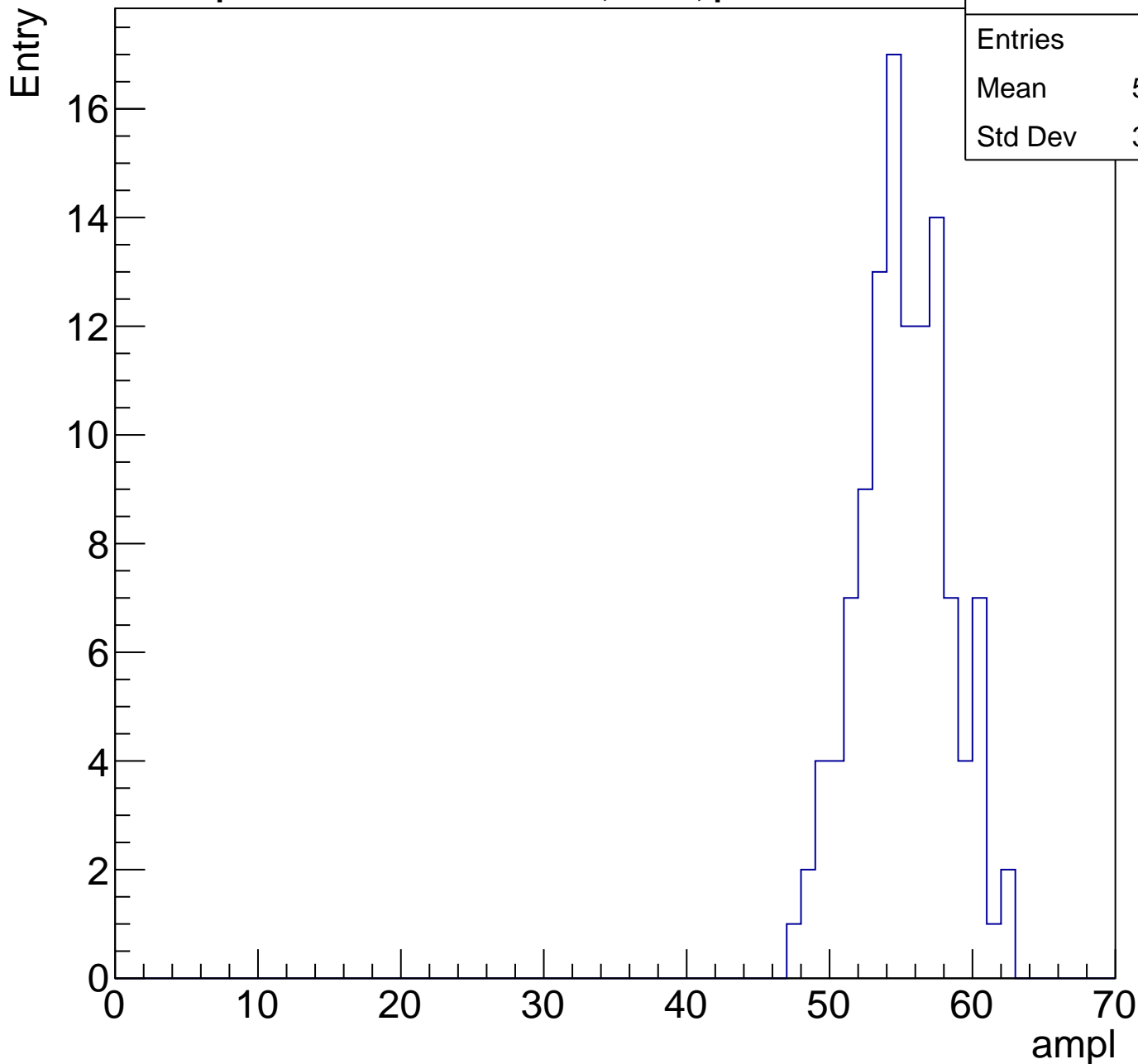
Entry



B1L001S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	54.72
Std Dev	3.167



B1L001S, U14-ch79, adc5

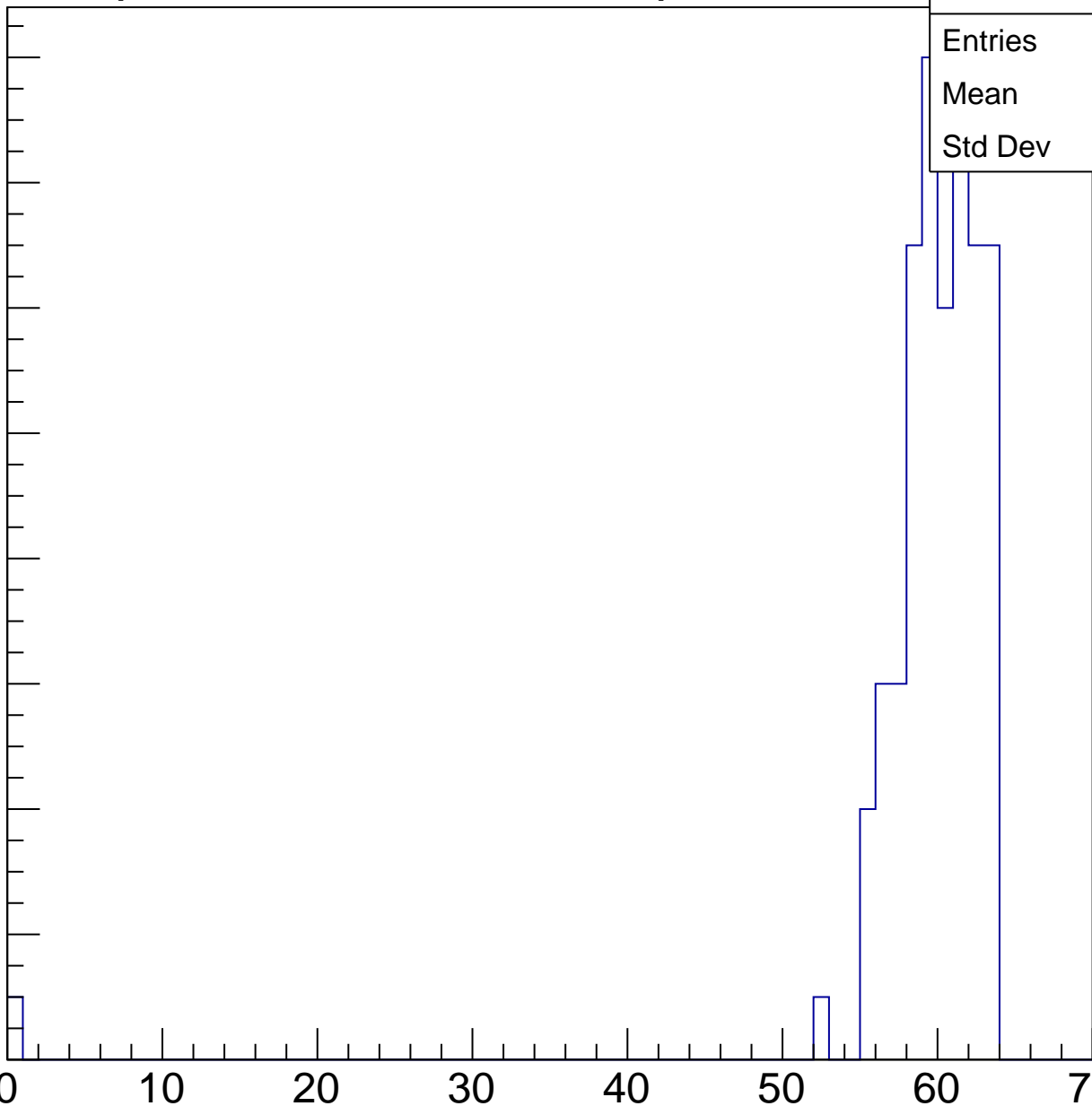
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	59.08
Std Dev	6.389

ampl

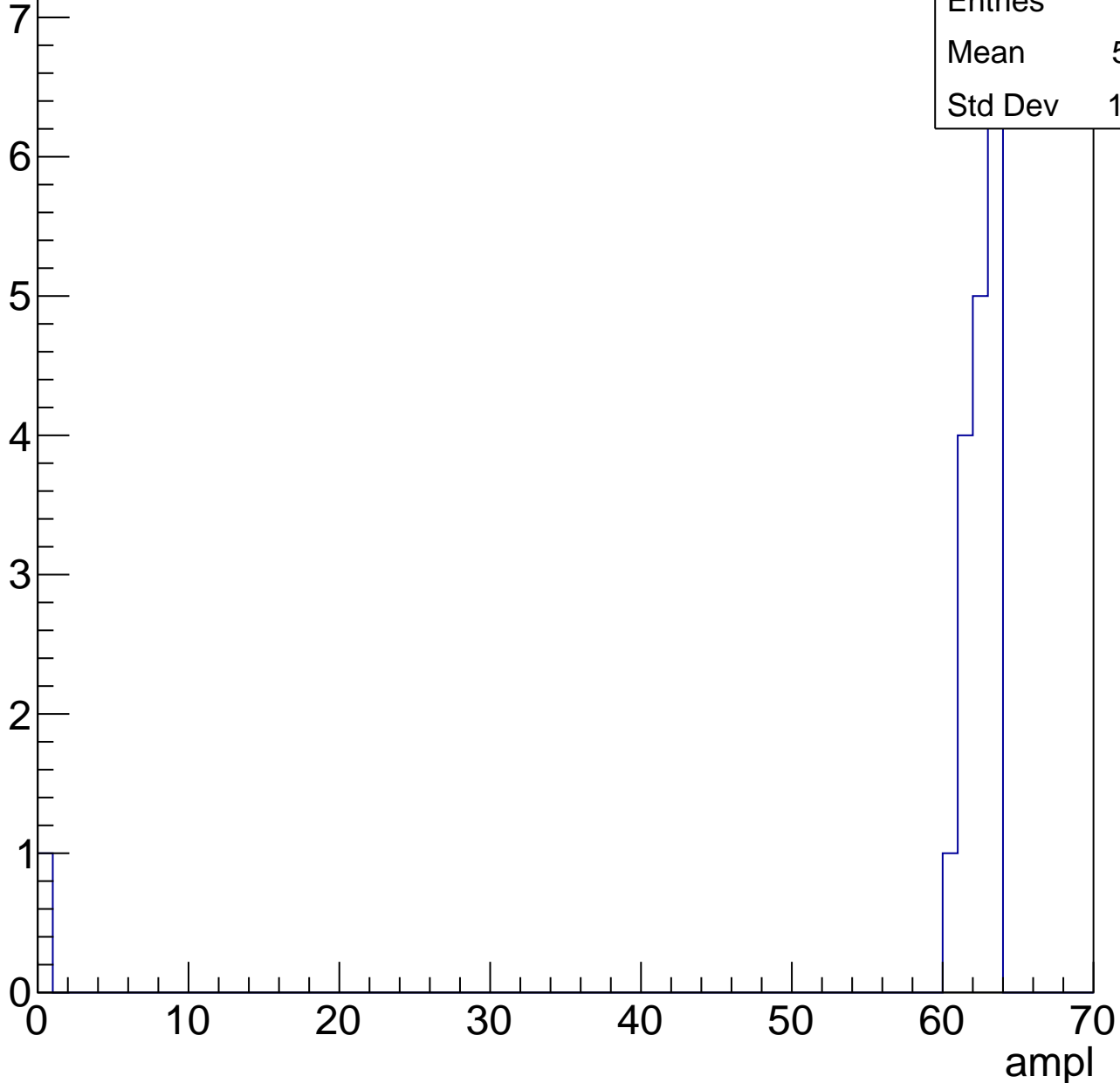


B1L001S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	58.61
Std Dev	14.24



B1L001S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U14-ch80, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.43
Std Dev	3.966

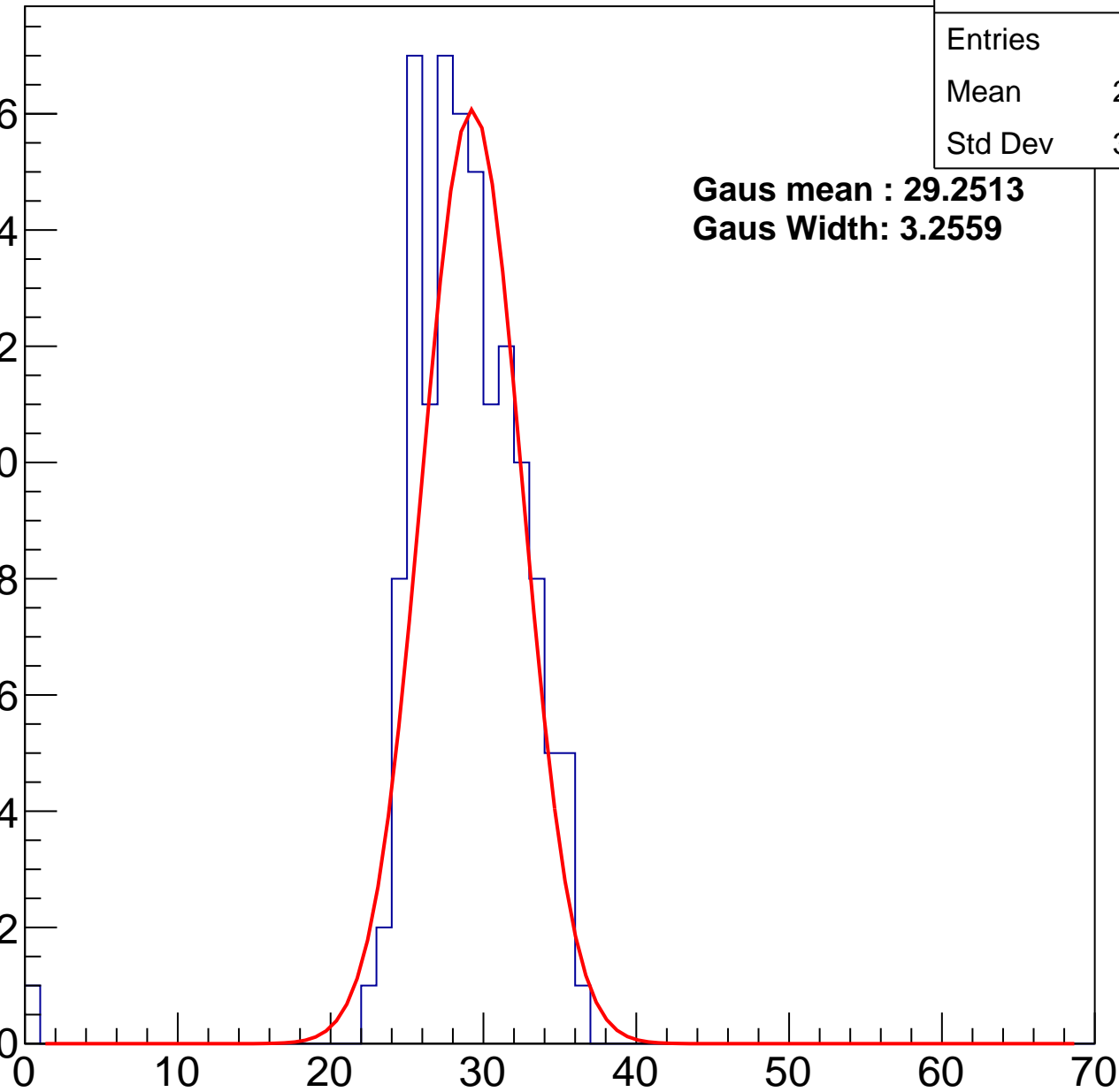
Gaus mean : 29.2513

Gaus Width: 3.2559

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch80, adc1

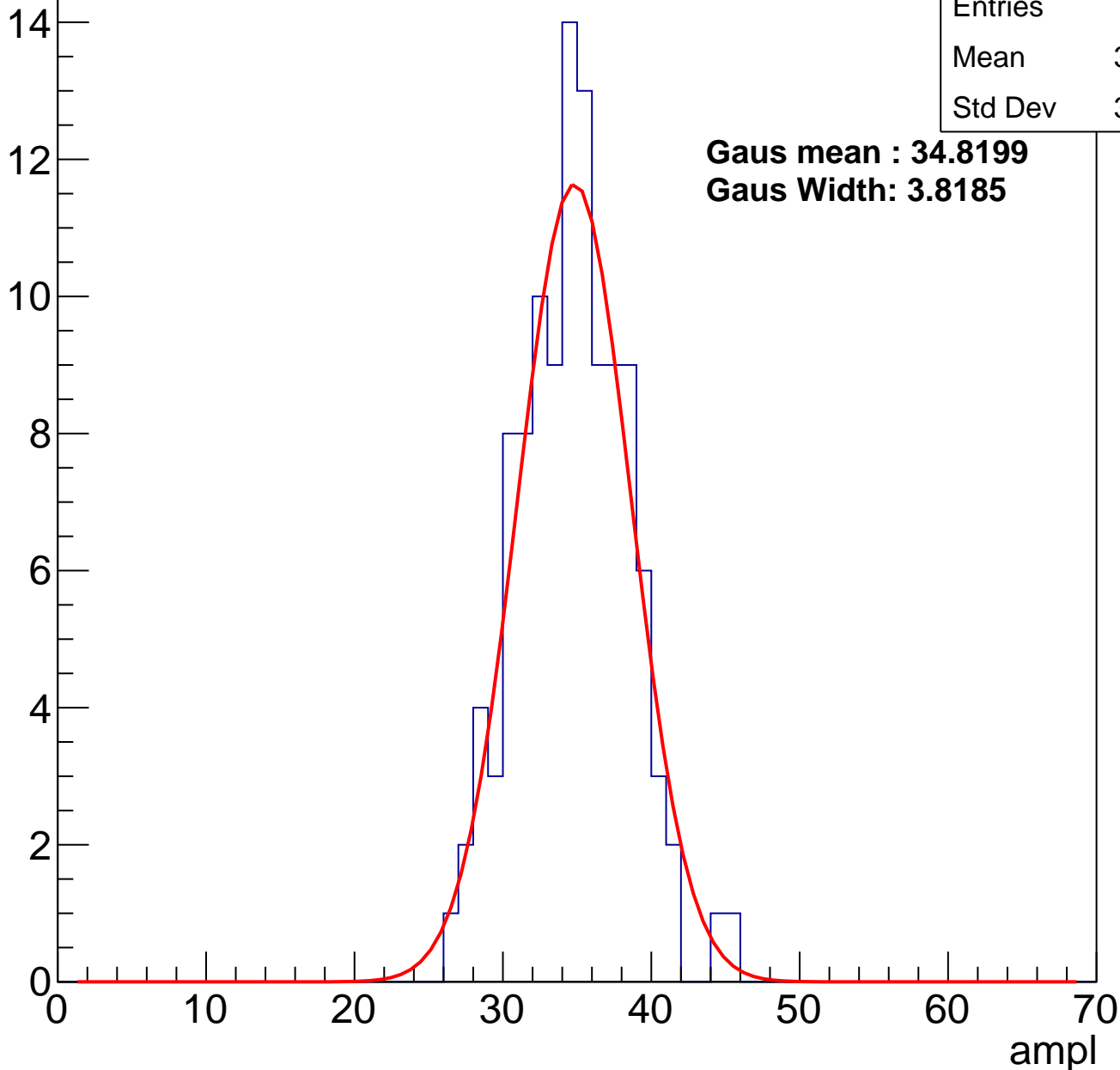
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	34.28
Std Dev	3.626

Gaus mean : 34.8199

Gaus Width: 3.8185

Entry



B1L001S, U14-ch80, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	40.59
Std Dev	3.384

Gaus mean : 41.0792

Gaus Width: 3.0925

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

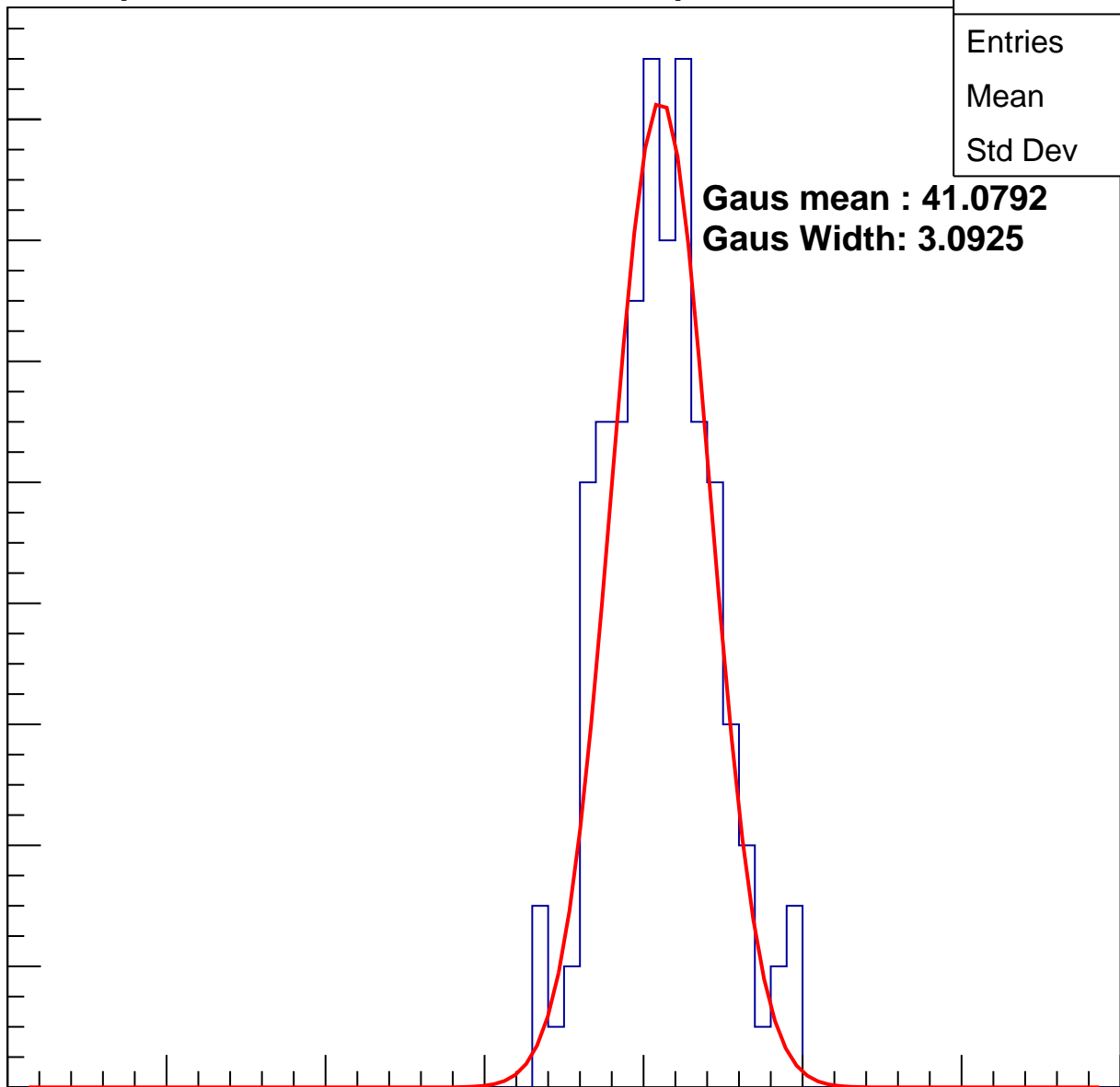
40

50

60

70

ampl

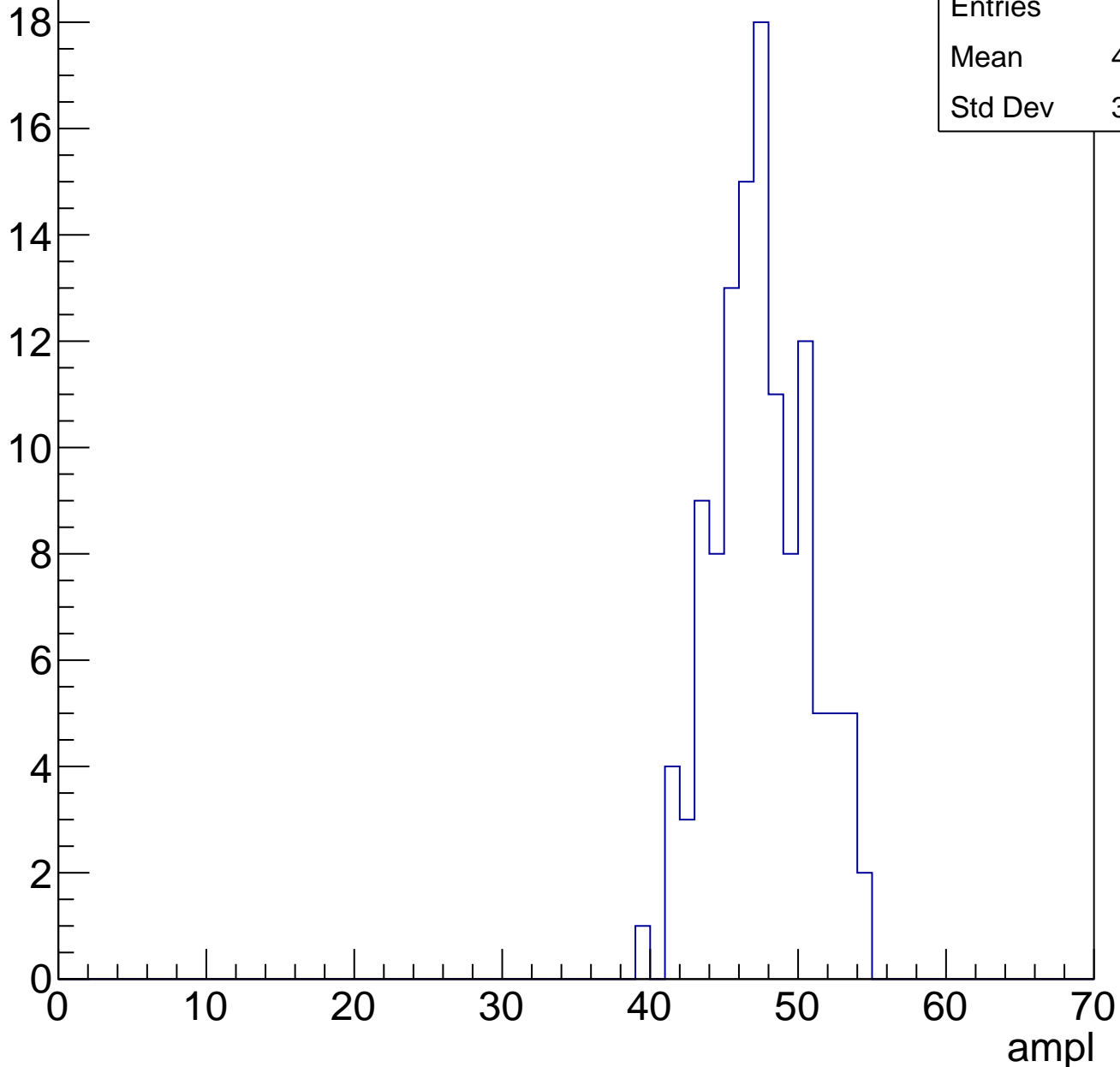


B1L001S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	47.03
Std Dev	3.175

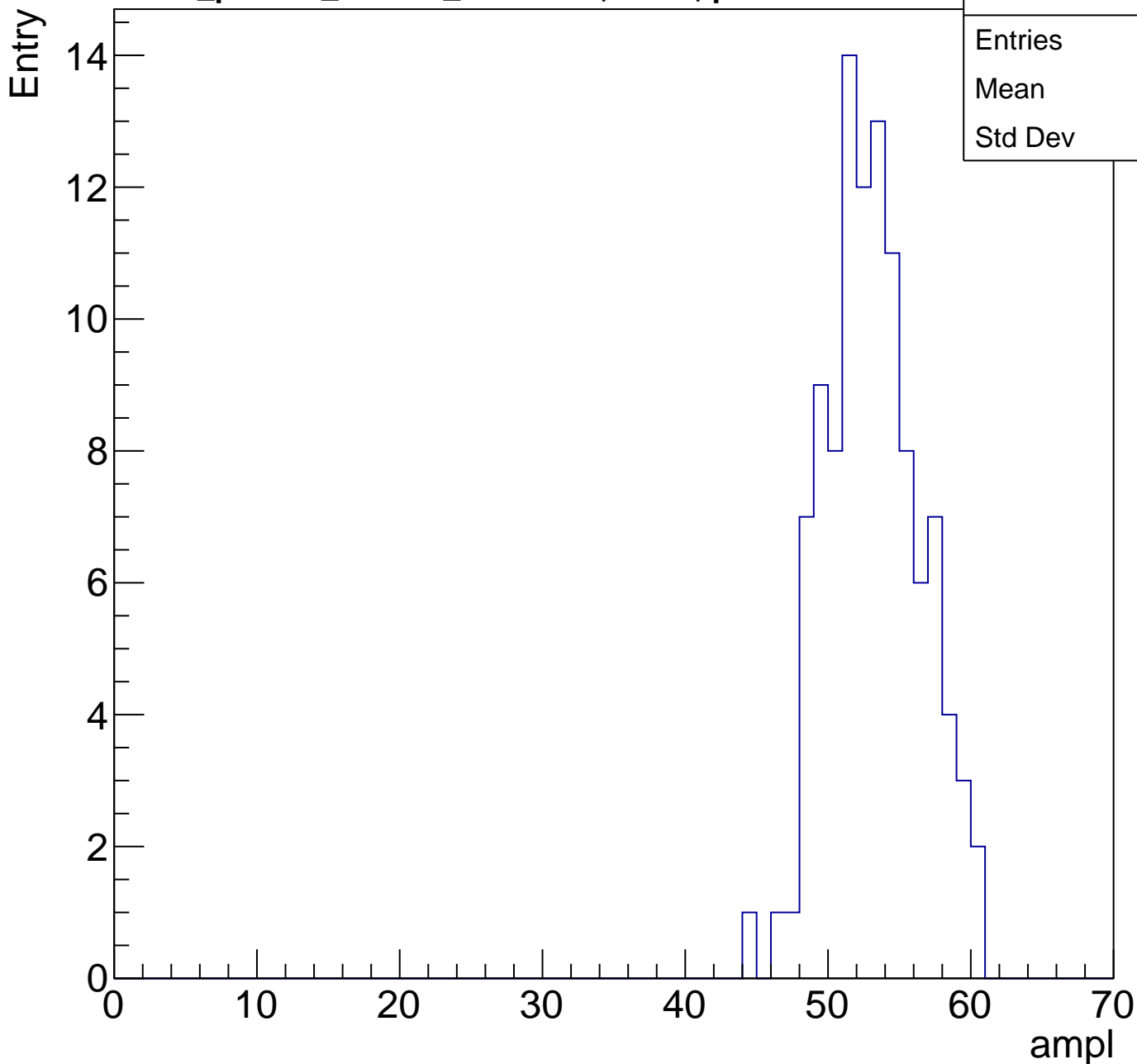
Entry



B1L001S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	52.7
Std Dev	3.25



B1L001S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

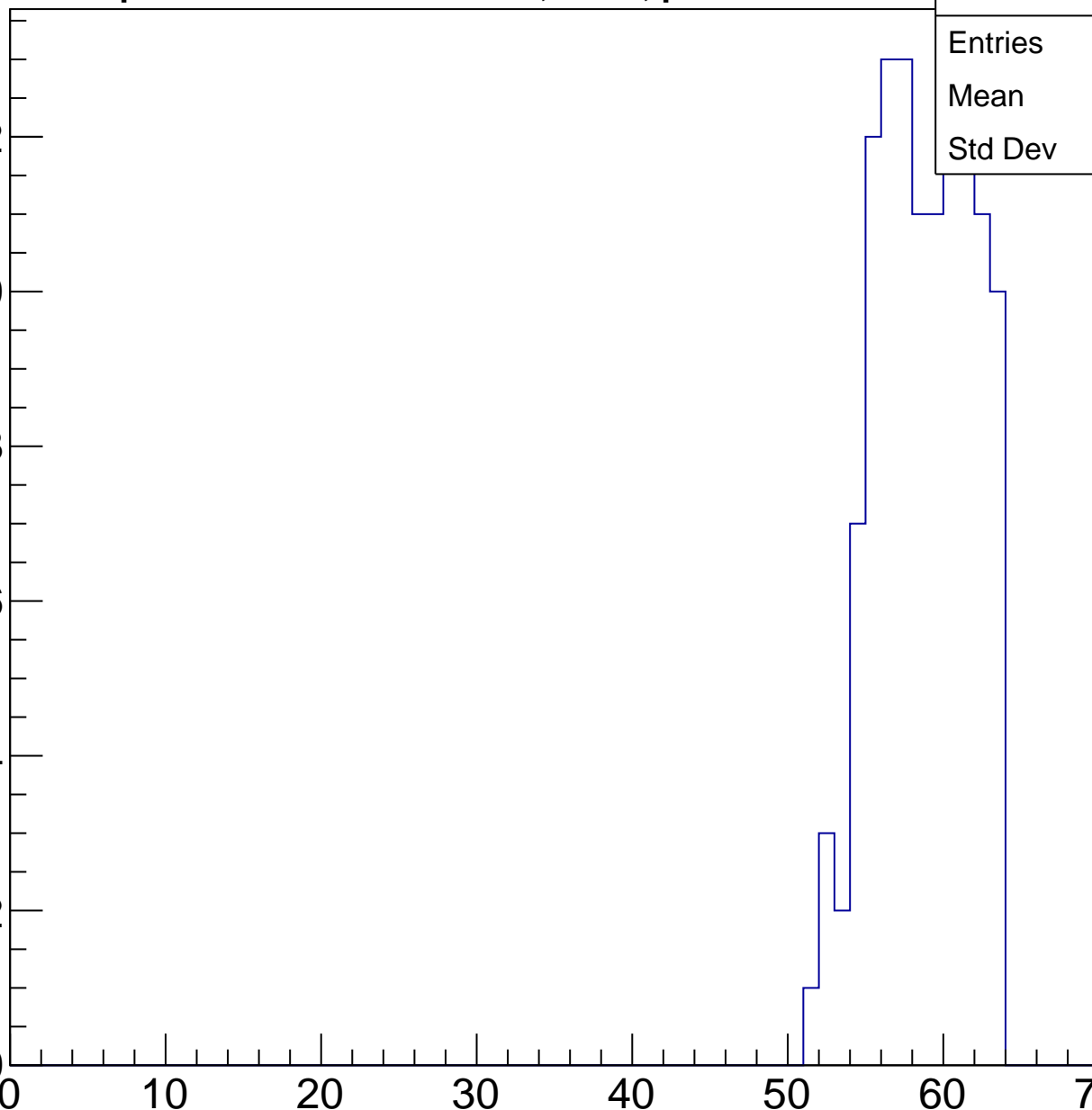
50

60

70

ampl

Entries	120
Mean	58.27
Std Dev	3.013

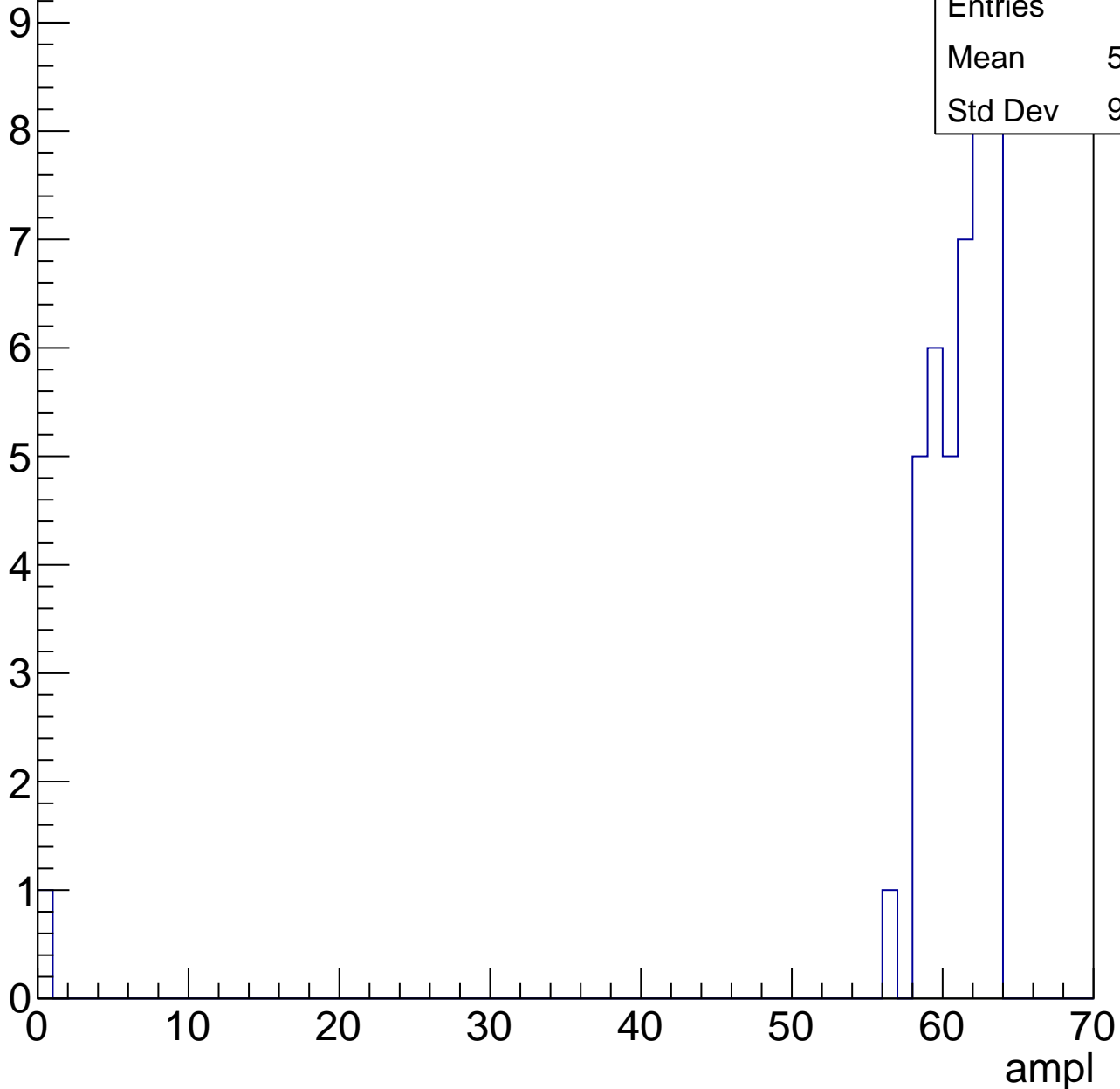


B1L001S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

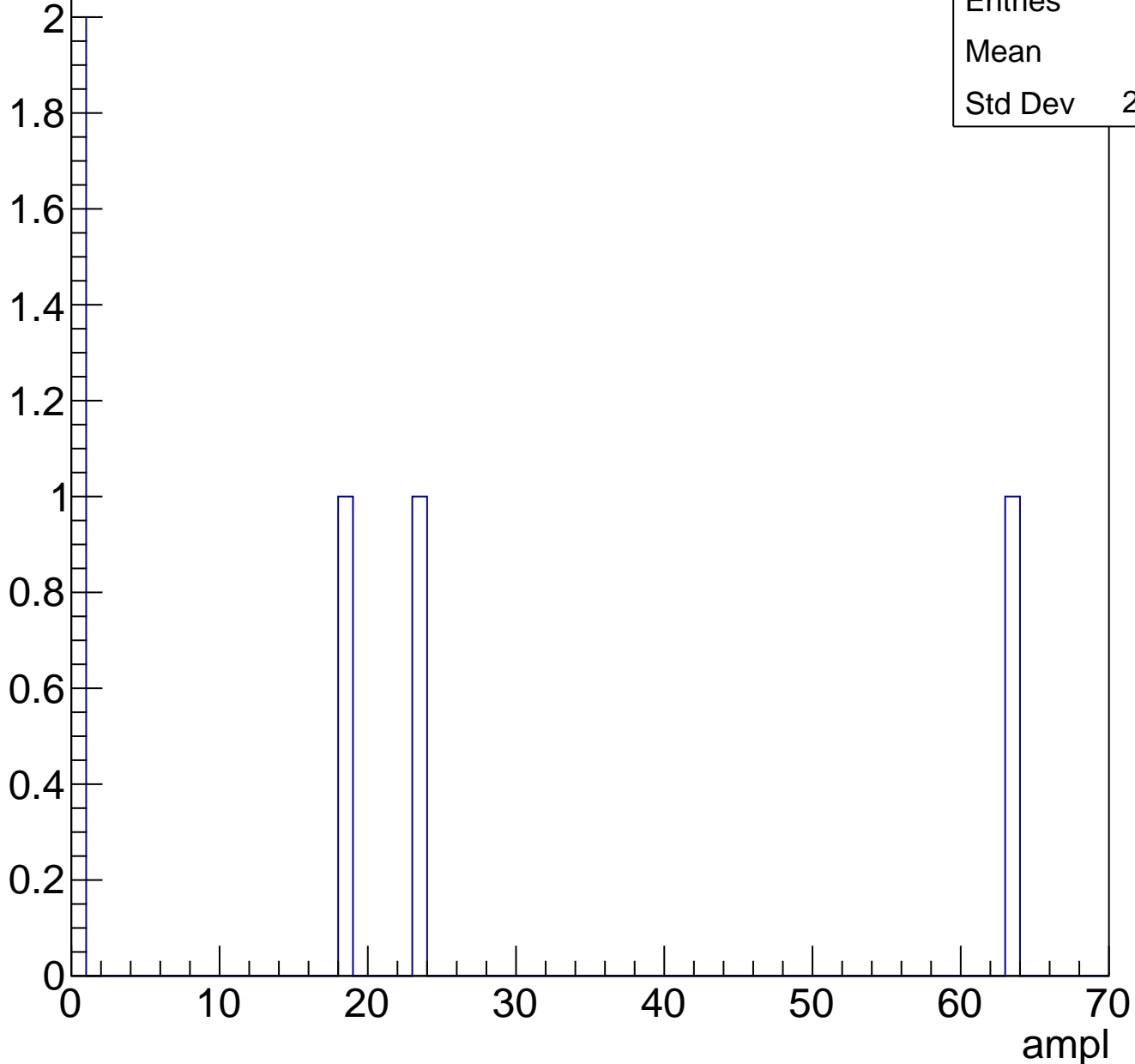
Entries	43
Mean	59.35
Std Dev	9.336



B1L001S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch81, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	30.19
Std Dev	3.28

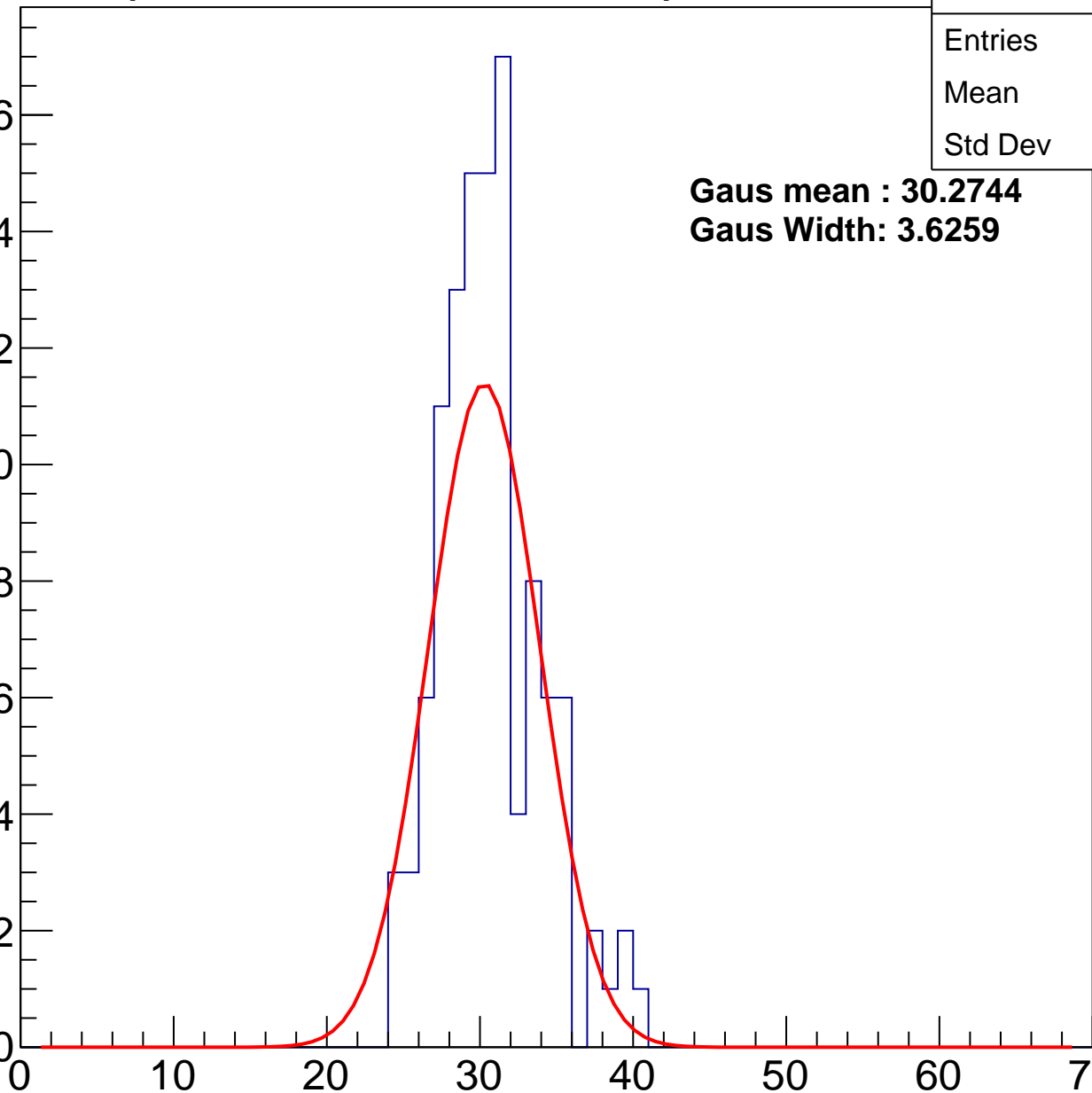
Gaus mean : 30.2744

Gaus Width: 3.6259

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch81, adc1

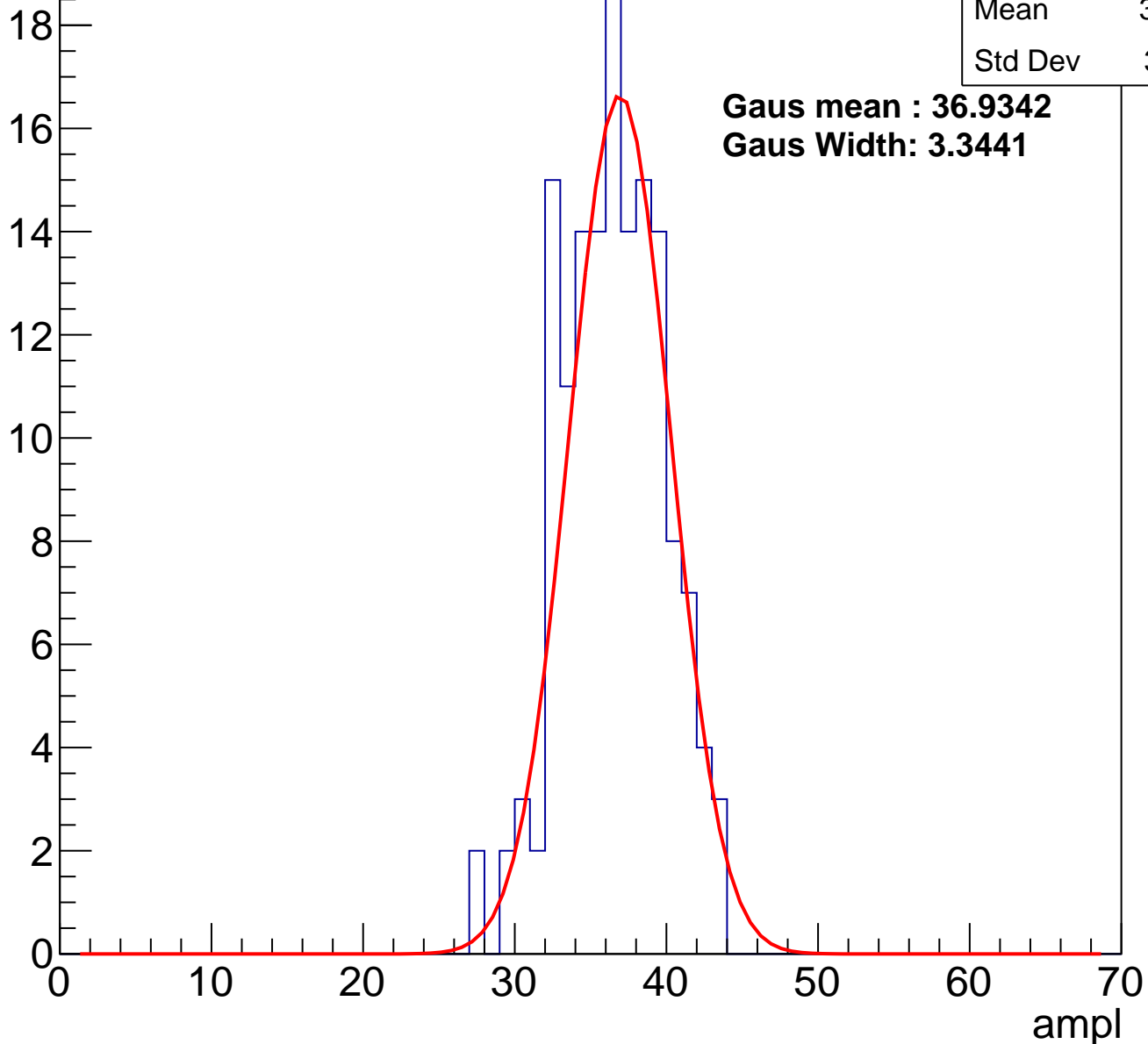
calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	36.02
Std Dev	3.321

Gaus mean : 36.9342

Gaus Width: 3.3441

Entry



B1L001S, U14-ch81, adc2

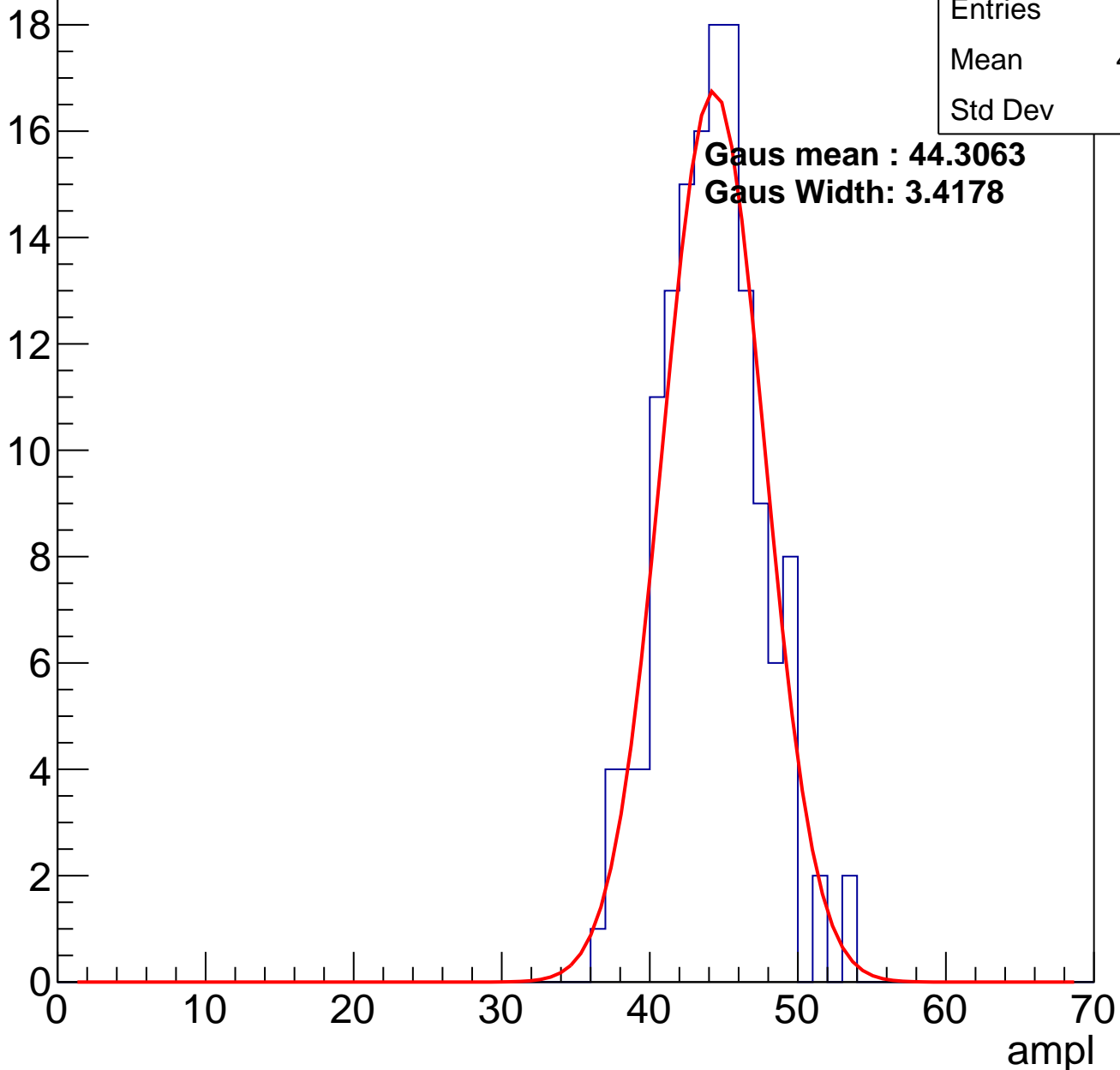
calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	43.71
Std Dev	3.3

Entry

Gaus mean : 44.3063

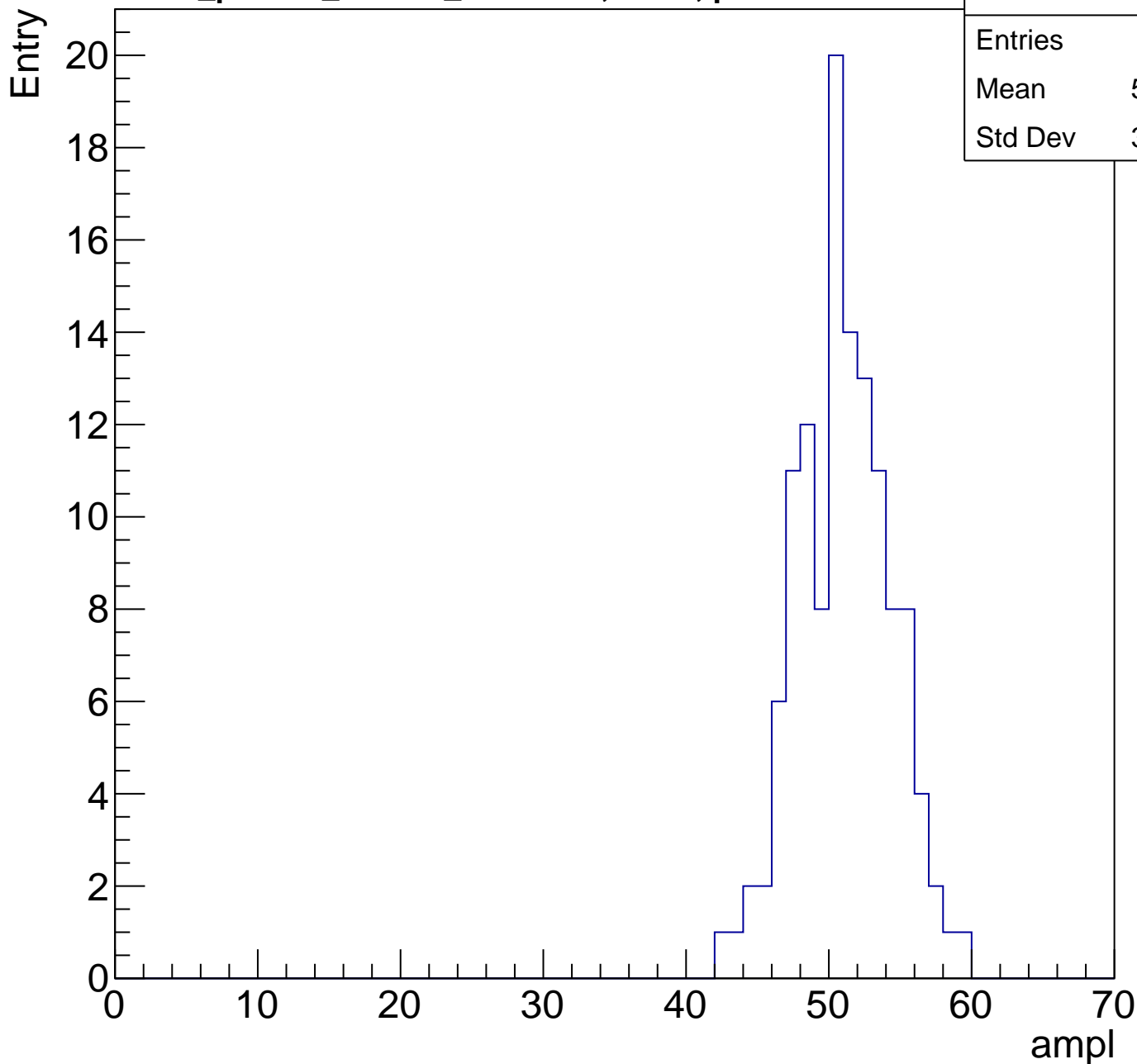
Gaus Width: 3.4178



B1L001S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#2, port C2

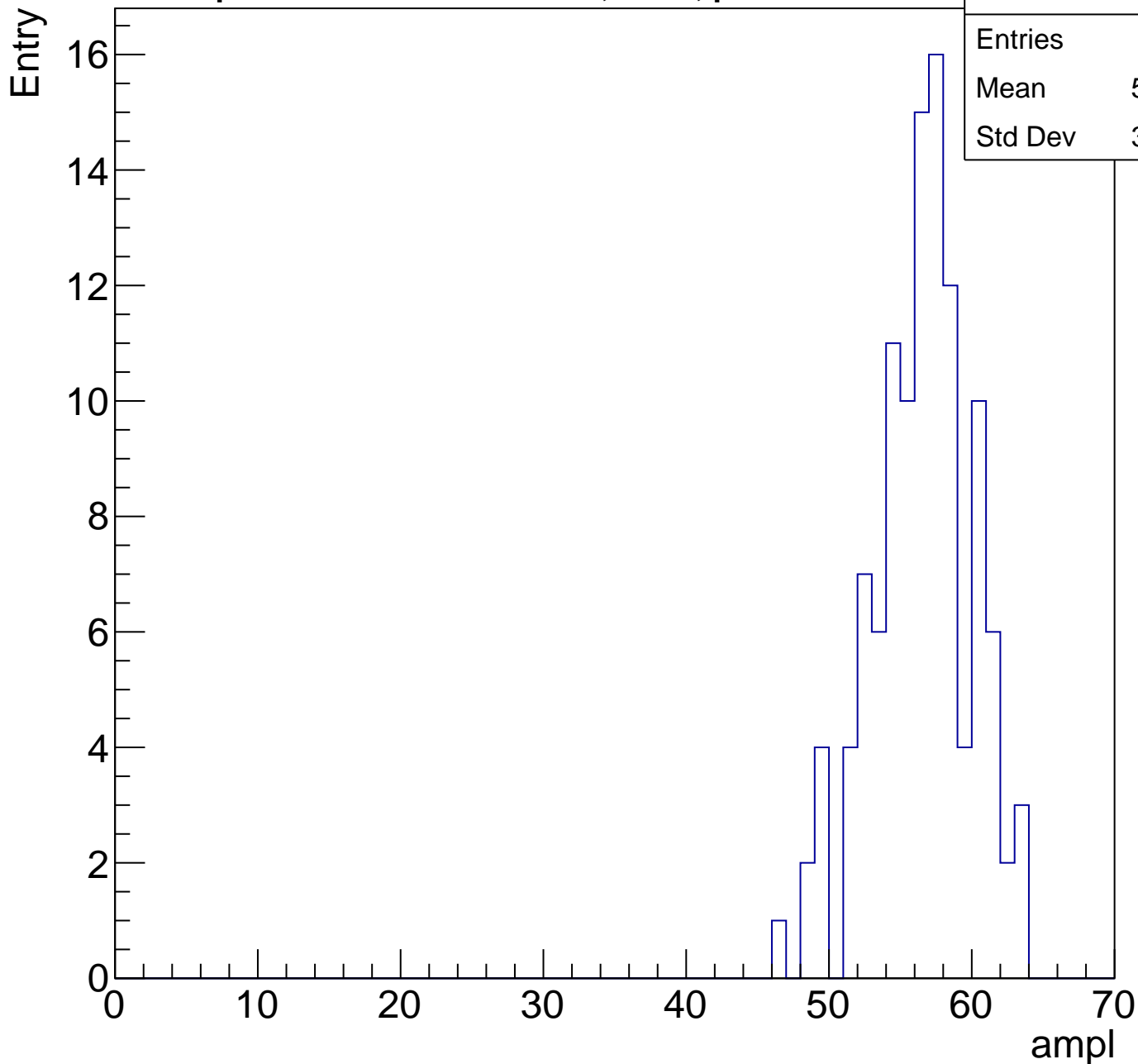
Entries	125
Mean	50.59
Std Dev	3.252



B1L001S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	56.03
Std Dev	3.486



B1L001S, U14-ch81, adc5

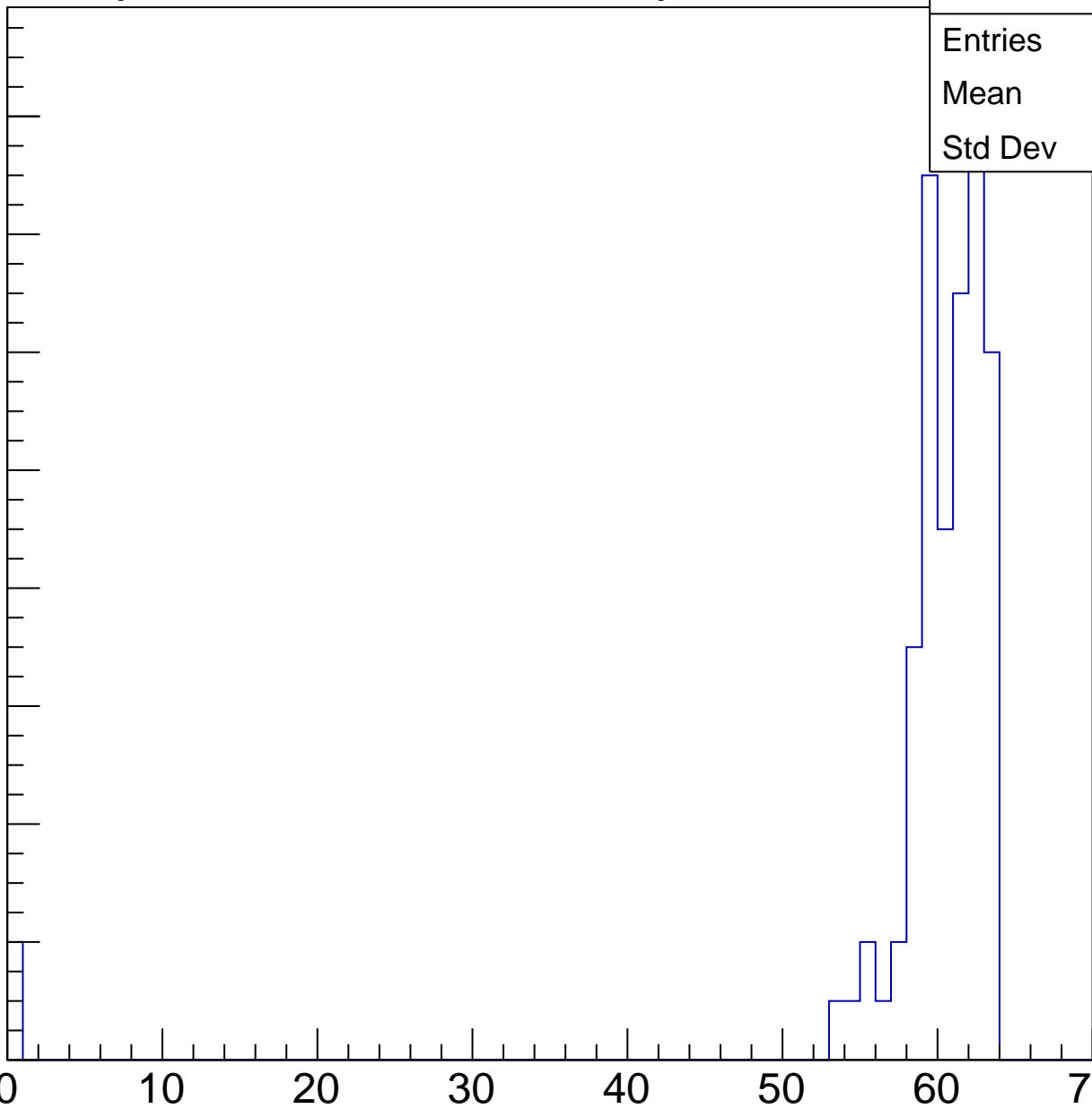
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	82
Mean	58.79
Std Dev	9.552

ampl

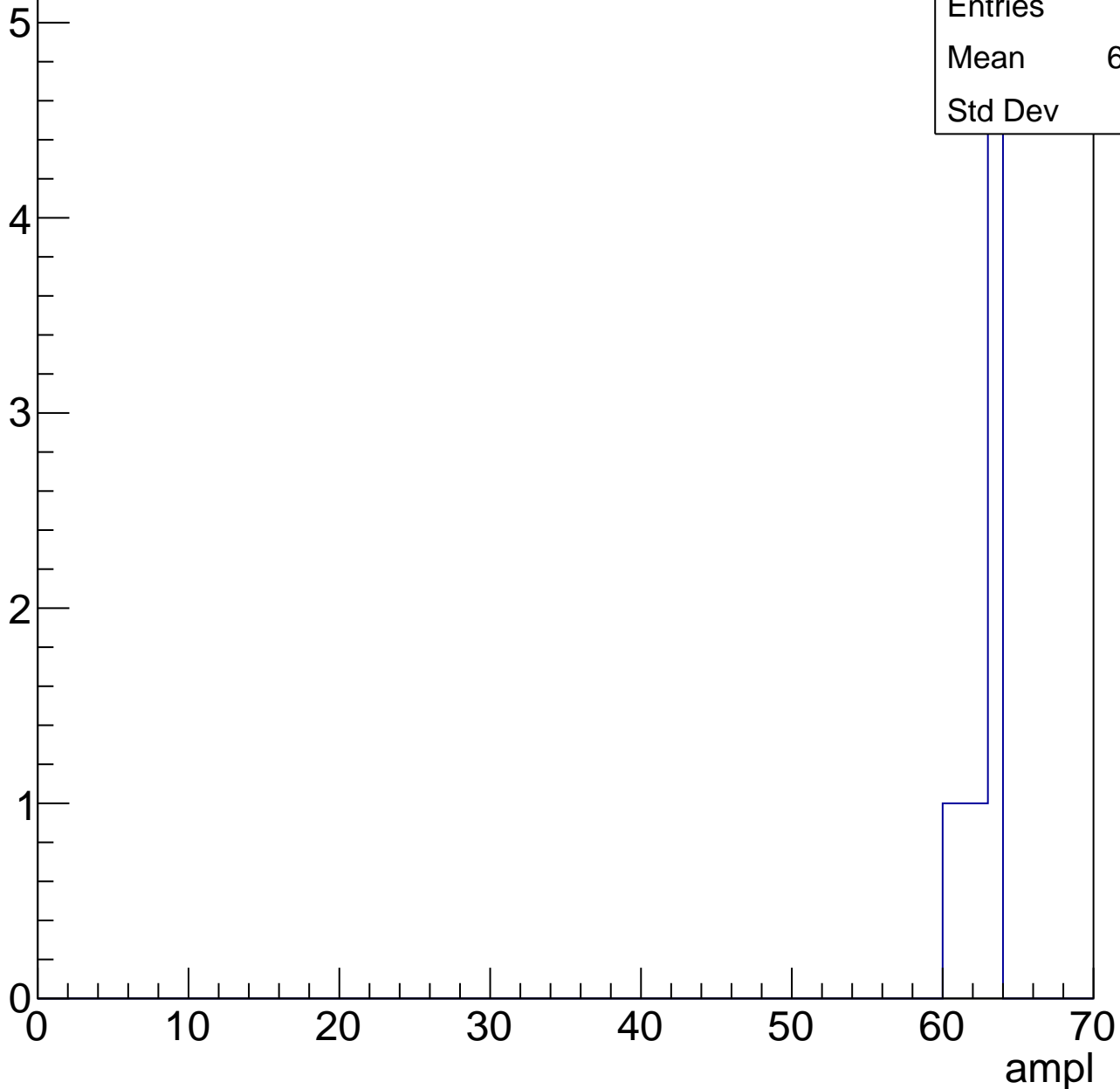


B1L001S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	8
Mean	62.25
Std Dev	1.09



B1L001S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L001S, U14-ch82, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	29.17
Std Dev	4.094

Gaus mean : 30.0670

Gaus Width: 3.1915

Entry

18
16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch82, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	35.56
Std Dev	3.154

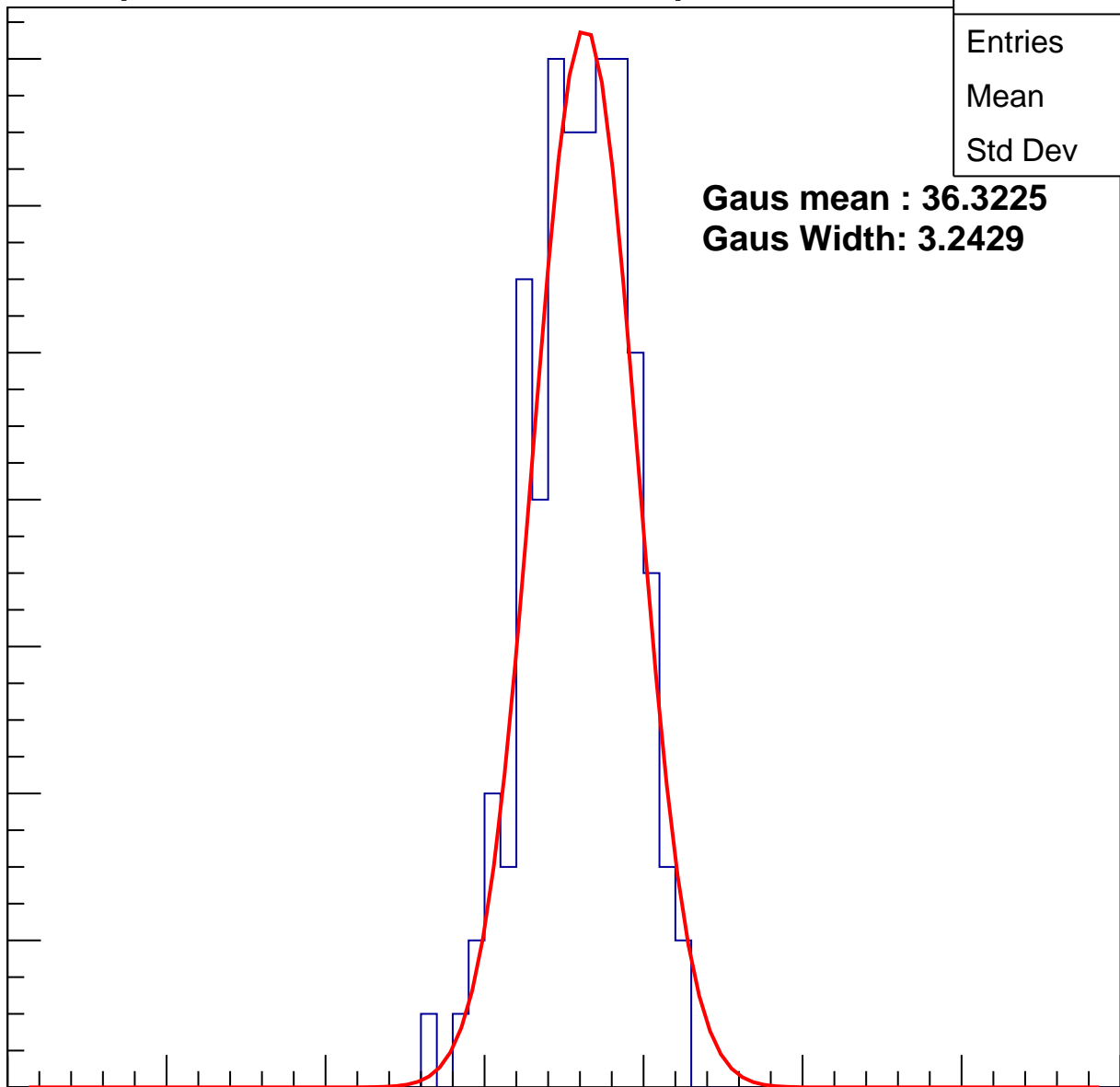
Gaus mean : 36.3225
Gaus Width: 3.2429

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch82, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	41.97
Std Dev	3.388

Gaus mean : 42.3550

Gaus Width: 3.3958

Entry

16

14

12

10

8

6

4

2

0

0

10

20

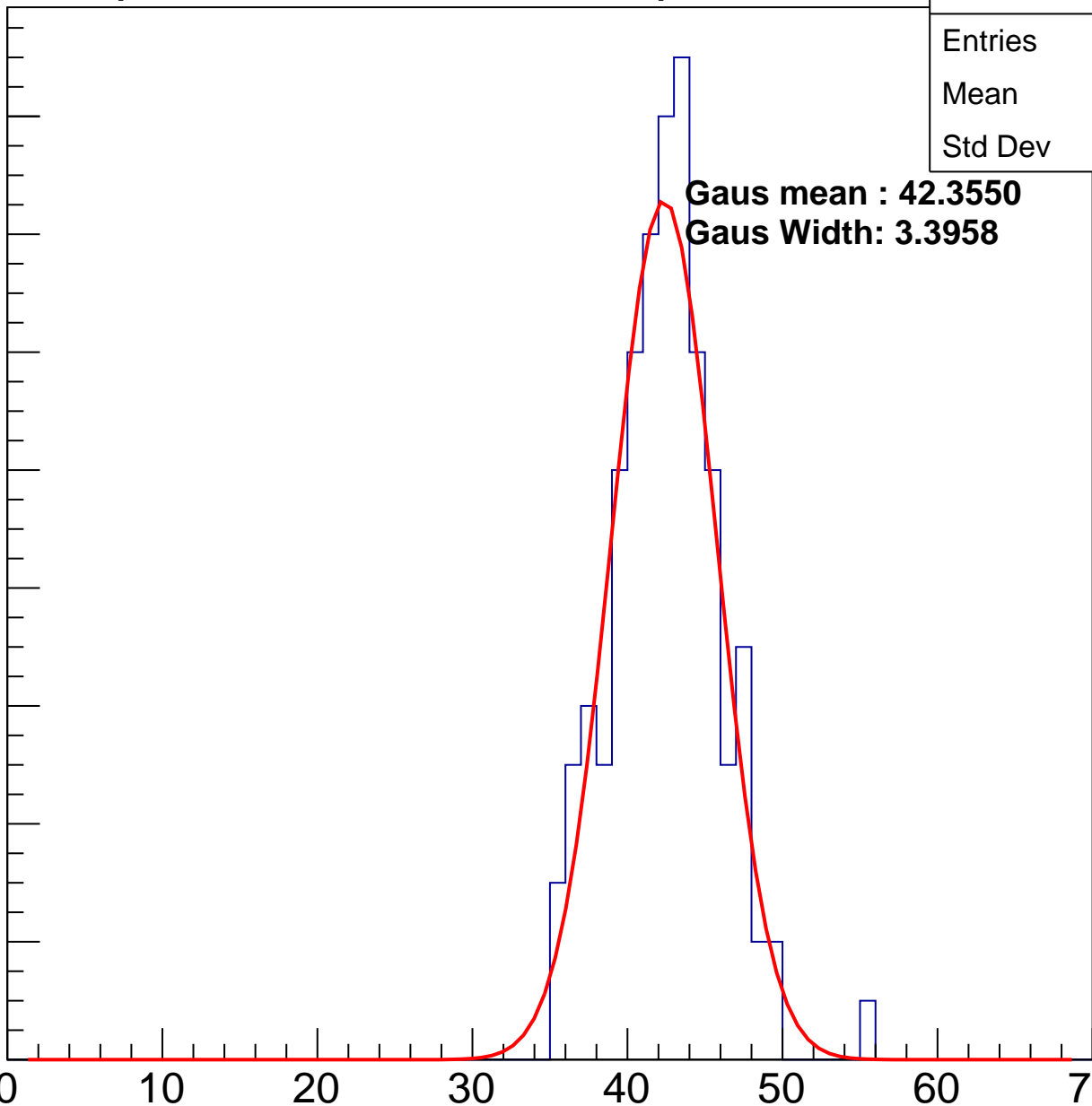
30

40

50

60

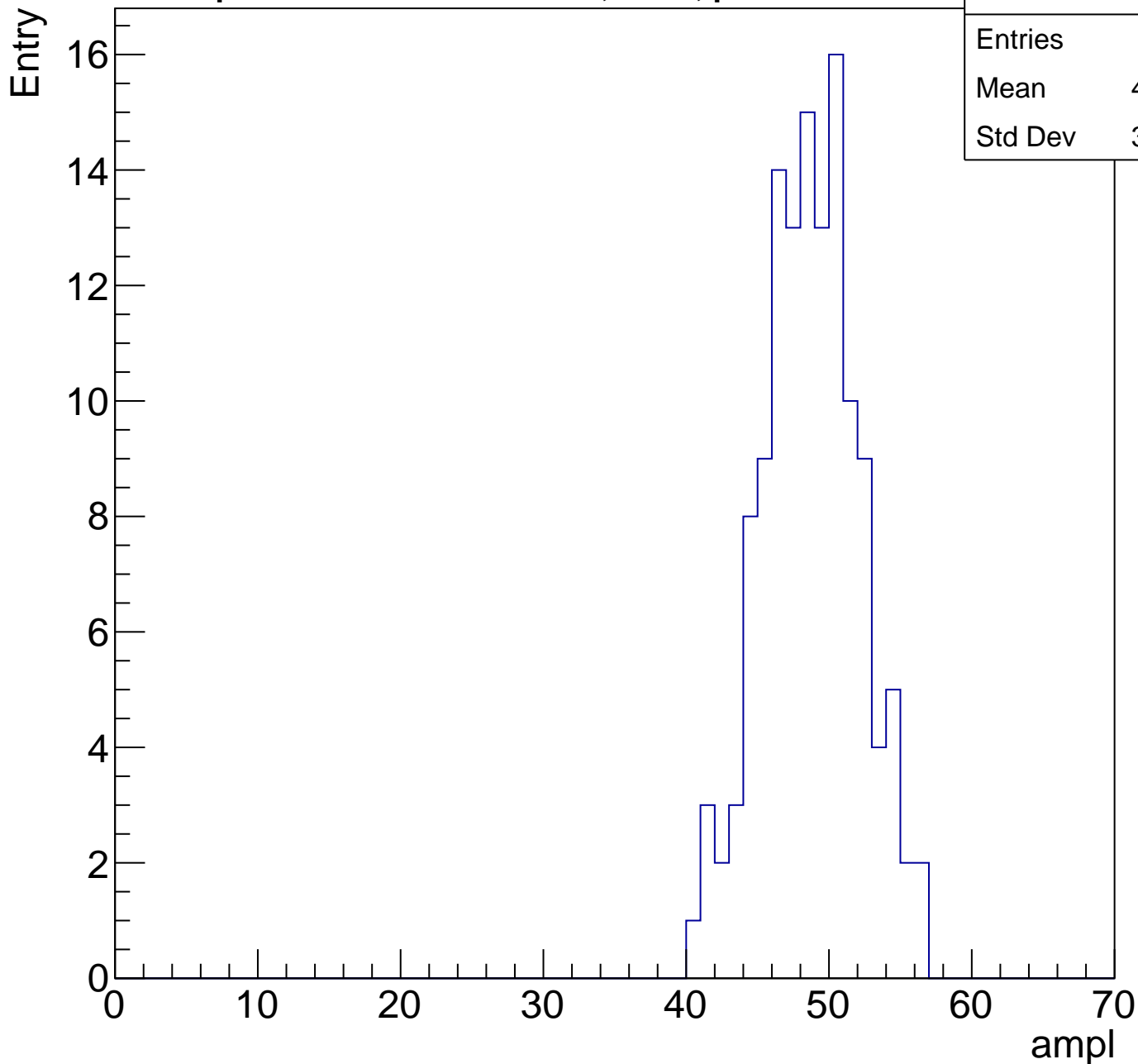
ampl



B1L001S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#2, port C2

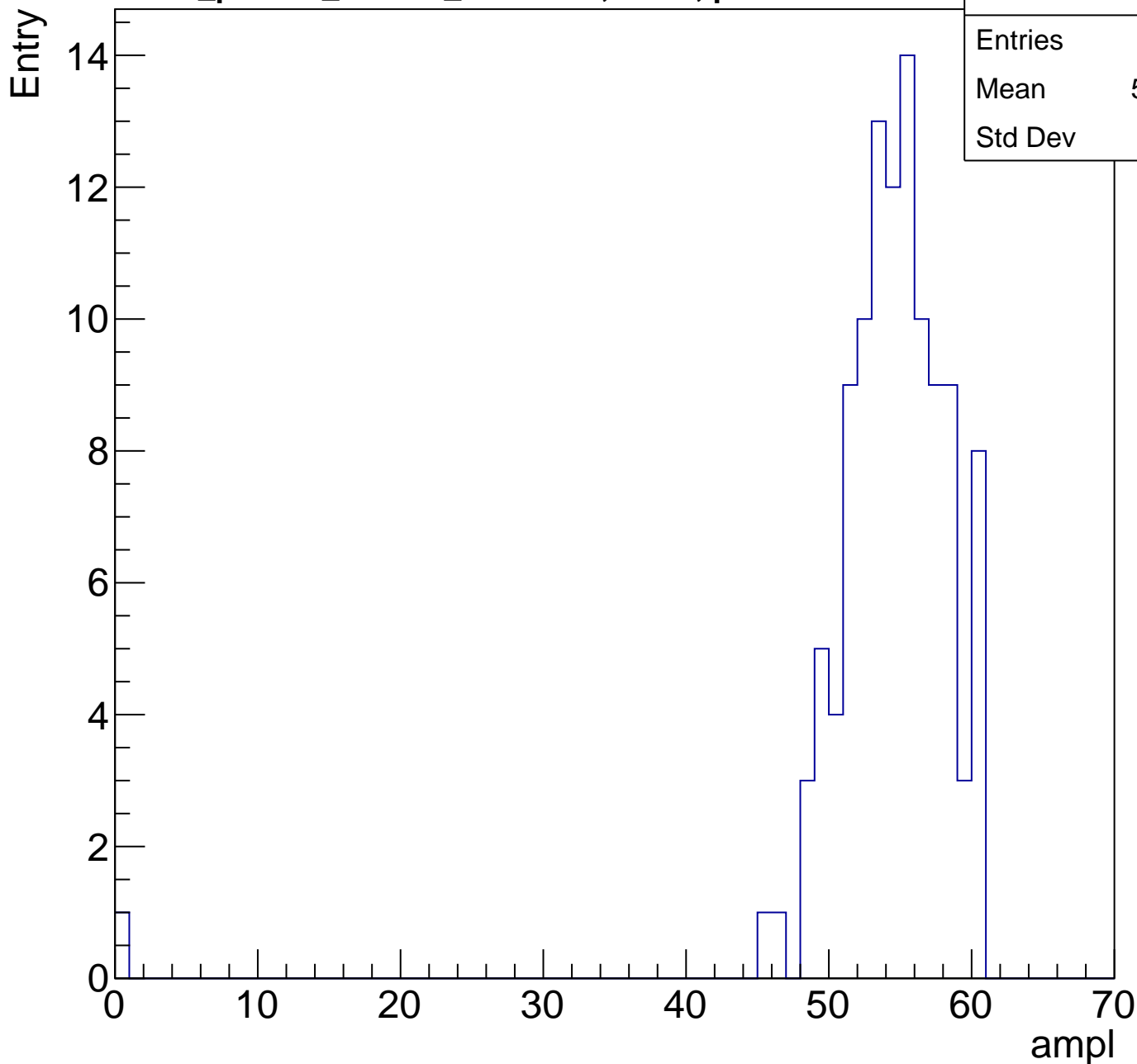
Entries	129
Mean	48.27
Std Dev	3.345



B1L001S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	53.73
Std Dev	6.07



B1L001S, U14-ch82, adc5

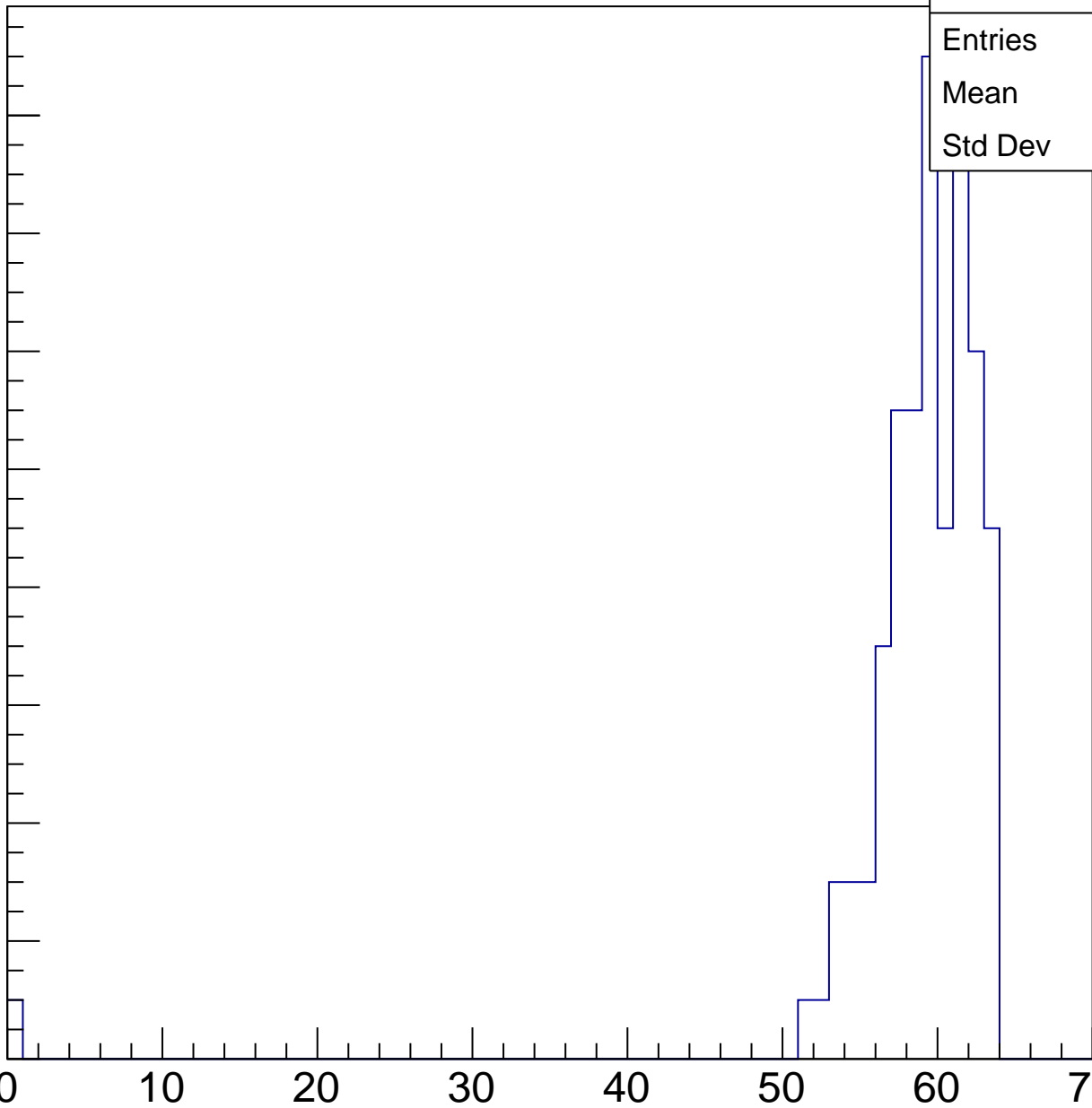
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	105
Mean	58.45
Std Dev	6.355

ampl



B1L001S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	25
Mean	61.8
Std Dev	1.549

Entry

10

8

6

4

2

0

0

10

20

30

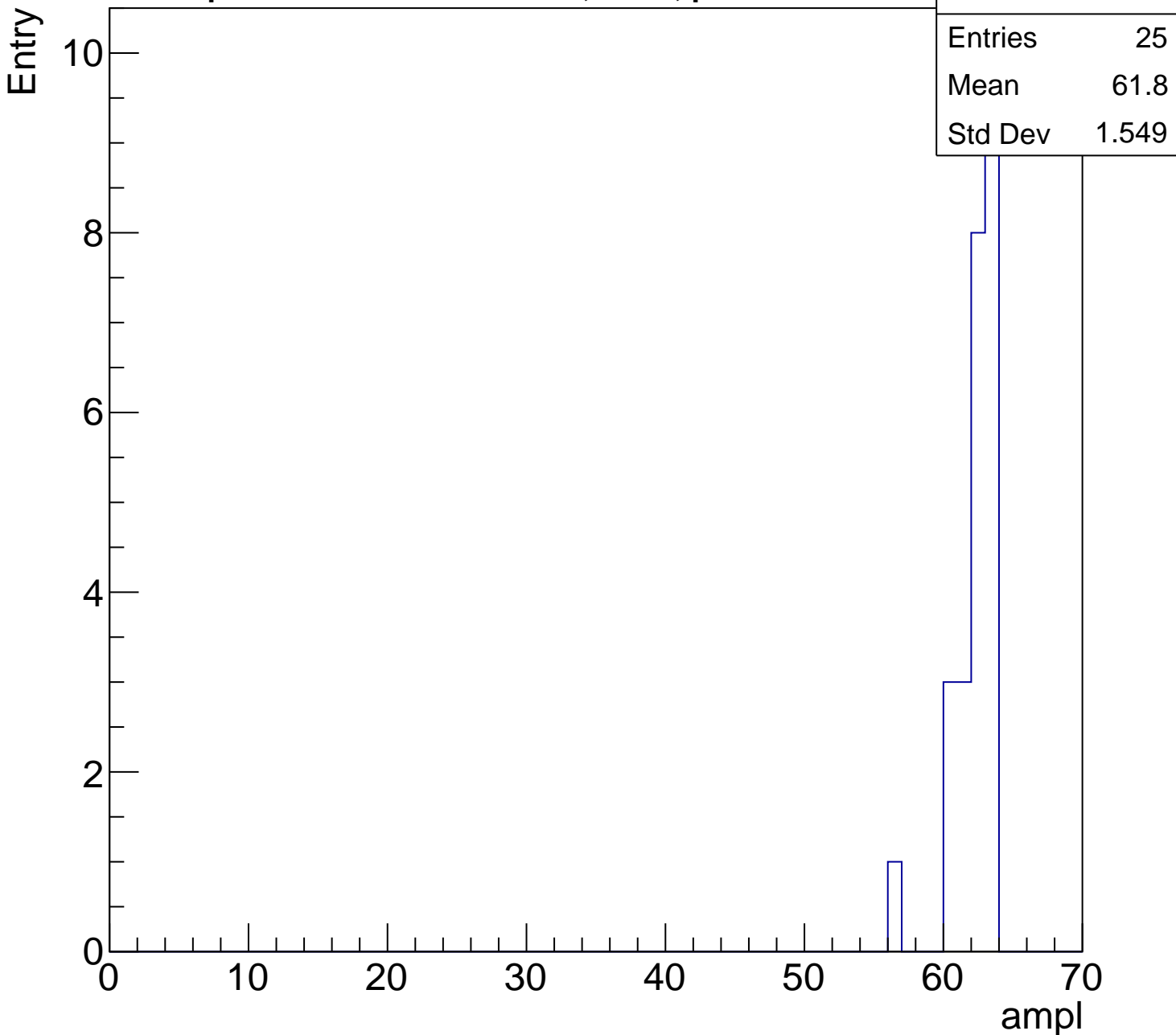
40

50

60

70

ampl



B1L001S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



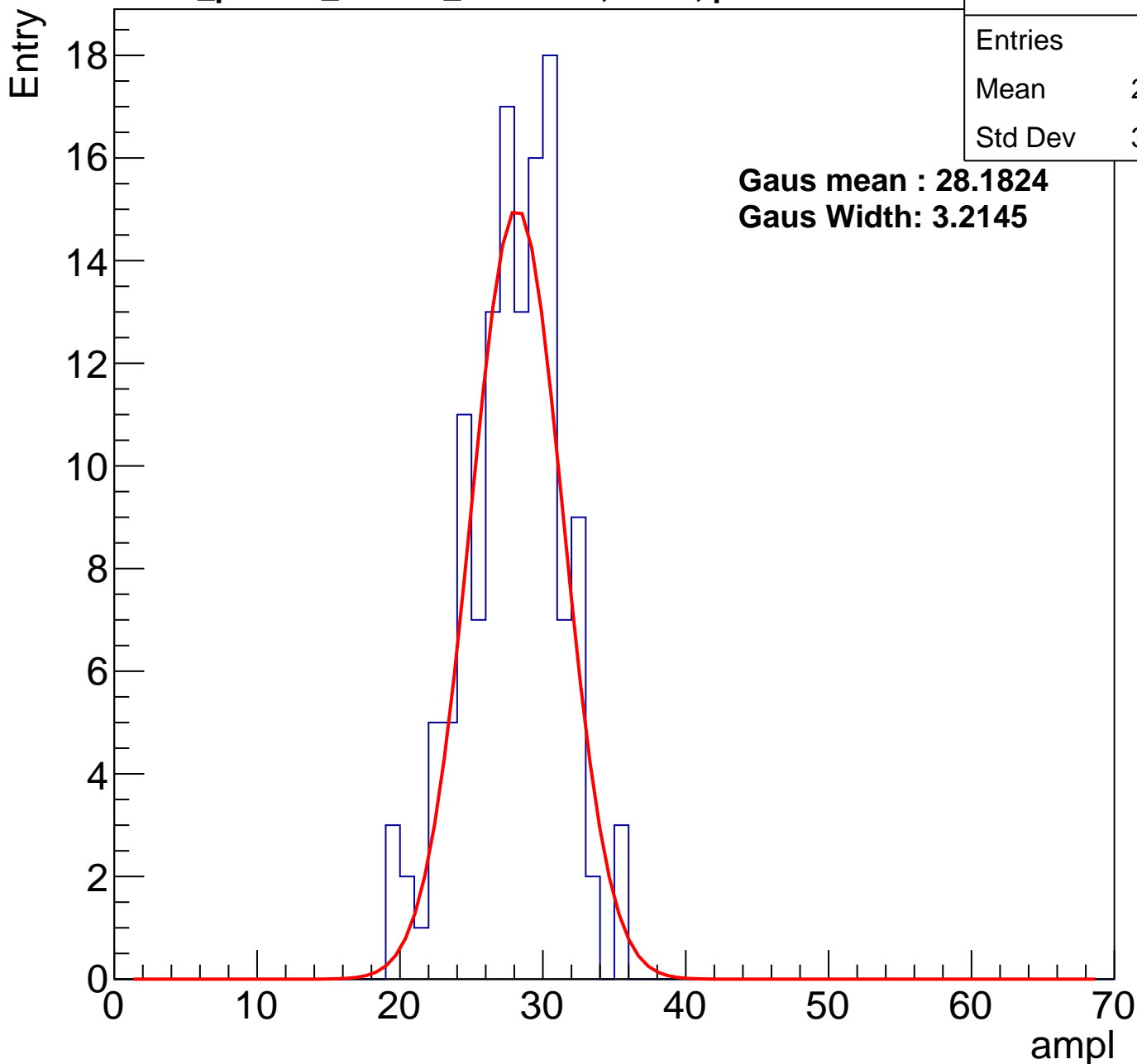
B1L001S, U14-ch83, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	27.45
Std Dev	3.354

Gaus mean : 28.1824

Gaus Width: 3.2145



B1L001S, U14-ch83, adc1

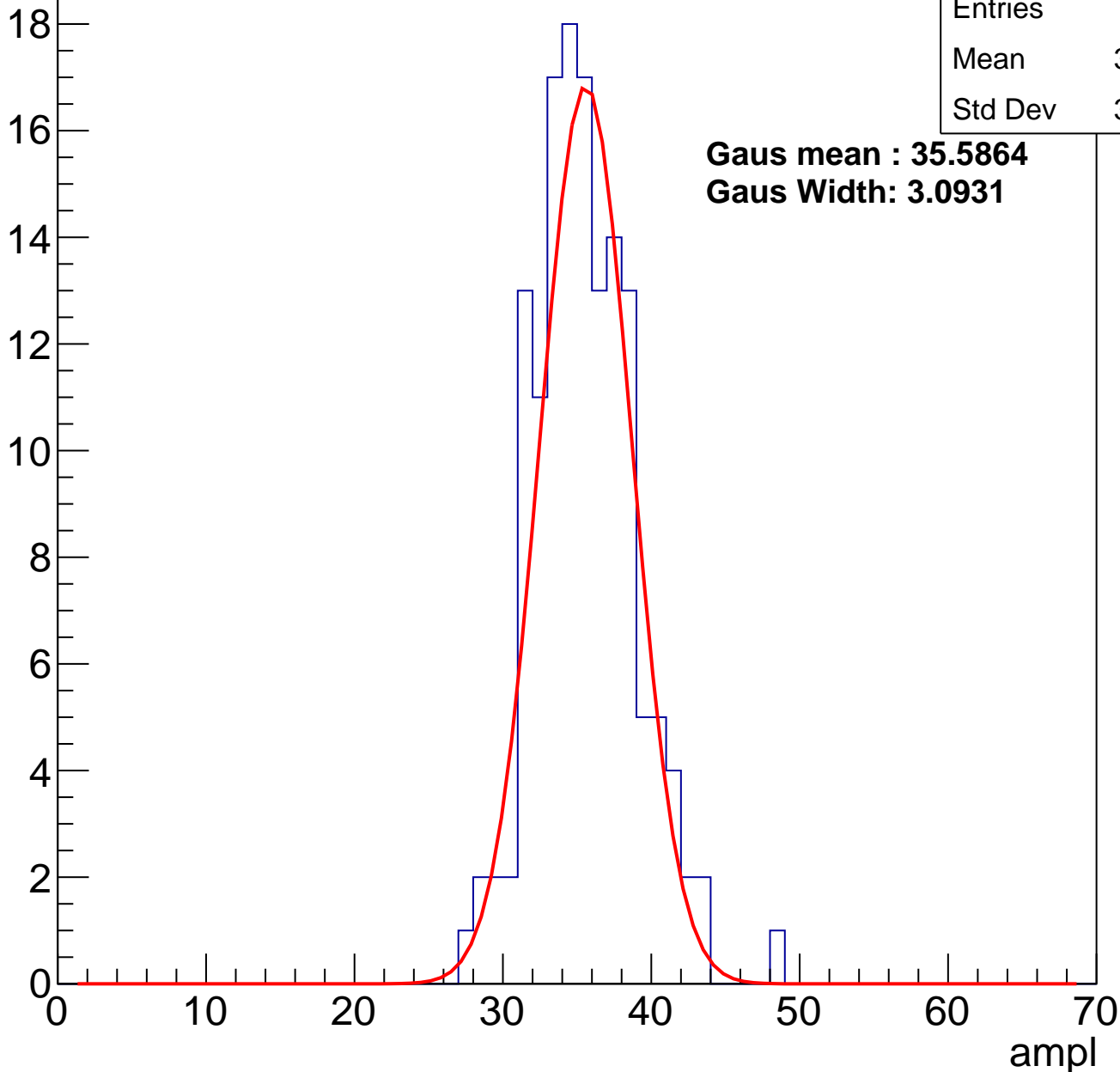
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	35.08
Std Dev	3.363

Gaus mean : 35.5864

Gaus Width: 3.0931

Entry



B1L001S, U14-ch83, adc2

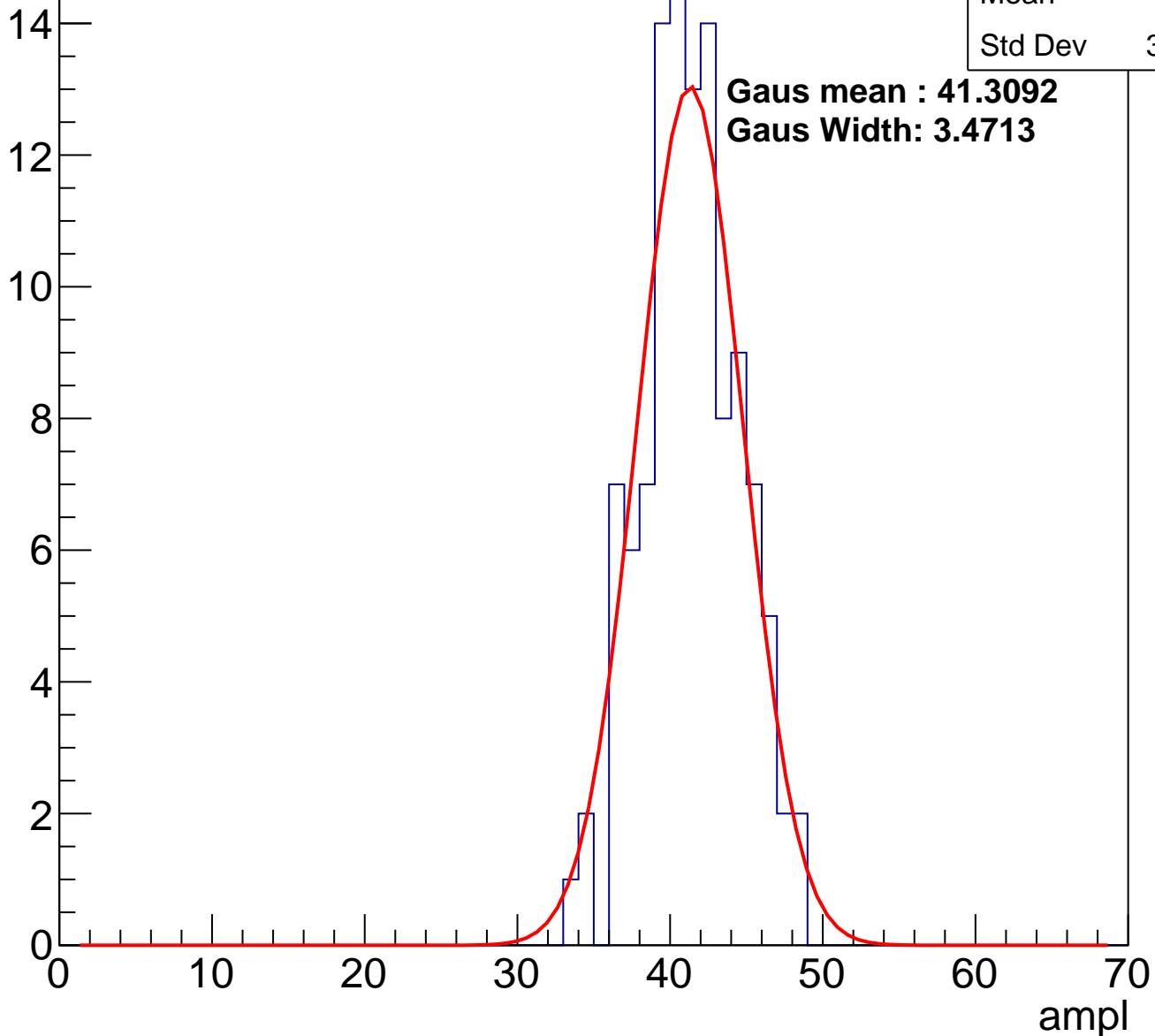
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	40.92
Std Dev	3.134

Gaus mean : 41.3092

Gaus Width: 3.4713

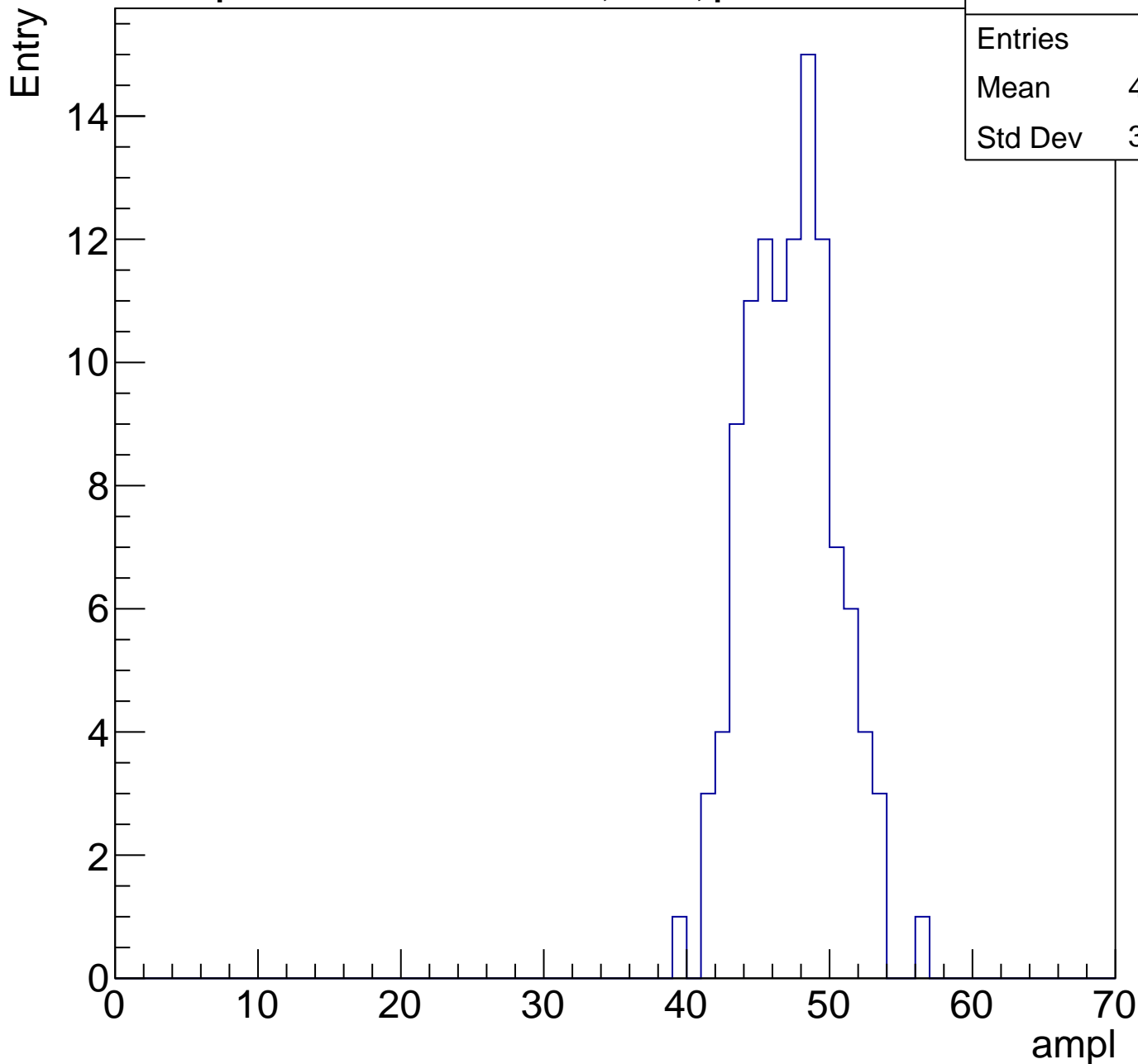
Entry



B1L001S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#2, port C2

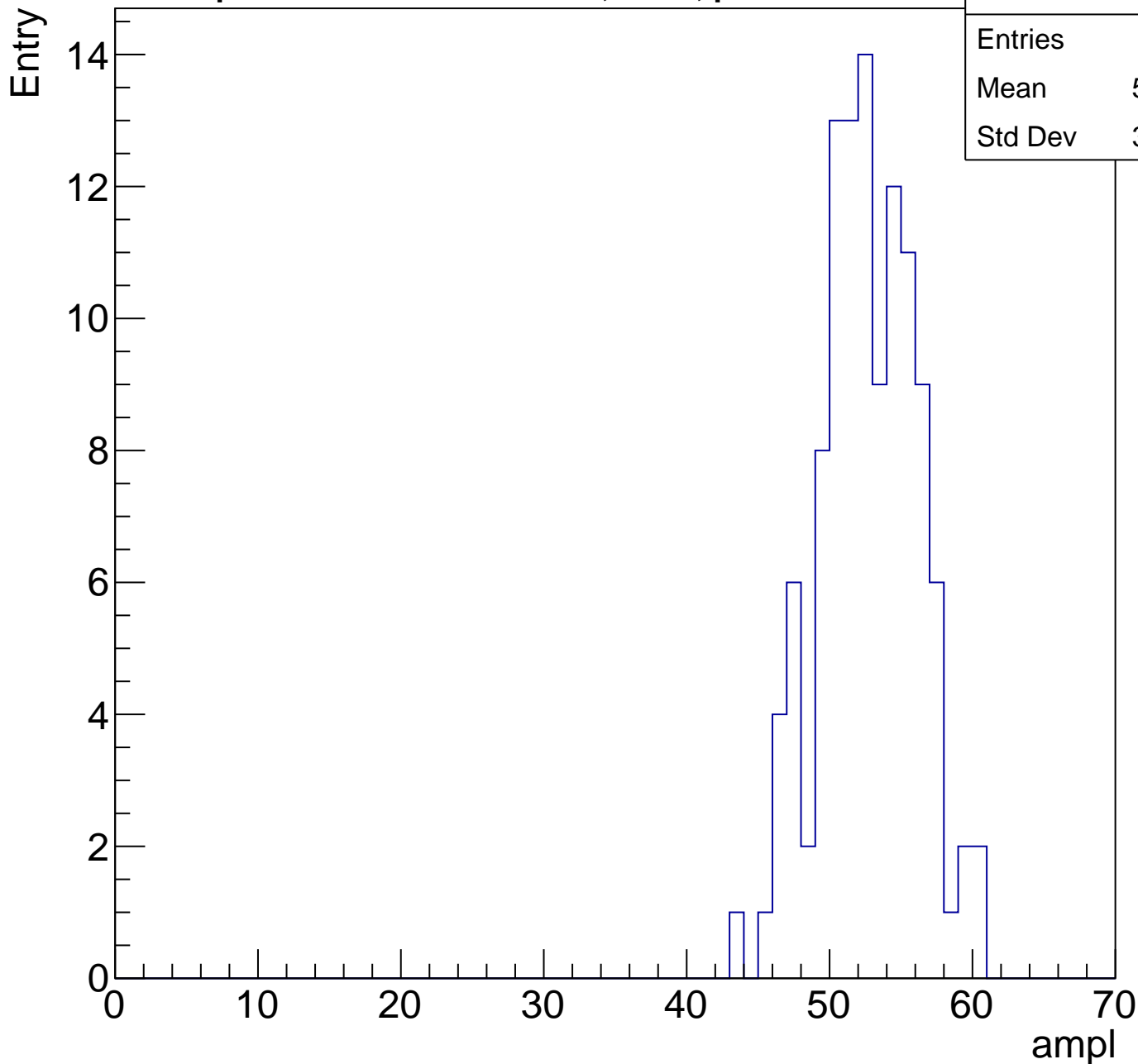
Entries	111
Mean	46.83
Std Dev	3.122



B1L001S, U14-ch83, adc4

calib_packv5_042523_0143.root, FC#2, port C2

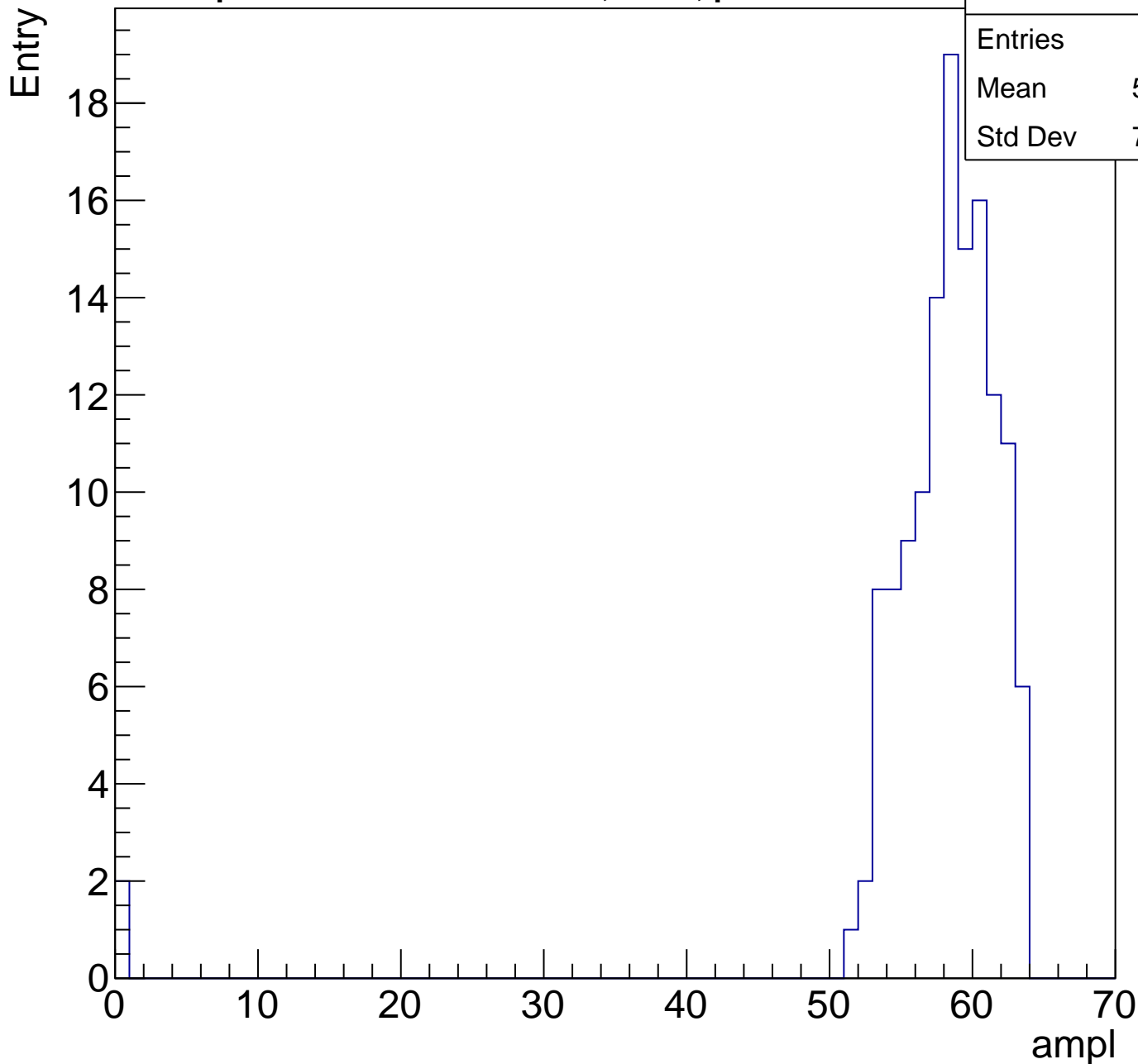
Entries	114
Mean	52.24
Std Dev	3.375



B1L001S, U14-ch83, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	57.17
Std Dev	7.625



B1L001S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

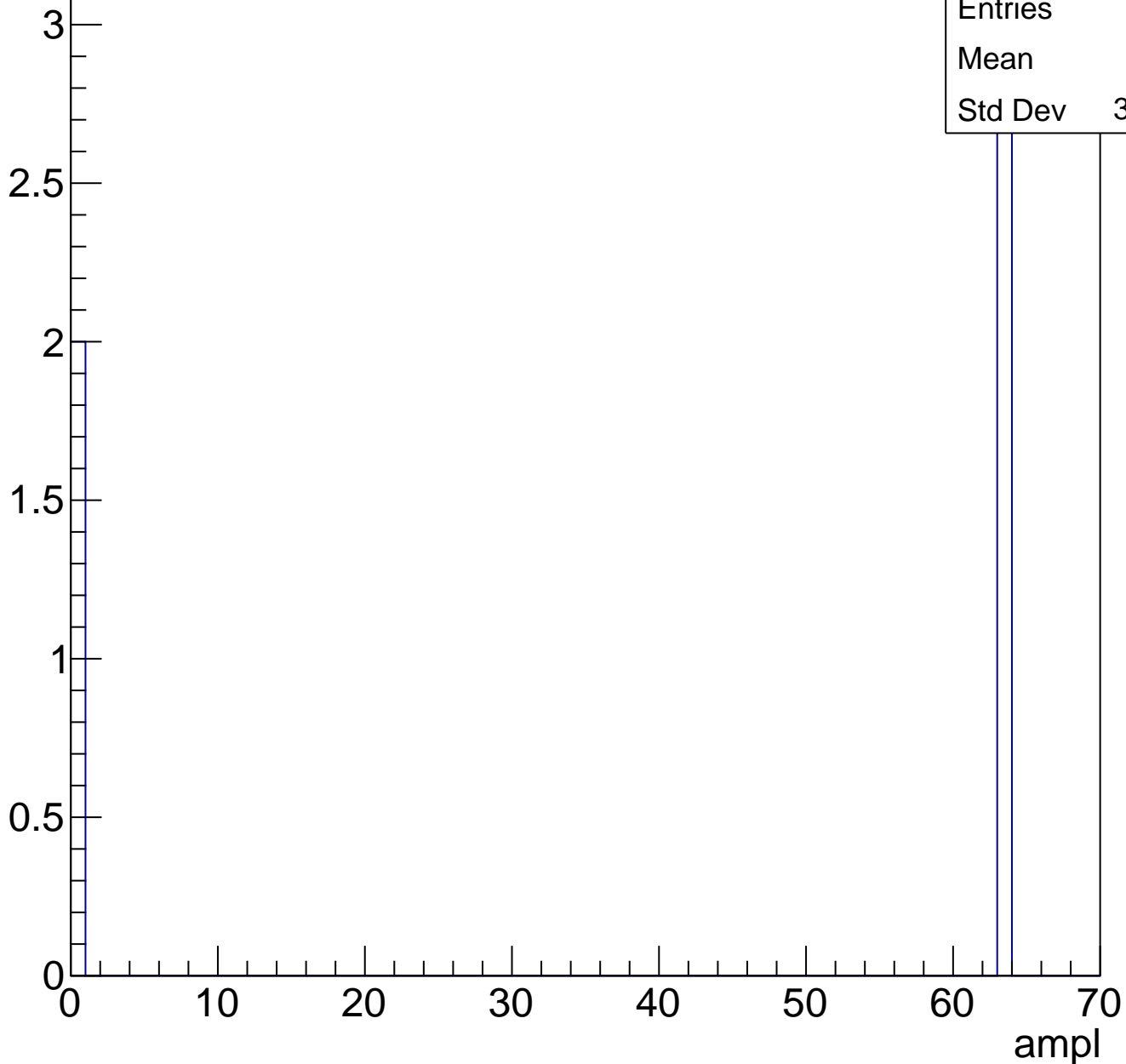
ampl

Entries	33
Mean	61.7
Std Dev	1.243

B1L001S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch84, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	28.95
Std Dev	2.719

Gaus mean : 29.3731

Gaus Width: 2.7786

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

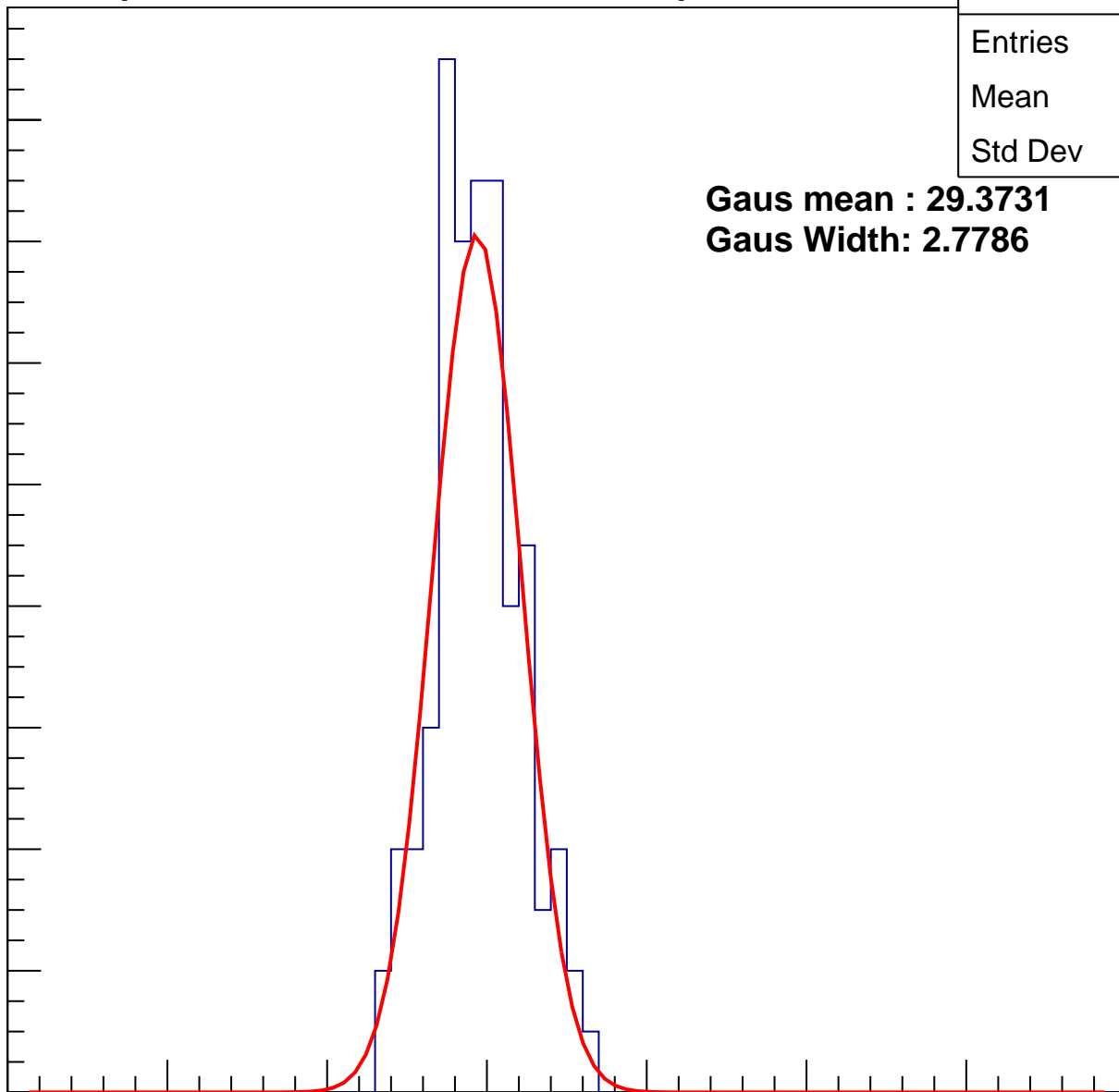
40

50

60

70

ampl



B1L001S, U14-ch84, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	35.25
Std Dev	3.383

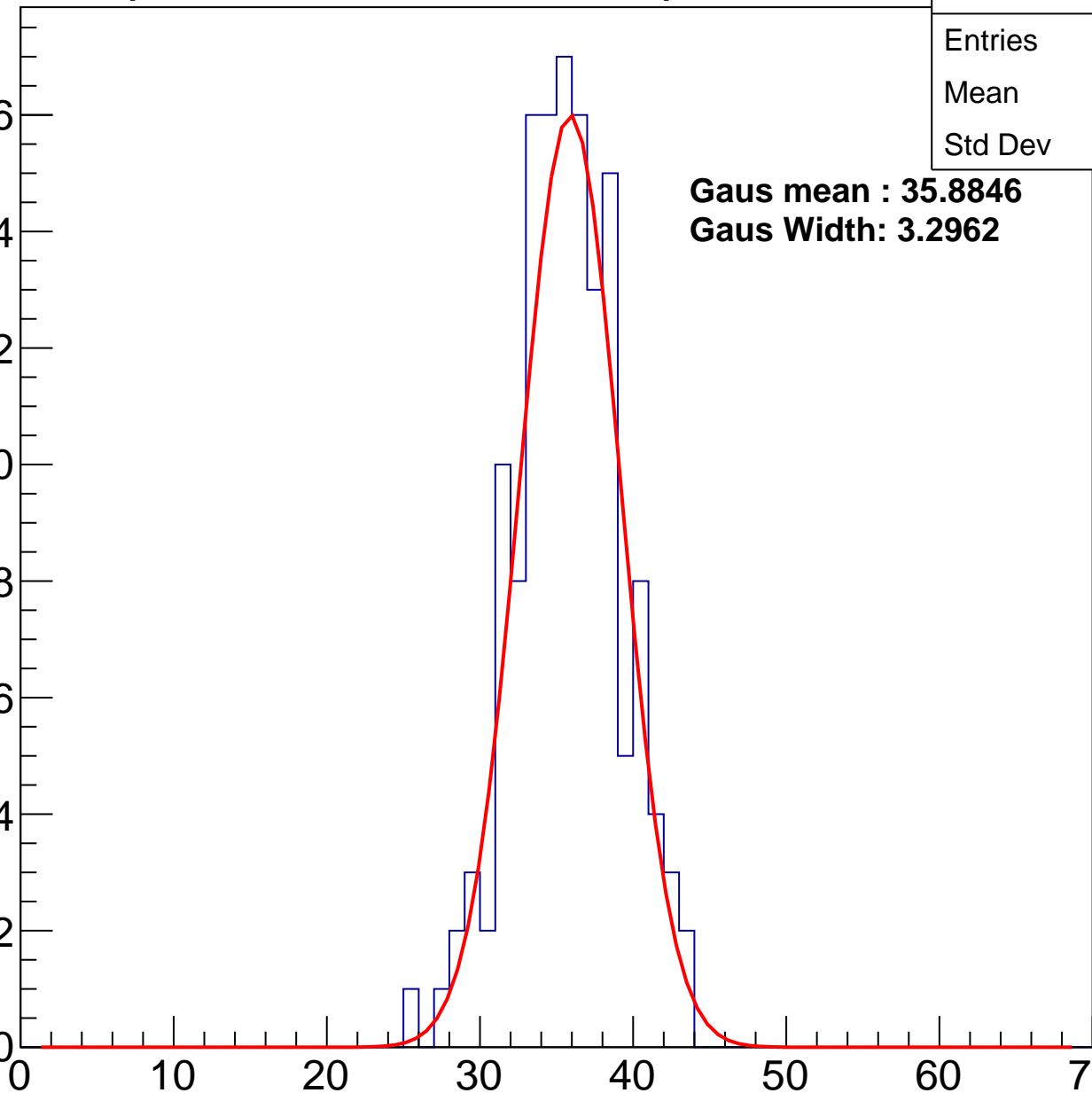
Gaus mean : 35.8846

Gaus Width: 3.2962

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch84, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	41.62
Std Dev	3.043

Gaus mean : 41.9425

Gaus Width: 3.0418

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#2, port C2

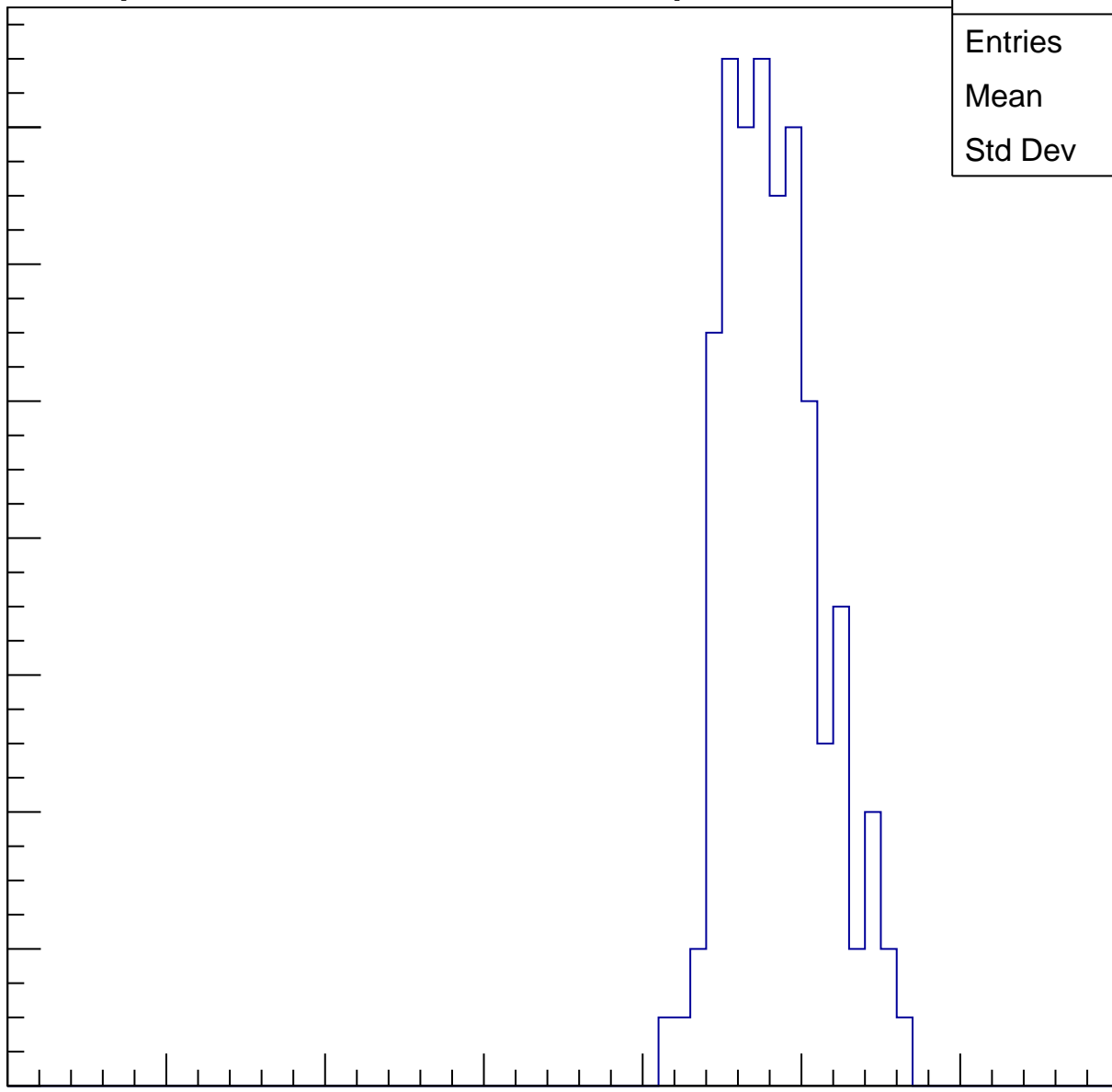
Entries	117
Mean	47.81
Std Dev	3.038

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

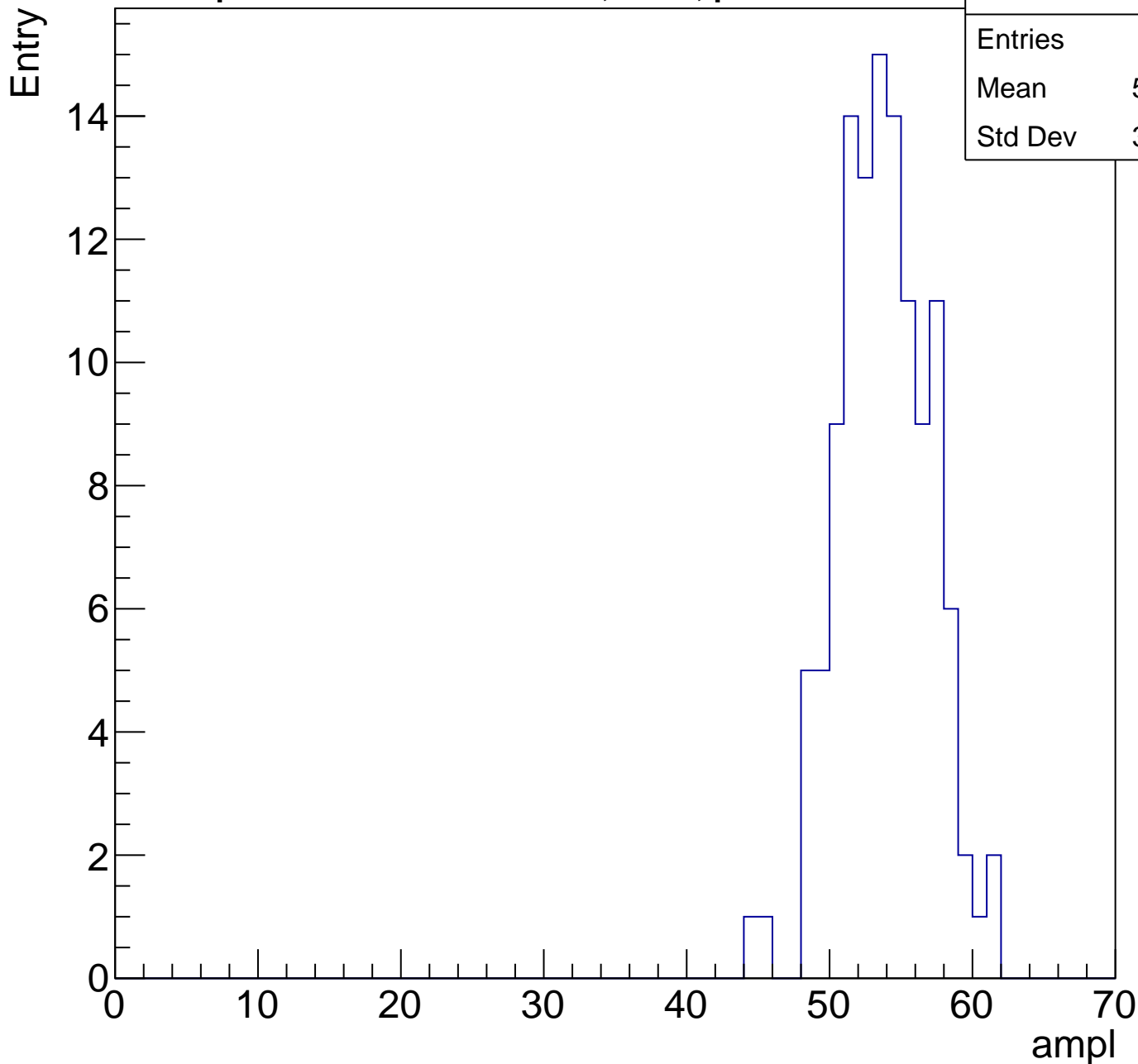
ampl



B1L001S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#2, port C2

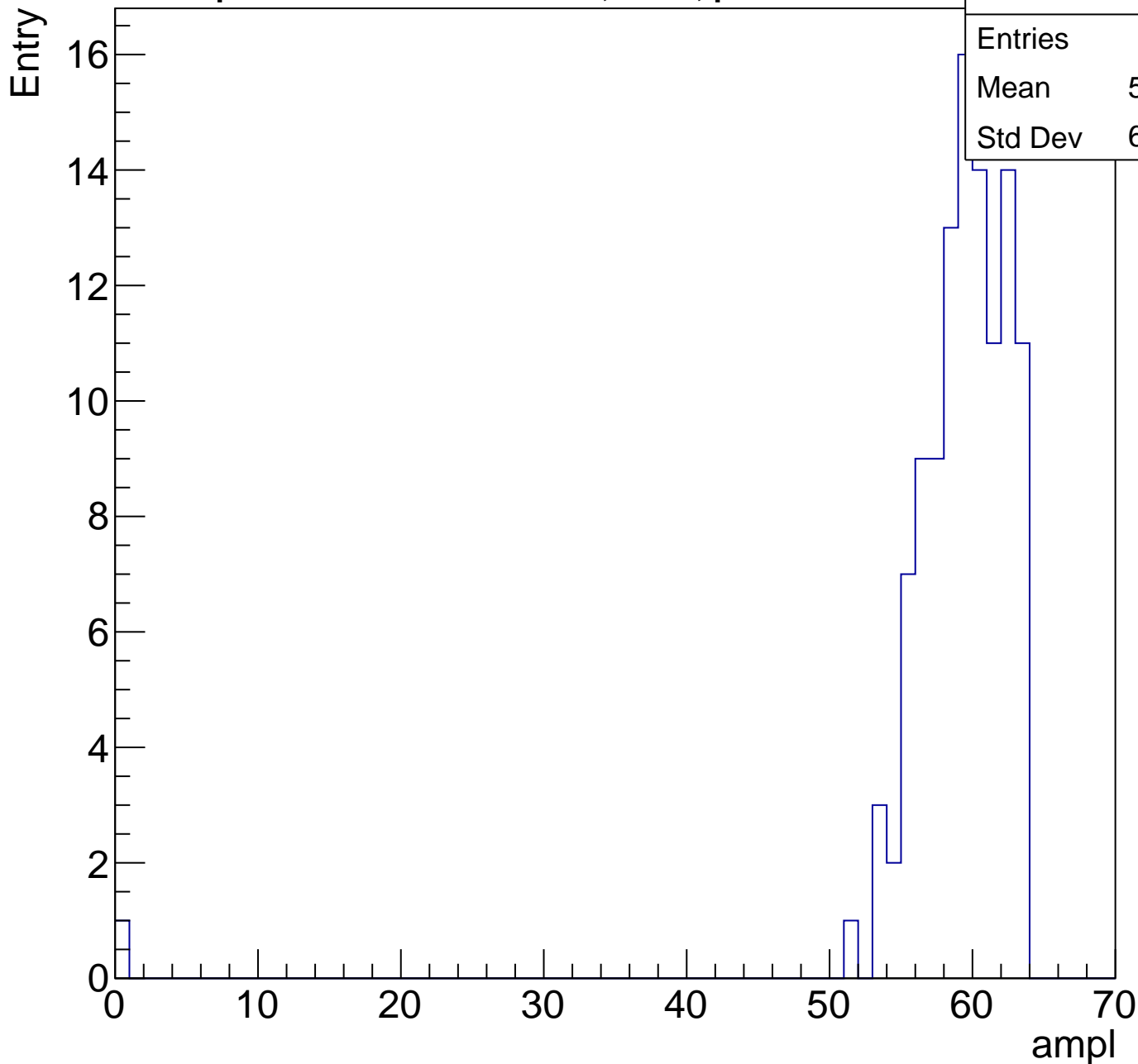
Entries	119
Mean	53.35
Std Dev	3.174



B1L001S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	58.47
Std Dev	6.205

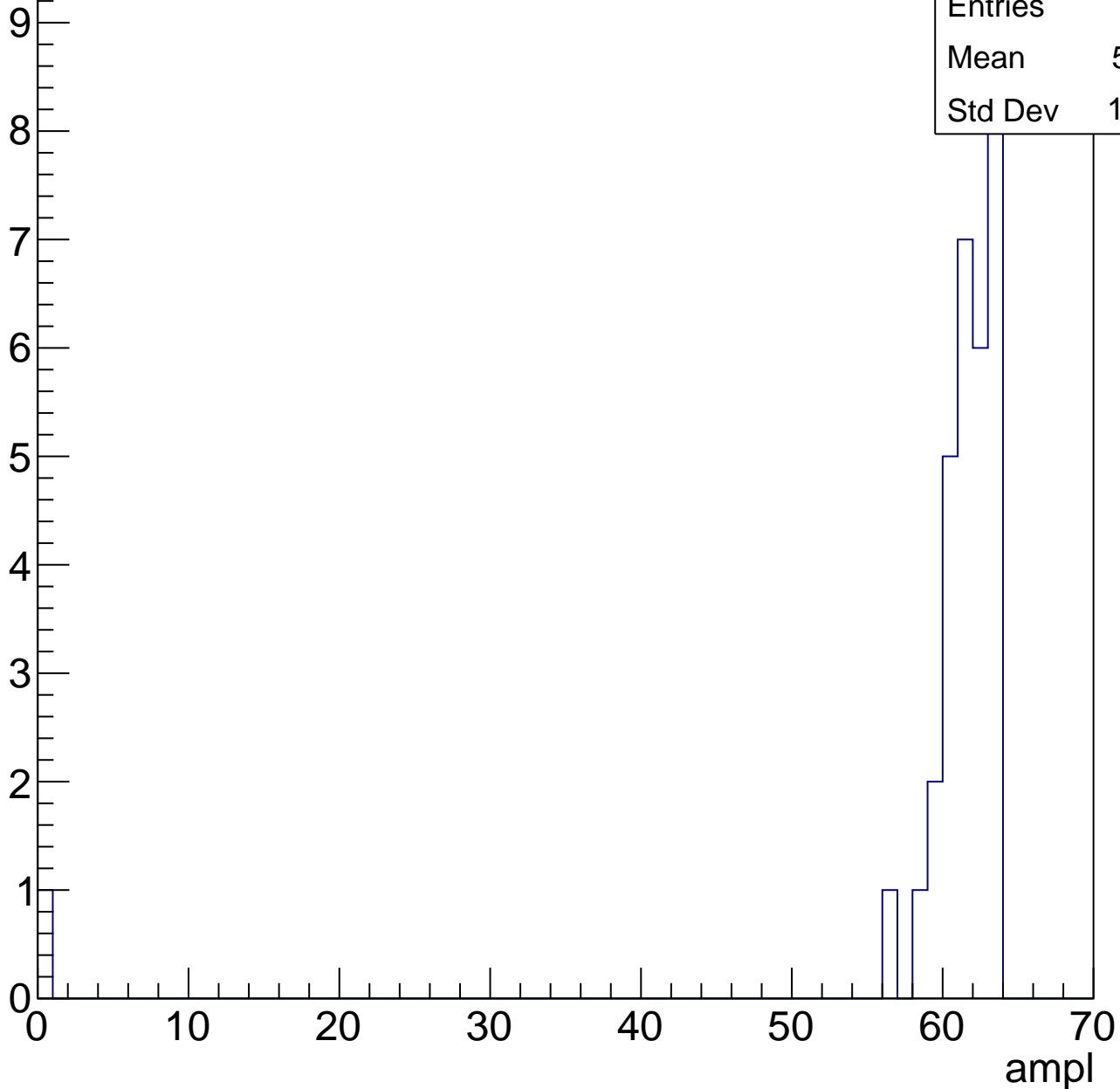


B1L001S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	32
Mean	59.31
Std Dev	10.78



B1L001S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	22
Std Dev	0

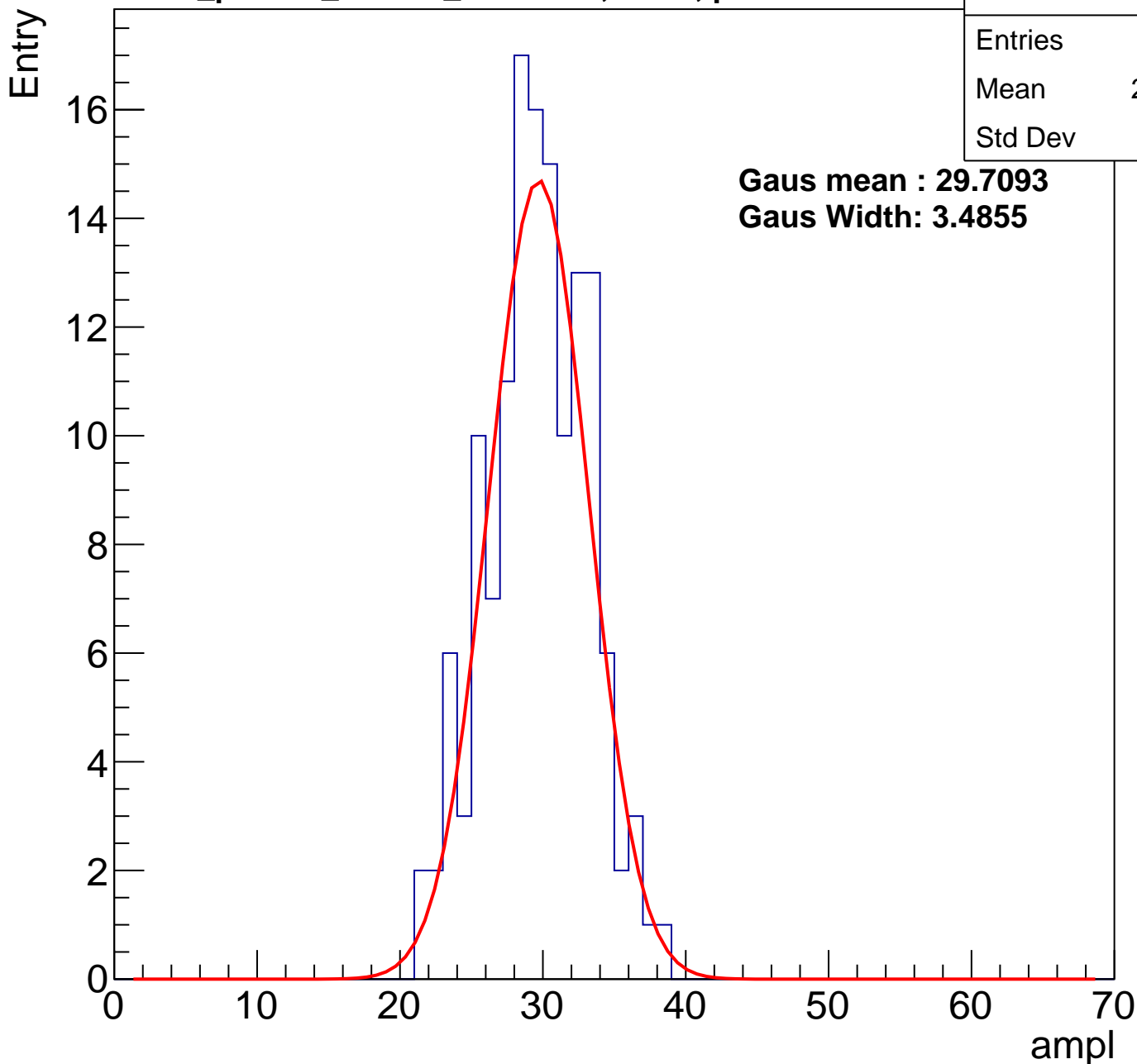
B1L001S, U14-ch85, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	29.18
Std Dev	3.5

Gaus mean : 29.7093

Gaus Width: 3.4855



B1L001S, U14-ch85, adc1

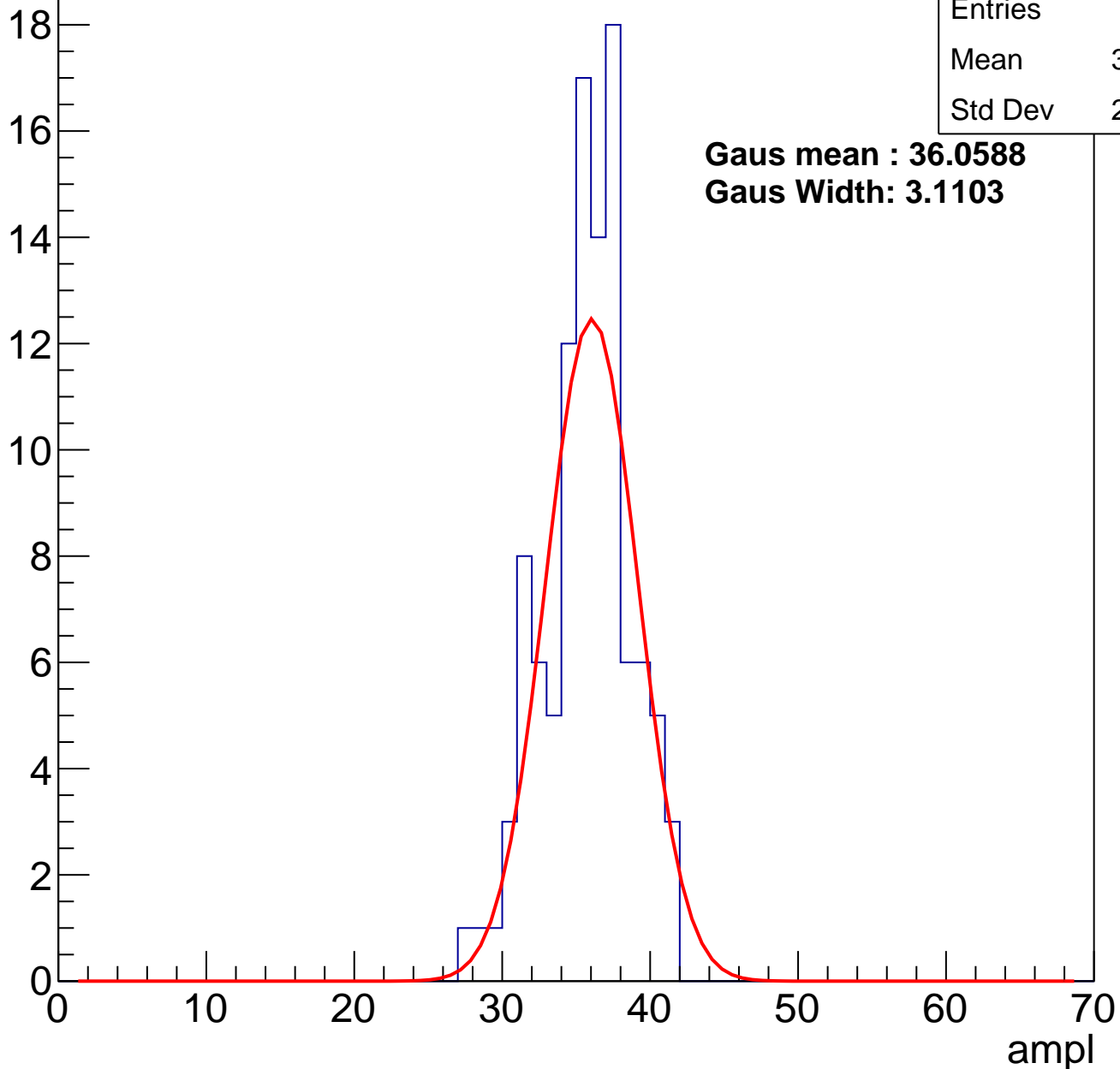
calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	35.25
Std Dev	2.914

Gaus mean : 36.0588

Gaus Width: 3.1103

Entry



B1L001S, U14-ch85, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	41.88
Std Dev	3.559

Gaus mean : 42.9346

Gaus Width: 3.0270

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

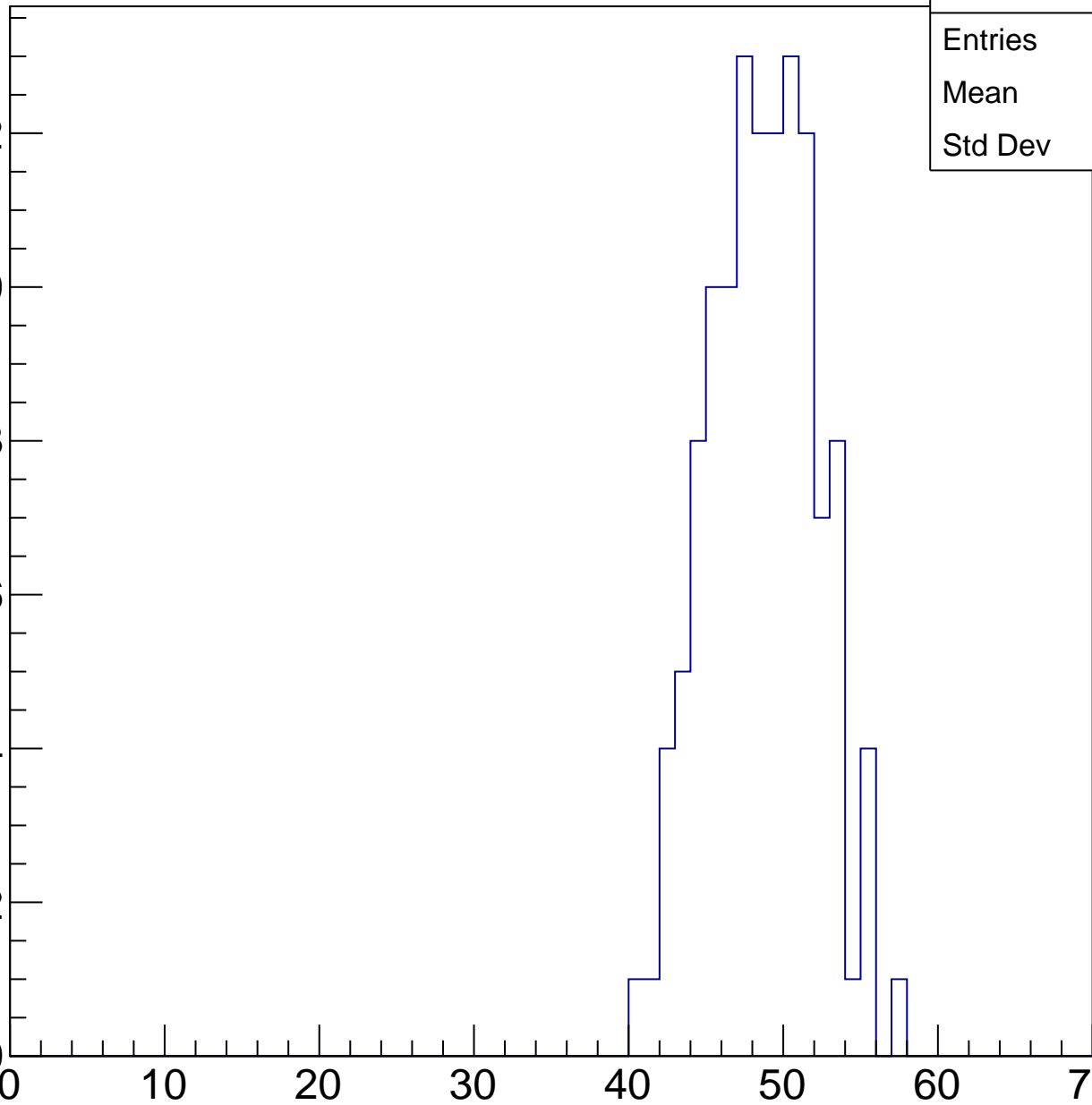
40

50

60

ampl

Entries	122
Mean	48.21
Std Dev	3.441



B1L001S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	110
Mean	54.08
Std Dev	3.222

Entry

12

10

8

6

4

2

0

0

10

20

30

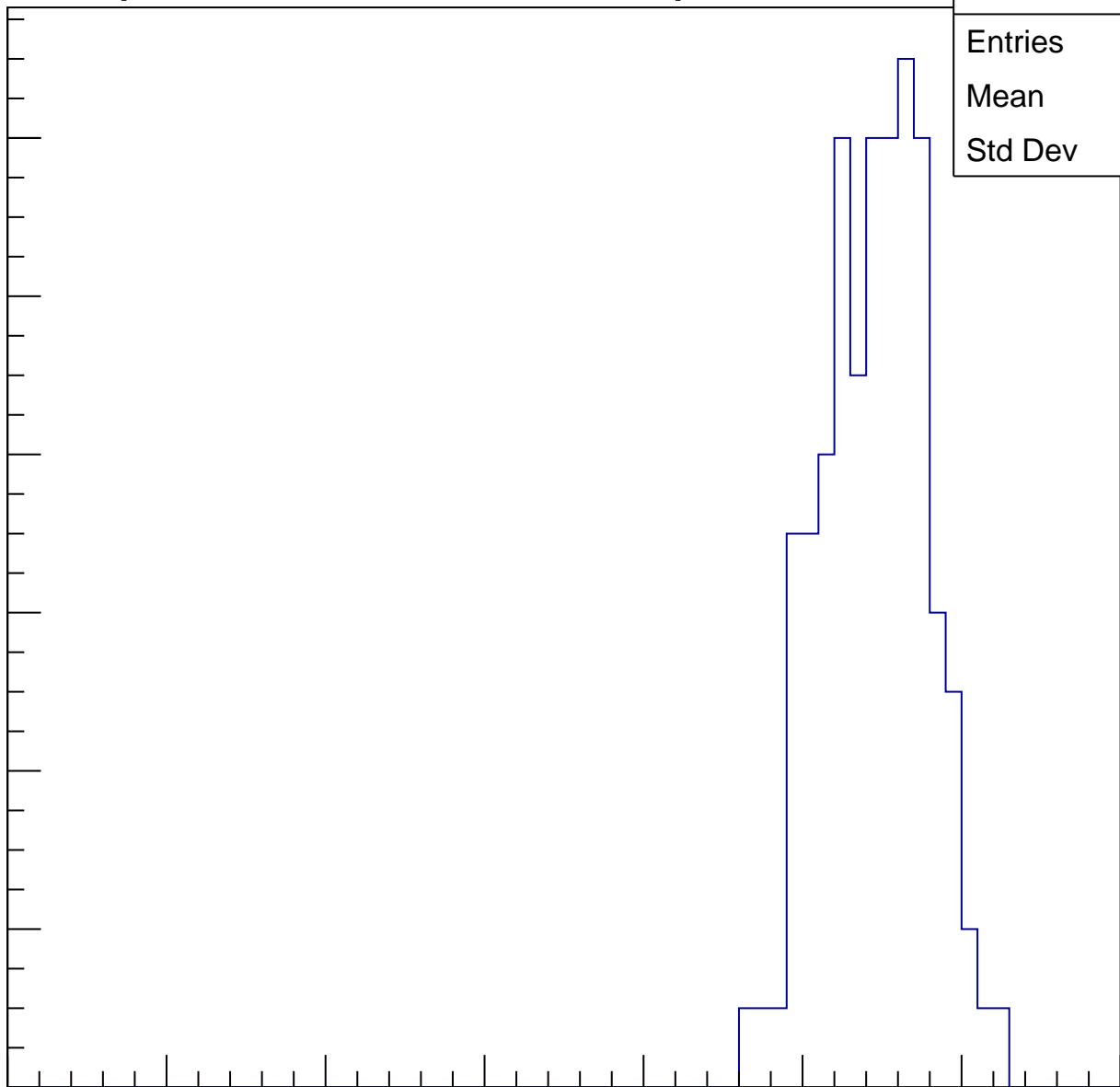
40

50

60

70

ampl



B1L001S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#2, port C2

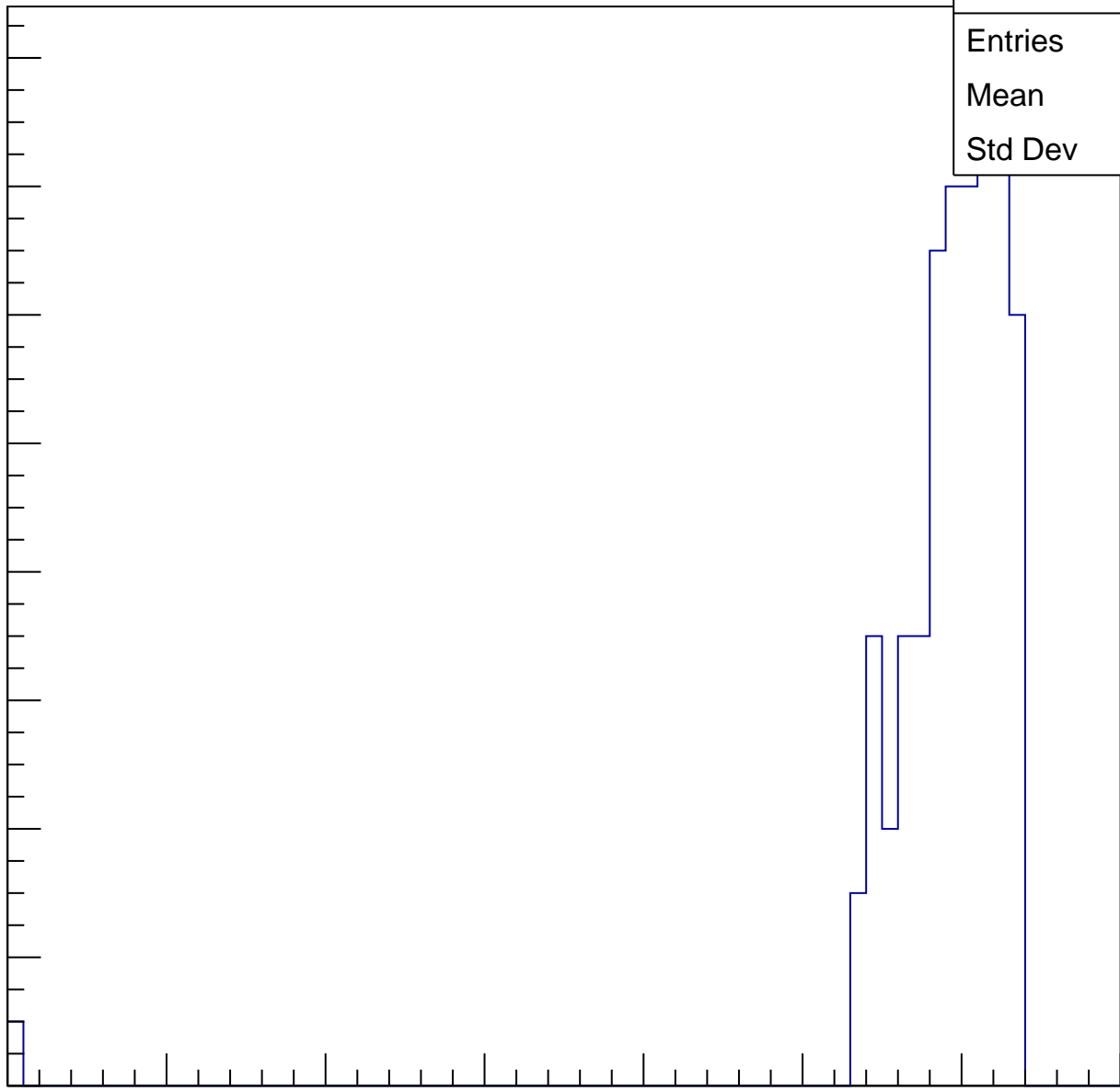
Entries	113
Mean	58.68
Std Dev	6.191

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

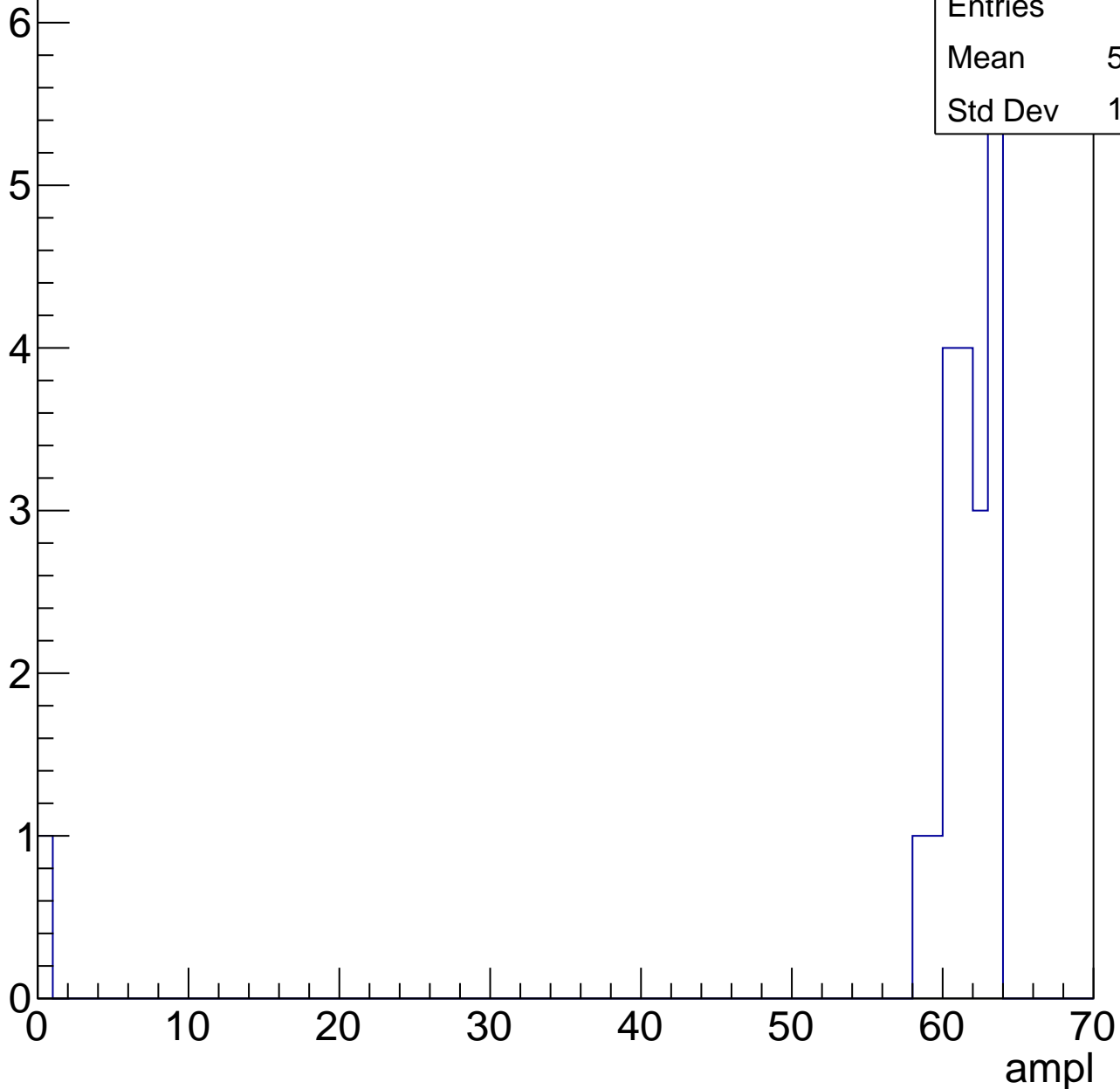


B1L001S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

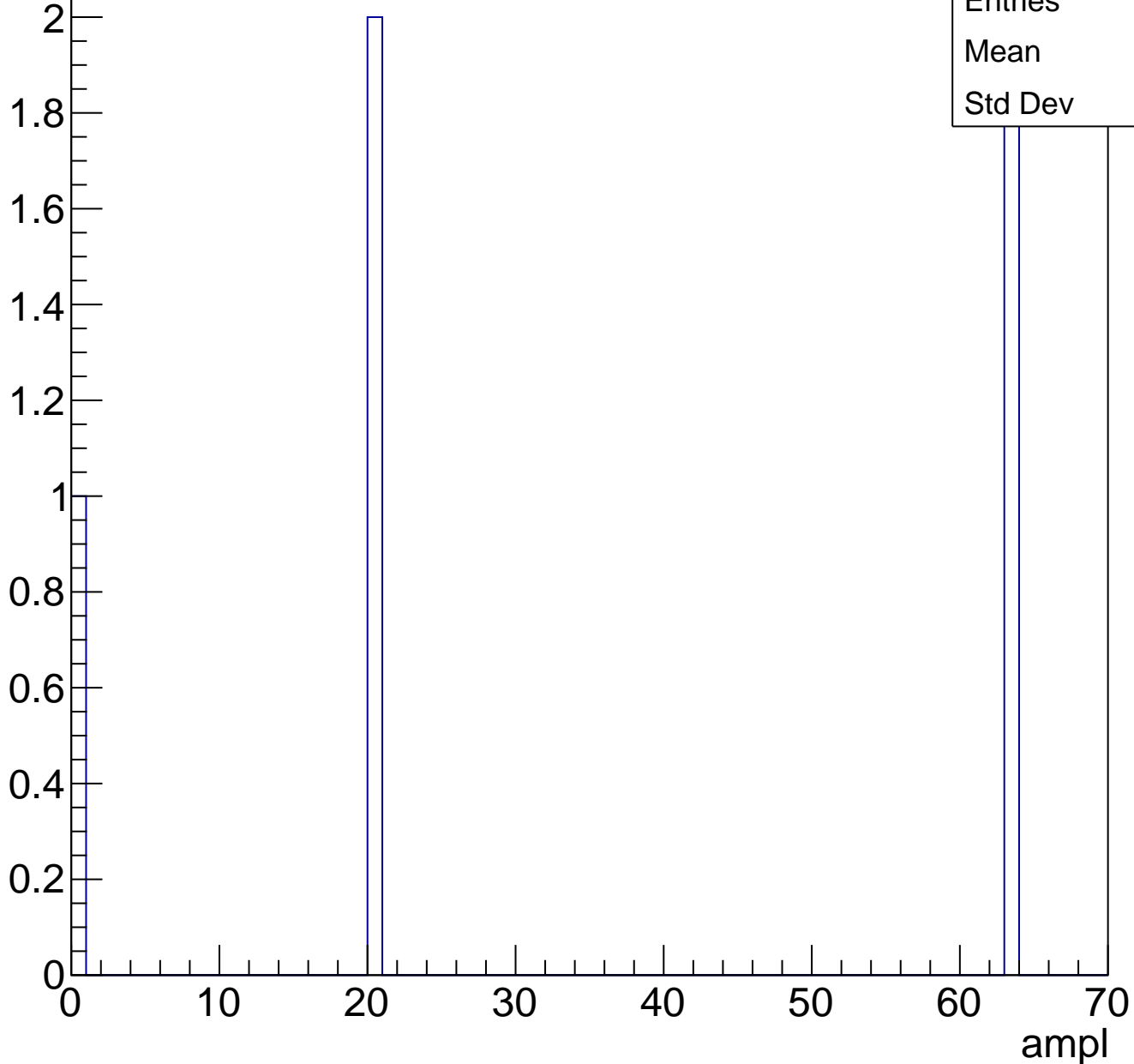
Entries	20
Mean	58.25
Std Dev	13.44



B1L001S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



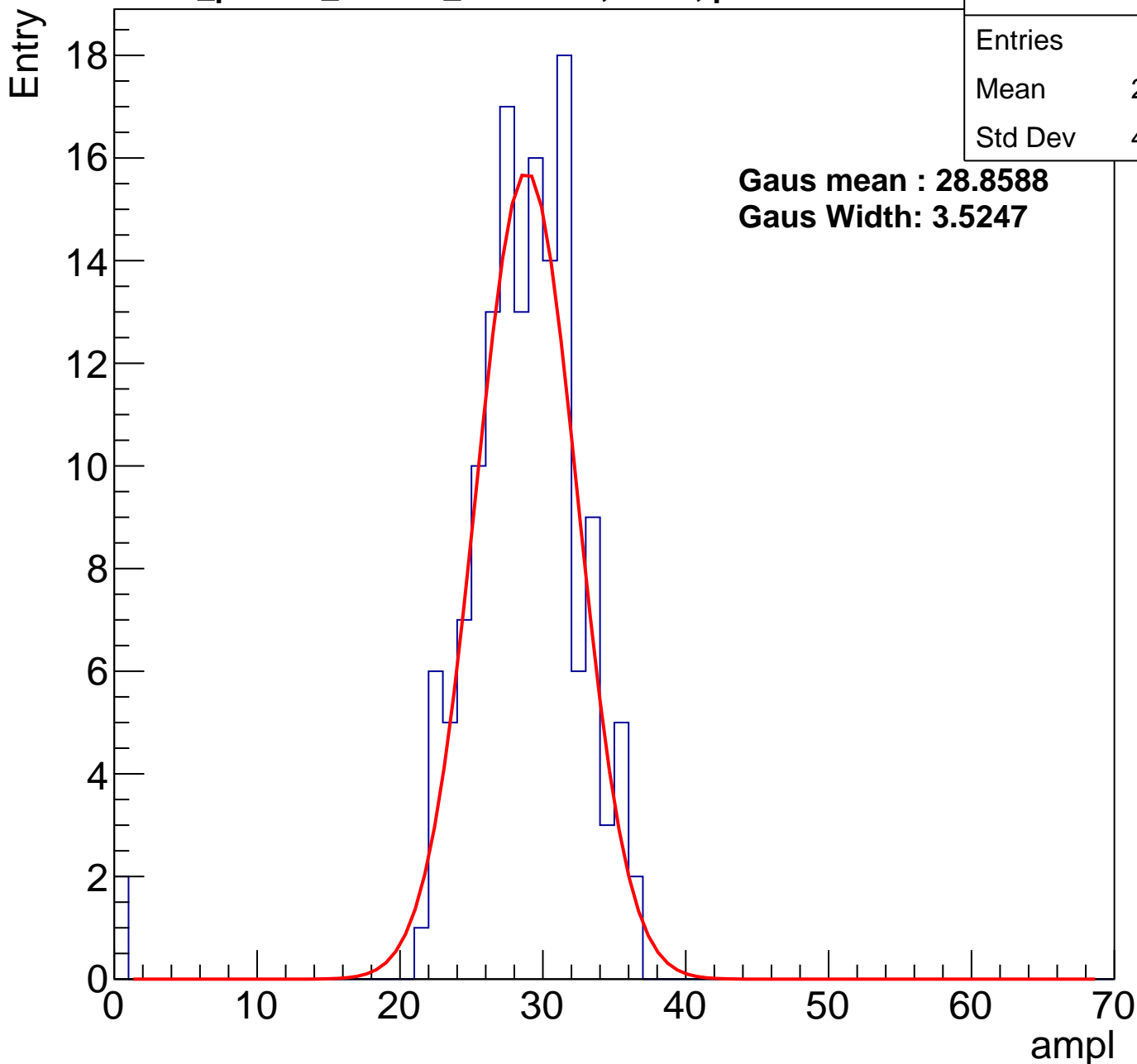
B1L001S, U14-ch86, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	28.07
Std Dev	4.712

Gaus mean : 28.8588

Gaus Width: 3.5247



B1L001S, U14-ch86, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	83
Mean	33.82
Std Dev	2.756

Gaus mean : 36.1563

Gaus Width: 4.0778

Entry

12

10

8

6

4

2

0

0

10

20

30

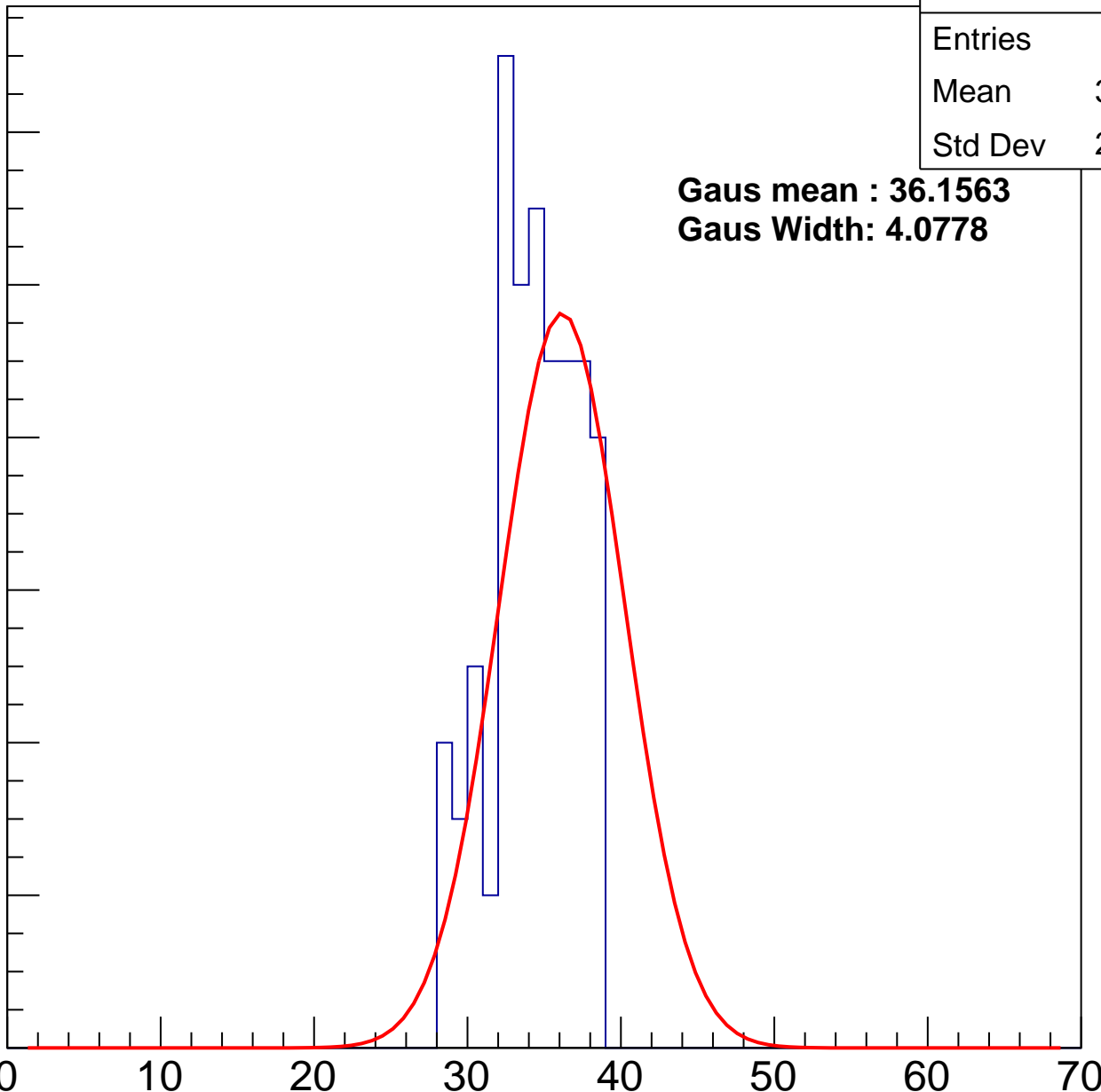
40

50

60

70

ampl



B1L001S, U14-ch86, adc2

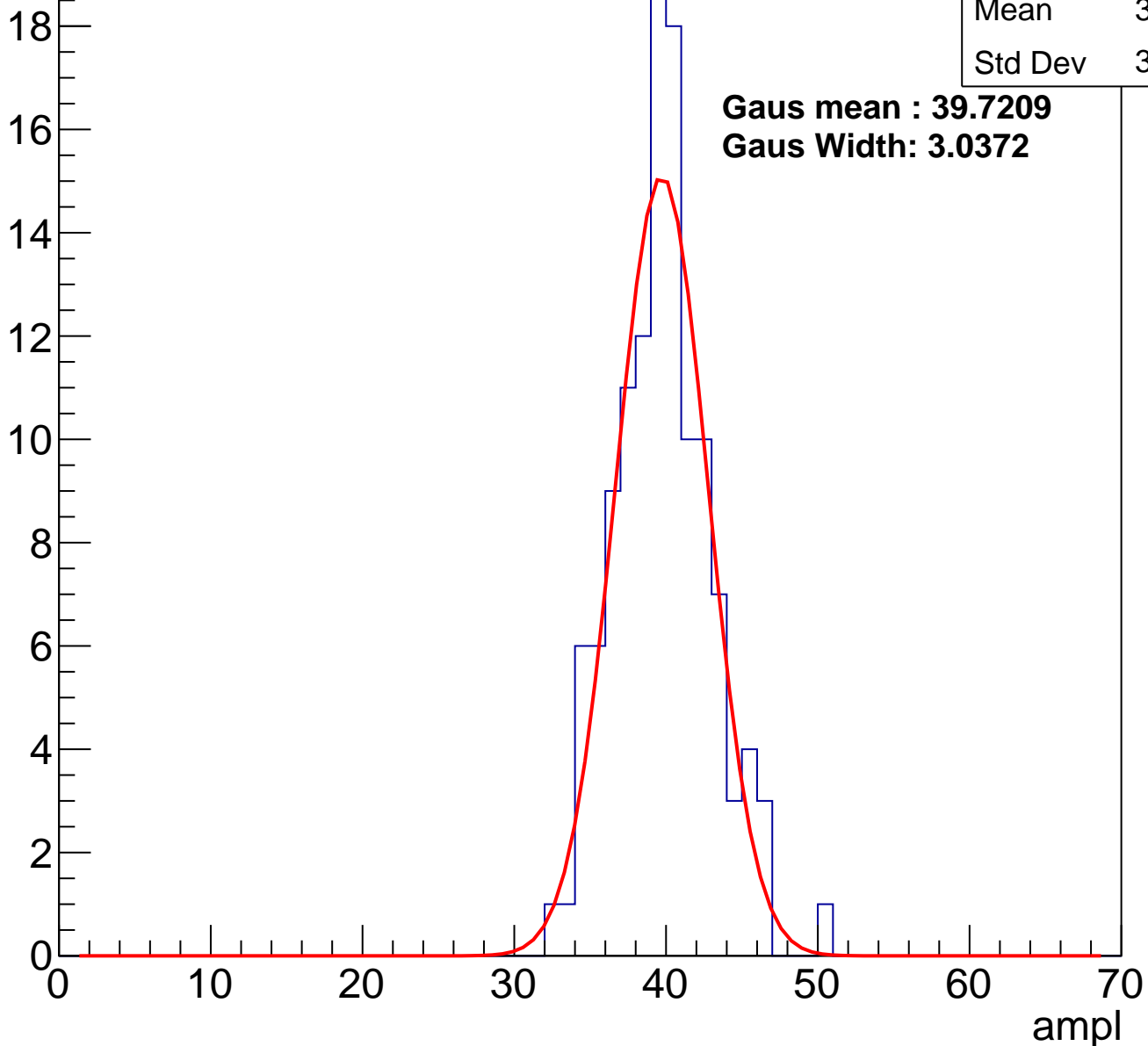
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	39.32
Std Dev	3.152

Gaus mean : 39.7209

Gaus Width: 3.0372

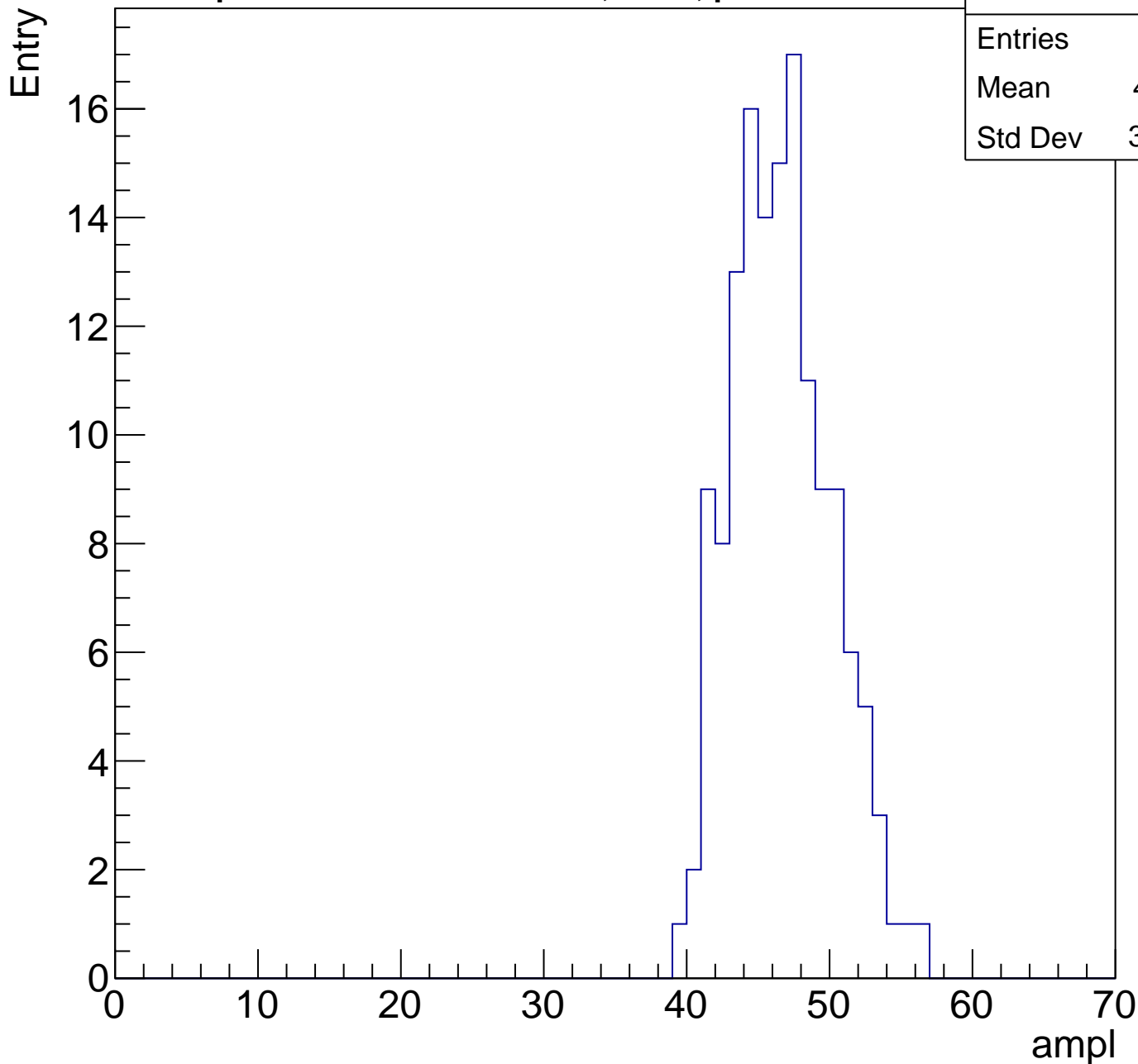
Entry



B1L001S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#2, port C2

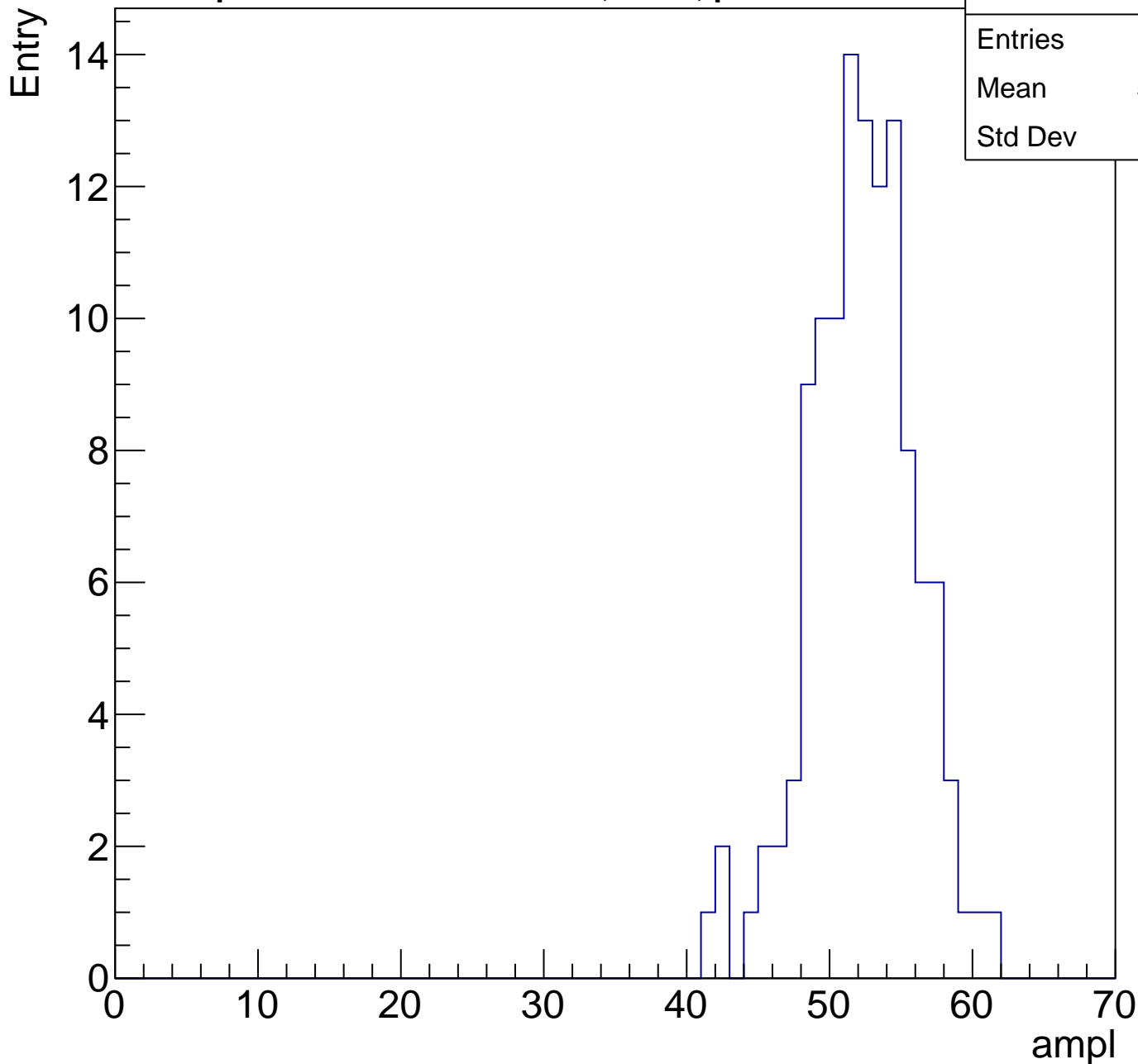
Entries	141
Mean	46.21
Std Dev	3.443



B1L001S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	51.81
Std Dev	3.68



B1L001S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

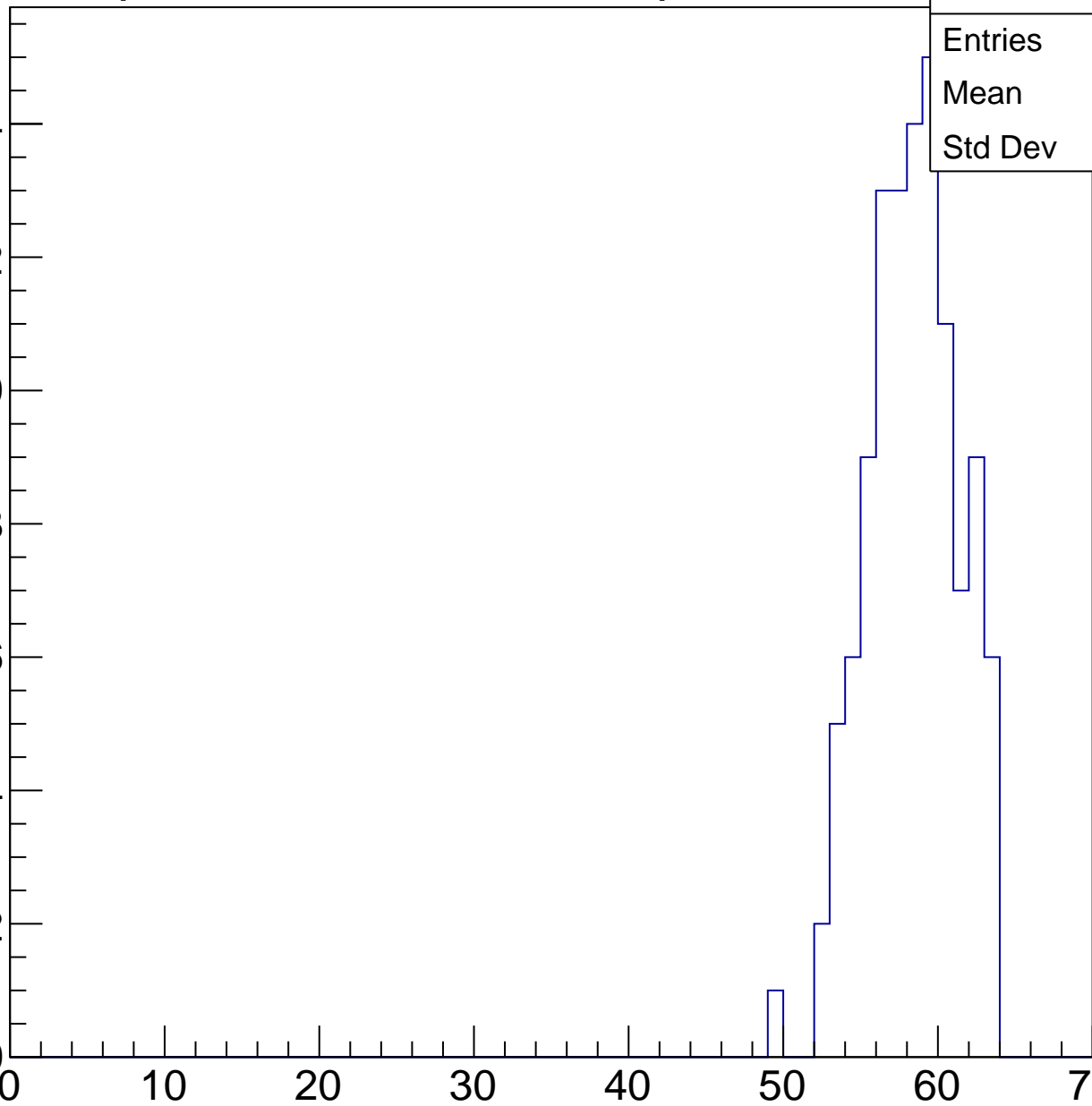
50

60

70

ampl

Entries	111
Mean	57.89
Std Dev	2.902



B1L001S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

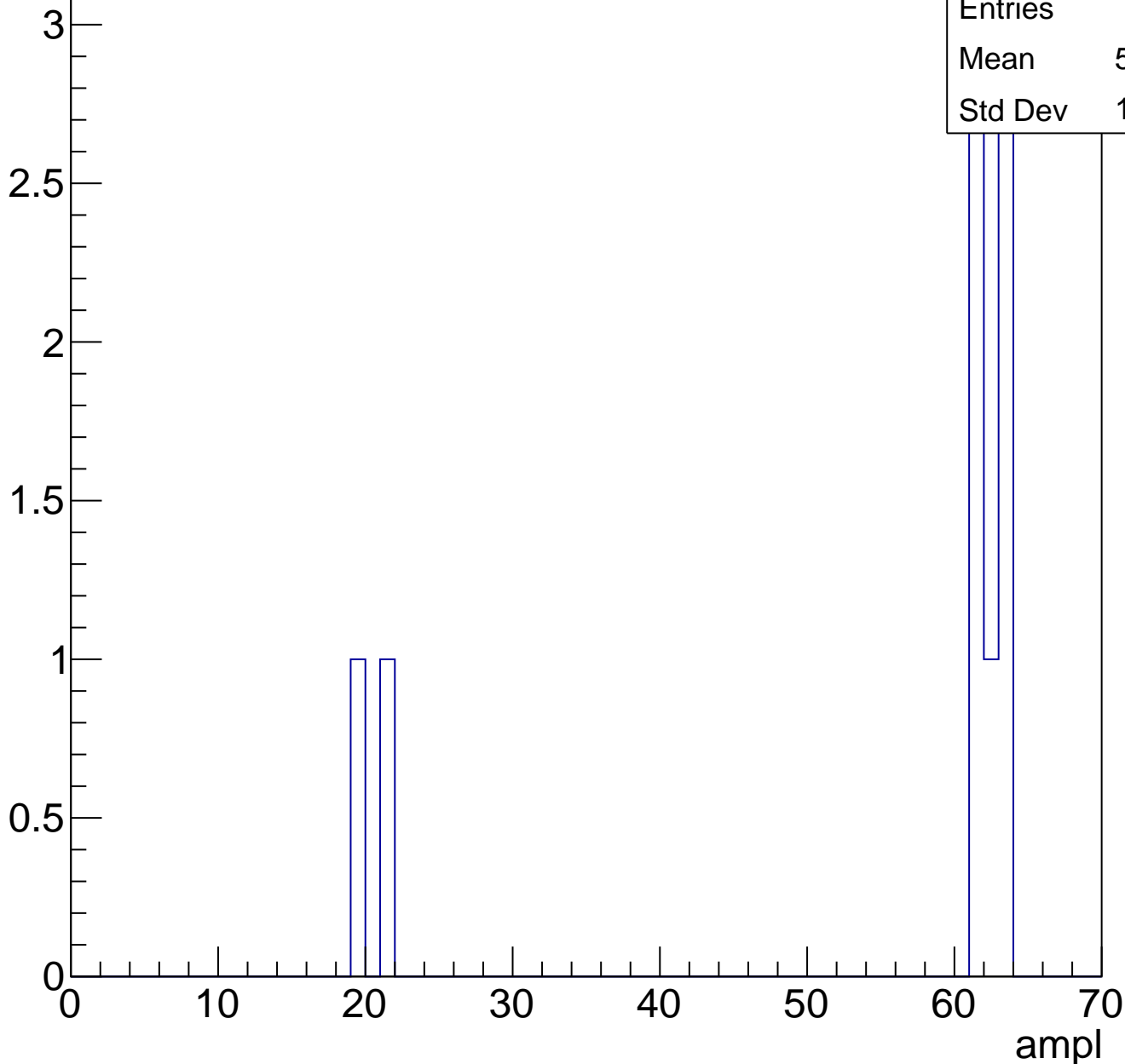
Entries	50
Mean	58.38
Std Dev	12.08

0

B1L001S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch87, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	28.58
Std Dev	3.309

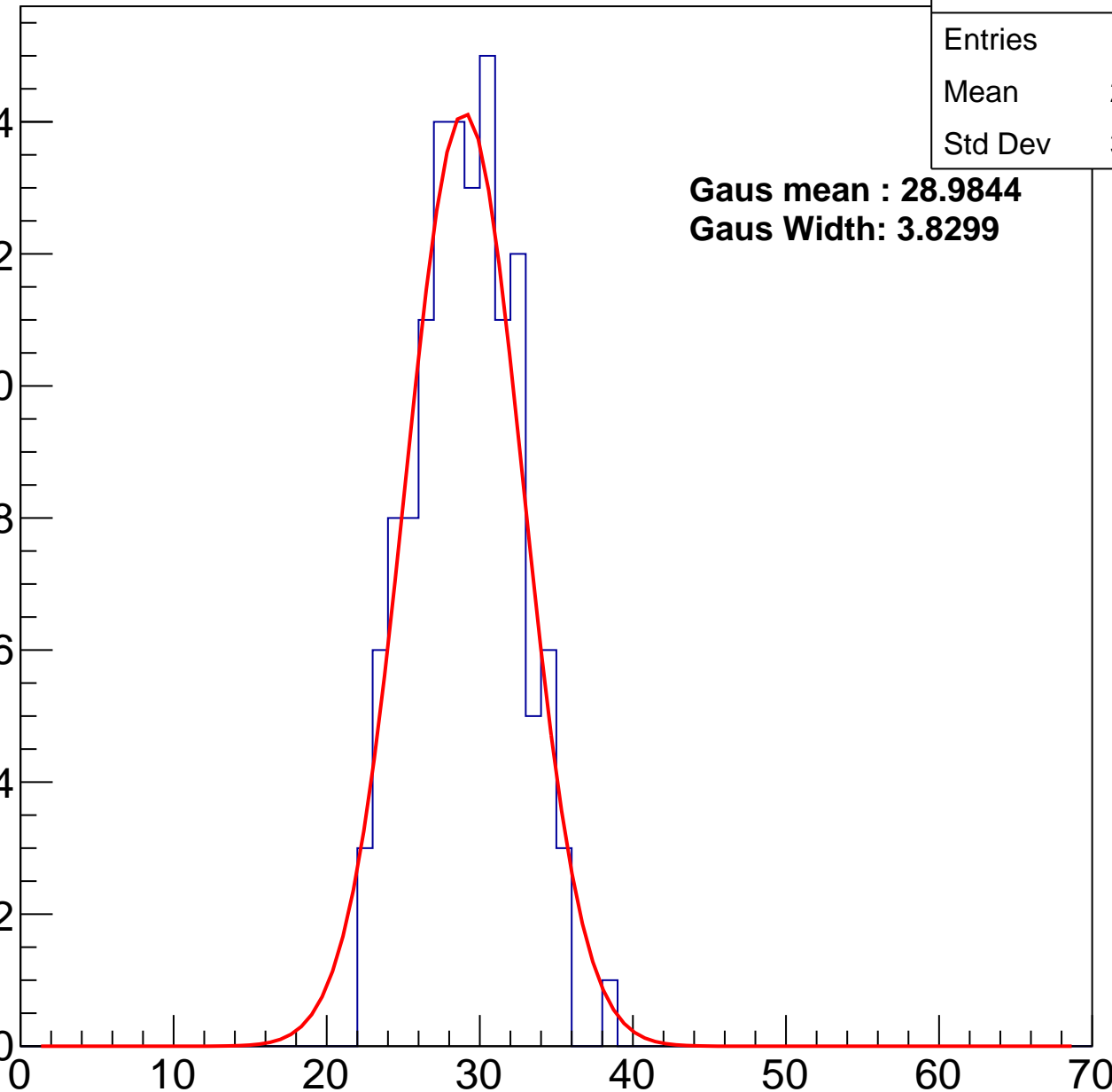
Gaus mean : 28.9844

Gaus Width: 3.8299

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch87, adc1

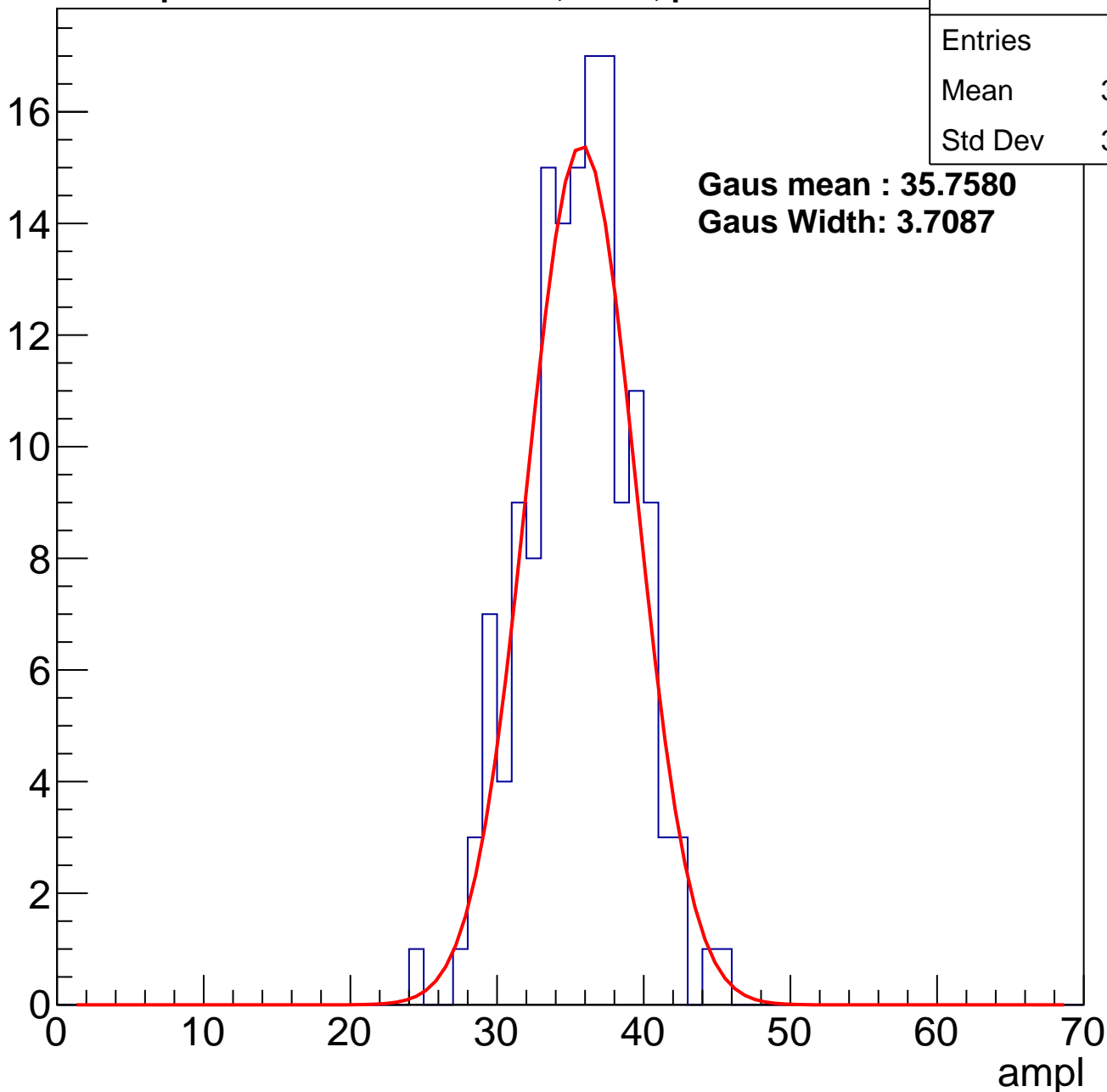
calib_packv5_042523_0143.root, FC#2, port C2

Entries	148
Mean	35.13
Std Dev	3.653

Gaus mean : 35.7580

Gaus Width: 3.7087

Entry



B1L001S, U14-ch87, adc2

calib_packv5_042523_0143.root, FC#2, port C2

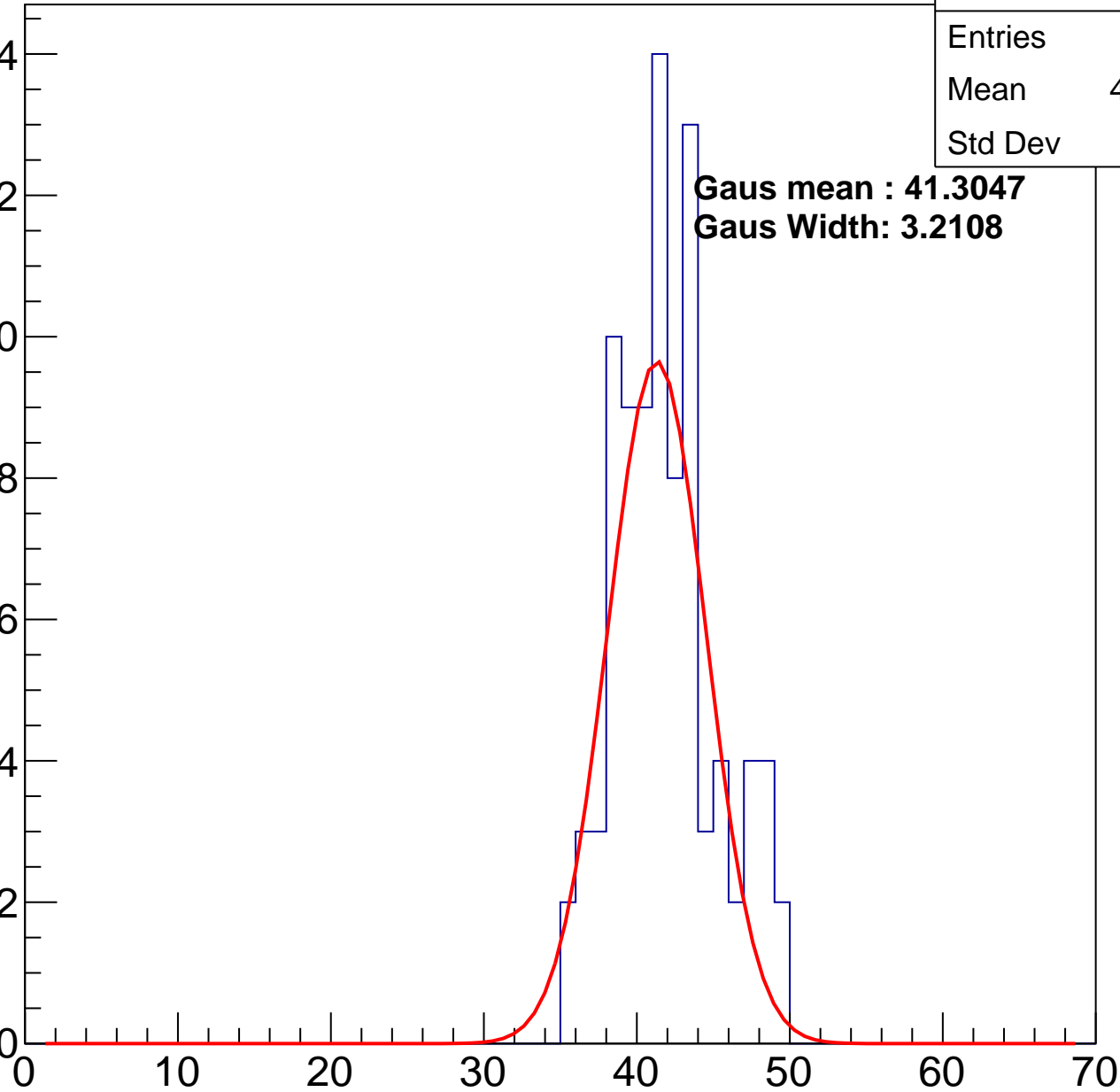
Entry

14
12
10
8
6
4
2
0

Entries	90
Mean	41.46
Std Dev	3.3

Gaus mean : 41.3047
Gaus Width: 3.2108

ampl

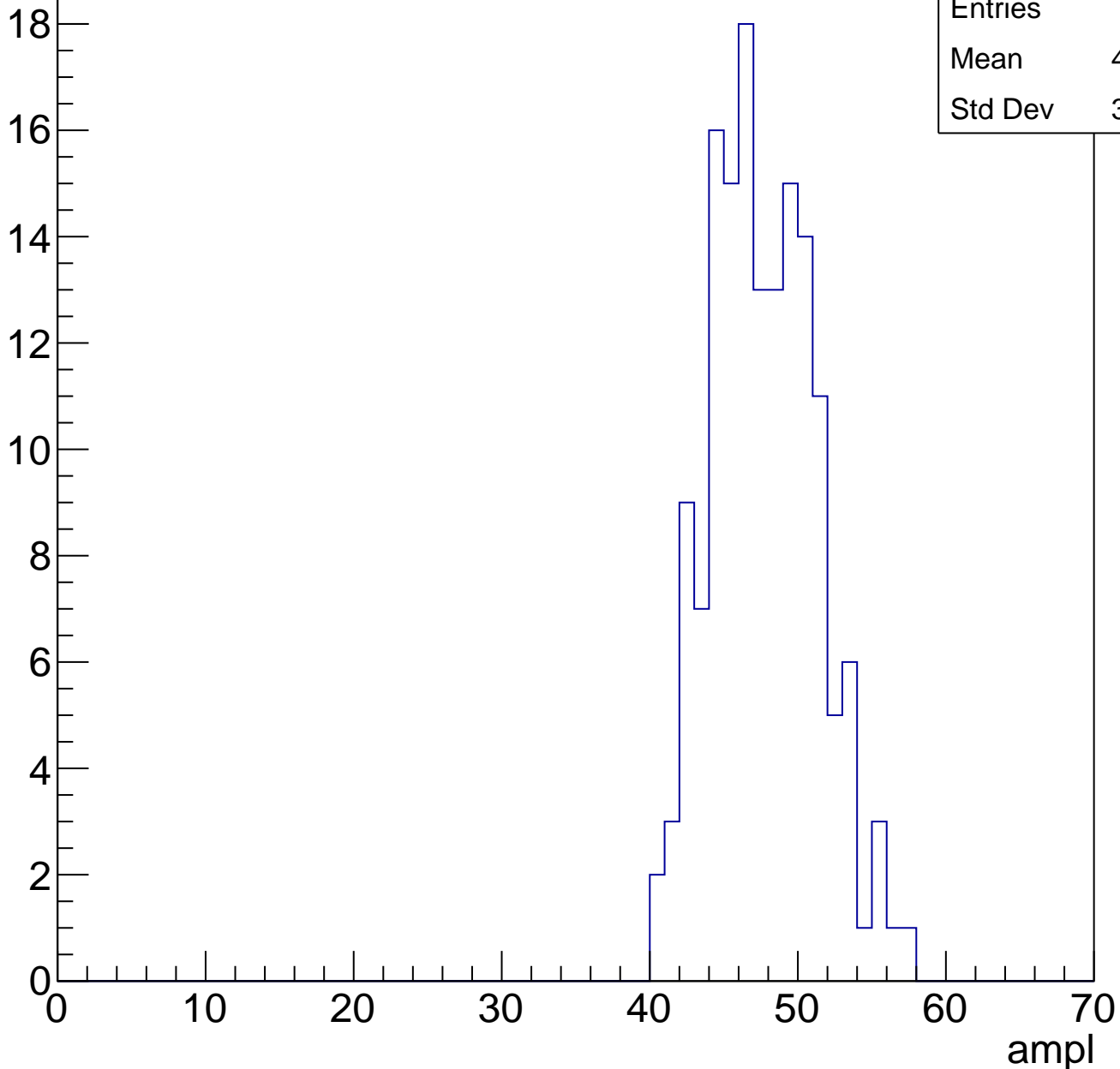


B1L001S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	47.25
Std Dev	3.518

Entry



B1L001S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#2, port C2

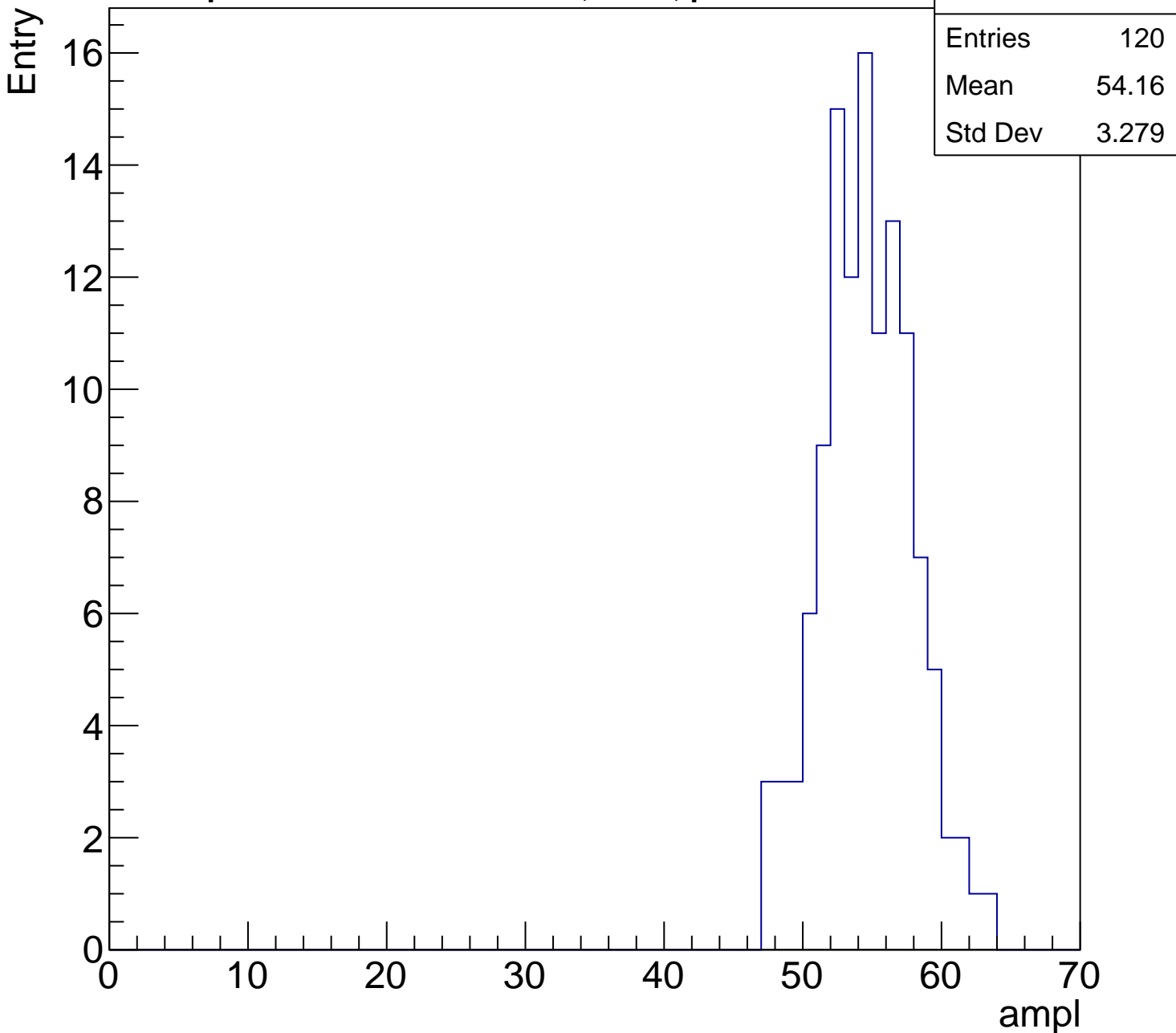
Entries	120
Mean	54.16
Std Dev	3.279

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

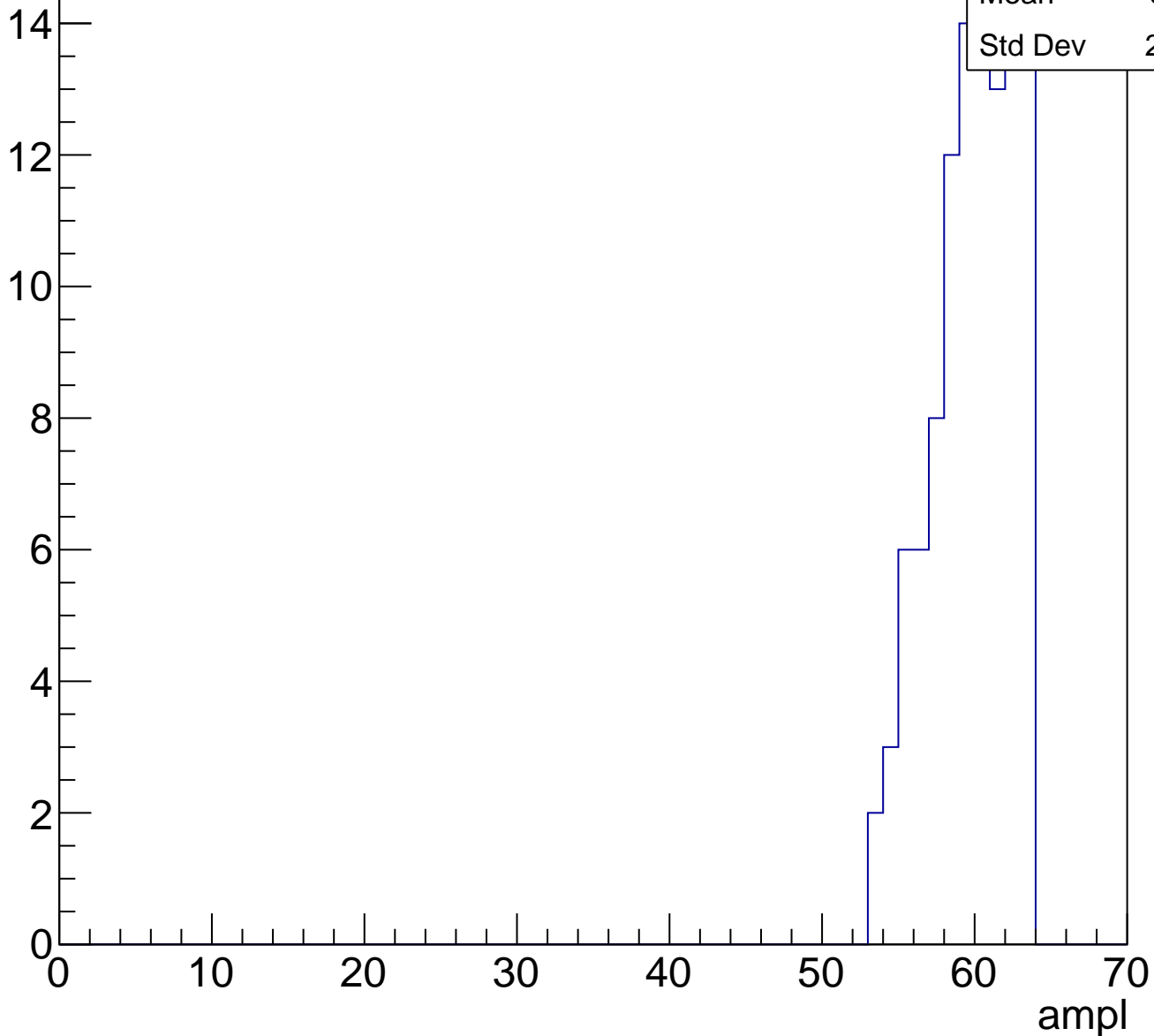
ampl



B1L001S, U14-ch87, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

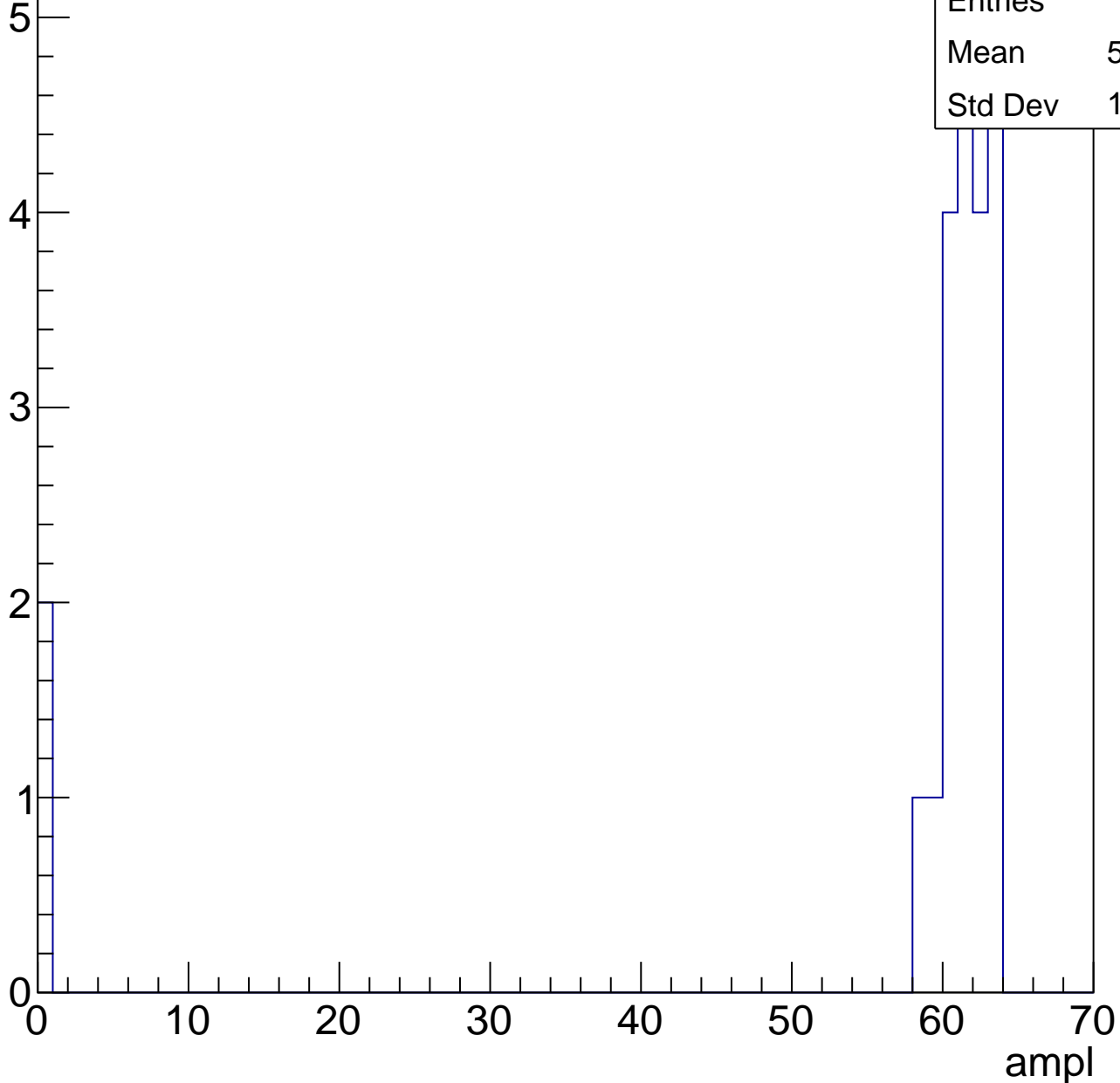


B1L001S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	22
Mean	55.68
Std Dev	17.66



B1L001S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	2
Mean	0
Std Dev	0

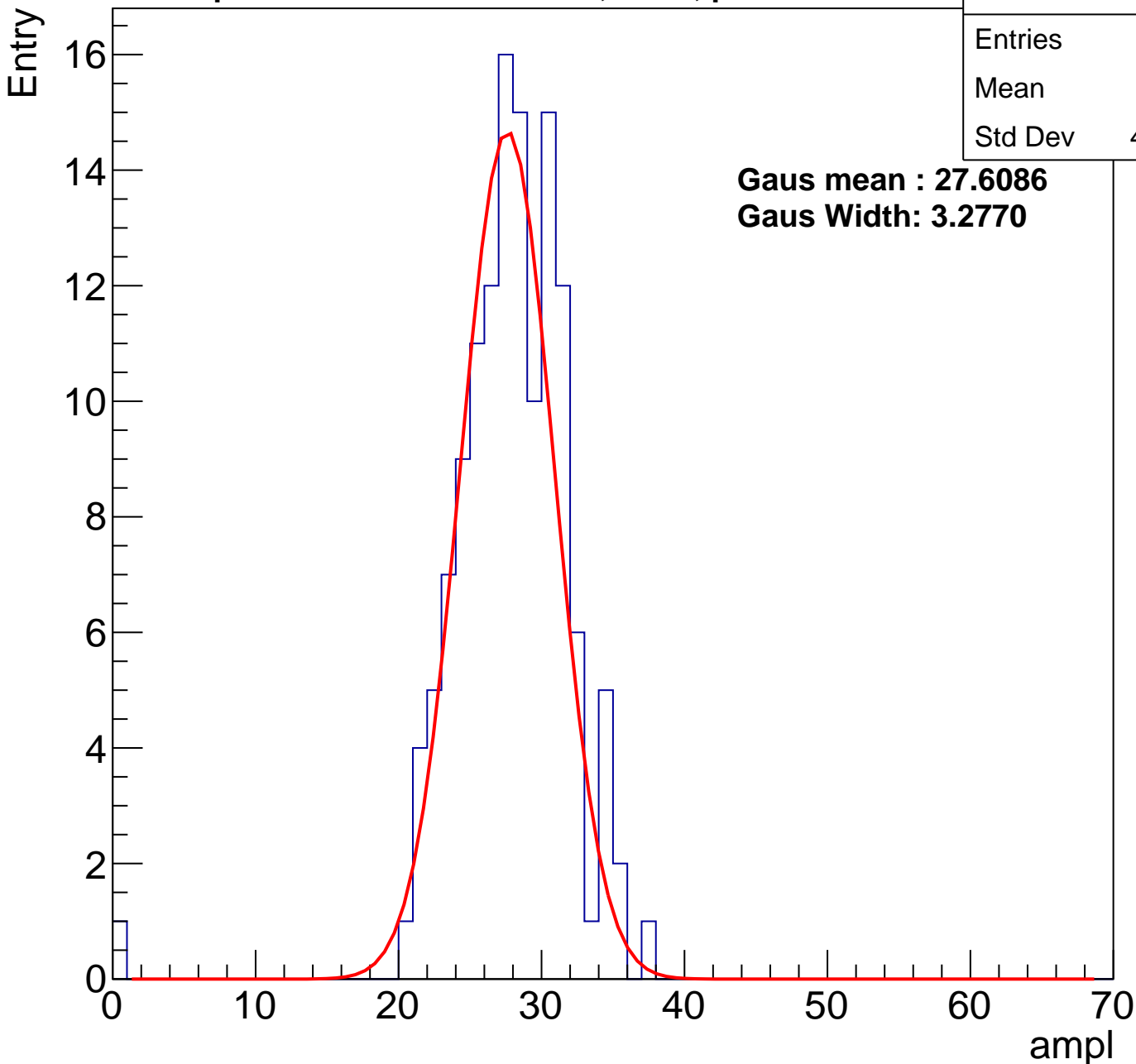
B1L001S, U14-ch88, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	27.4
Std Dev	4.173

Gaus mean : 27.6086

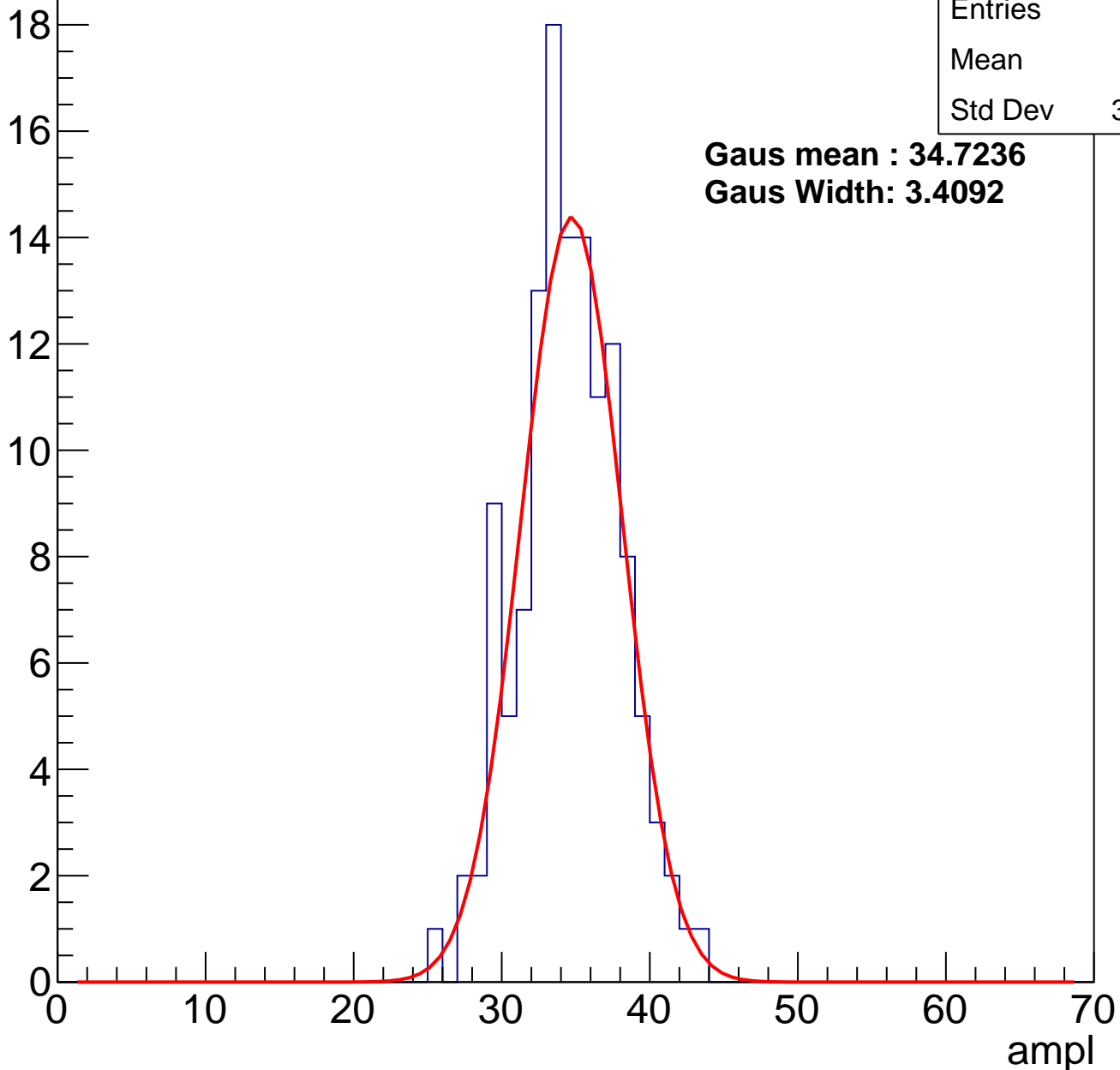
Gaus Width: 3.2770



B1L001S, U14-ch88, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry



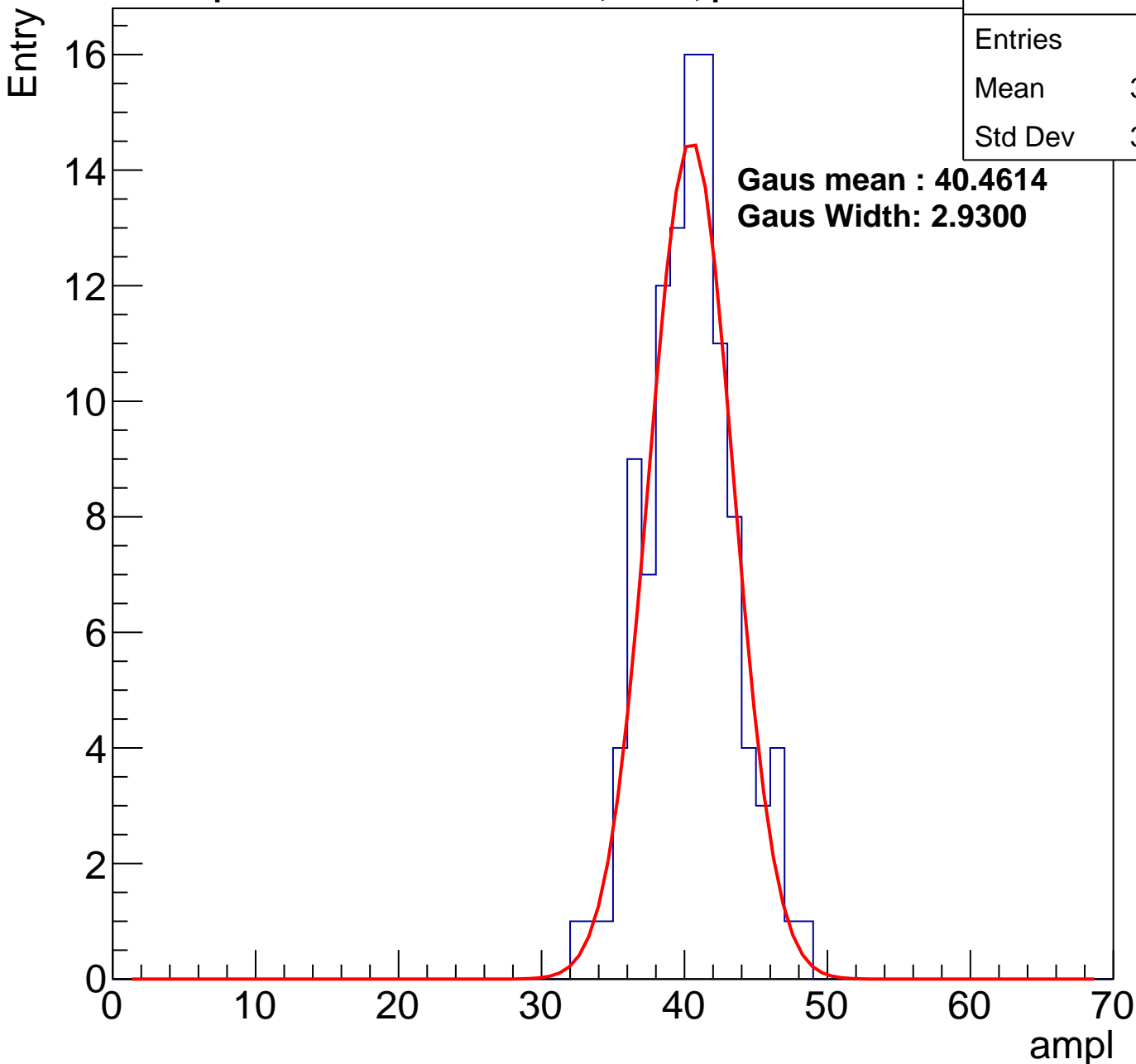
B1L001S, U14-ch88, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	39.97
Std Dev	3.054

Gaus mean : 40.4614

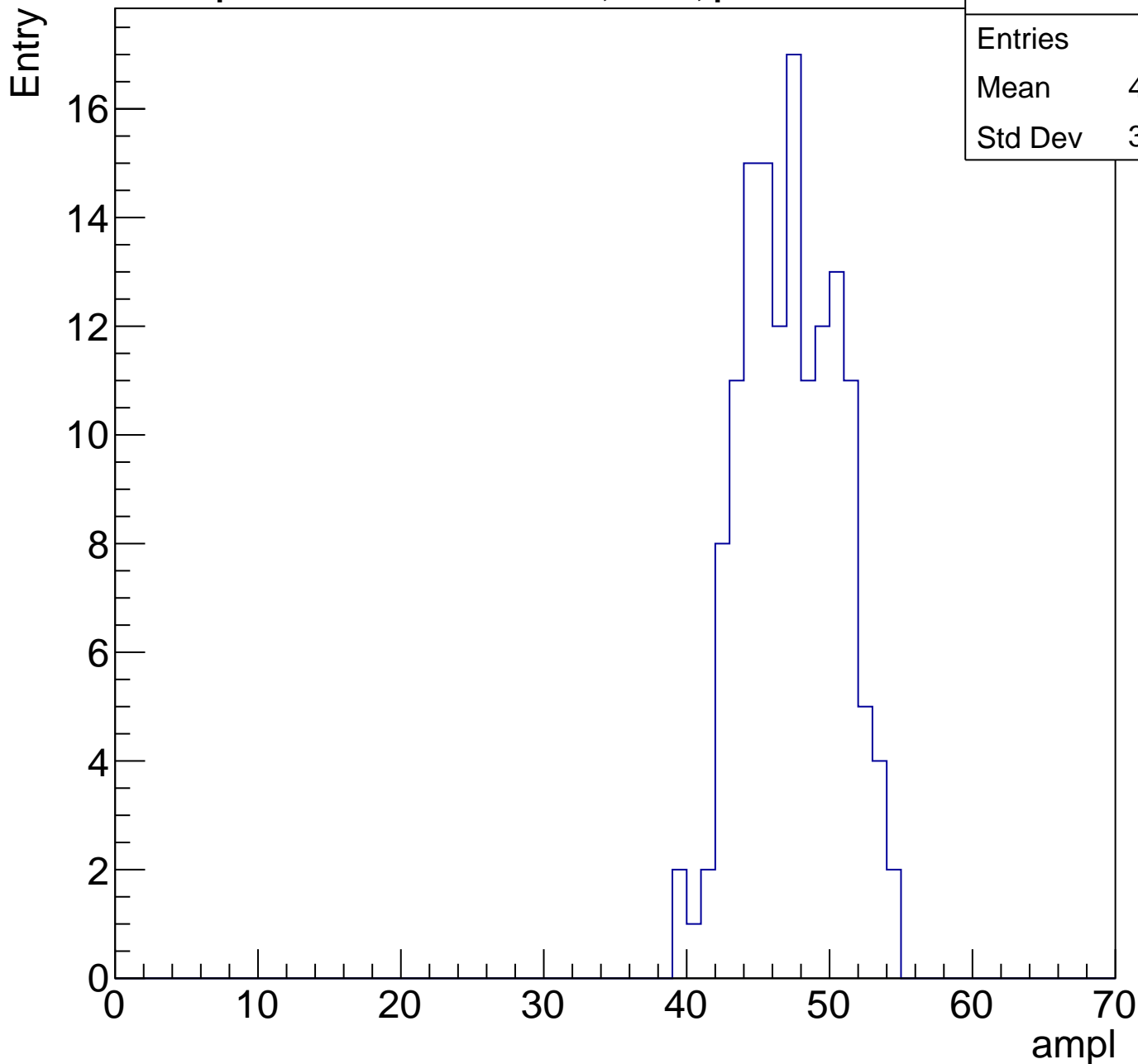
Gaus Width: 2.9300



B1L001S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	46.82
Std Dev	3.312

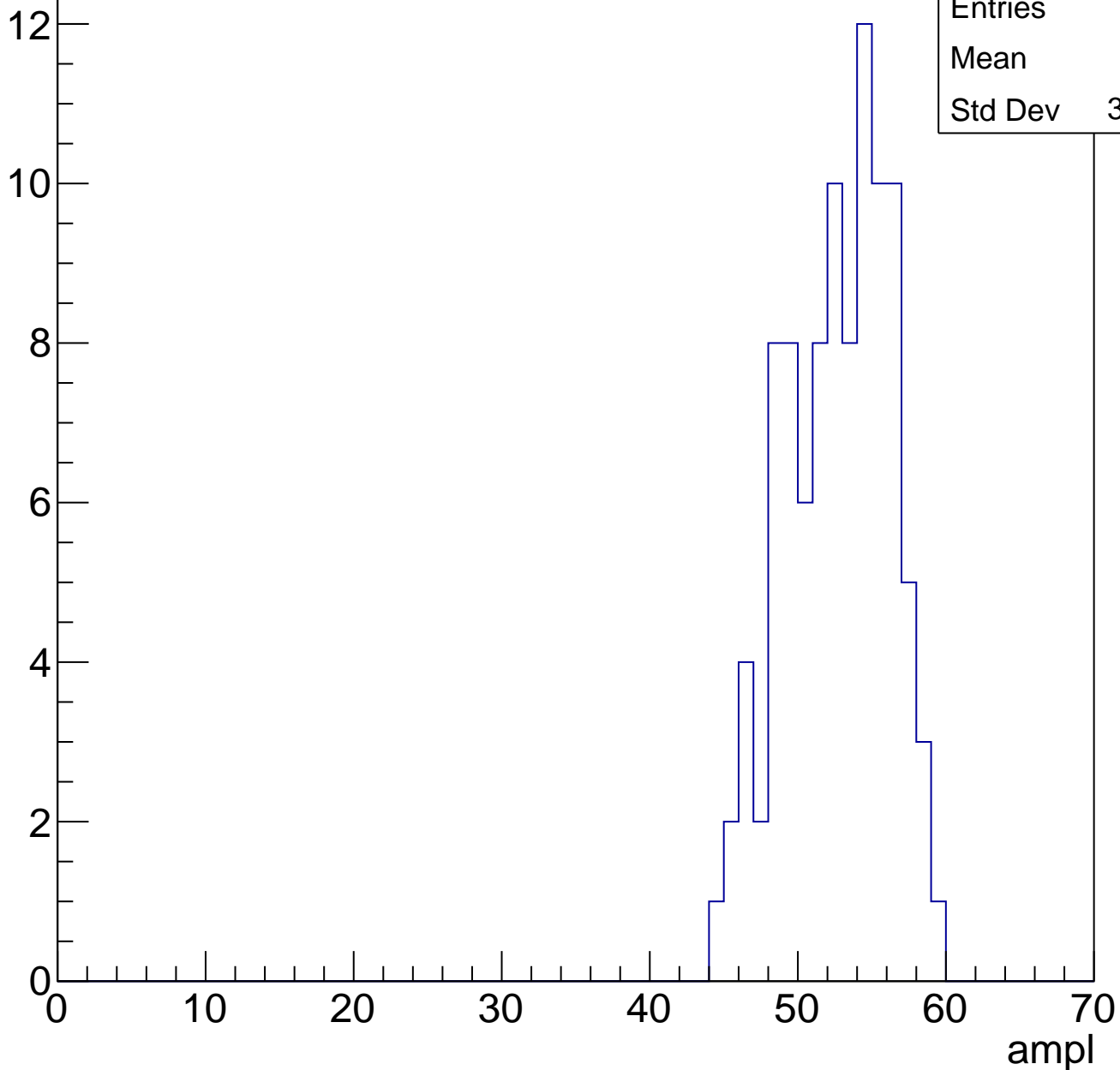


B1L001S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	52.2
Std Dev	3.458

Entry



B1L001S, U14-ch88, adc5

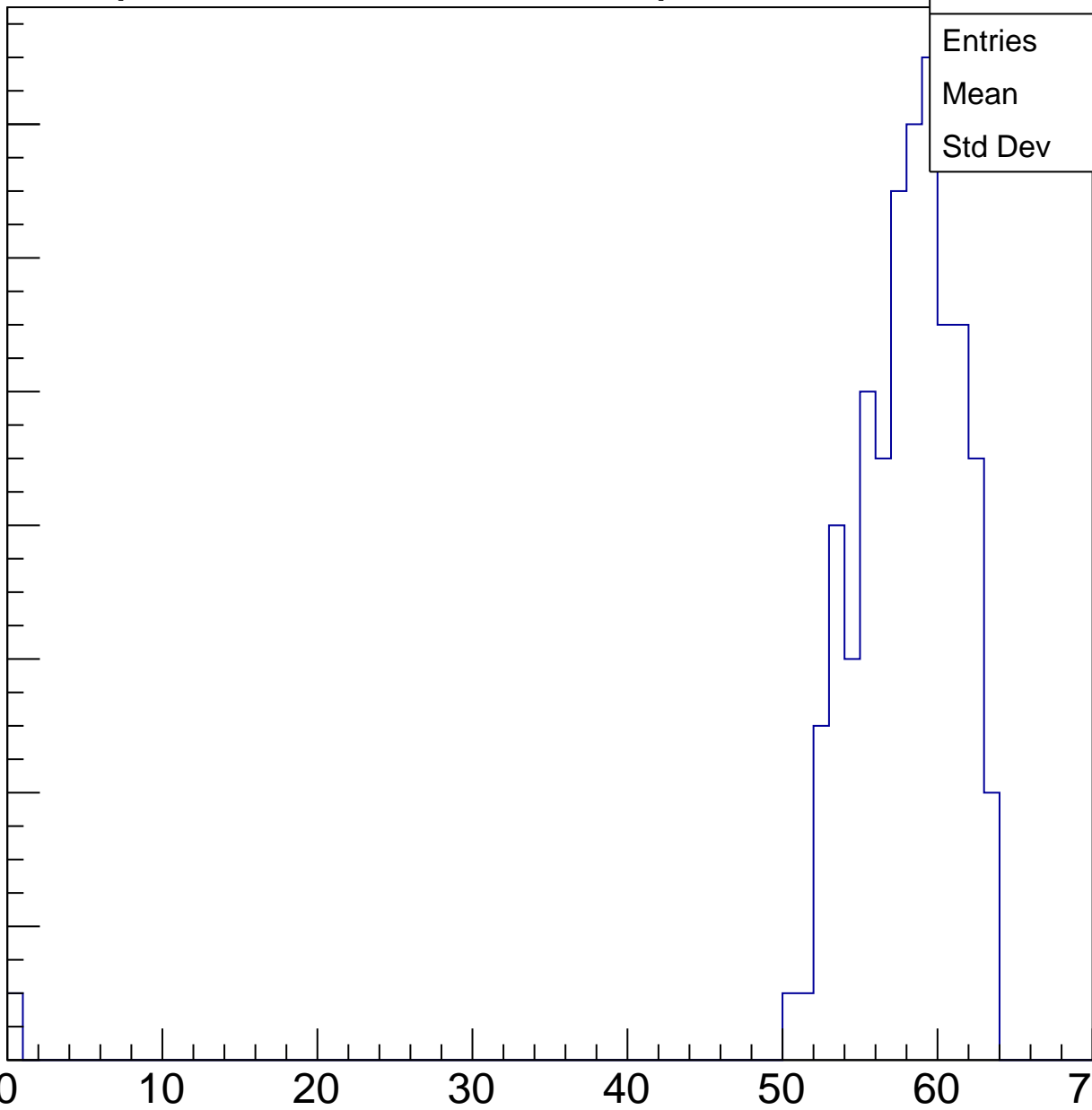
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	118
Mean	57.14
Std Dev	6.11

ampl



B1L001S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

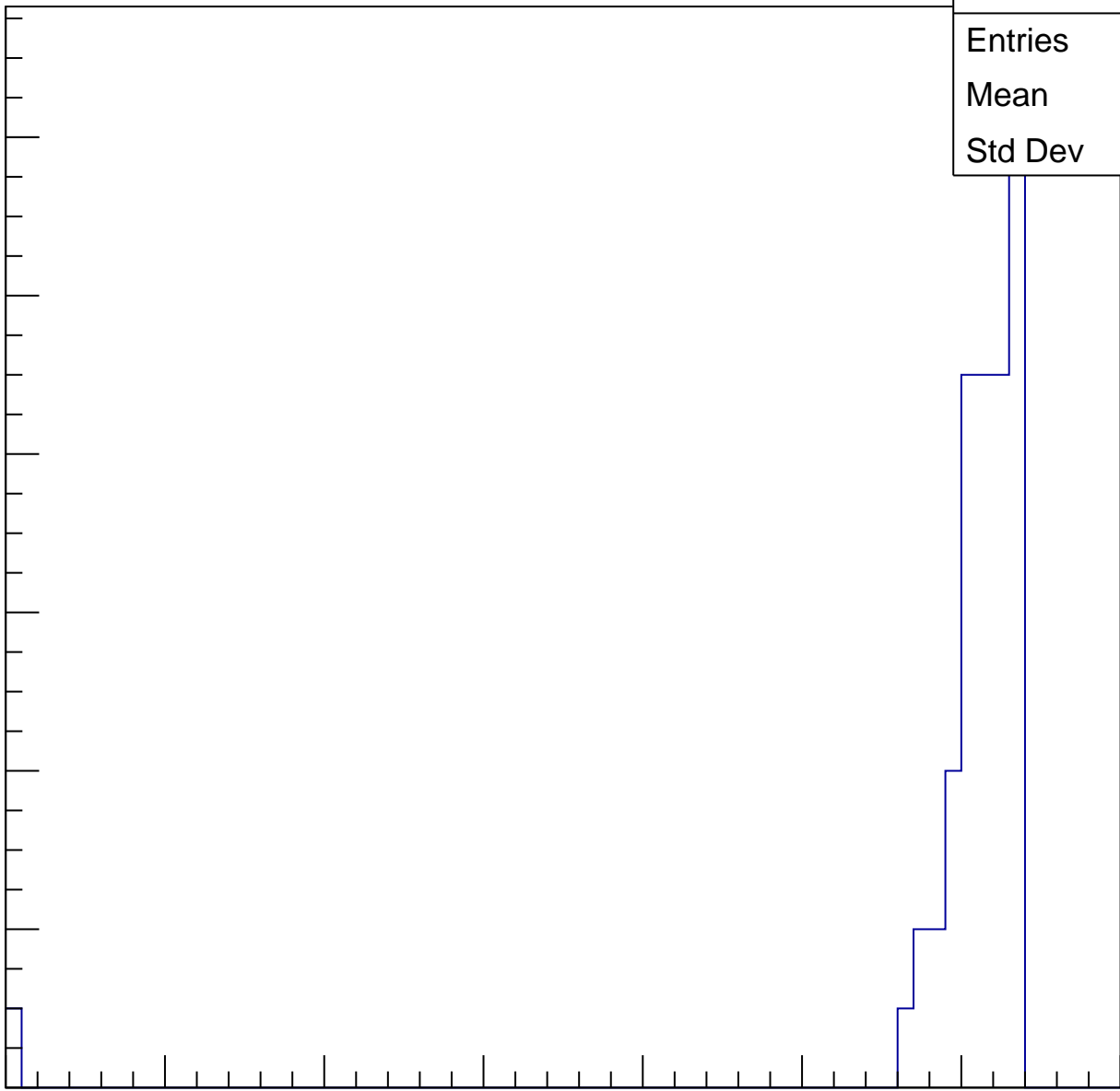
2

0

Entries	50
Mean	59.76
Std Dev	8.724

ampl

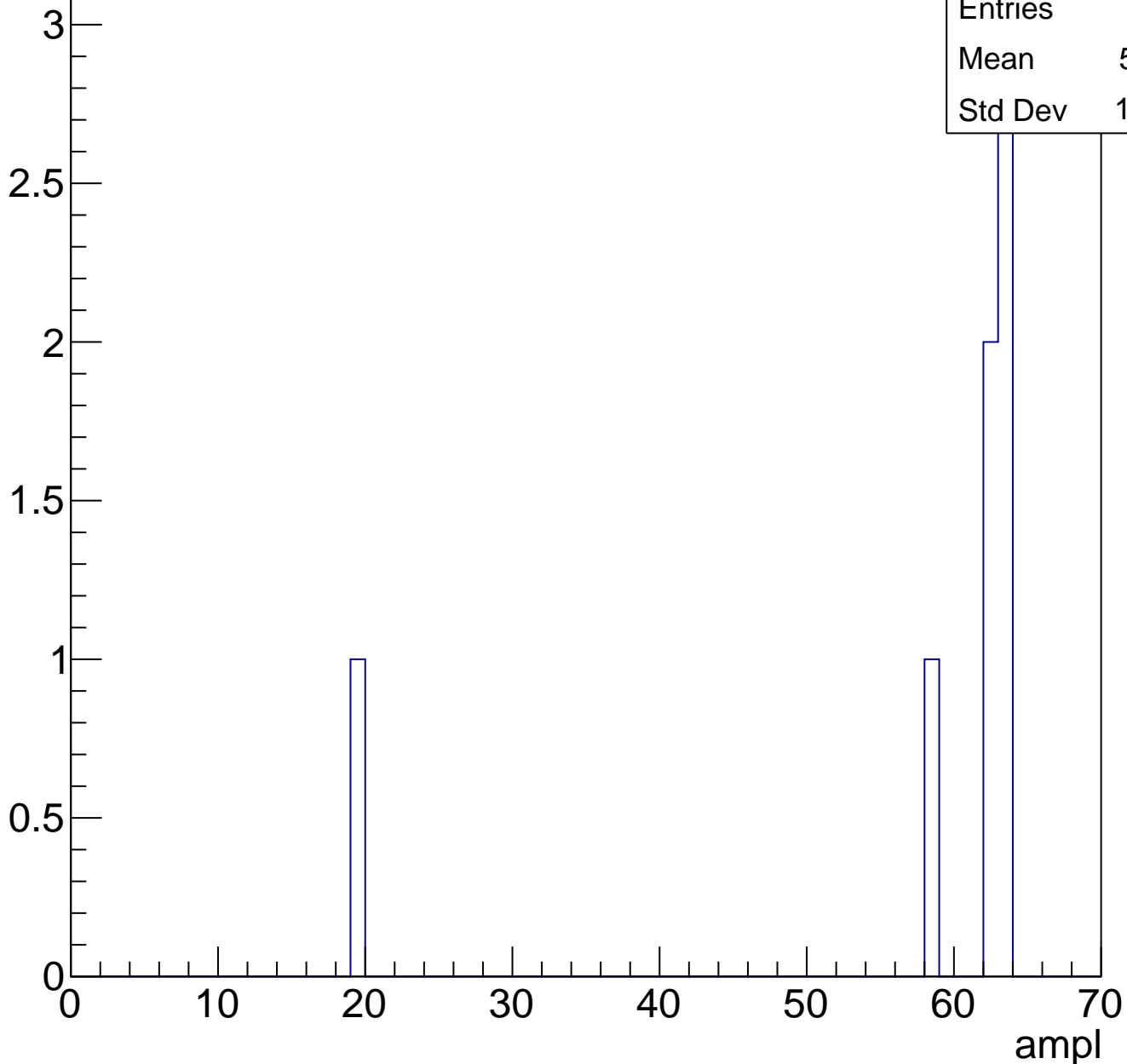
0 10 20 30 40 50 60 70



B1L001S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch89, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	27.2
Std Dev	5.476

Gaus mean : 28.2911

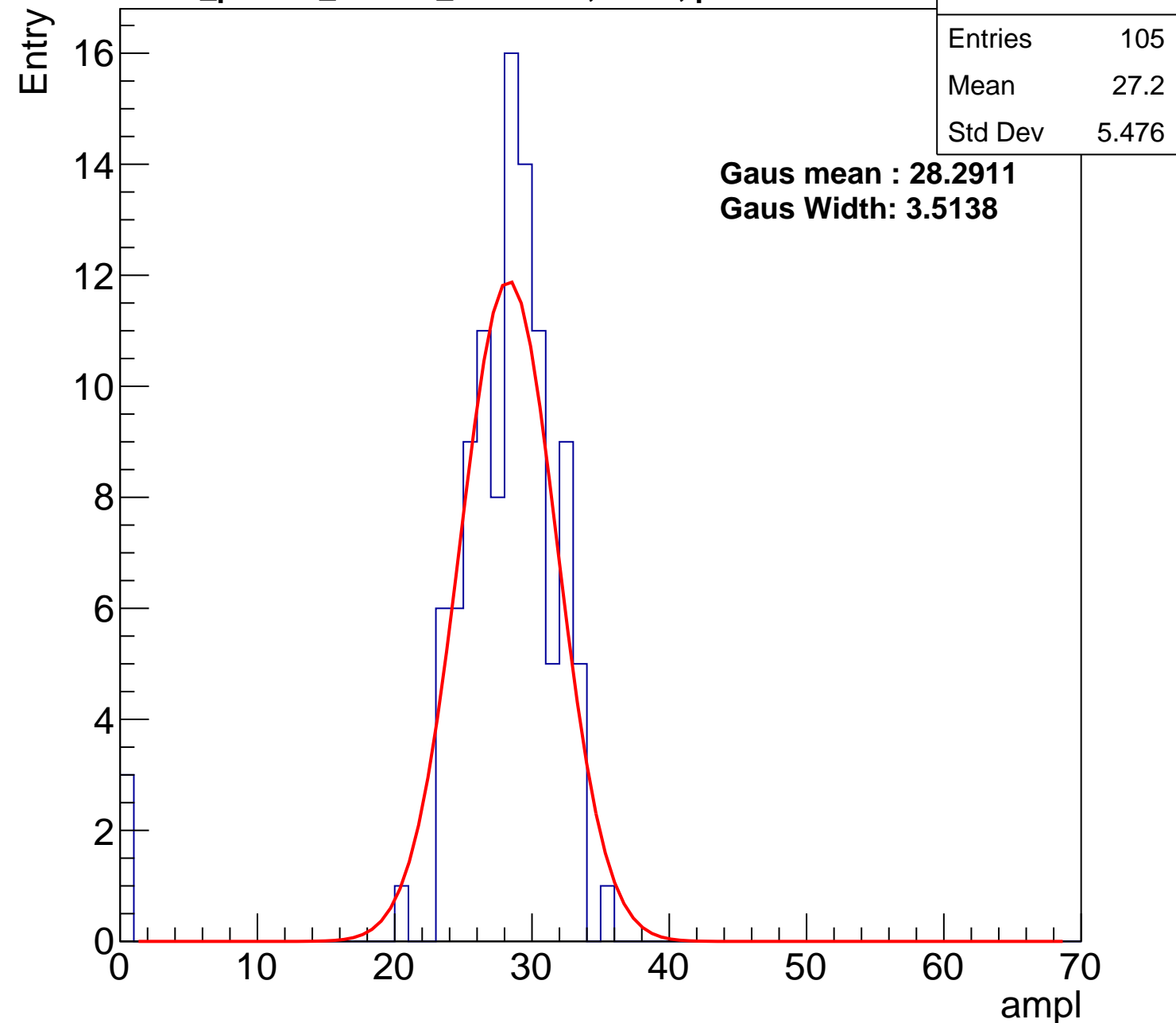
Gaus Width: 3.5138

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch89, adc1

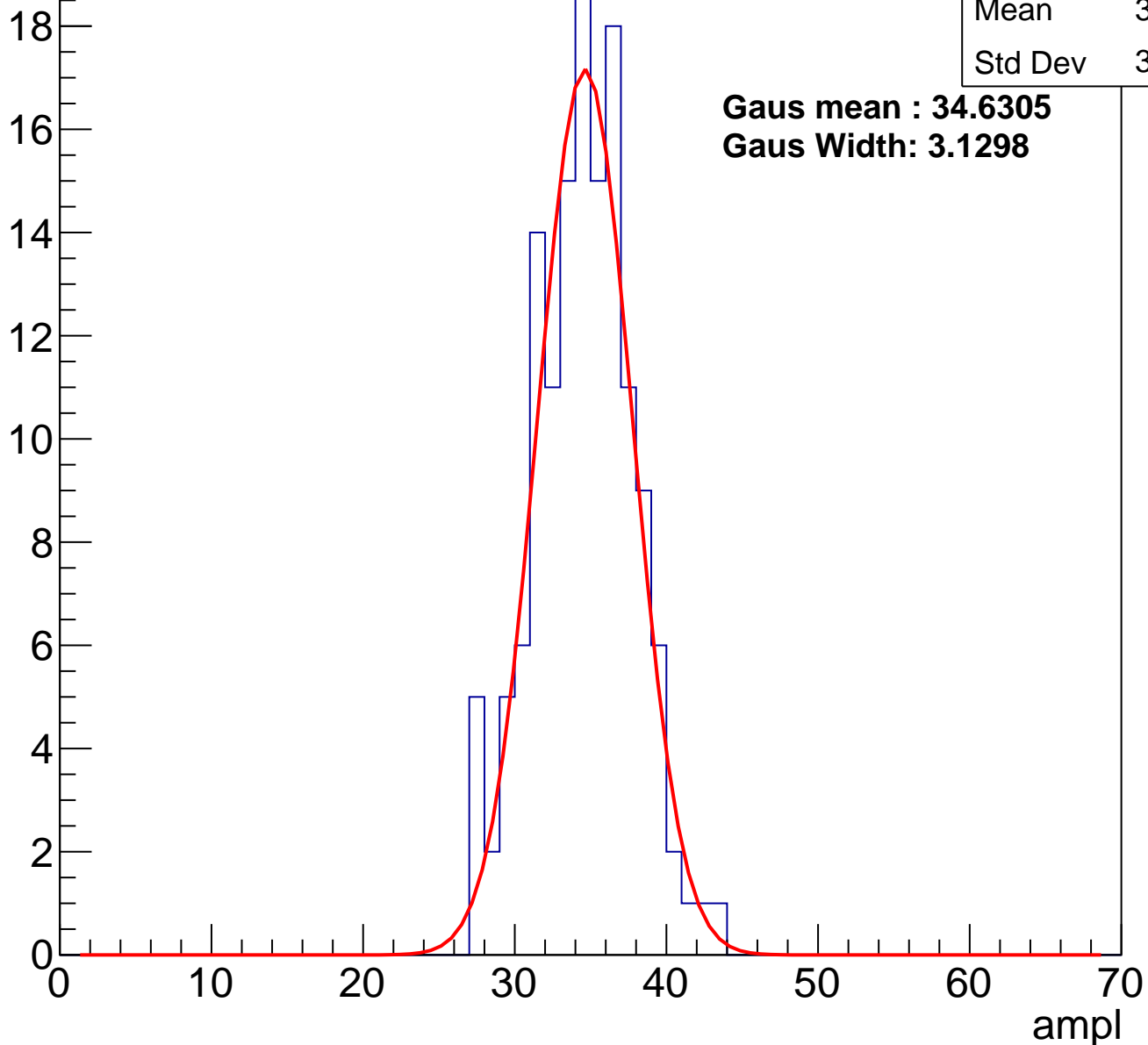
calib_packv5_042523_0143.root, FC#2, port C2

Entries	141
Mean	34.08
Std Dev	3.227

Gaus mean : 34.6305

Gaus Width: 3.1298

Entry



B1L001S, U14-ch89, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	41.33
Std Dev	3.594

Gaus mean : 41.6009

Gaus Width: 3.1765

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

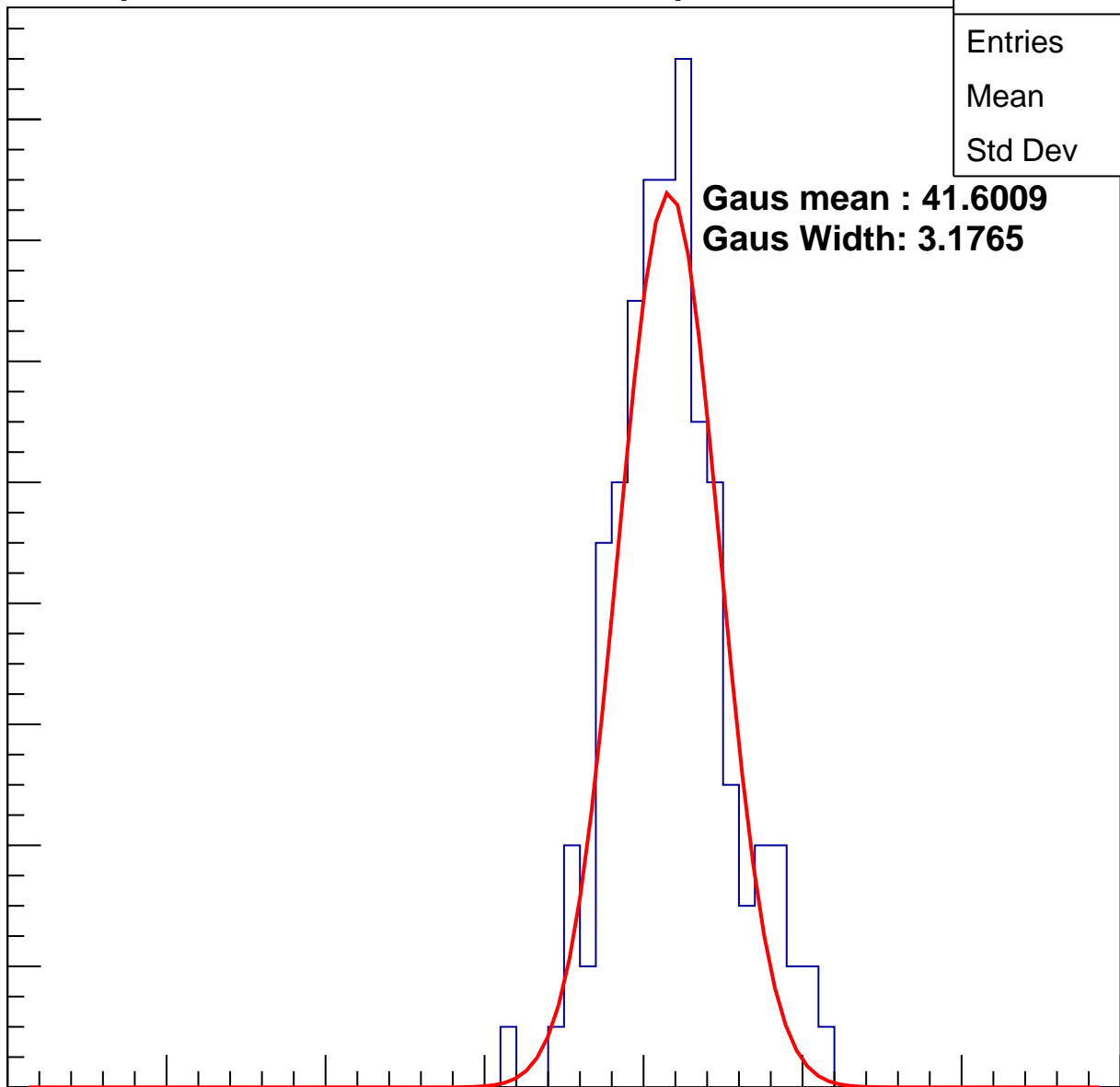
40

50

60

70

ampl



B1L001S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#2, port C2

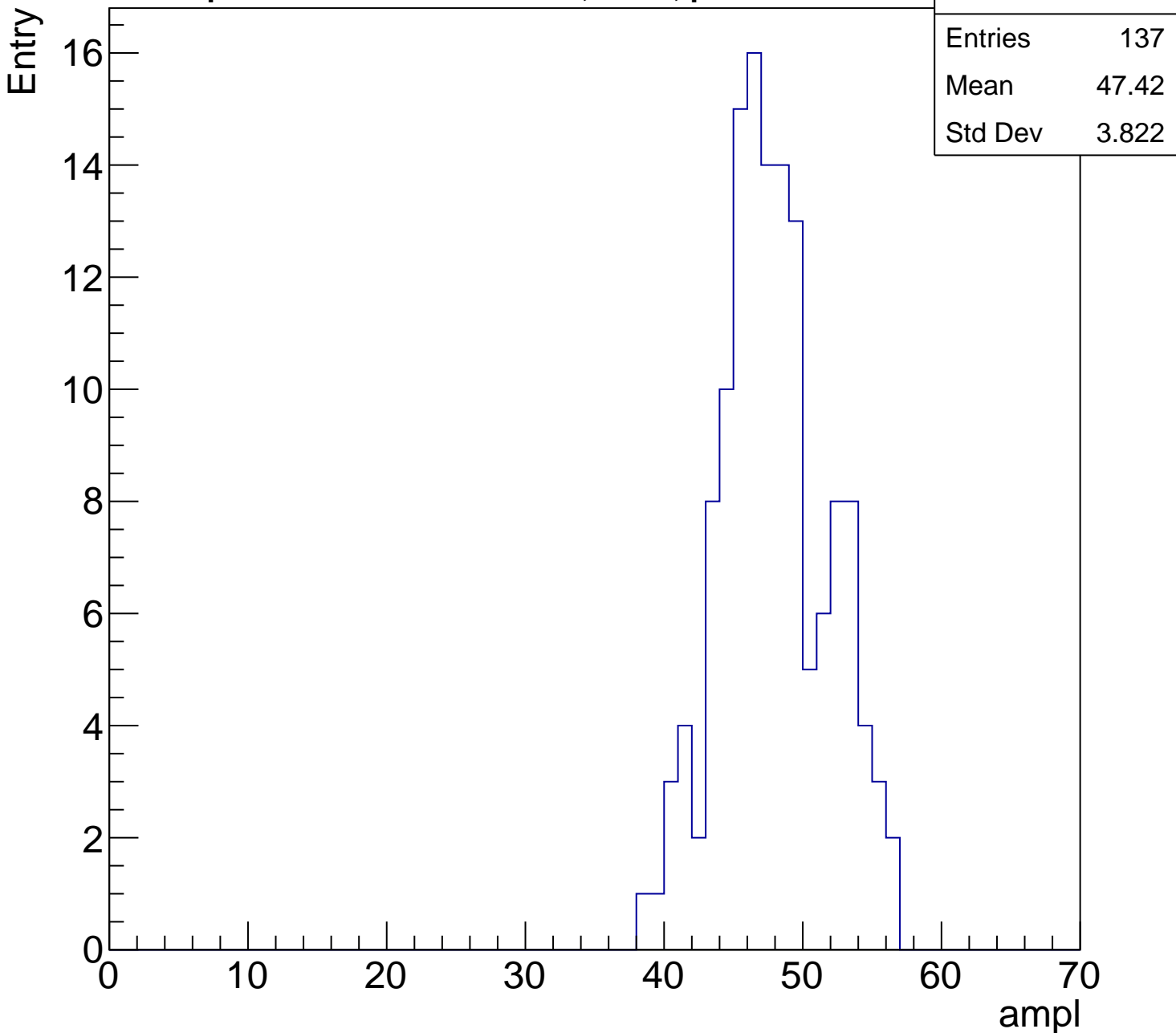
Entries	137
Mean	47.42
Std Dev	3.822

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

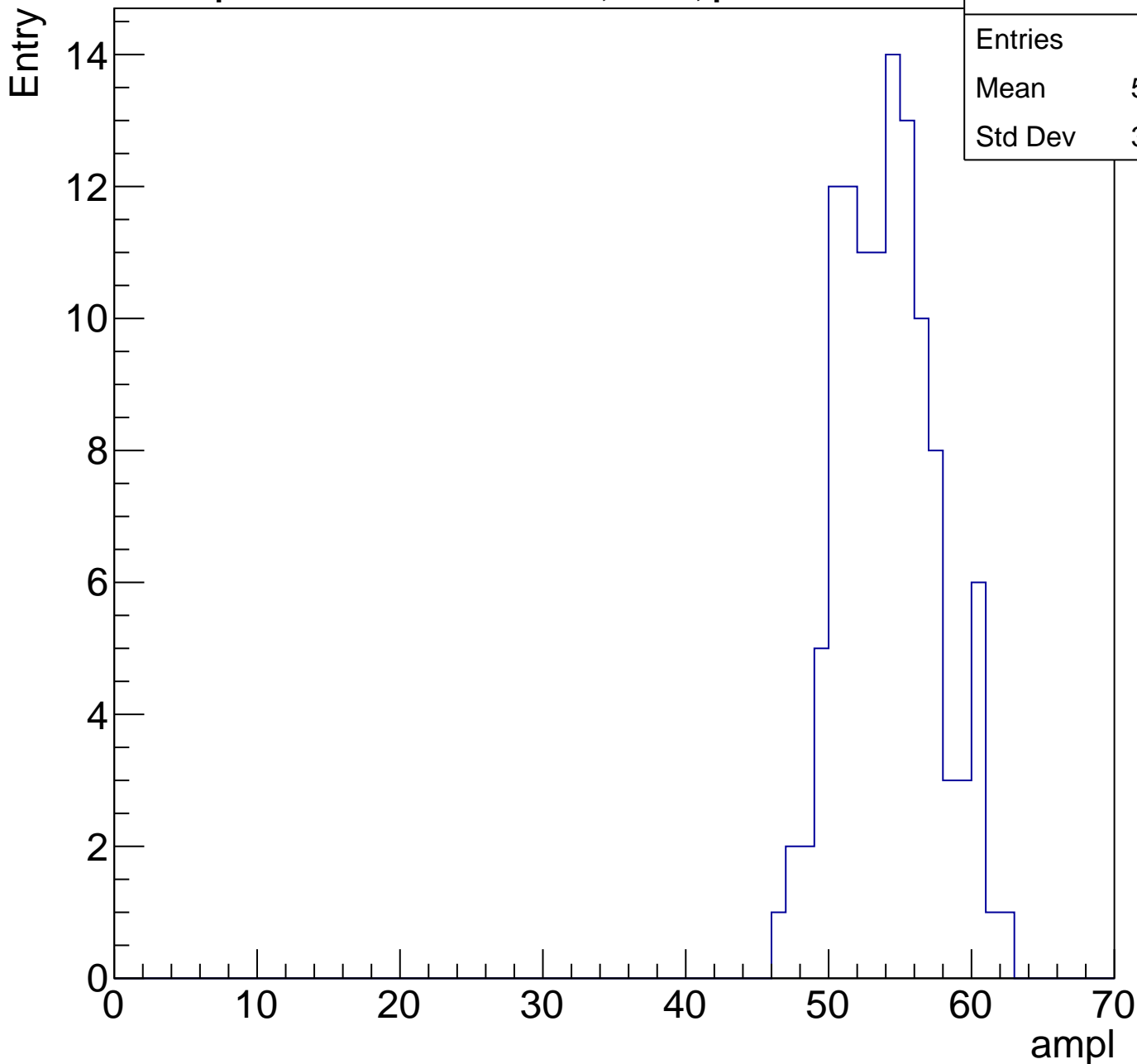
ampl



B1L001S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	53.64
Std Dev	3.332



B1L001S, U14-ch89, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	57.83
Std Dev	8.429

Entry

14

12

10

8

6

4

2

0

0

10

20

30

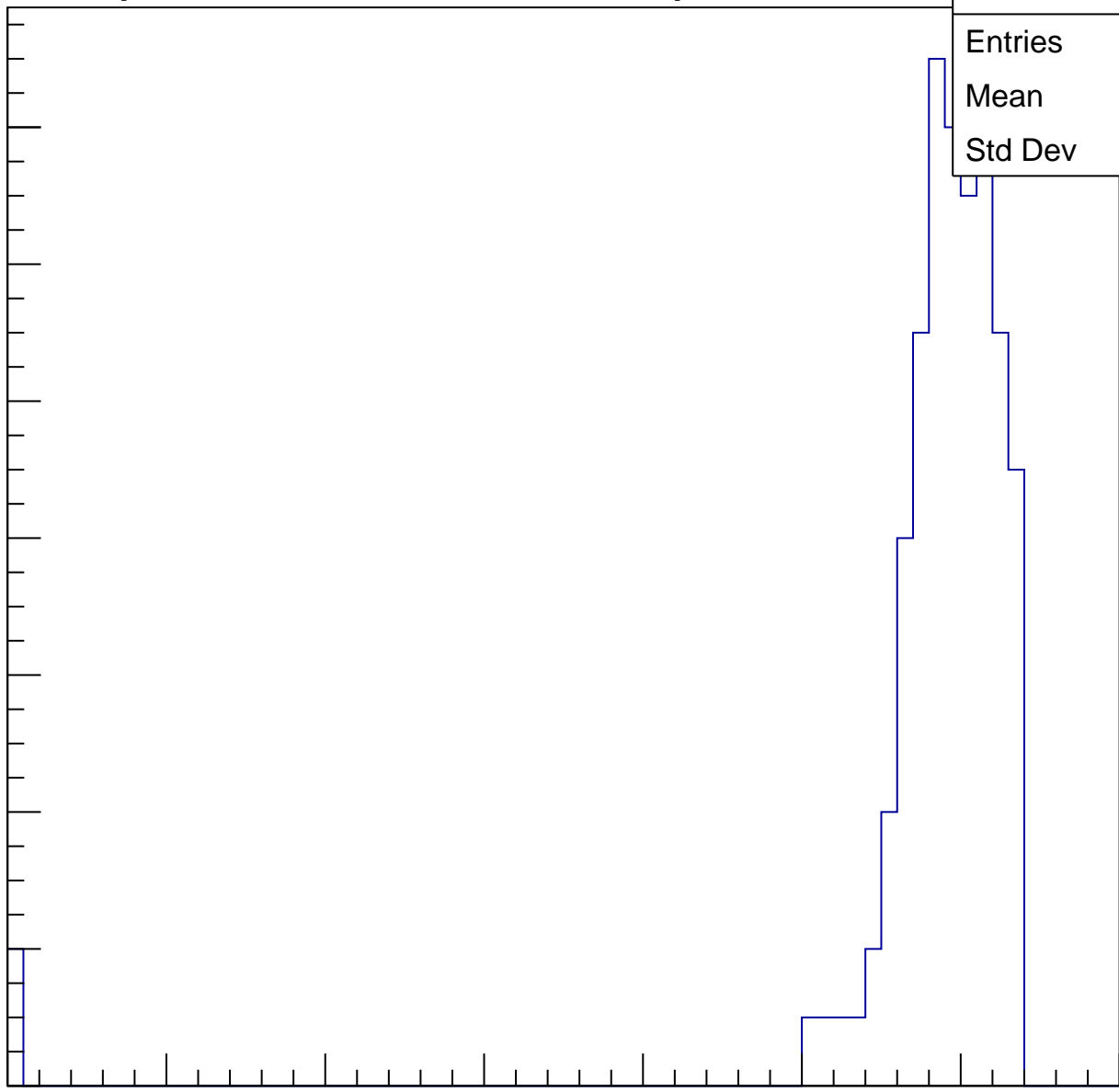
40

50

60

70

ampl

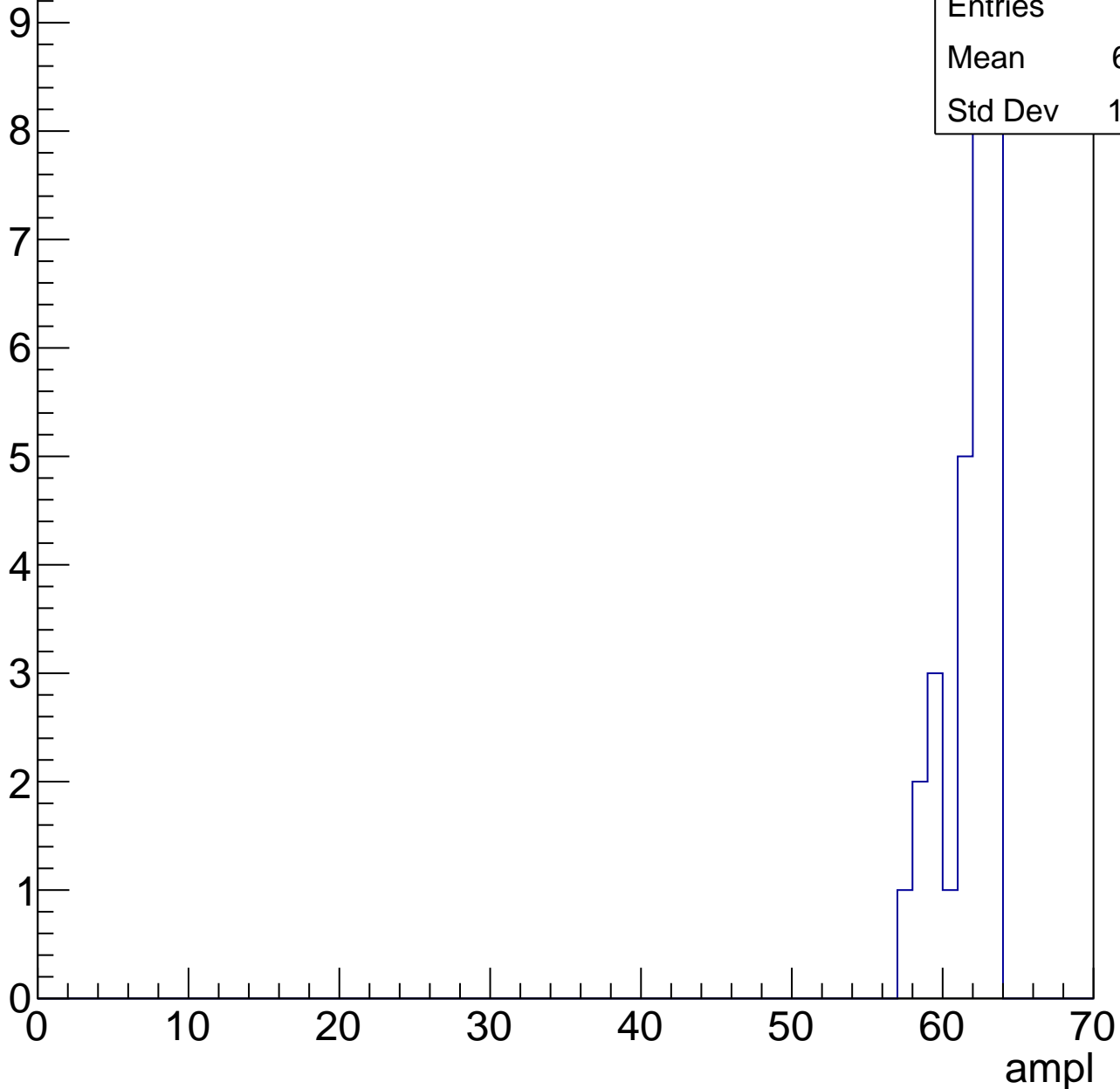


B1L001S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	29
Mean	61.31
Std Dev	1.744



B1L001S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch90, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	28.11
Std Dev	4.127

Gaus mean : 28.9286

Gaus Width: 3.4274

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

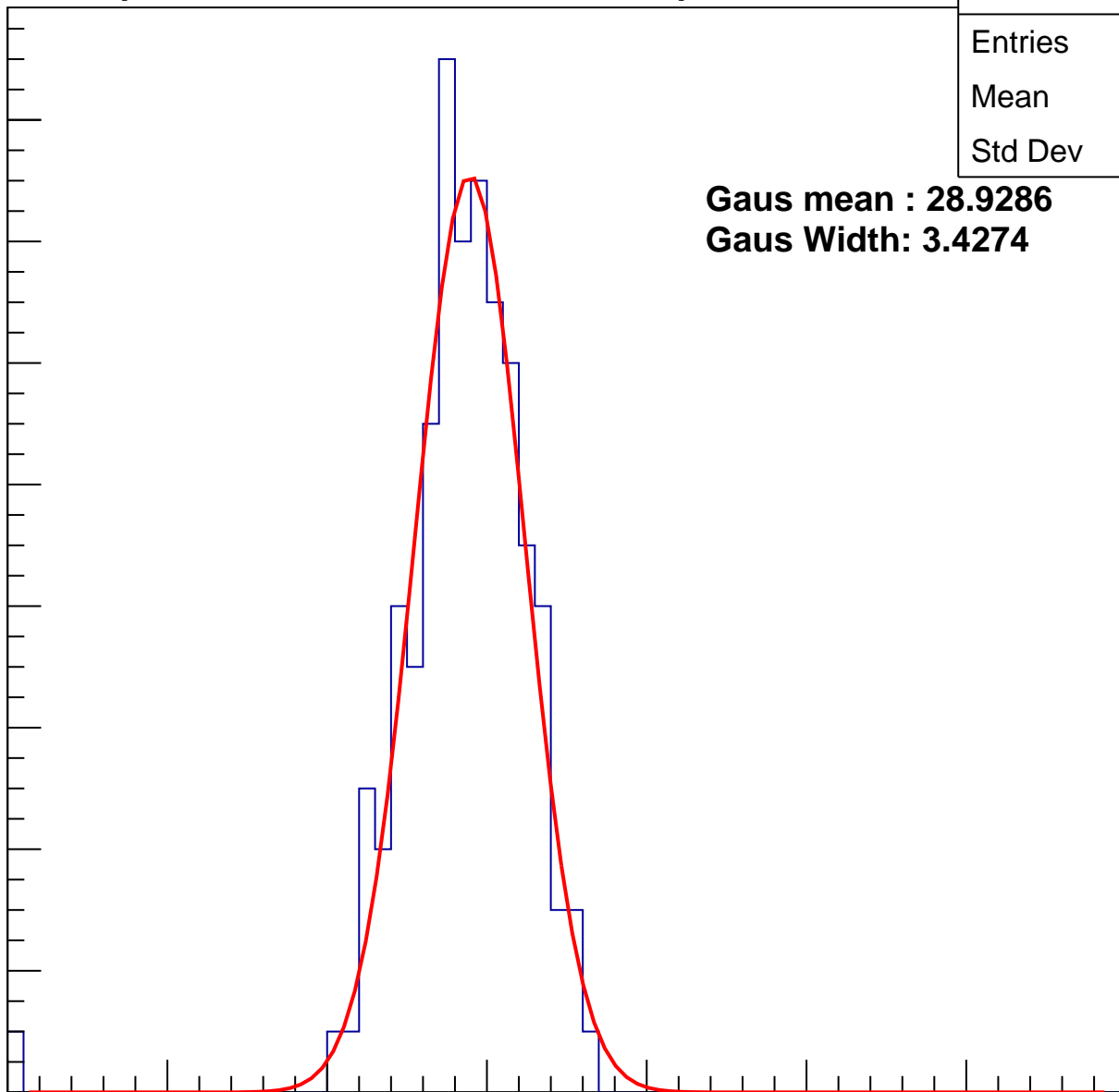
40

50

60

70

ampl



B1L001S, U14-ch90, adc1

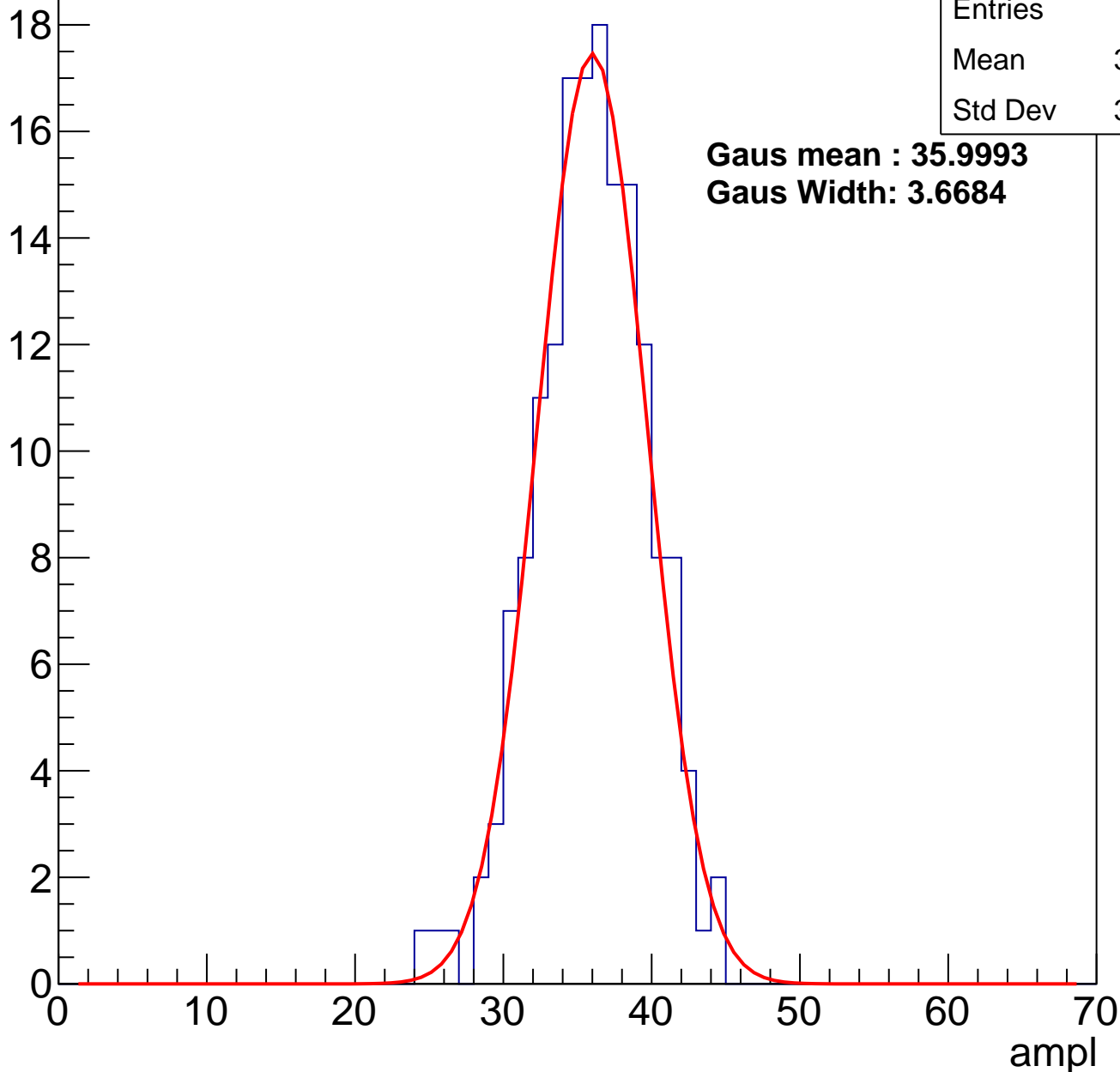
calib_packv5_042523_0143.root, FC#2, port C2

Entries	163
Mean	35.49
Std Dev	3.716

Gaus mean : 35.9993

Gaus Width: 3.6684

Entry



B1L001S, U14-ch90, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	88
Mean	42.05
Std Dev	2.868

Gaus mean : 42.5572

Gaus Width: 3.0689

0

2

4

6

8

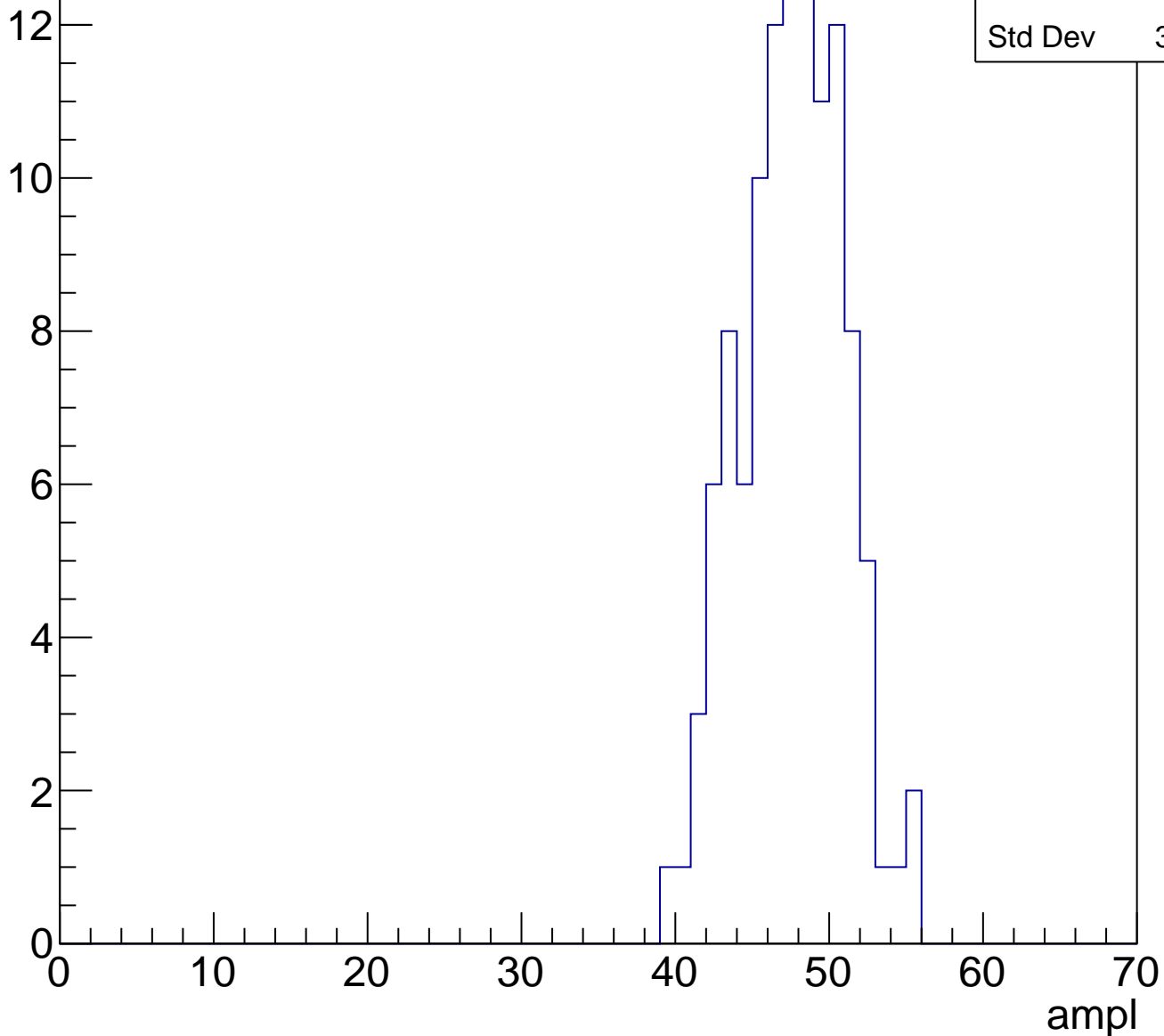
10

B1L001S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	47.11
Std Dev	3.306

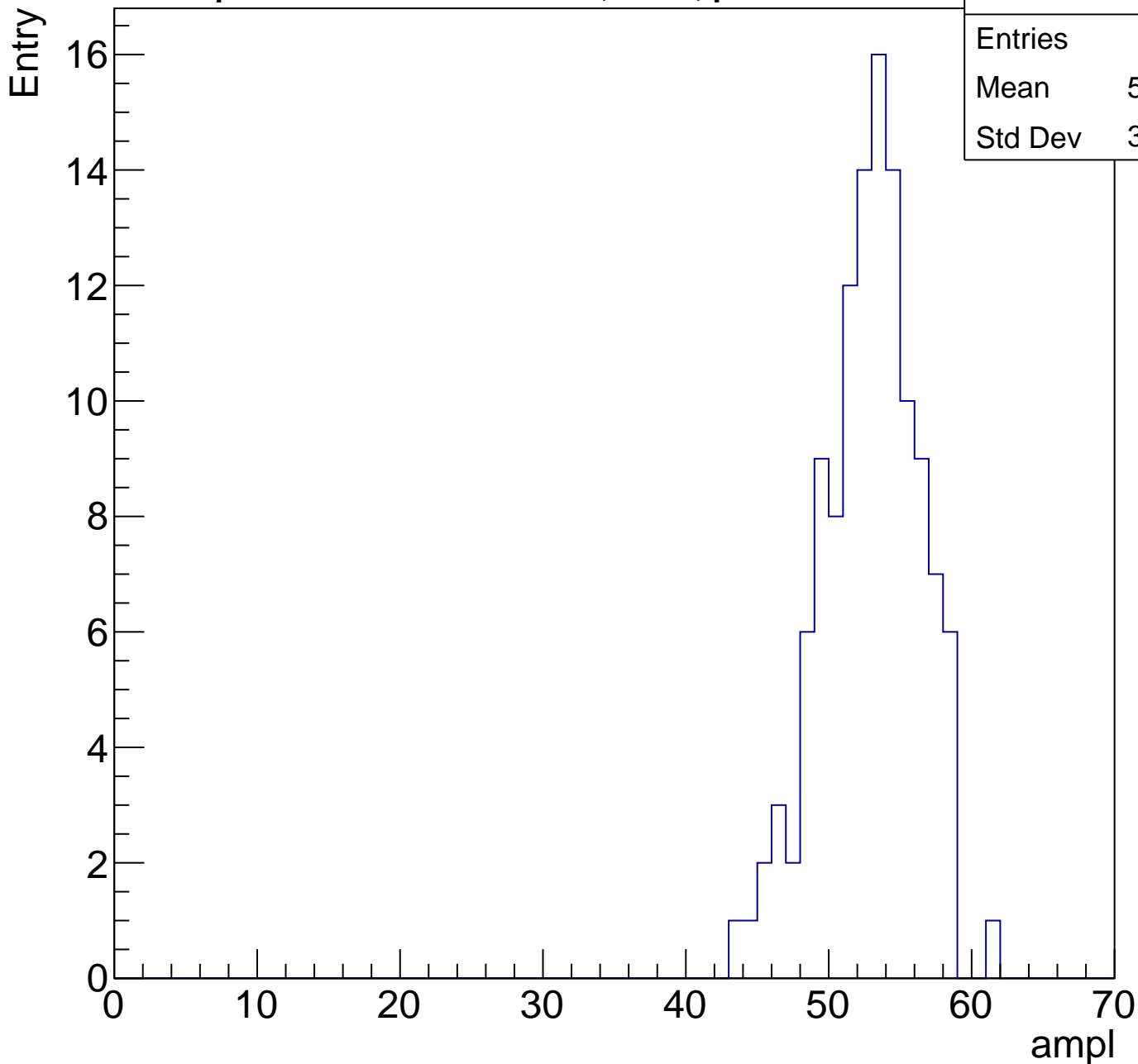
Entry



B1L001S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	52.43
Std Dev	3.374



B1L001S, U14-ch90, adc5

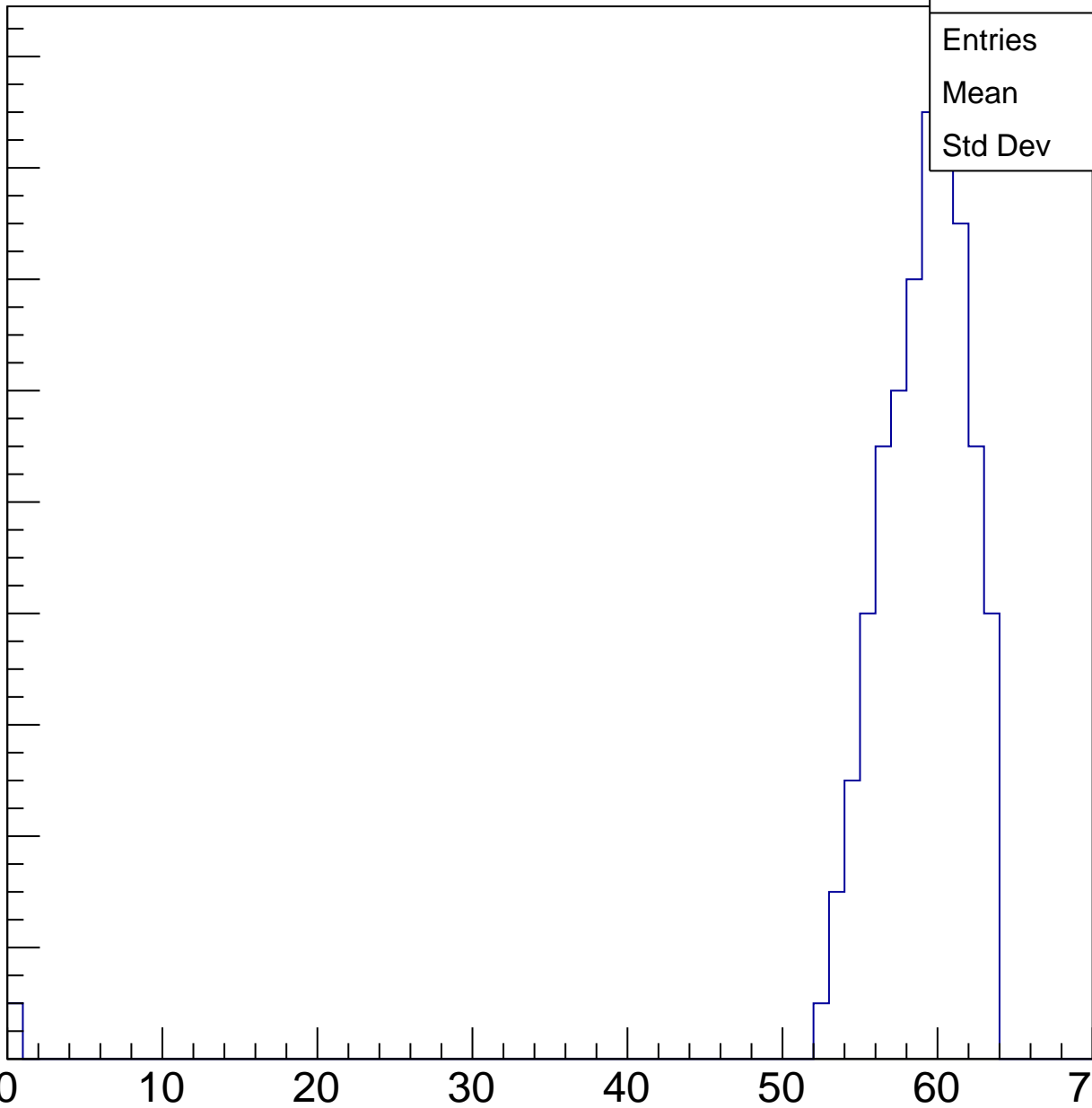
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	124
Mean	58.2
Std Dev	5.874

ampl



B1L001S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

Entries	30
Mean	59.57
Std Dev	11.17

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



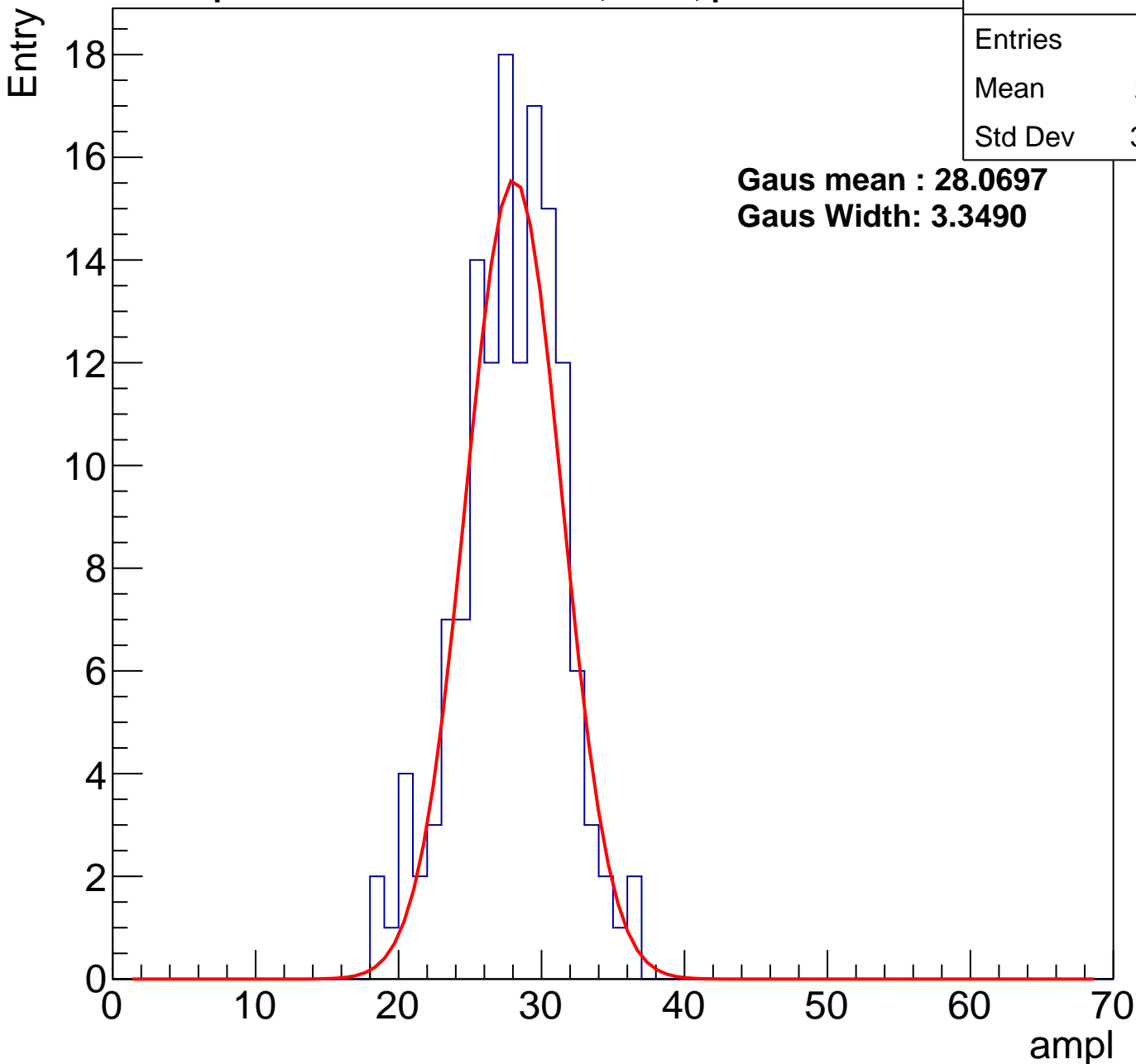
B1L001S, U14-ch91, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	27.41
Std Dev	3.563

Gaus mean : 28.0697

Gaus Width: 3.3490



B1L001S, U14-ch91, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	34.22
Std Dev	3.227

Gaus mean : 34.8274

Gaus Width: 3.1448

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

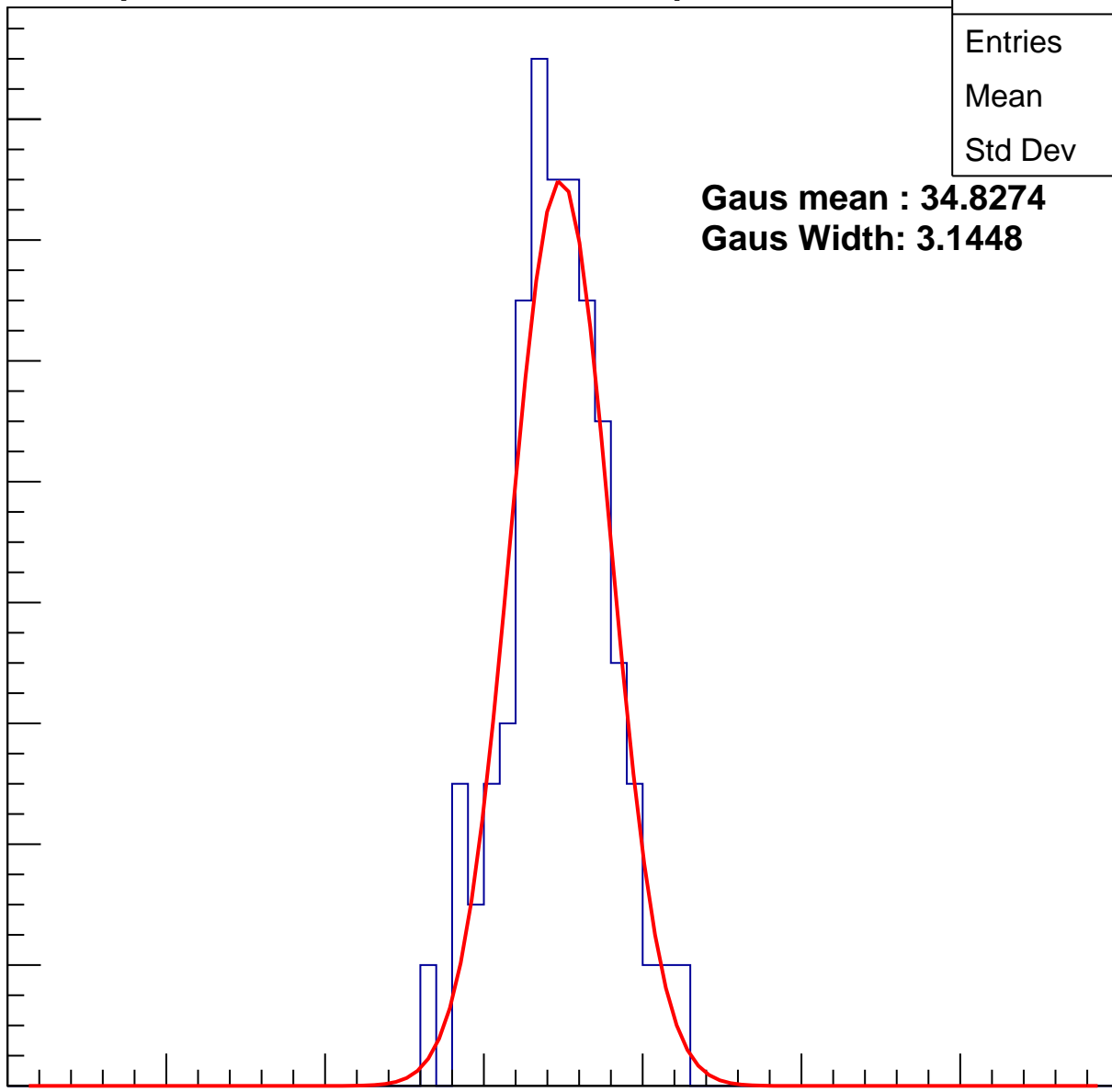
40

50

60

70

ampl



B1L001S, U14-ch91, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	40.39
Std Dev	3.351

Gaus mean : 40.8413

Gaus Width: 3.1120

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

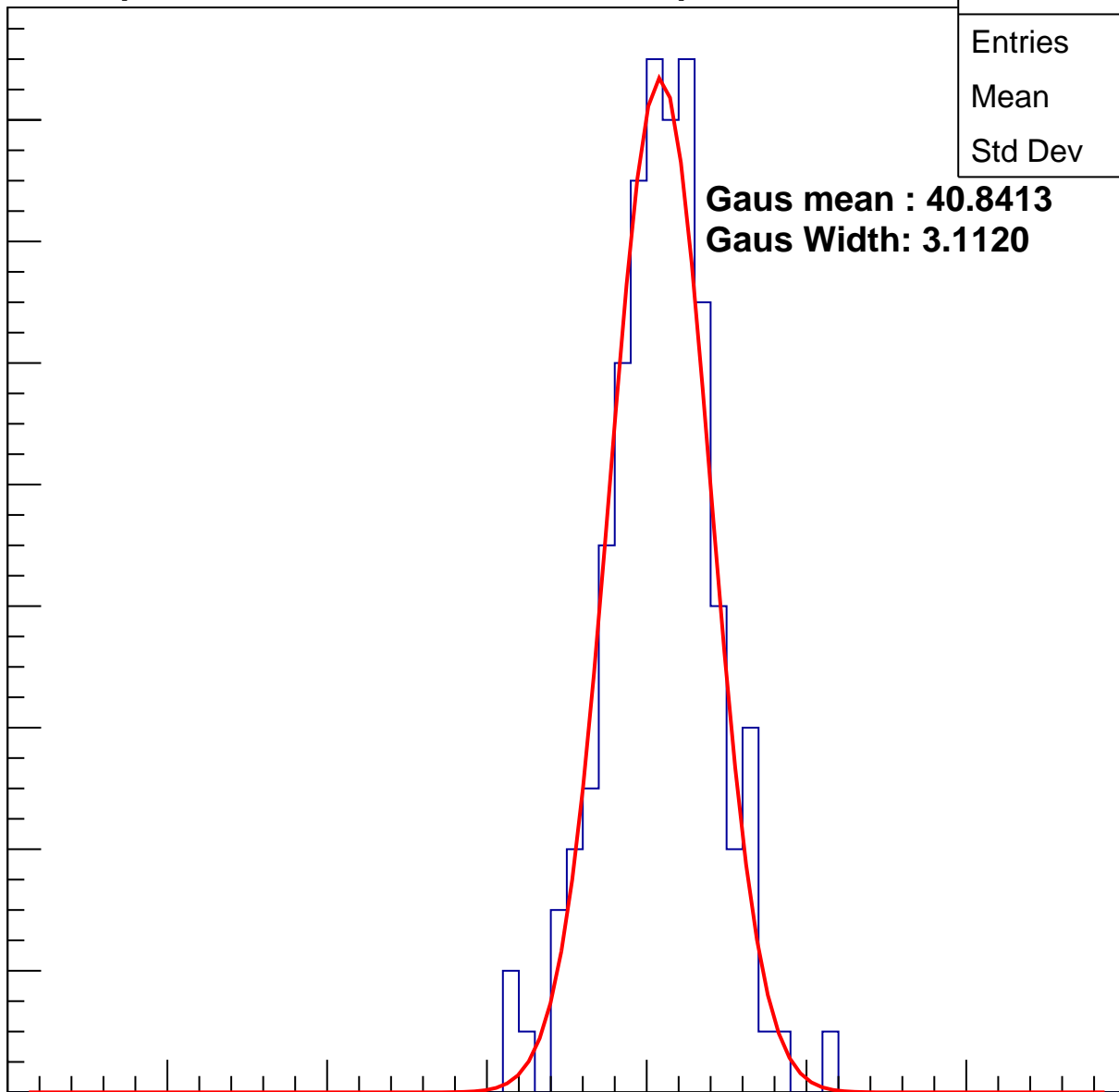
40

50

60

70

ampl



B1L001S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	47.63
Std Dev	3.326

Entry

16

14

12

10

8

6

4

2

0

0

10

20

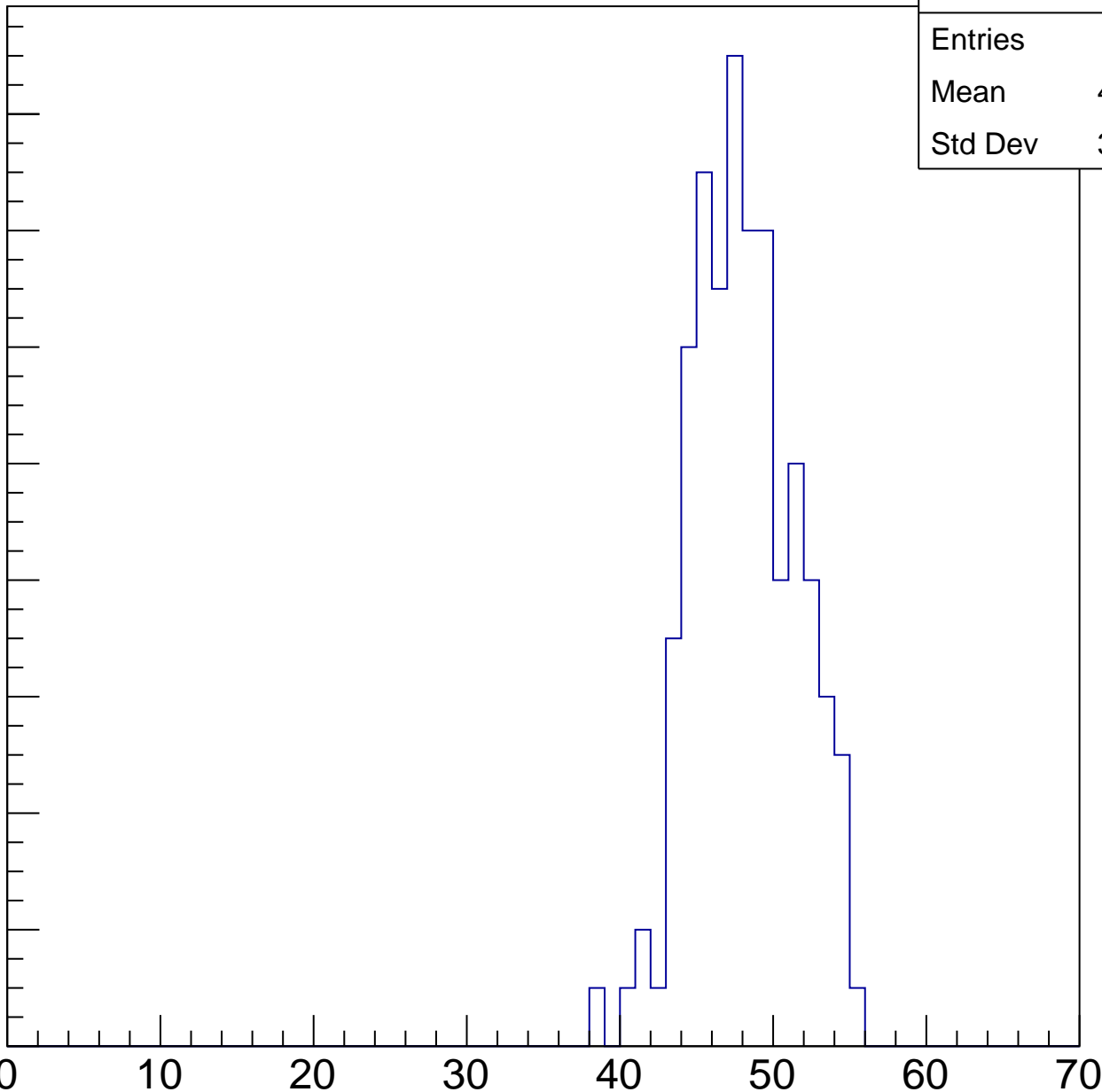
30

40

50

60

ampl



B1L001S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

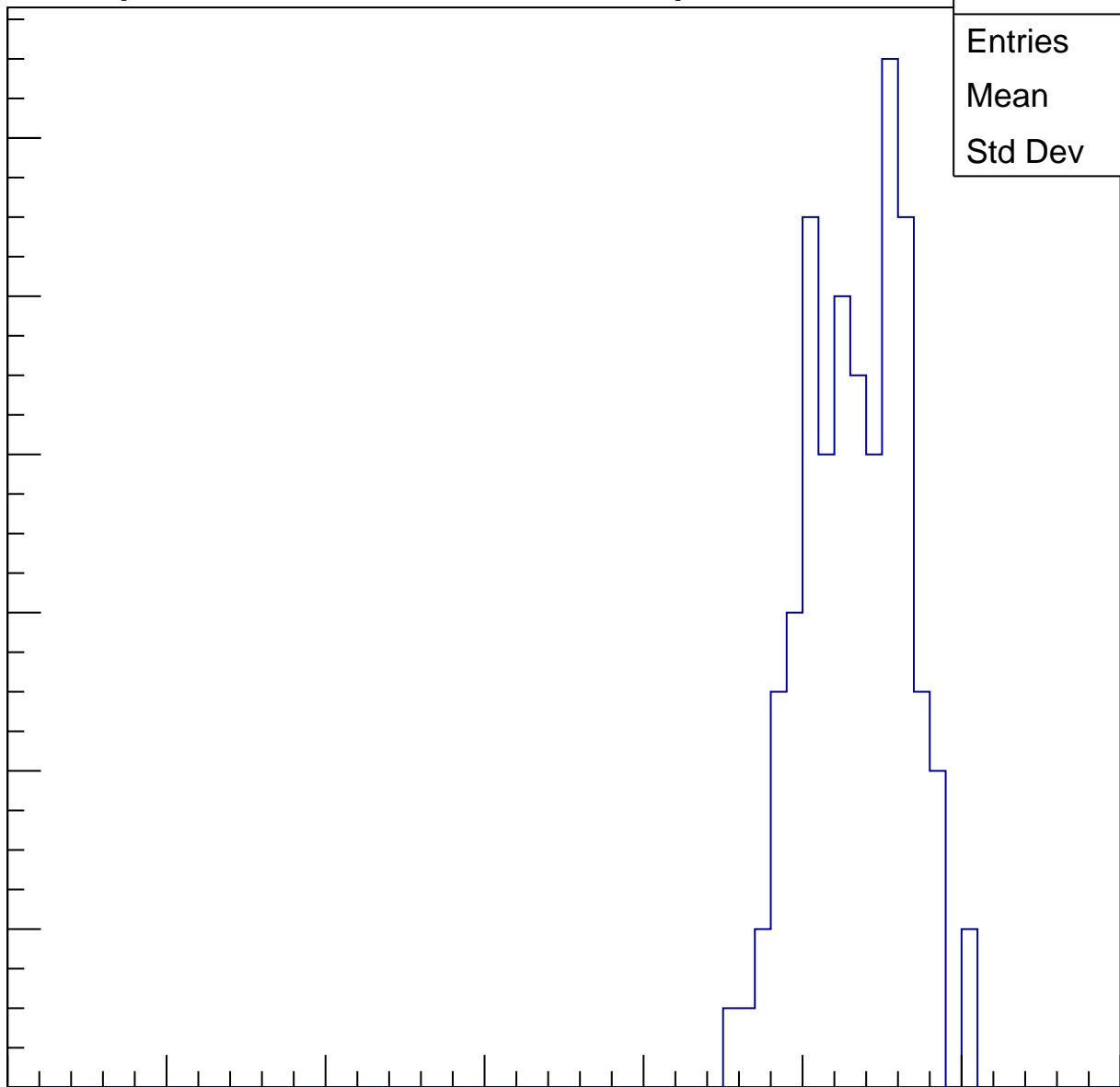
40

50

60

ampl

Entries	96
Mean	52.85
Std Dev	3.198

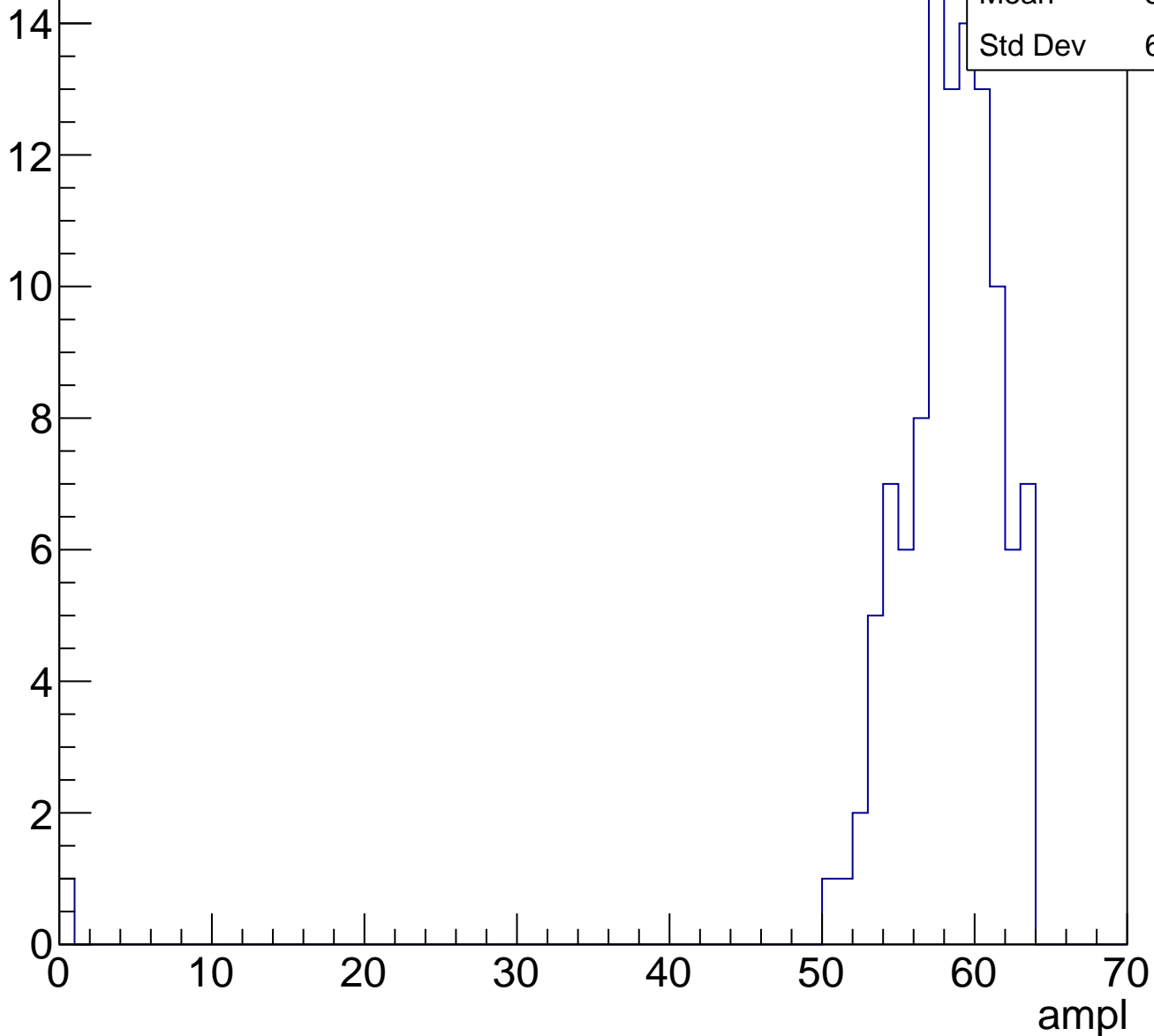


B1L001S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	57.47
Std Dev	6.269

Entry



B1L001S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

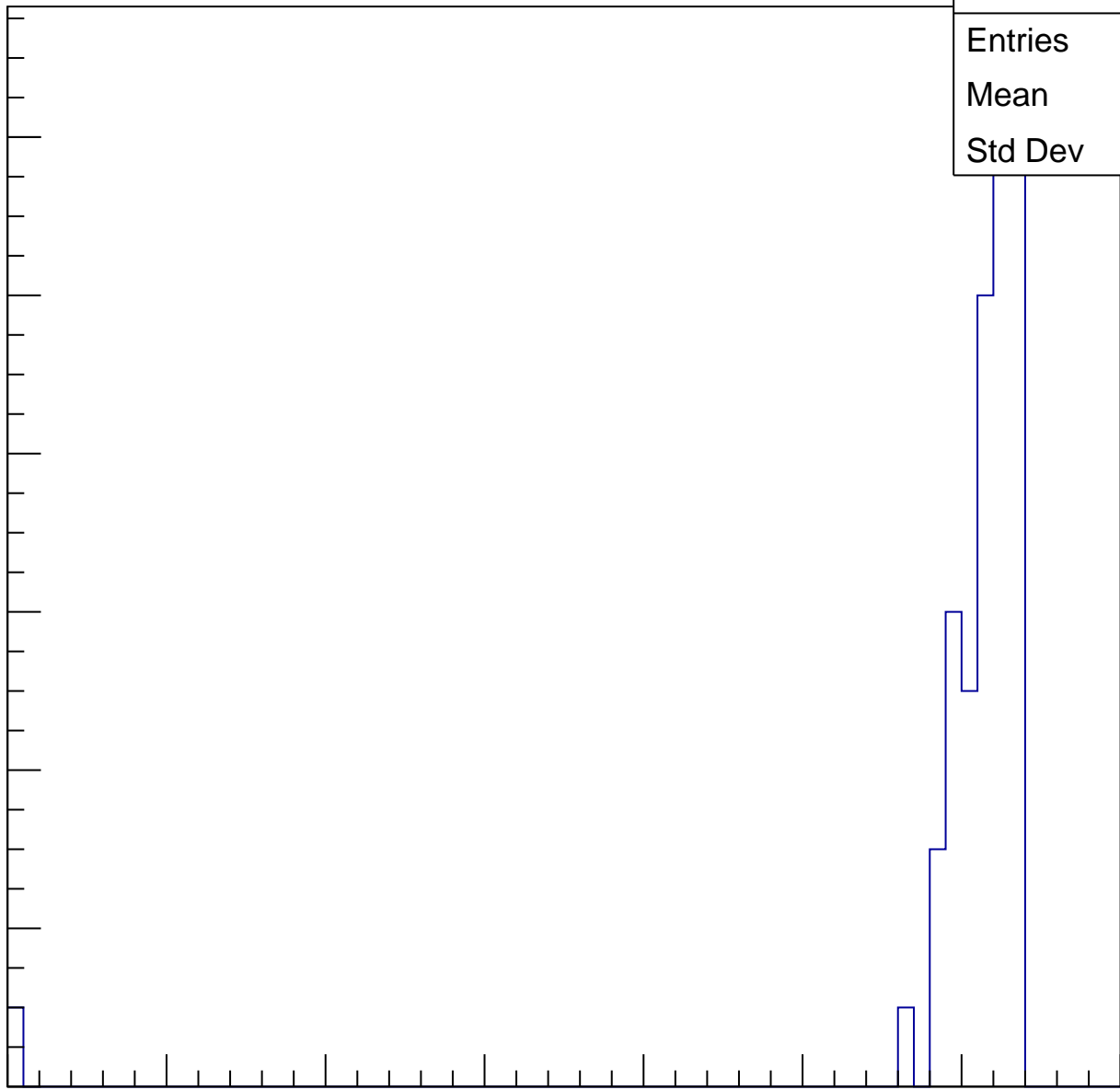
2

0

Entries	51
Mean	59.92
Std Dev	8.636

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L001S, U14-ch92, adc0

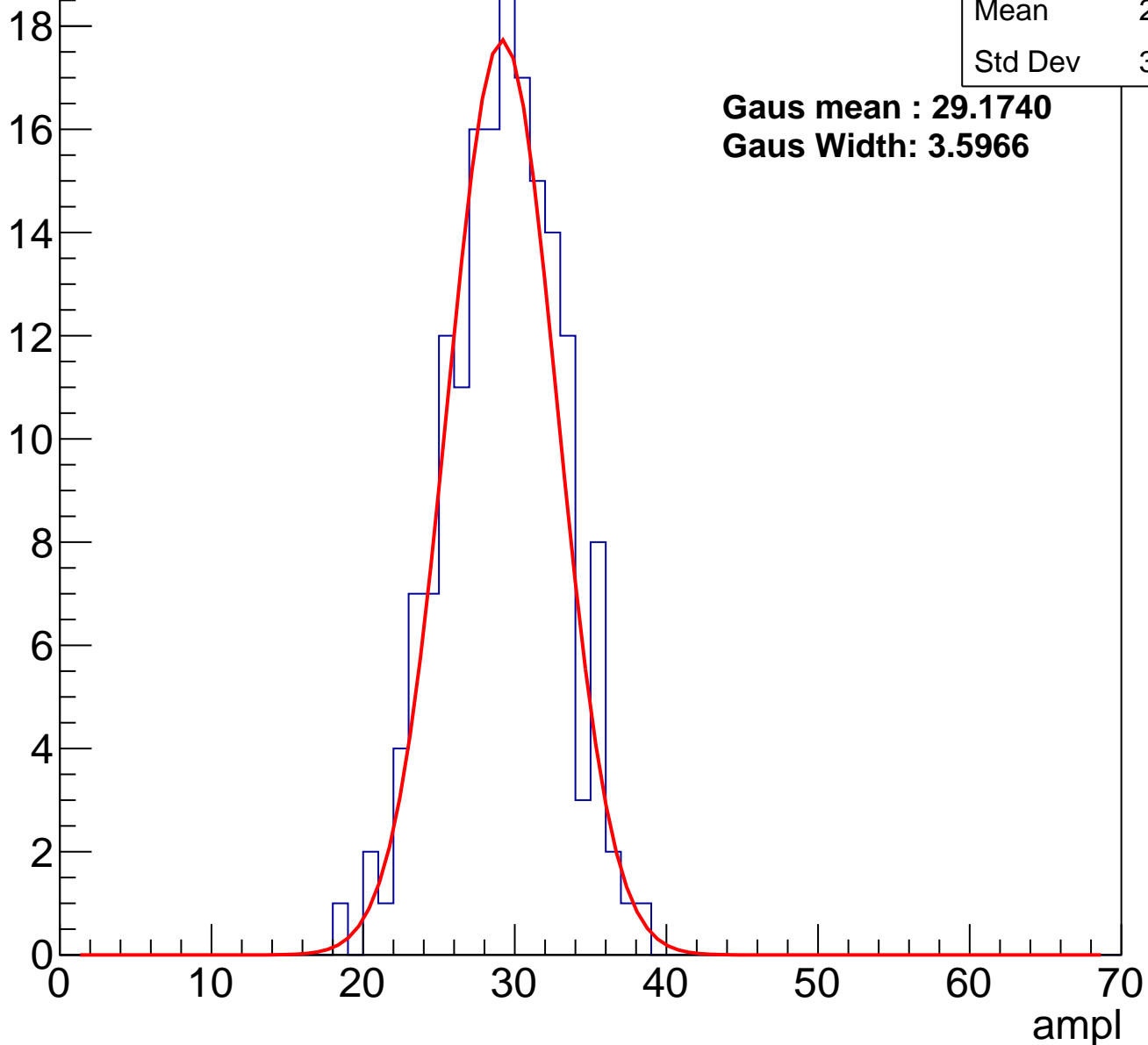
calib_packv5_042523_0143.root, FC#2, port C2

Entries	169
Mean	28.76
Std Dev	3.715

Gaus mean : 29.1740

Gaus Width: 3.5966

Entry



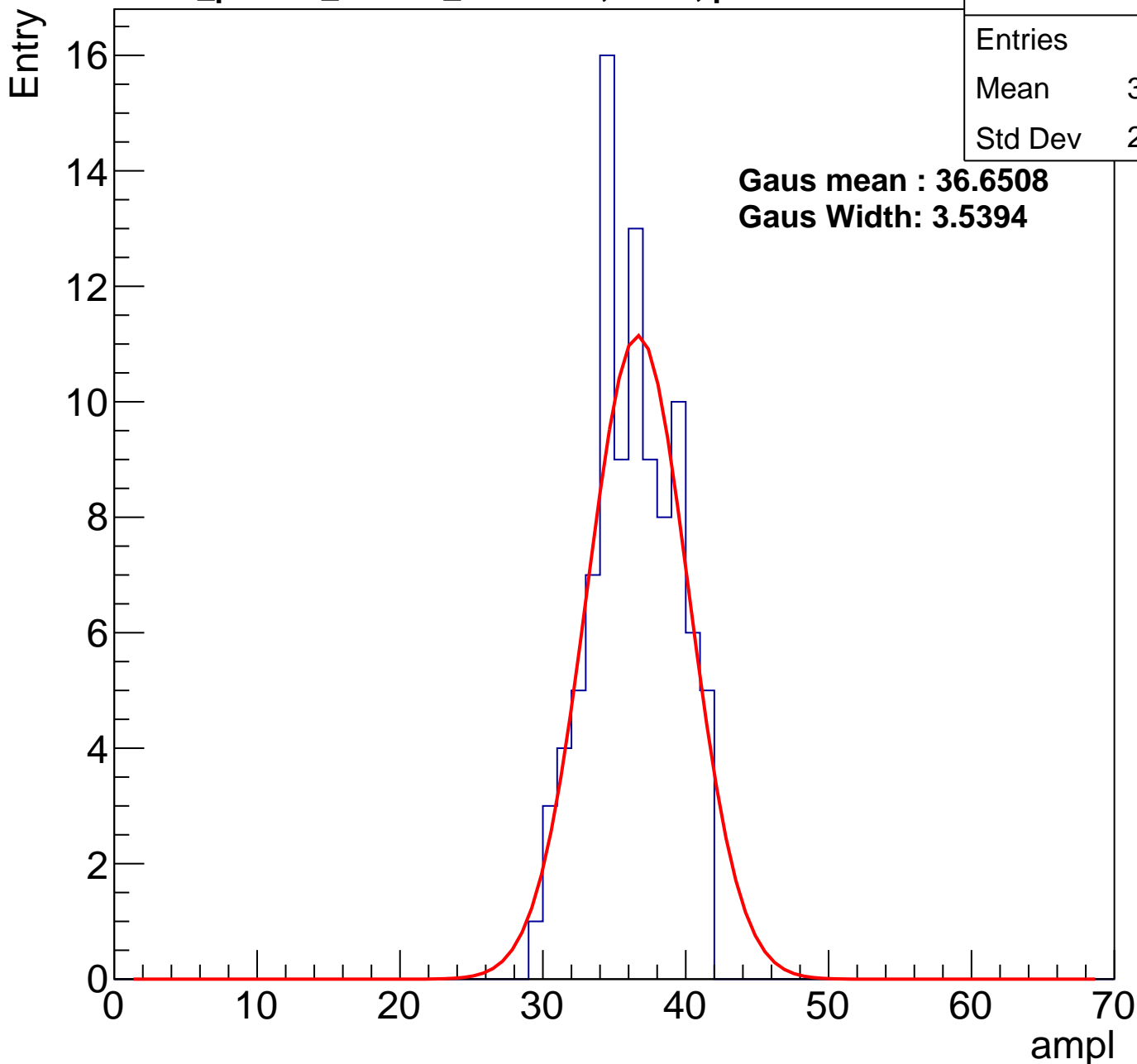
B1L001S, U14-ch92, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	96
Mean	35.76
Std Dev	2.922

Gaus mean : 36.6508

Gaus Width: 3.5394



B1L001S, U14-ch92, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	40.24
Std Dev	3.014

Gaus mean : 40.8863

Gaus Width: 2.9229

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

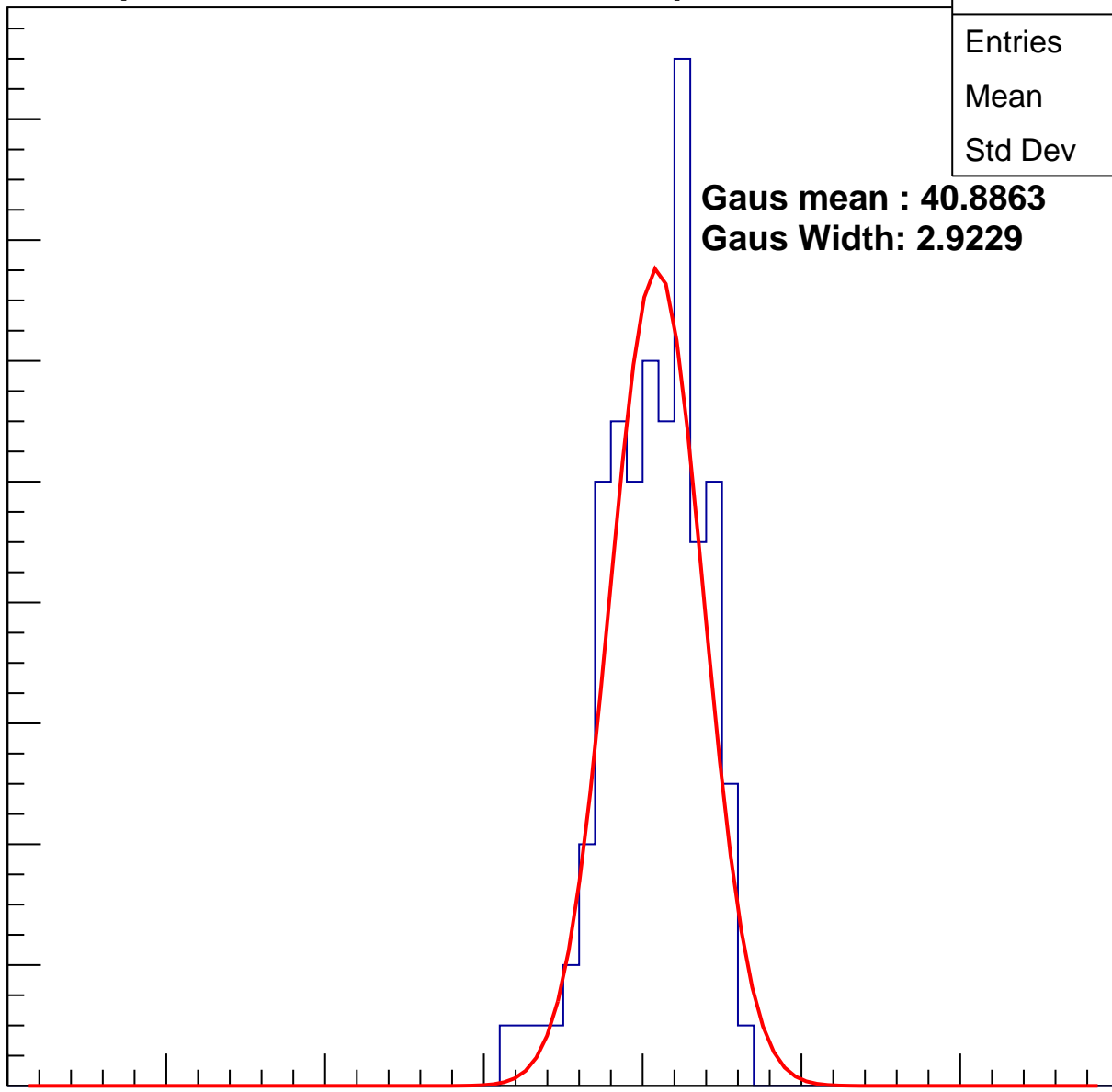
40

50

60

70

ampl

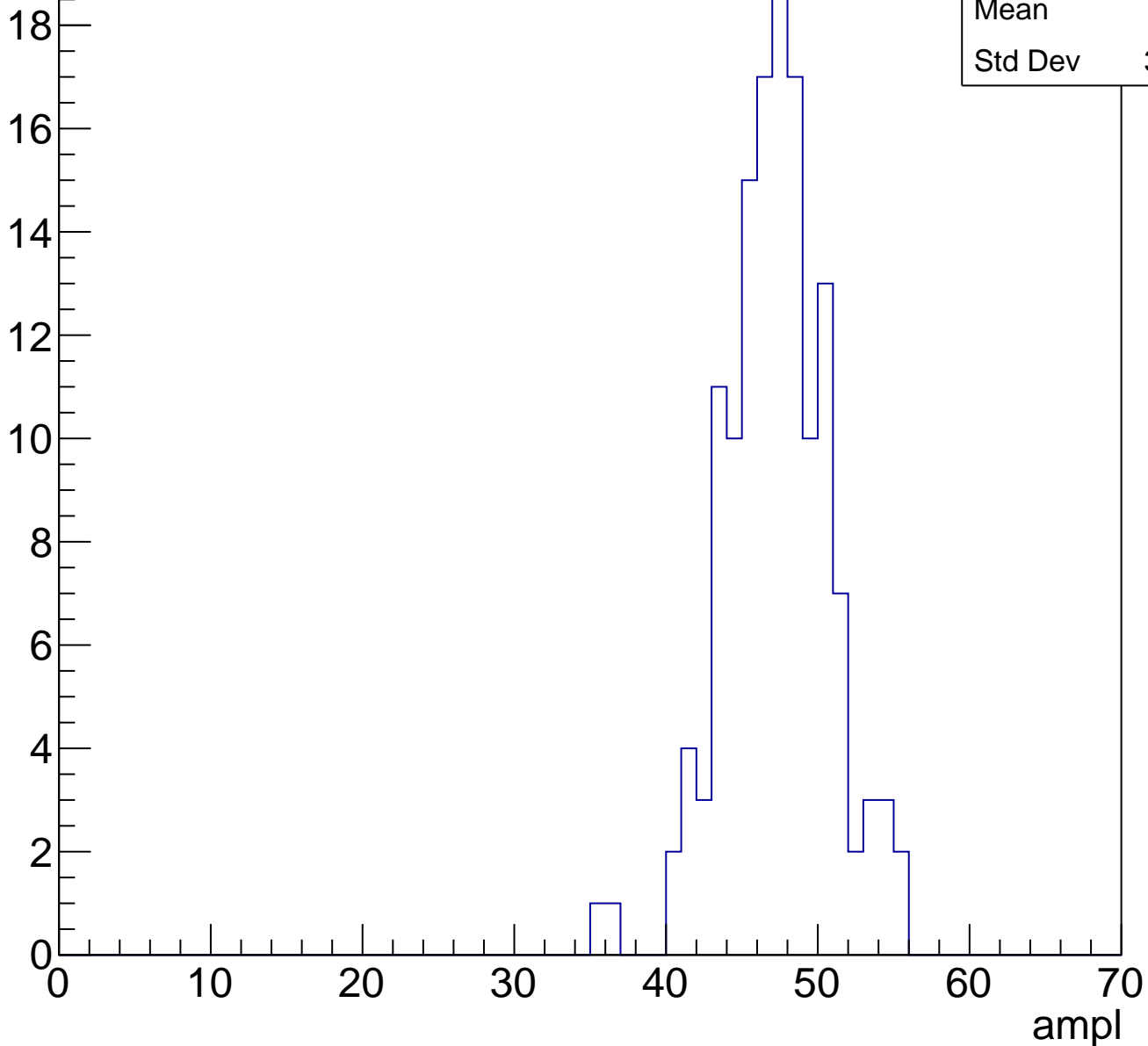


B1L001S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	46.8
Std Dev	3.421

Entry



B1L001S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#2, port C2

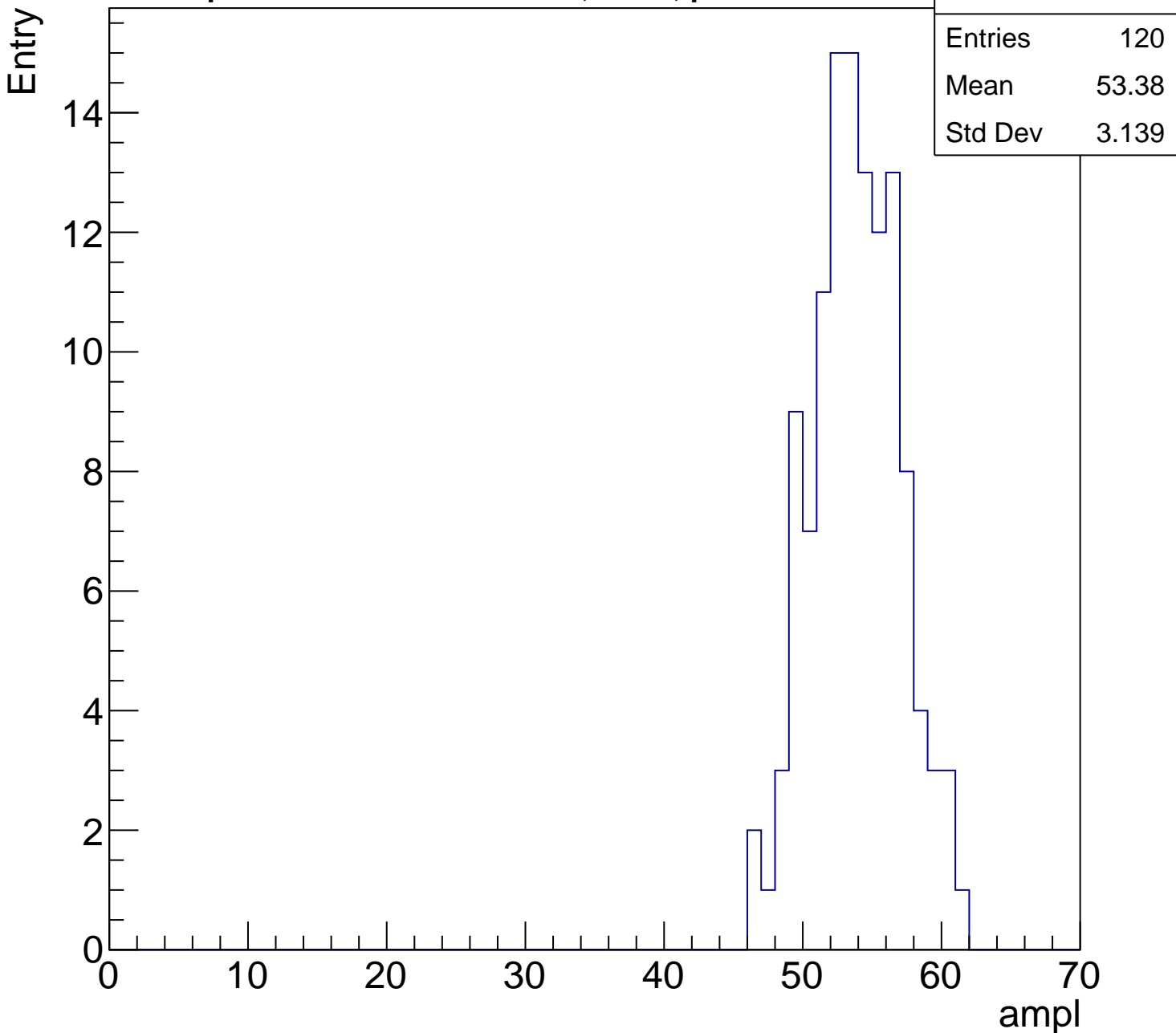
Entries	120
Mean	53.38
Std Dev	3.139

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch92, adc5

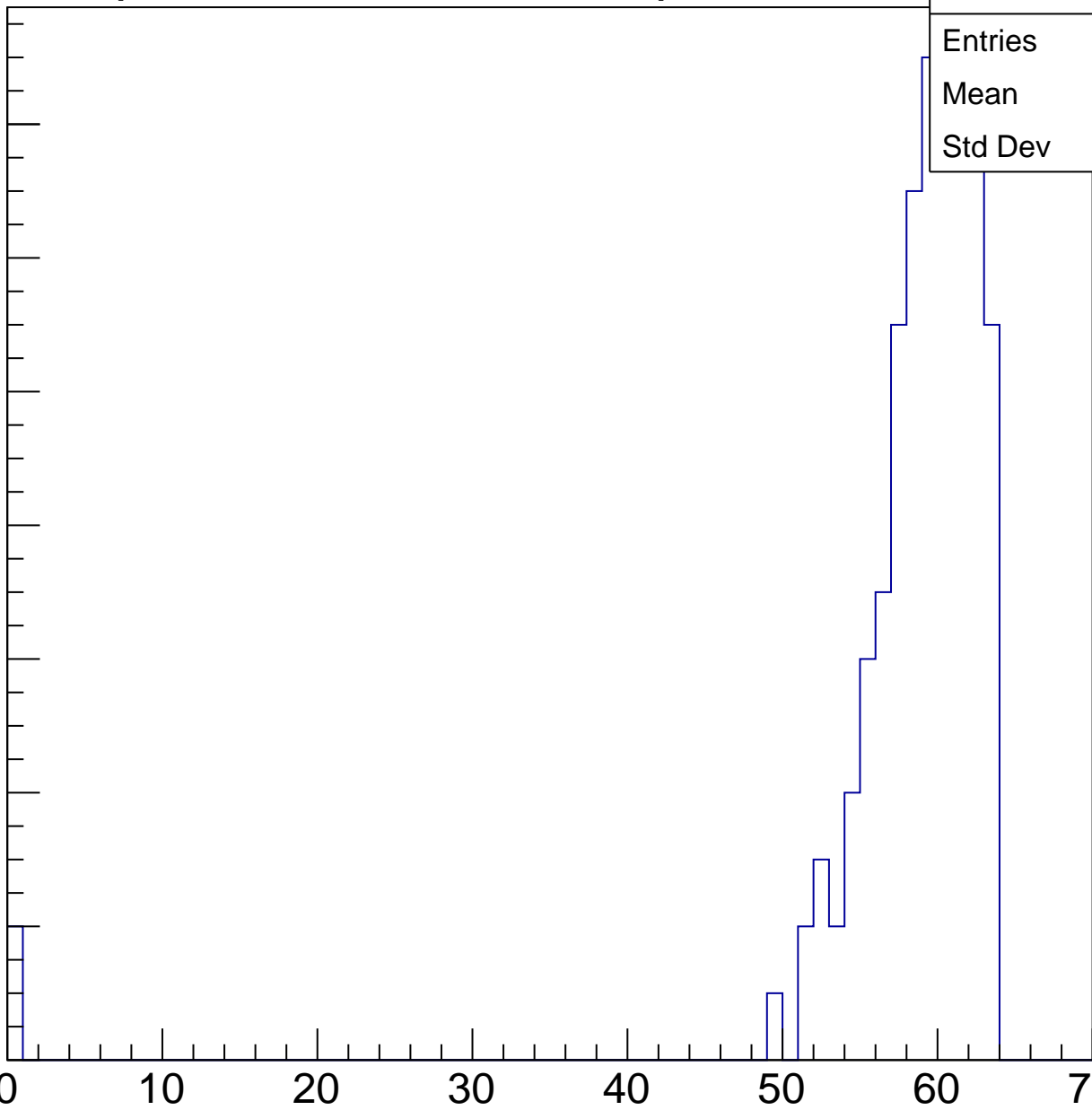
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	57.78
Std Dev	8.128

ampl

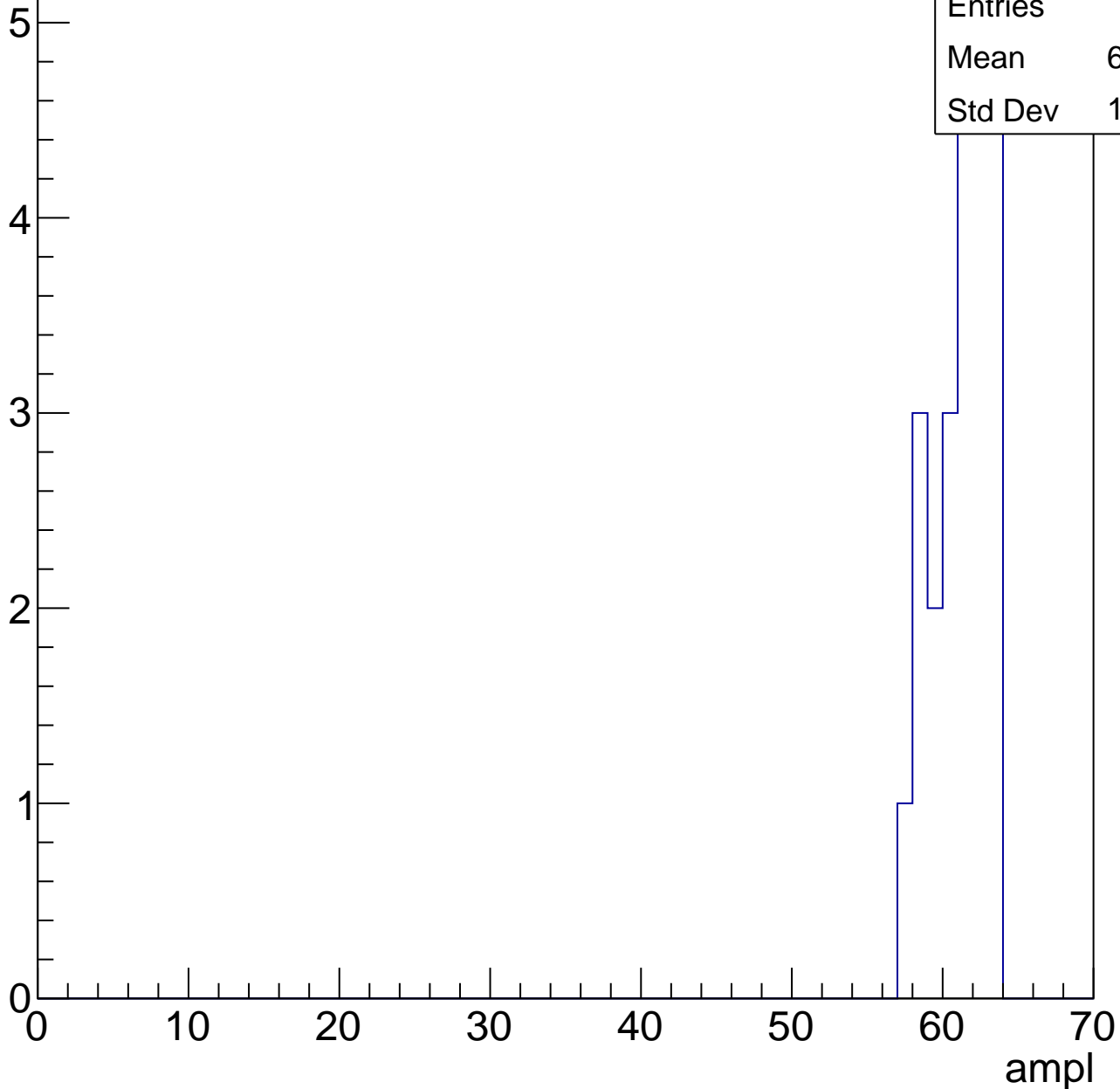


B1L001S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

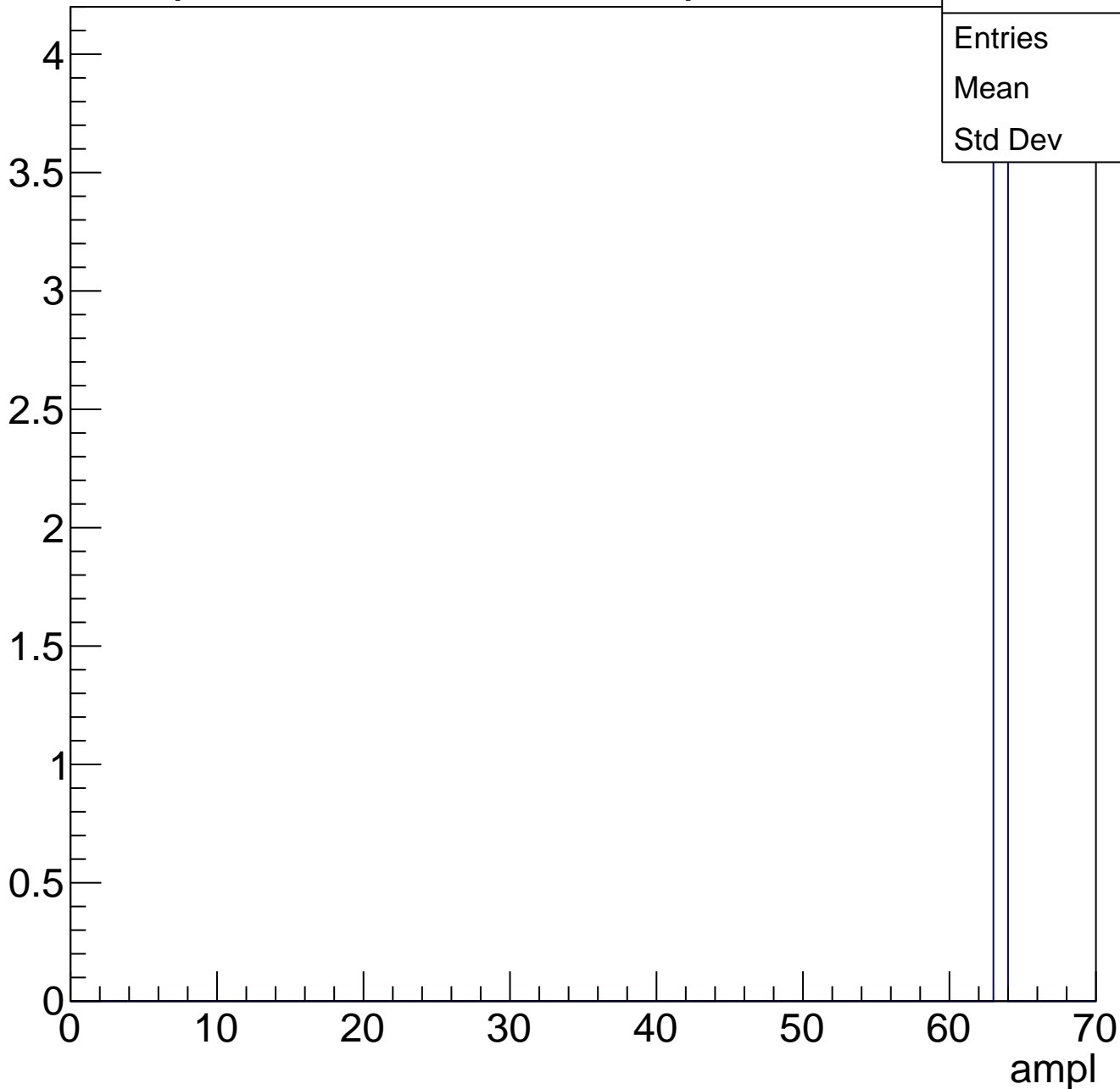
Entries	24
Mean	60.79
Std Dev	1.802



B1L001S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch93, adc0

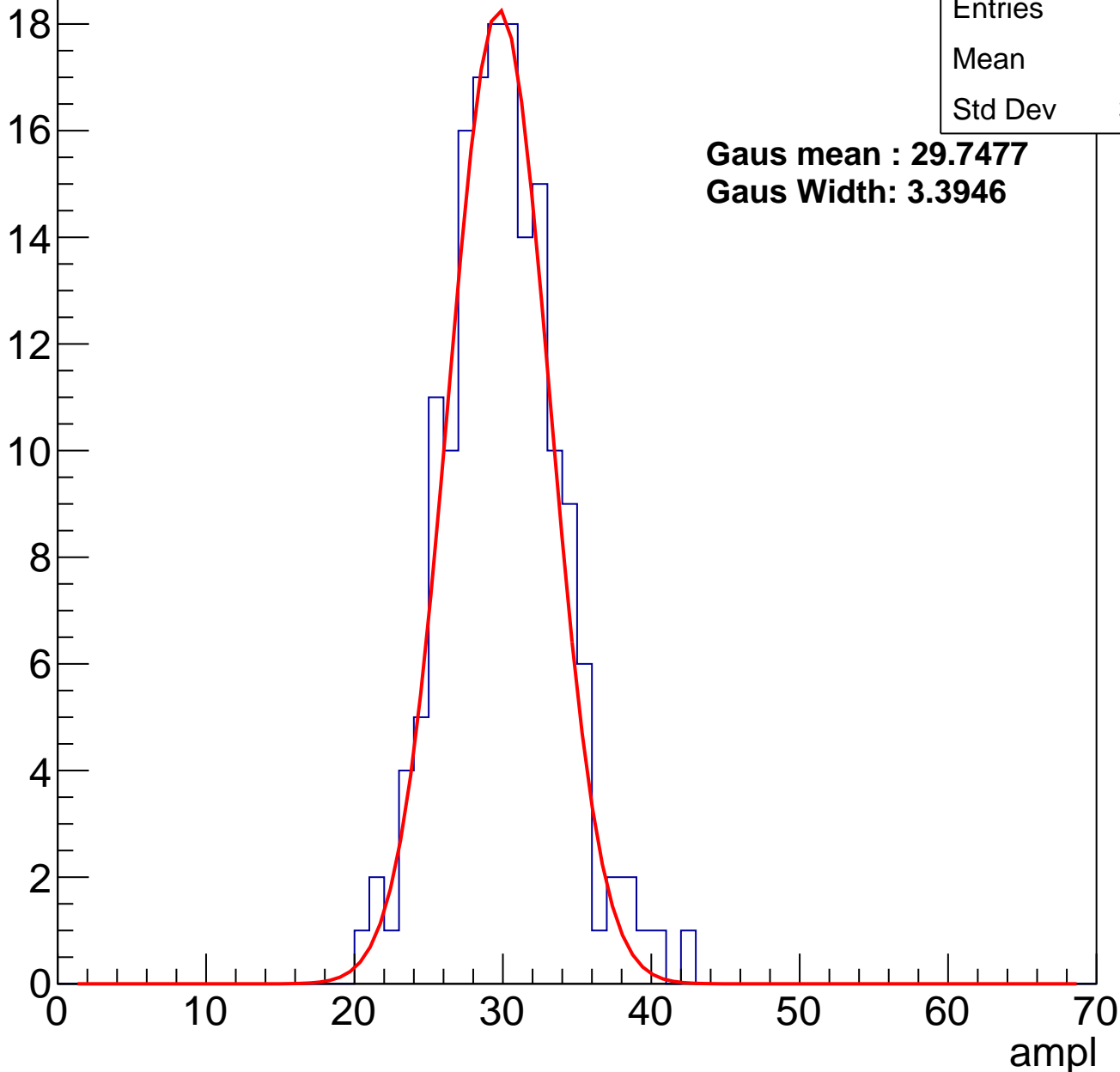
calib_packv5_042523_0143.root, FC#2, port C2

Entries	165
Mean	29.5
Std Dev	3.791

Gaus mean : 29.7477

Gaus Width: 3.3946

Entry



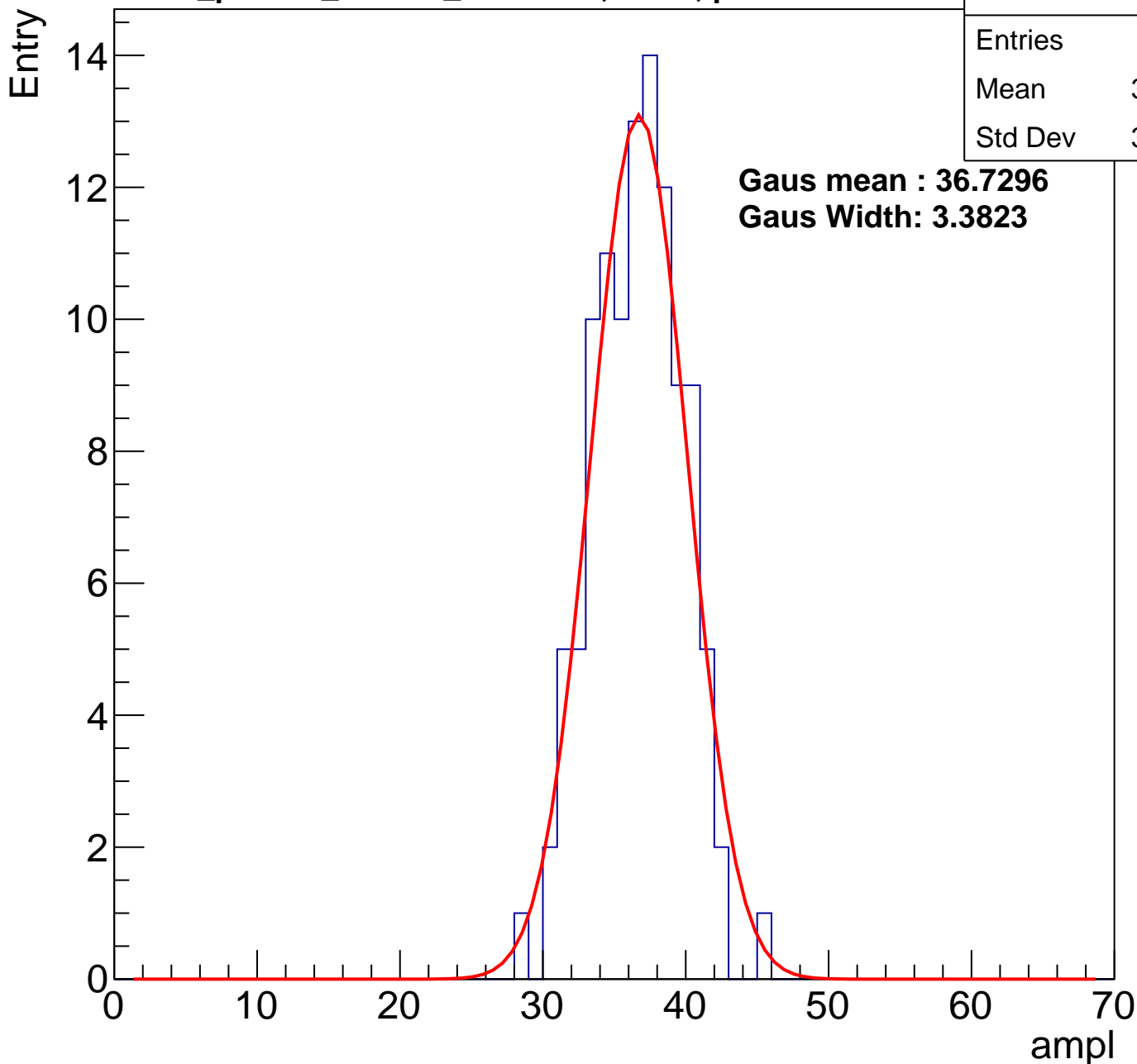
B1L001S, U14-ch93, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	36.18
Std Dev	3.098

Gaus mean : 36.7296

Gaus Width: 3.3823



B1L001S, U14-ch93, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	41.98
Std Dev	3.337

Gaus mean : 42.5150

Gaus Width: 3.7113

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

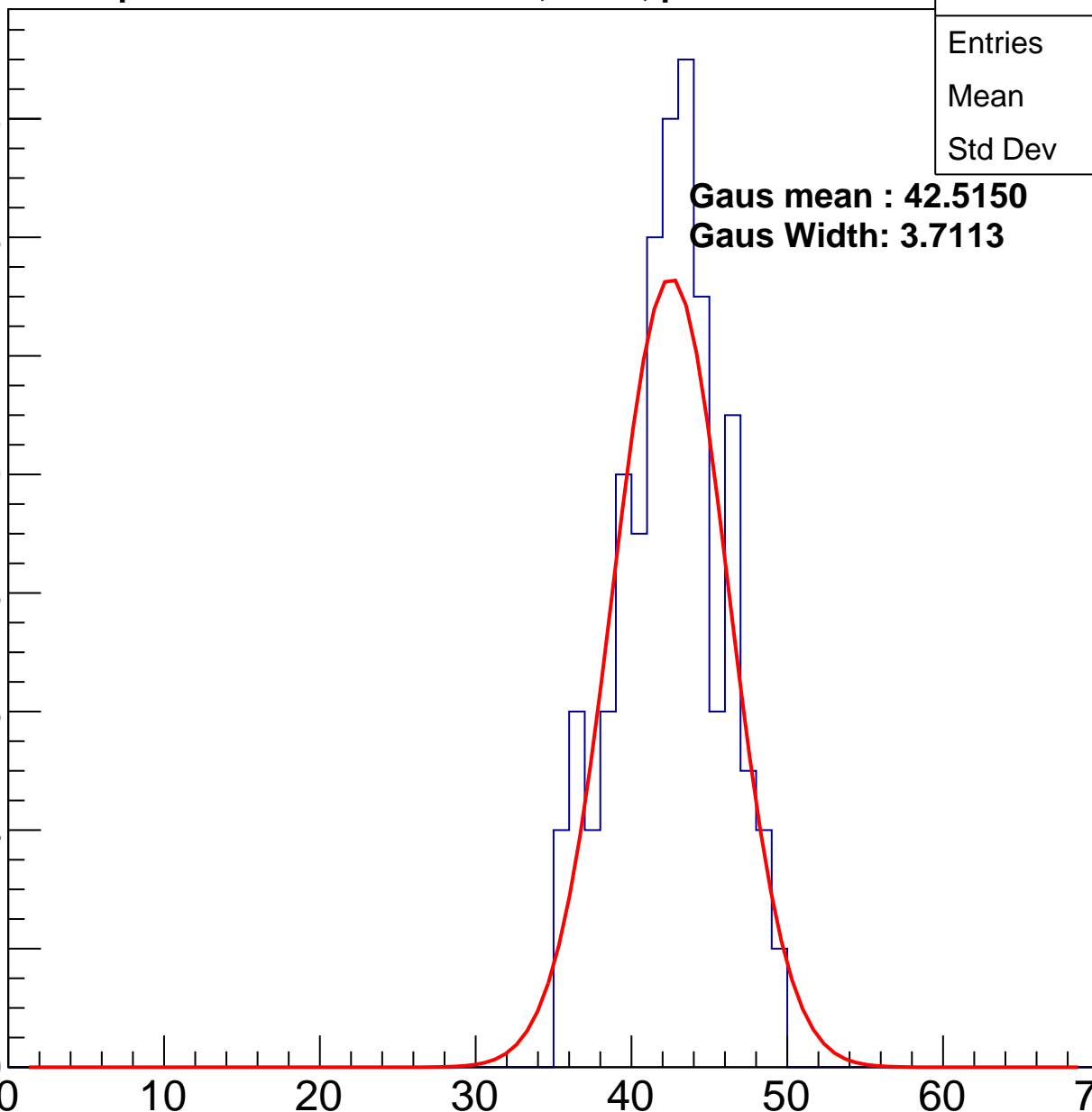
40

50

60

70

ampl



B1L001S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#2, port C2

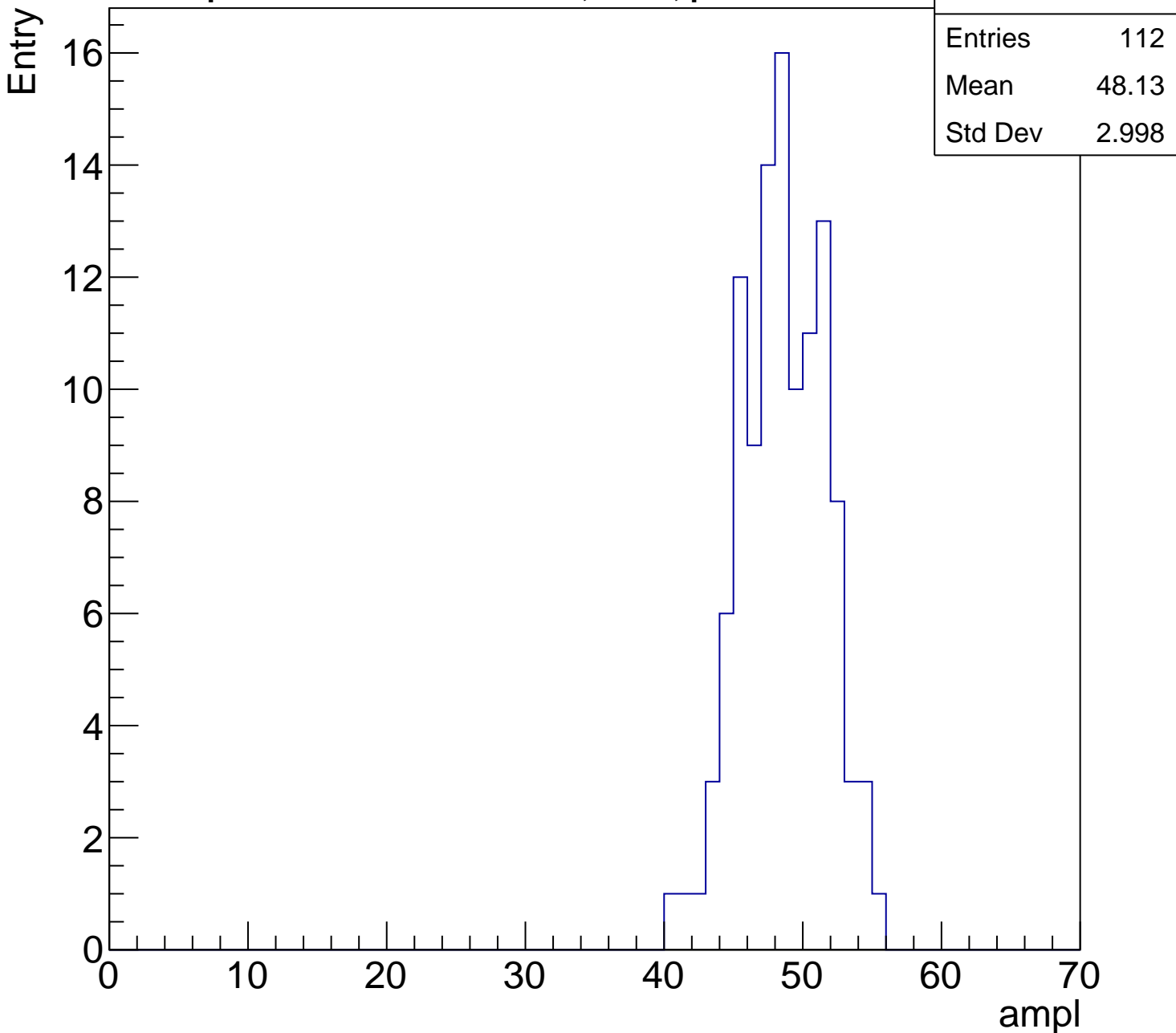
Entries	112
Mean	48.13
Std Dev	2.998

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

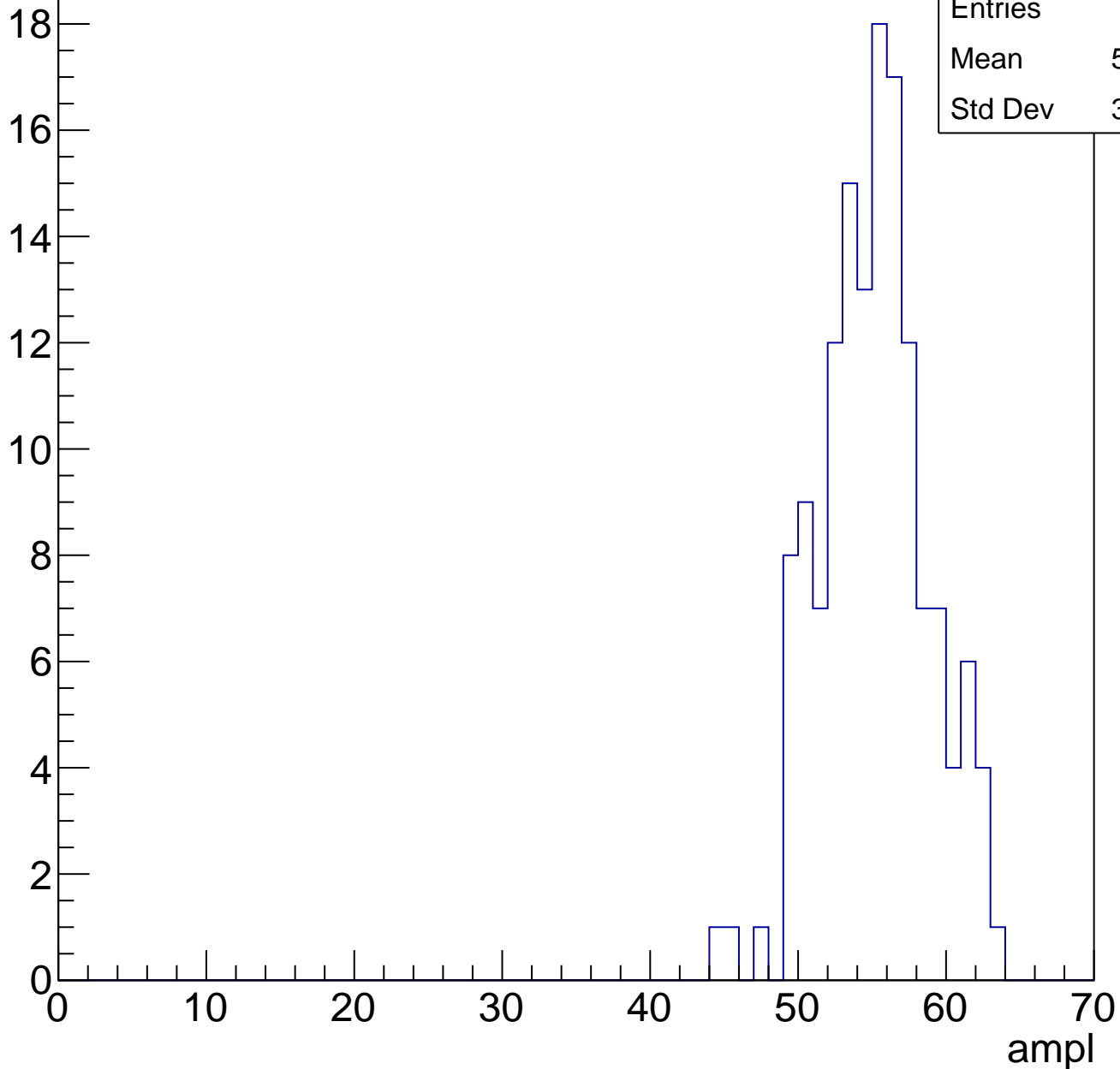


B1L001S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	54.67
Std Dev	3.637

Entry



B1L001S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

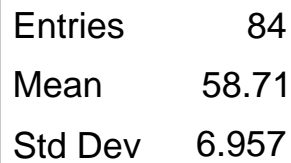
50

60

70

ampl

Entries	84
Mean	58.71
Std Dev	6.957

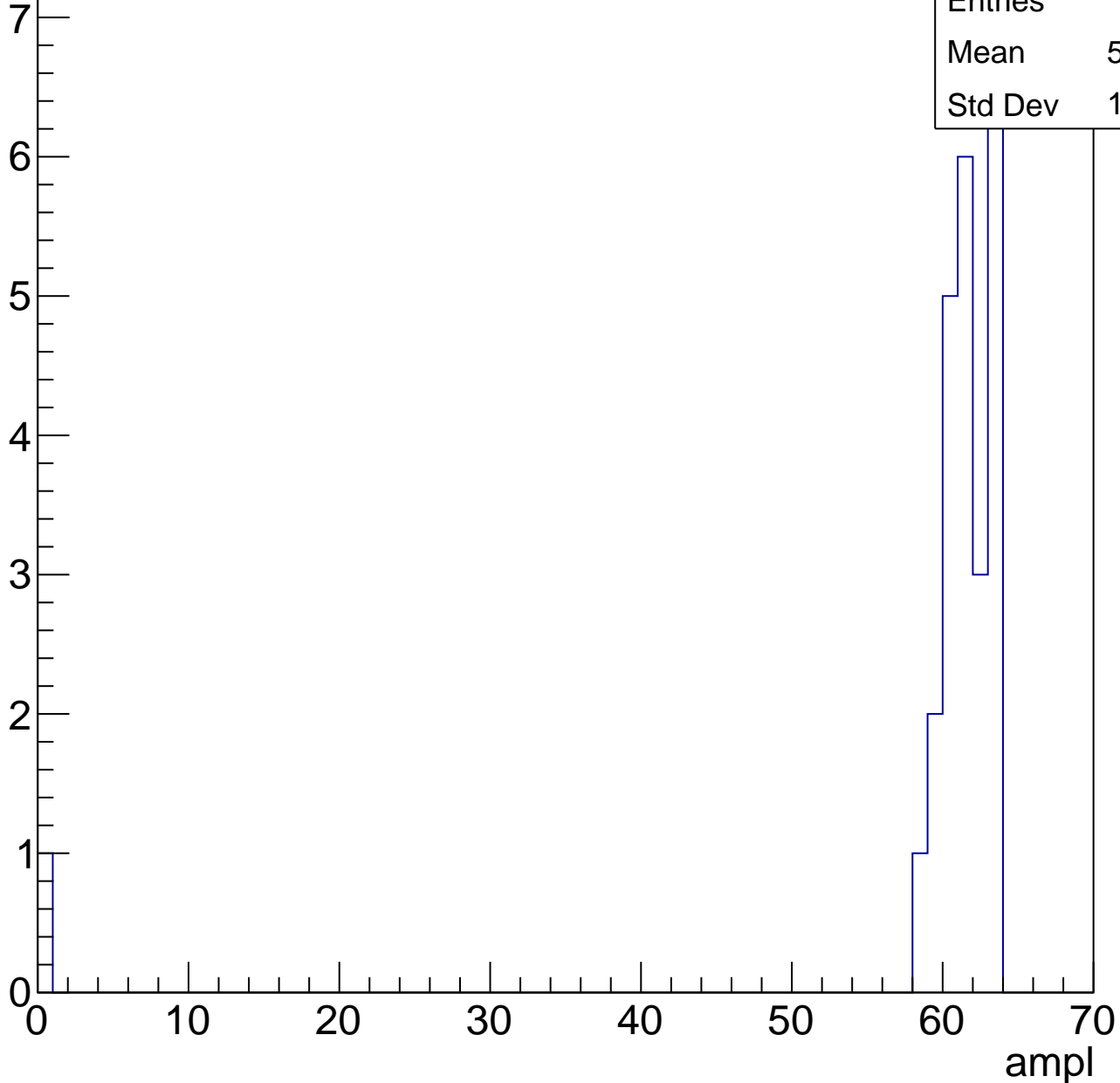


B1L001S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	25
Mean	58.76
Std Dev	12.08



B1L001S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch94, adc0

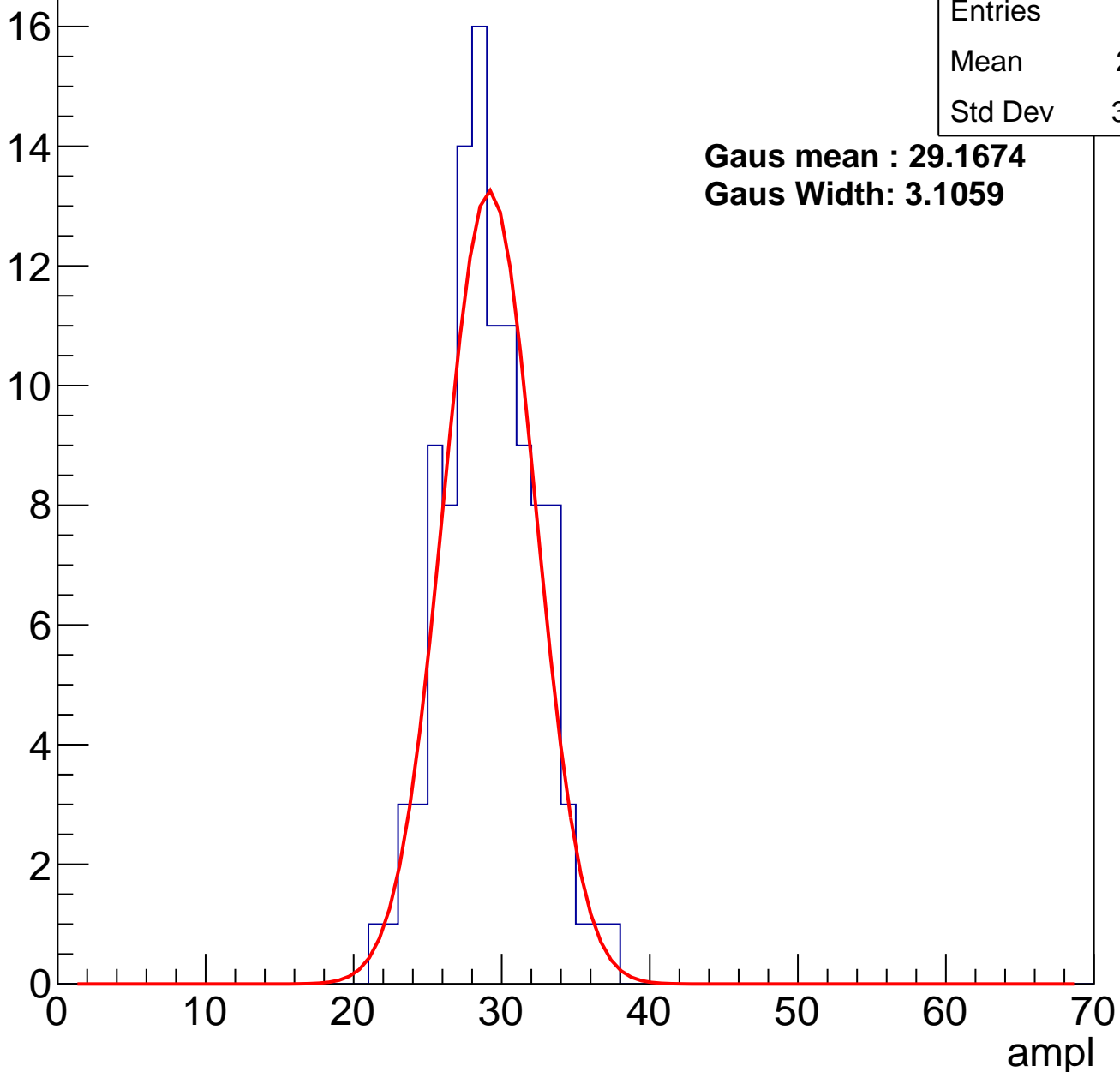
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	28.71
Std Dev	3.112

Gaus mean : 29.1674

Gaus Width: 3.1059

Entry



B1L001S, U14-ch94, adc1

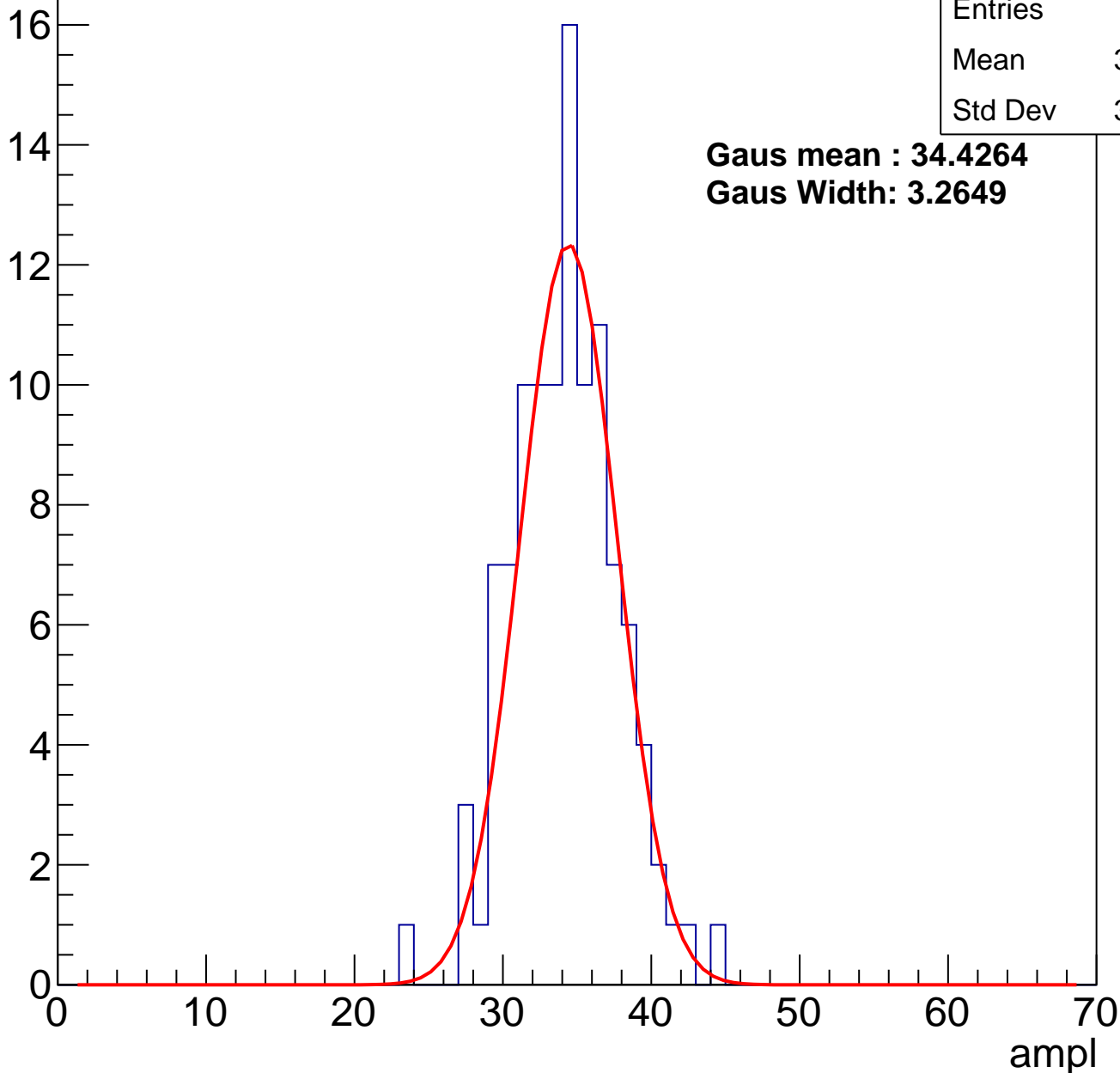
calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	33.75
Std Dev	3.499

Gaus mean : 34.4264

Gaus Width: 3.2649

Entry



B1L001S, U14-ch94, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	40.38
Std Dev	3.558

Gaus mean : 41.0763

Gaus Width: 3.4307

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

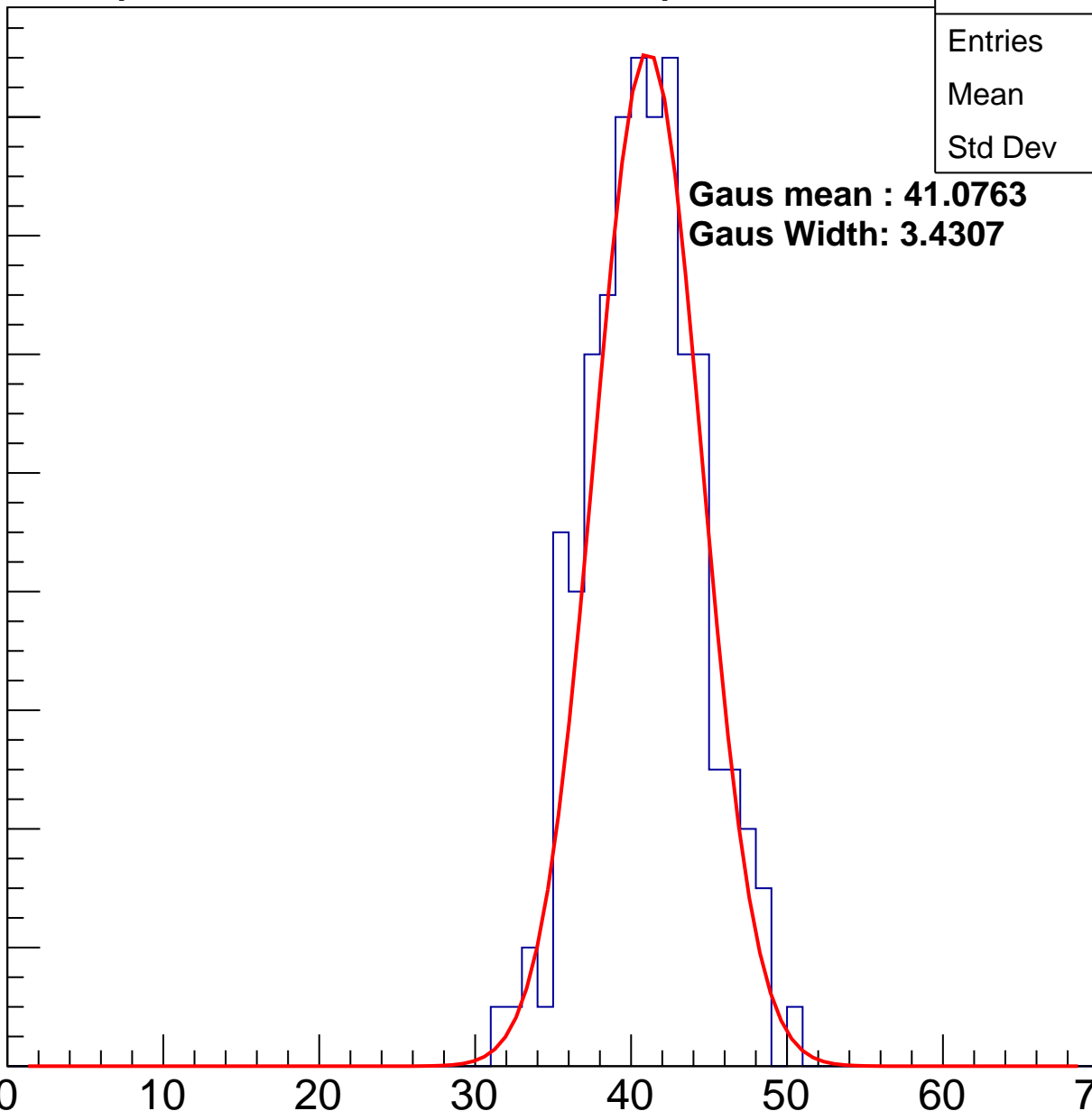
40

50

60

70

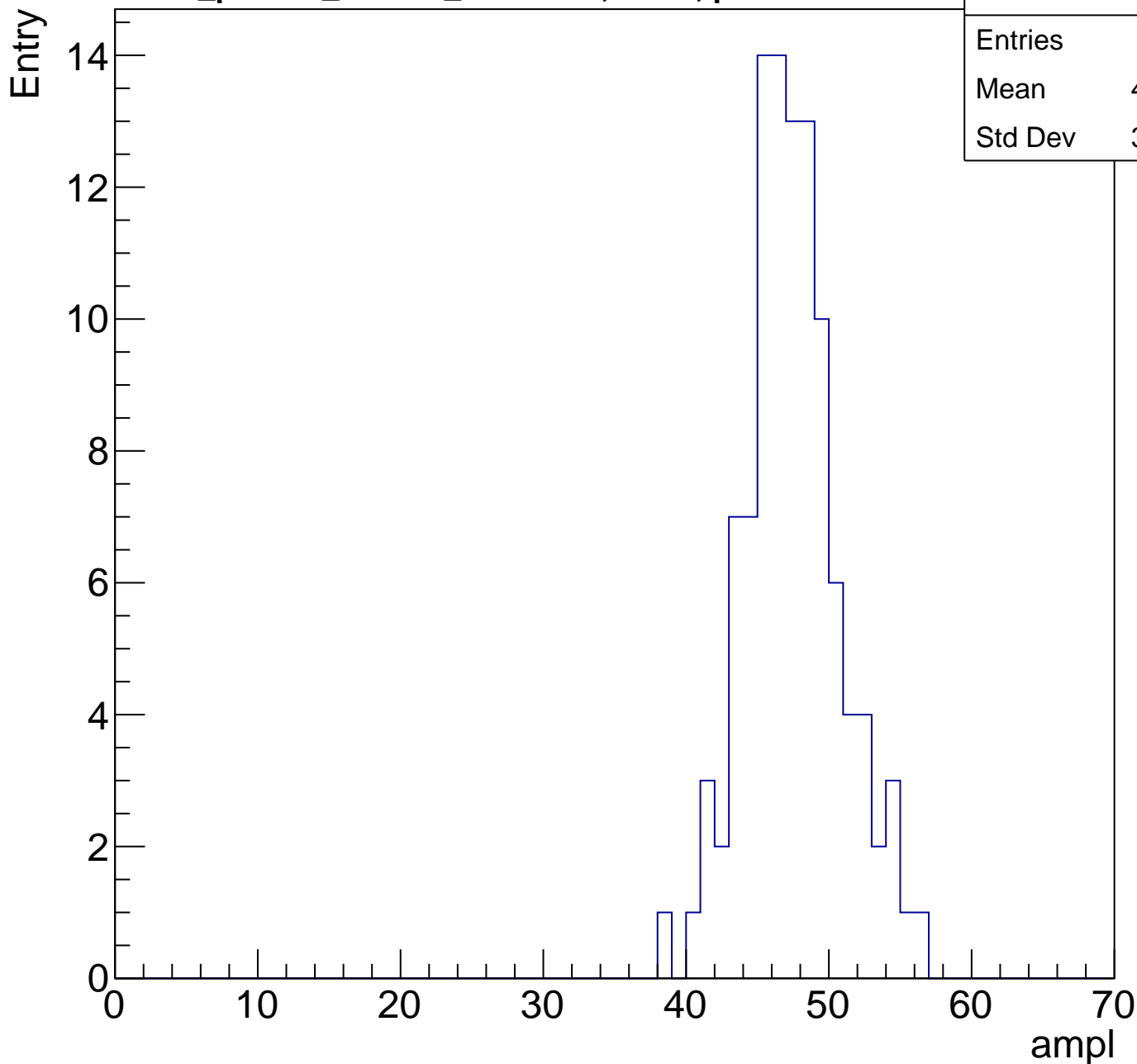
ampl



B1L001S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	47.02
Std Dev	3.319

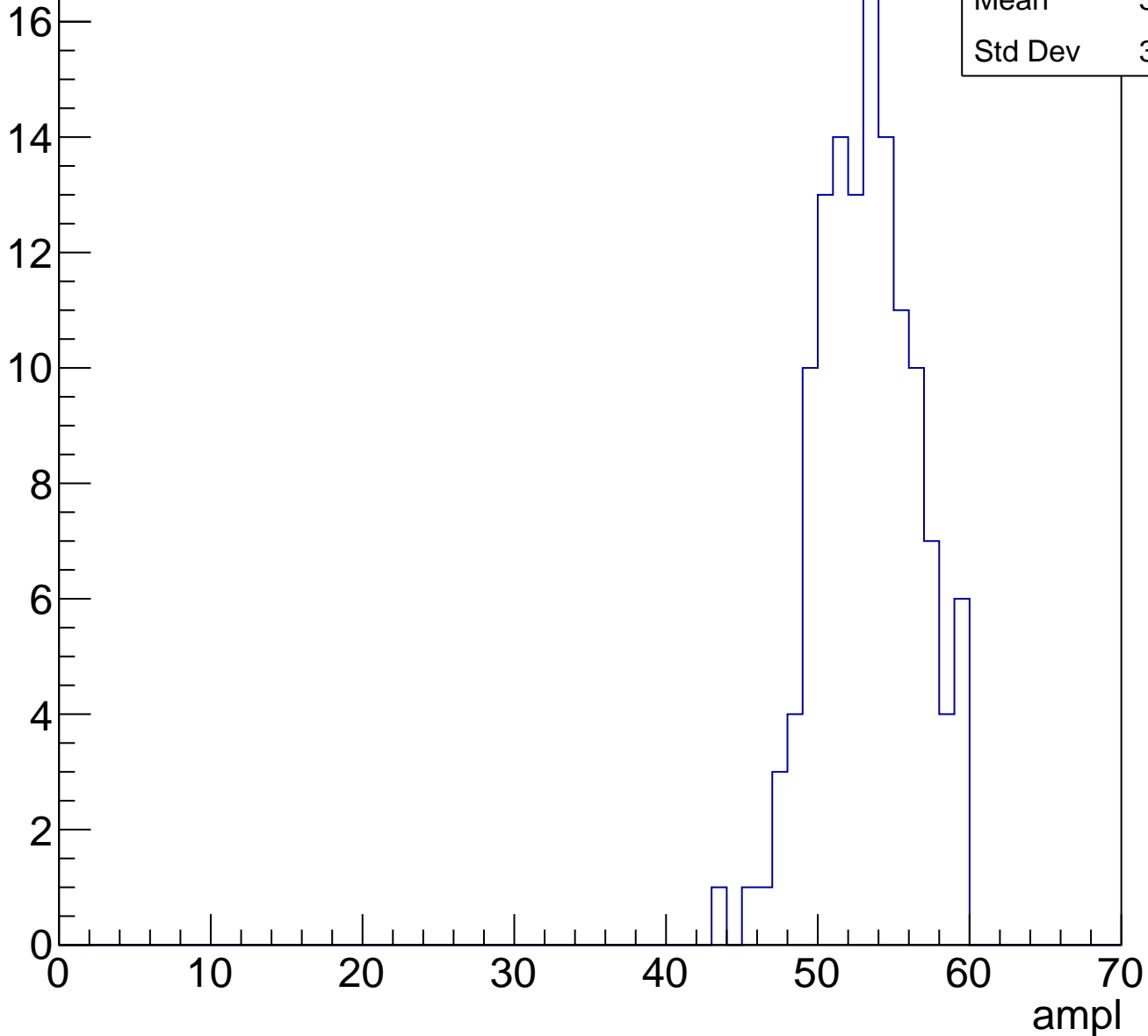


B1L001S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	129
Mean	52.74
Std Dev	3.216



B1L001S, U14-ch94, adc5

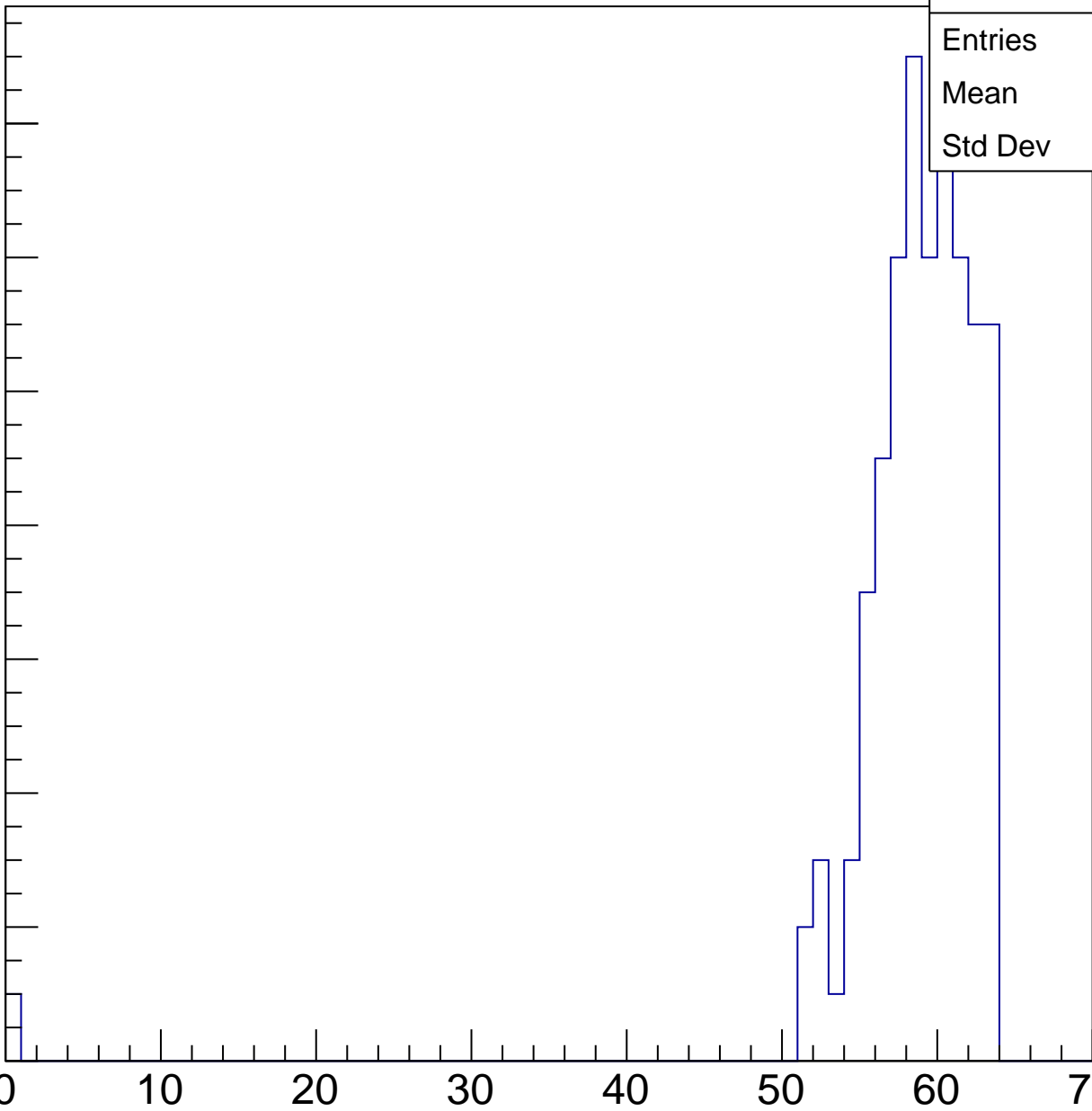
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	113
Mean	58.15
Std Dev	6.225

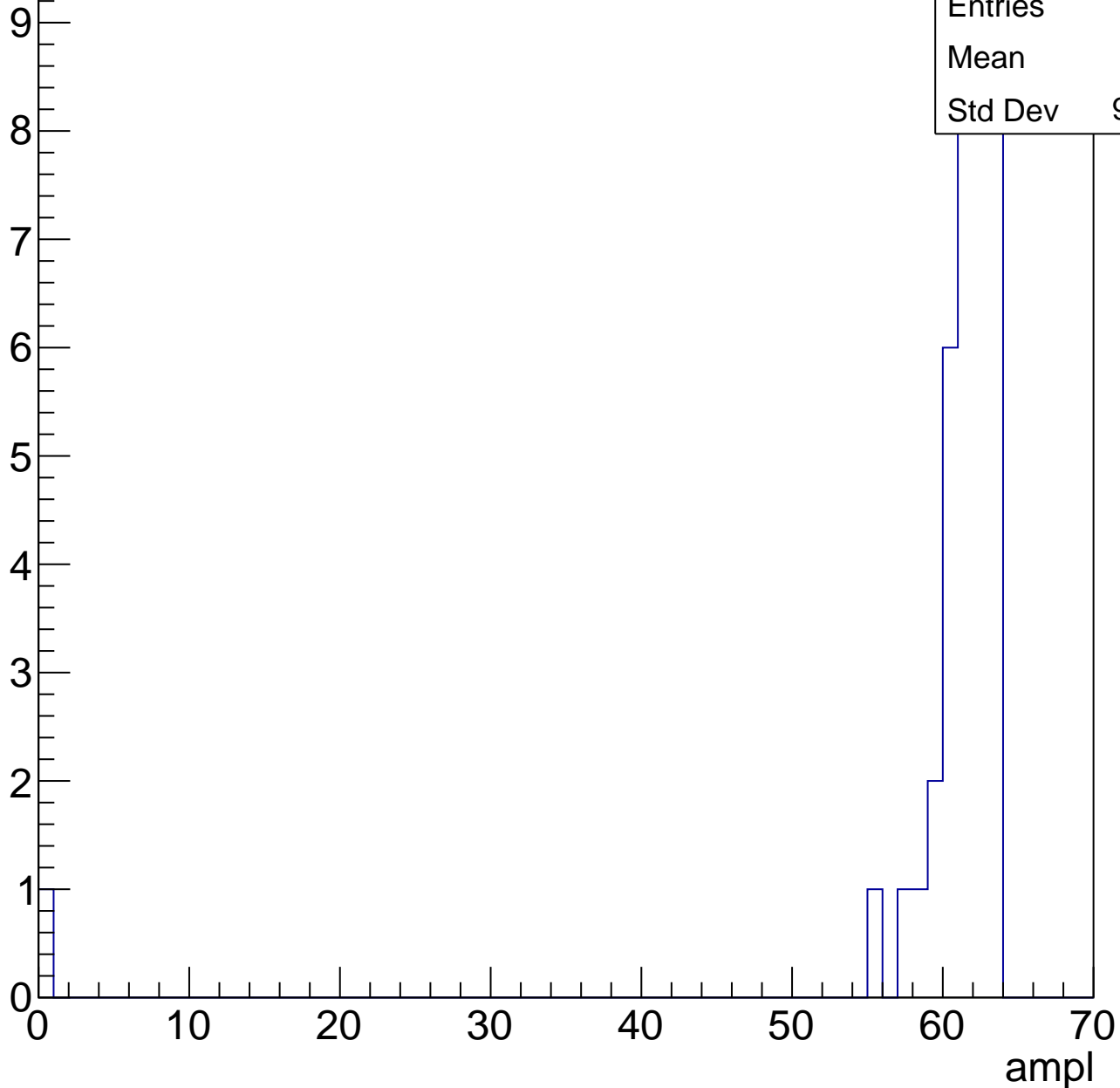
ampl



B1L001S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch95, adc0

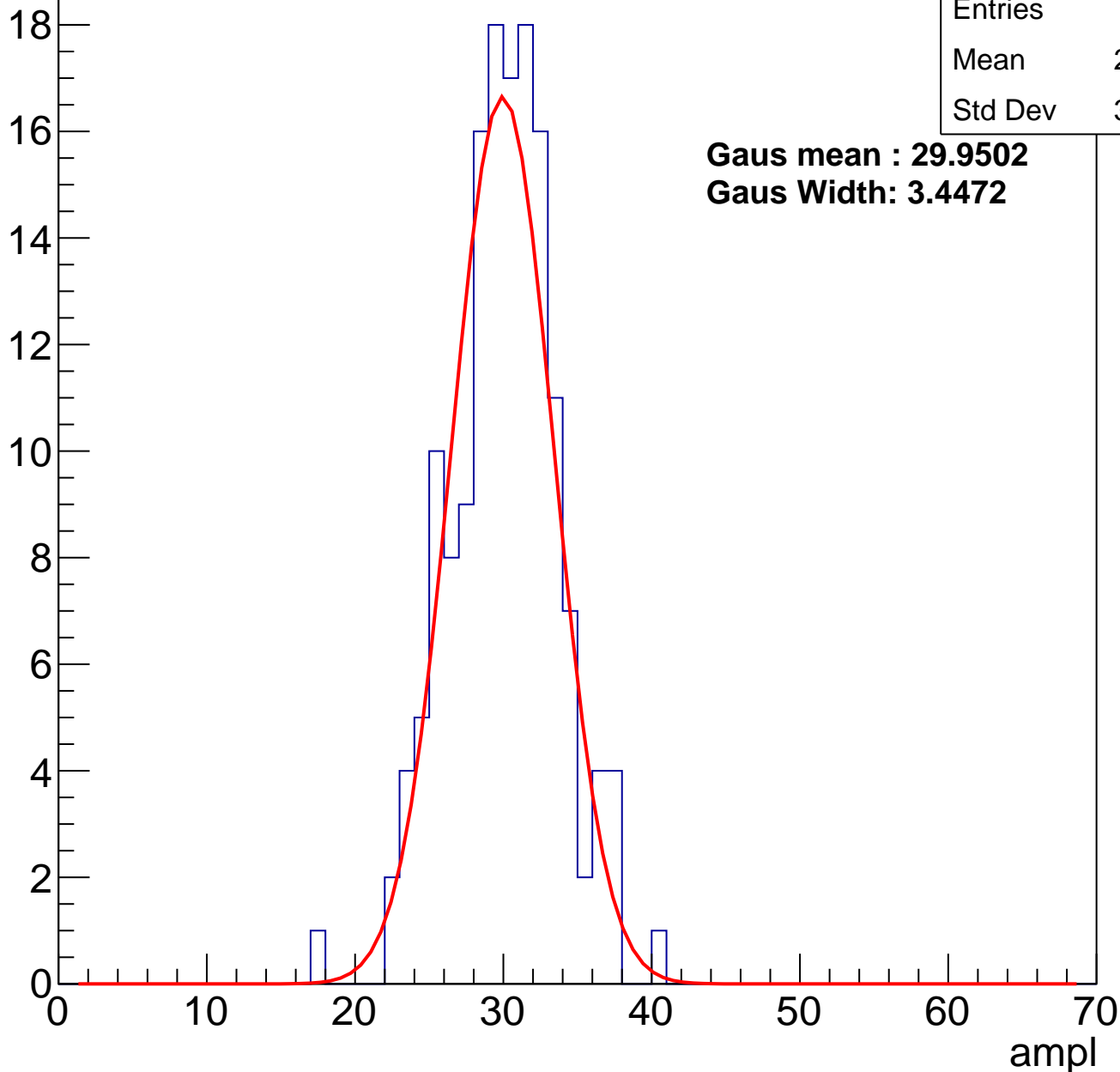
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	29.59
Std Dev	3.589

Gaus mean : 29.9502

Gaus Width: 3.4472

Entry



B1L001S, U14-ch95, adc1

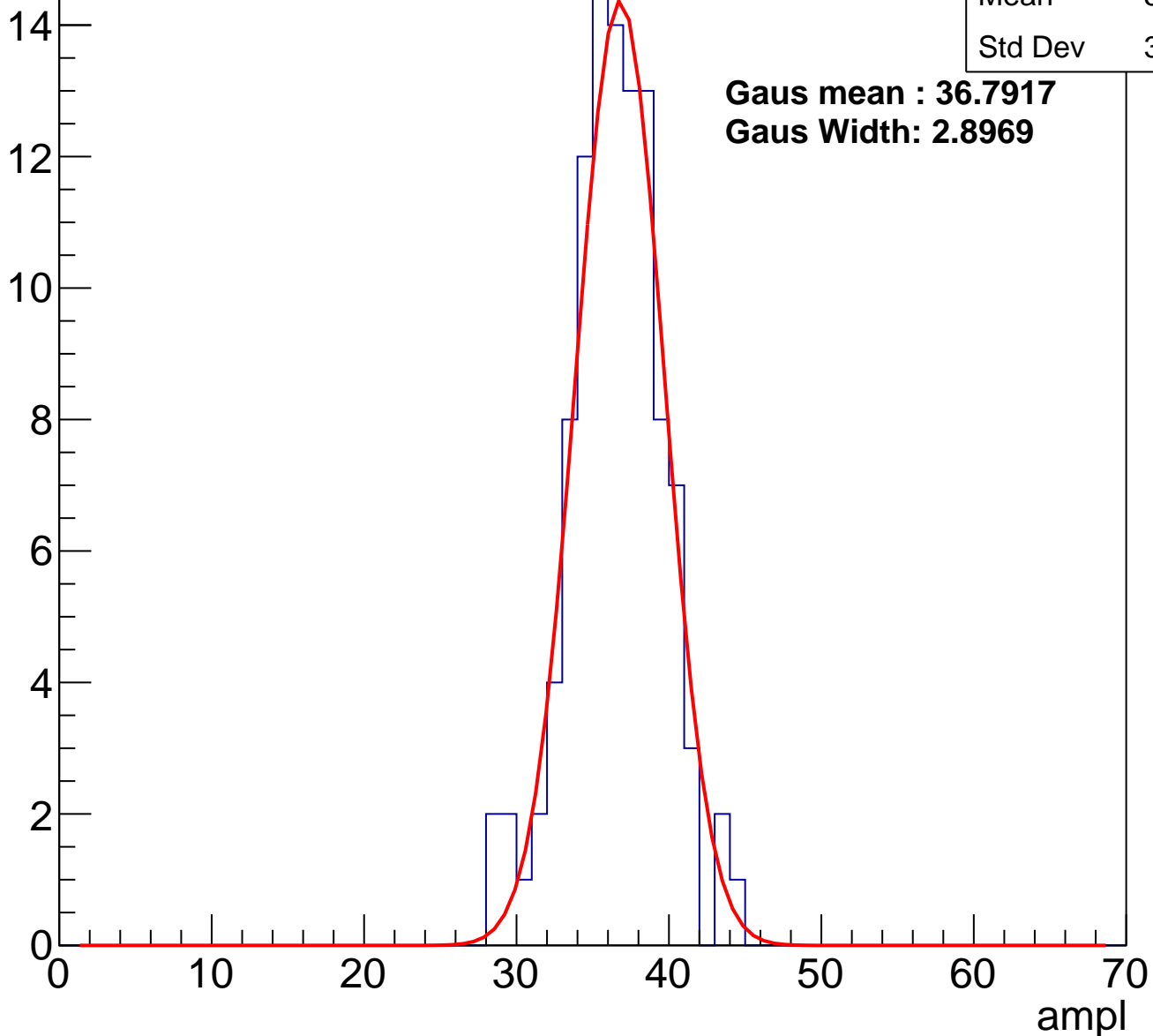
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	36.03
Std Dev	3.052

Gaus mean : 36.7917

Gaus Width: 2.8969

Entry



B1L001S, U14-ch95, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

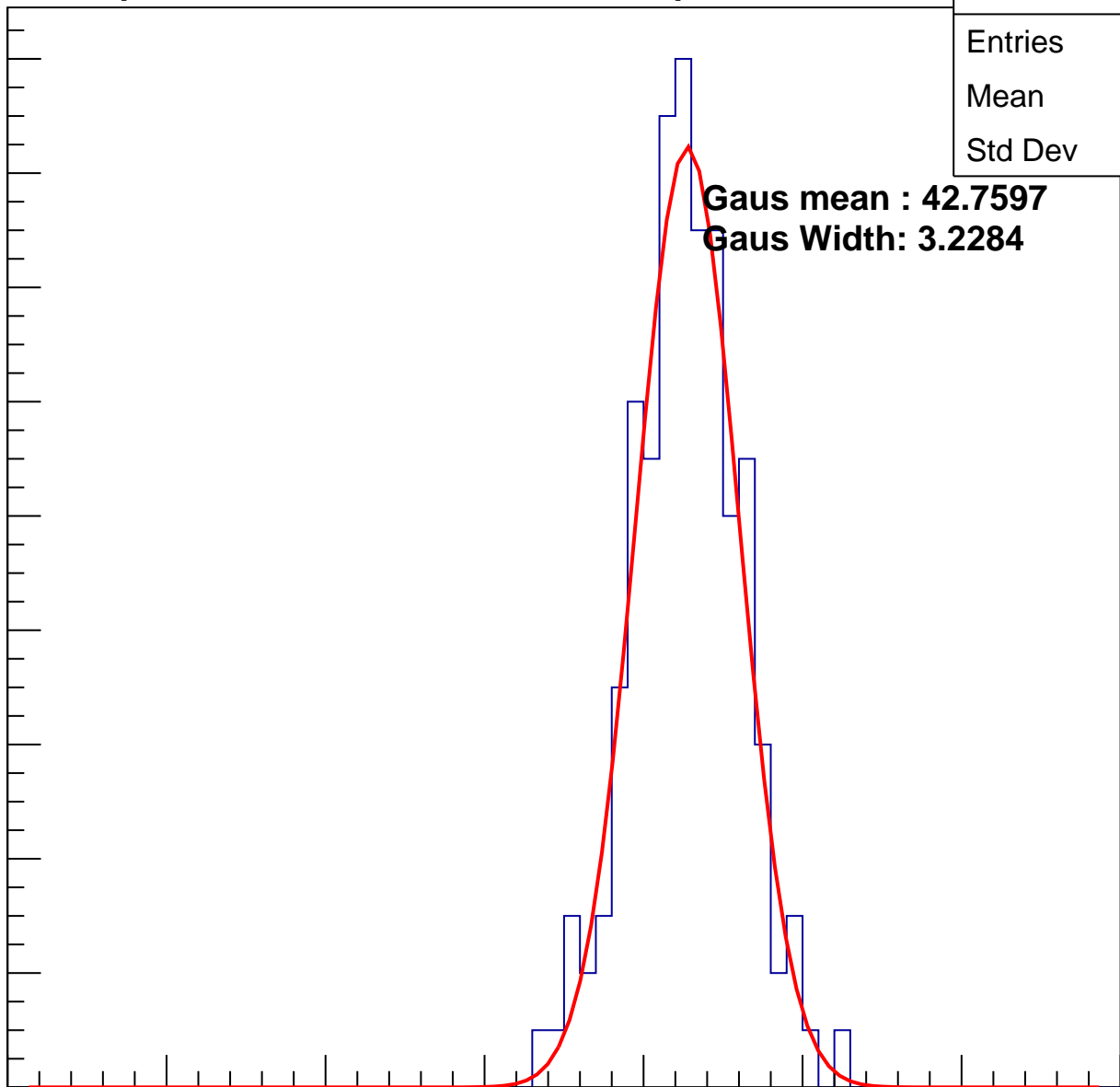
Entries	139
Mean	42.23
Std Dev	3.379

Gaus mean : 42.7597

Gaus Width: 3.2284

0 10 20 30 40 50 60 70

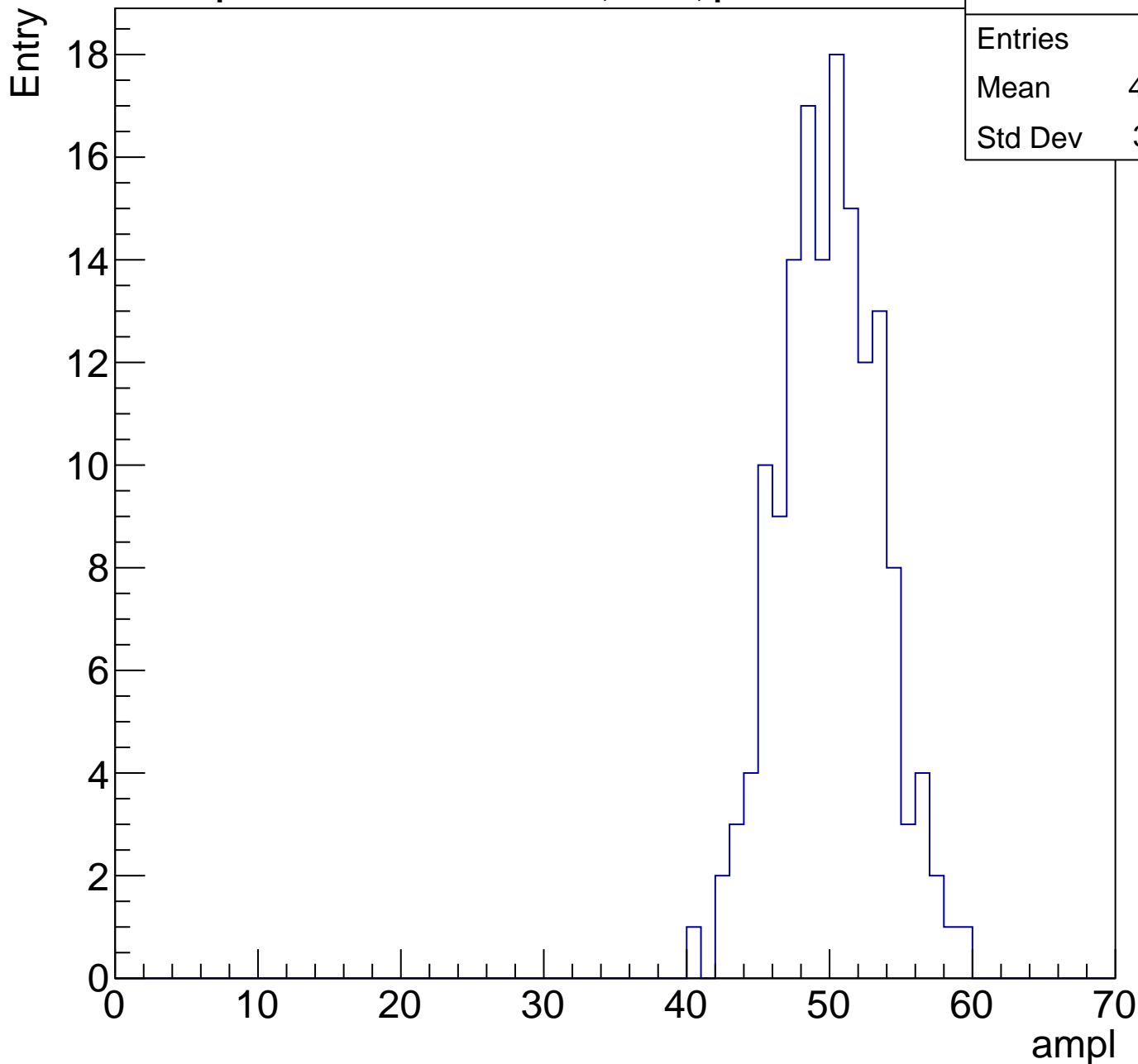
ampl



B1L001S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	49.56
Std Dev	3.511



B1L001S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

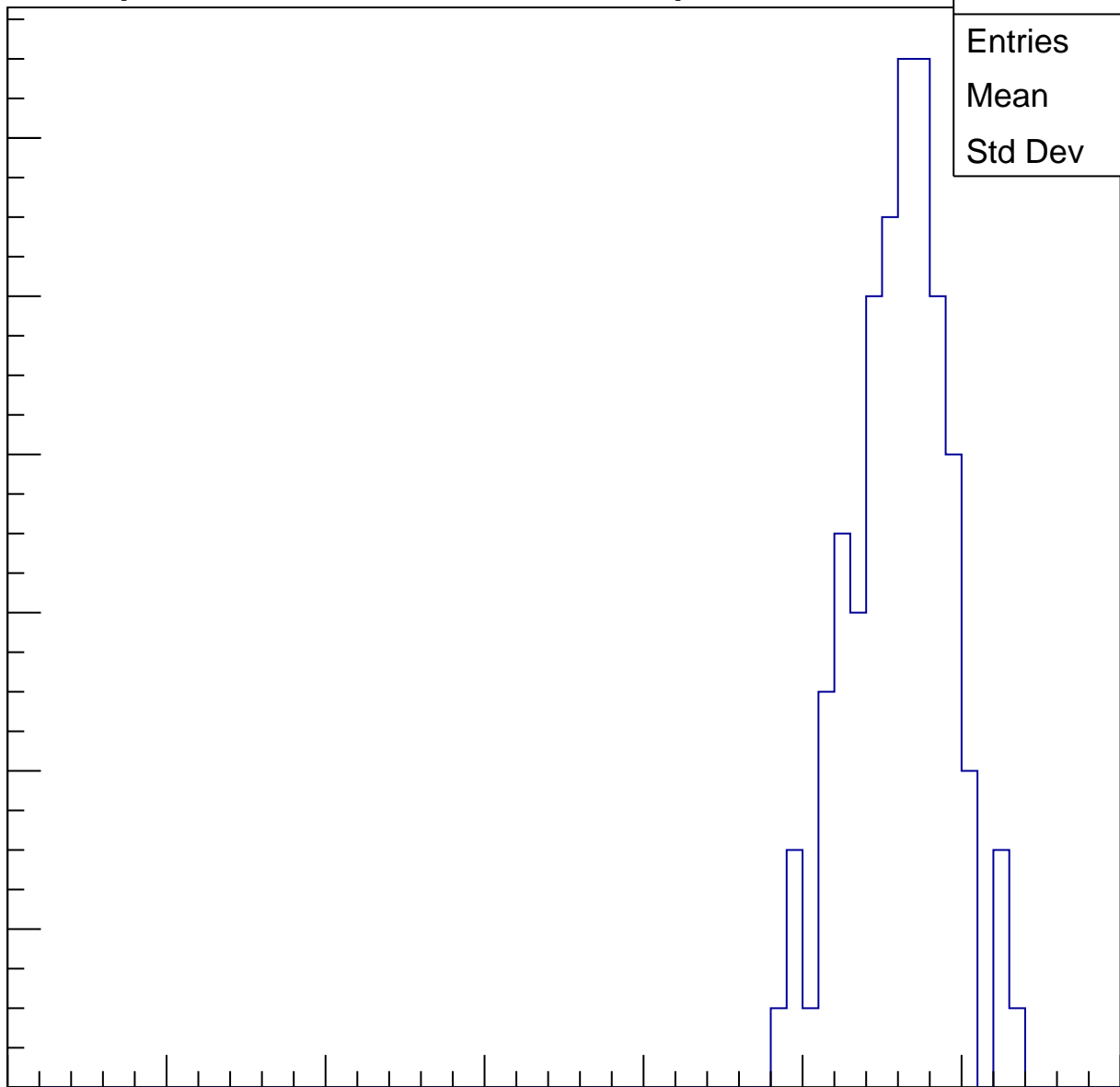
2

0

Entries	96
Mean	55.59
Std Dev	3.094

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch95, adc5

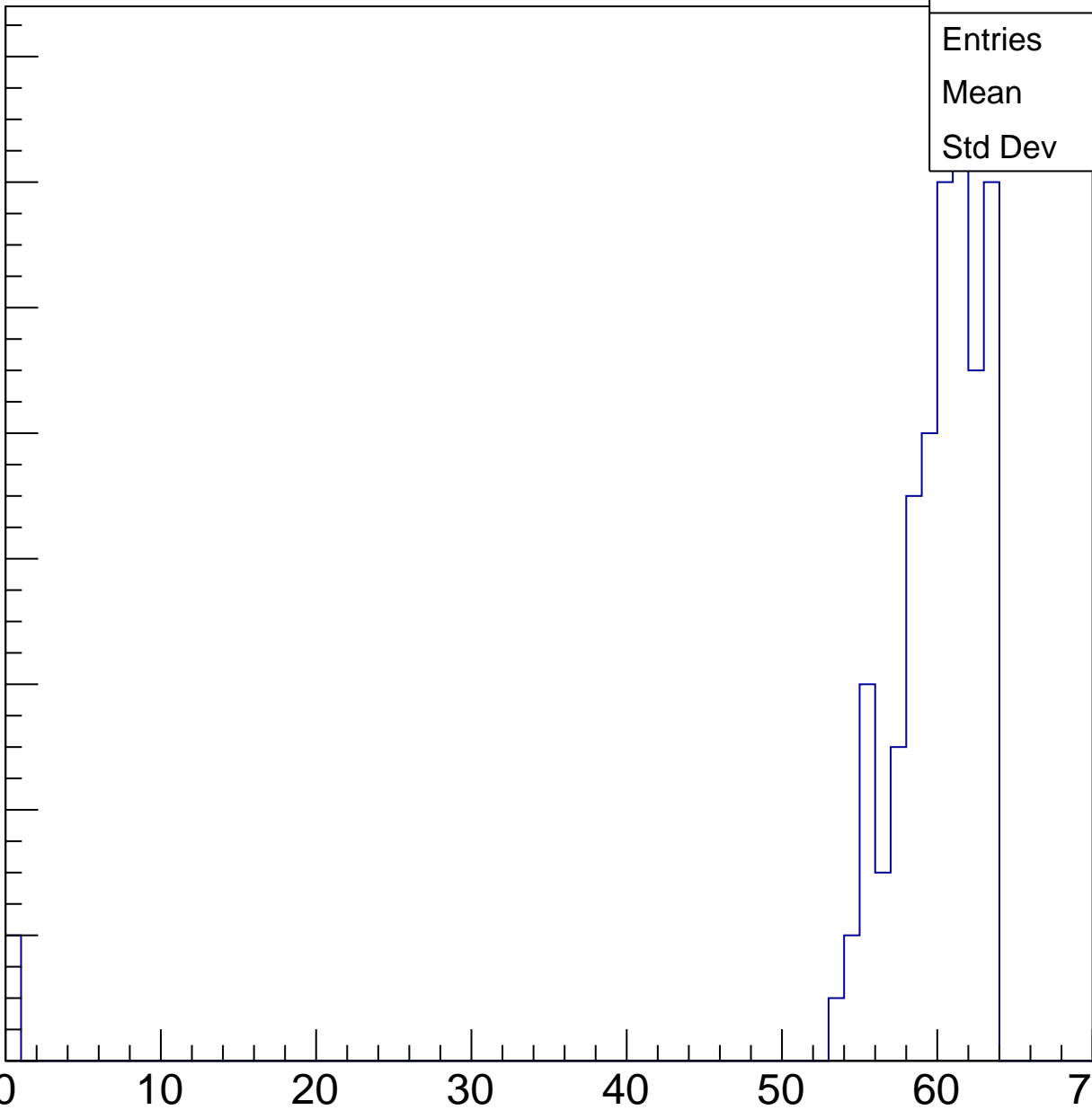
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	93
Mean	58.45
Std Dev	9.024

ampl

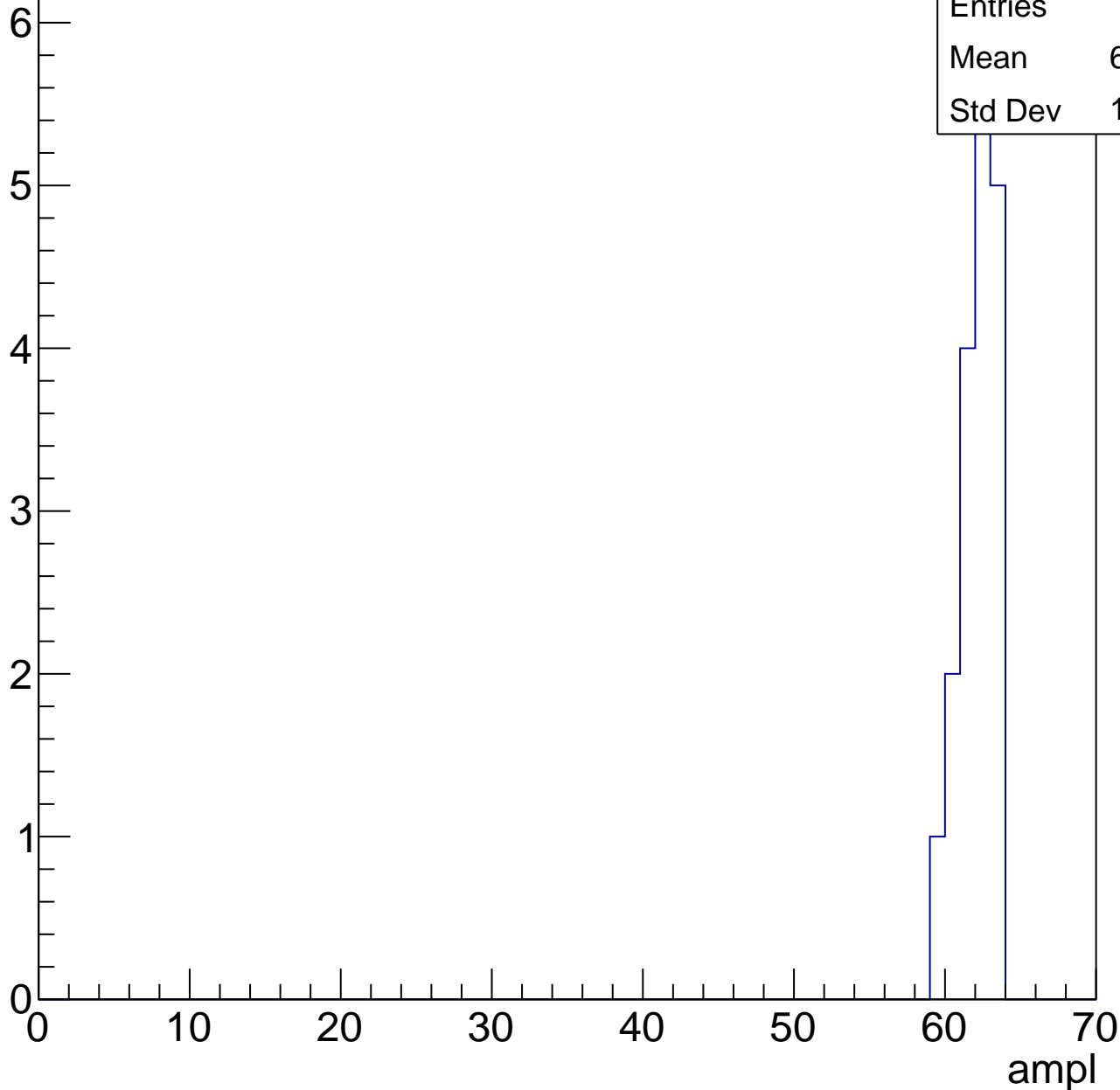


B1L001S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	18
Mean	61.67
Std Dev	1.155



B1L001S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



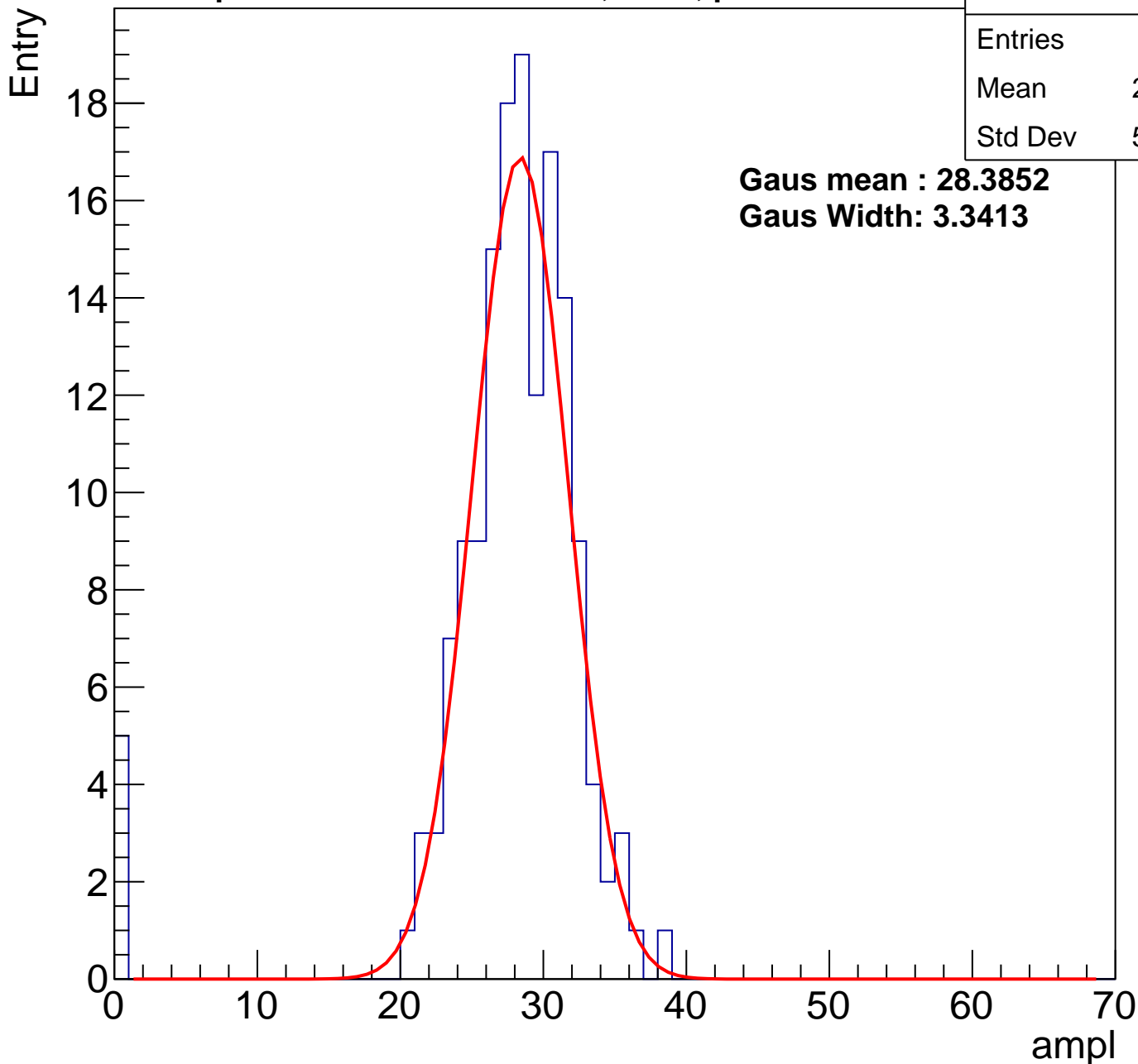
B1L001S, U14-ch96, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	27.09
Std Dev	5.974

Gaus mean : 28.3852

Gaus Width: 3.3413



B1L001S, U14-ch96, adc1

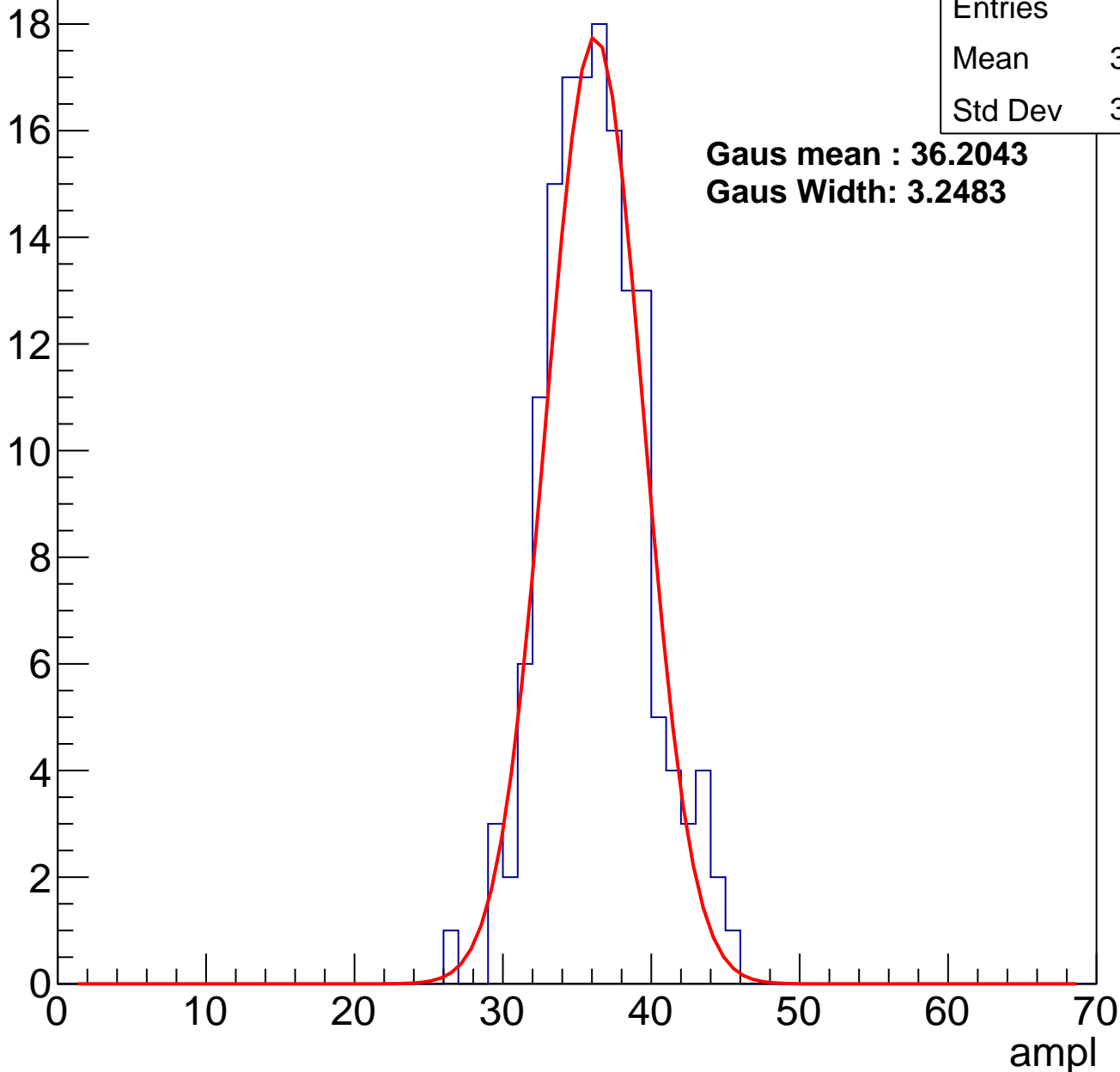
calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	35.86
Std Dev	3.395

Gaus mean : 36.2043

Gaus Width: 3.2483

Entry



B1L001S, U14-ch96, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	100
Mean	41.95
Std Dev	3.157

Gaus mean : 42.4030

Gaus Width: 3.0914

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

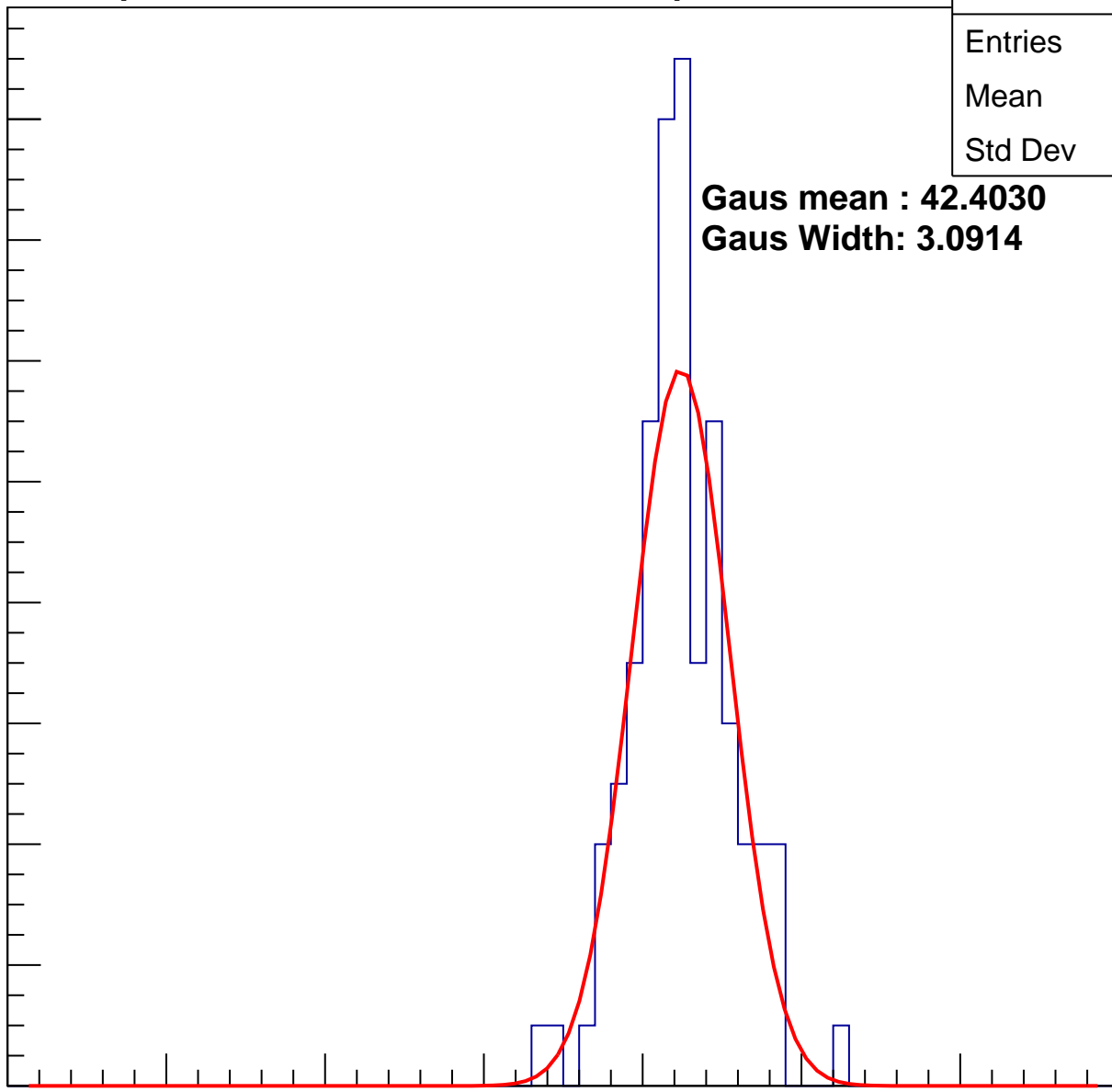
40

50

60

70

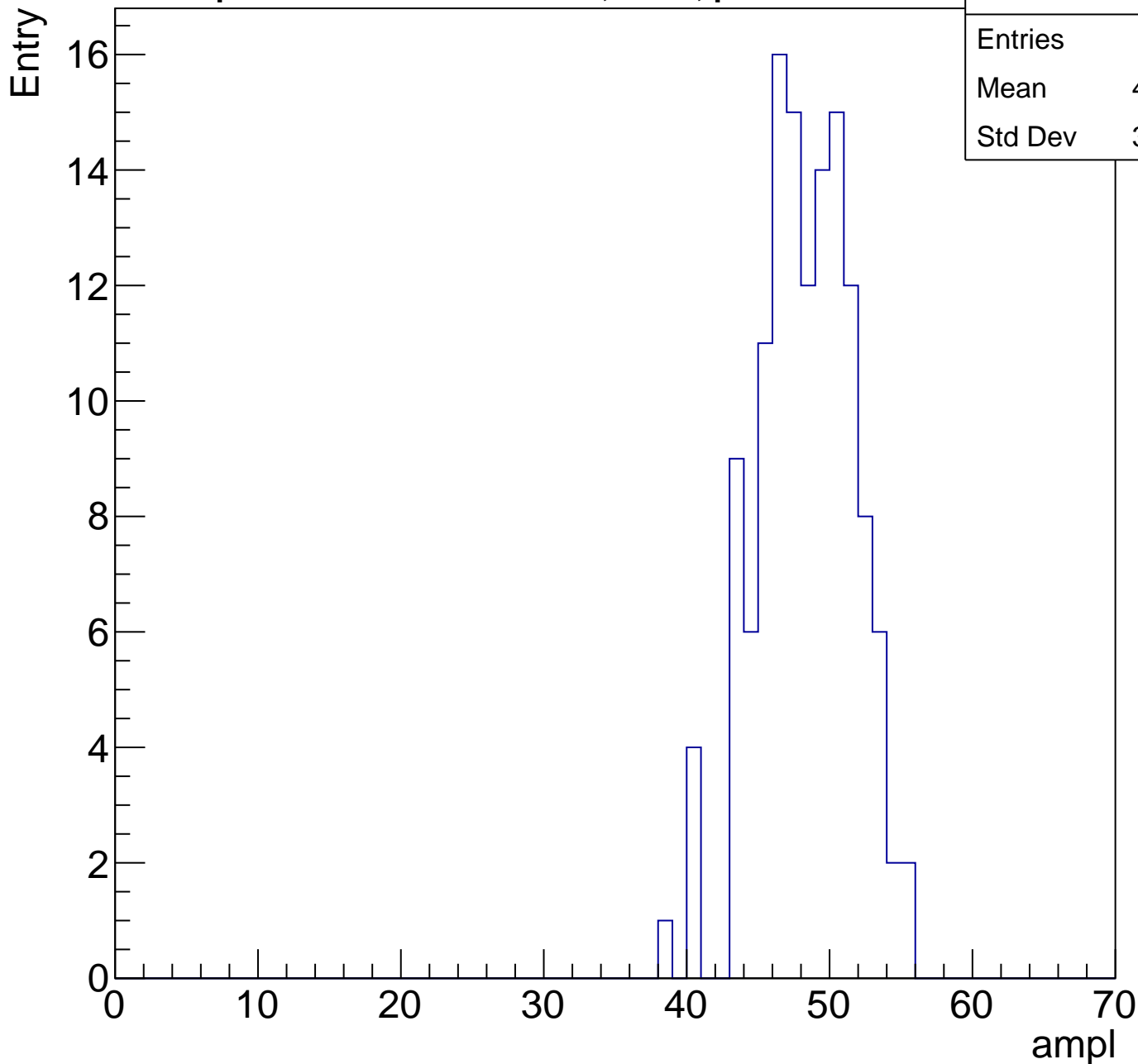
ampl



B1L001S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#2, port C2

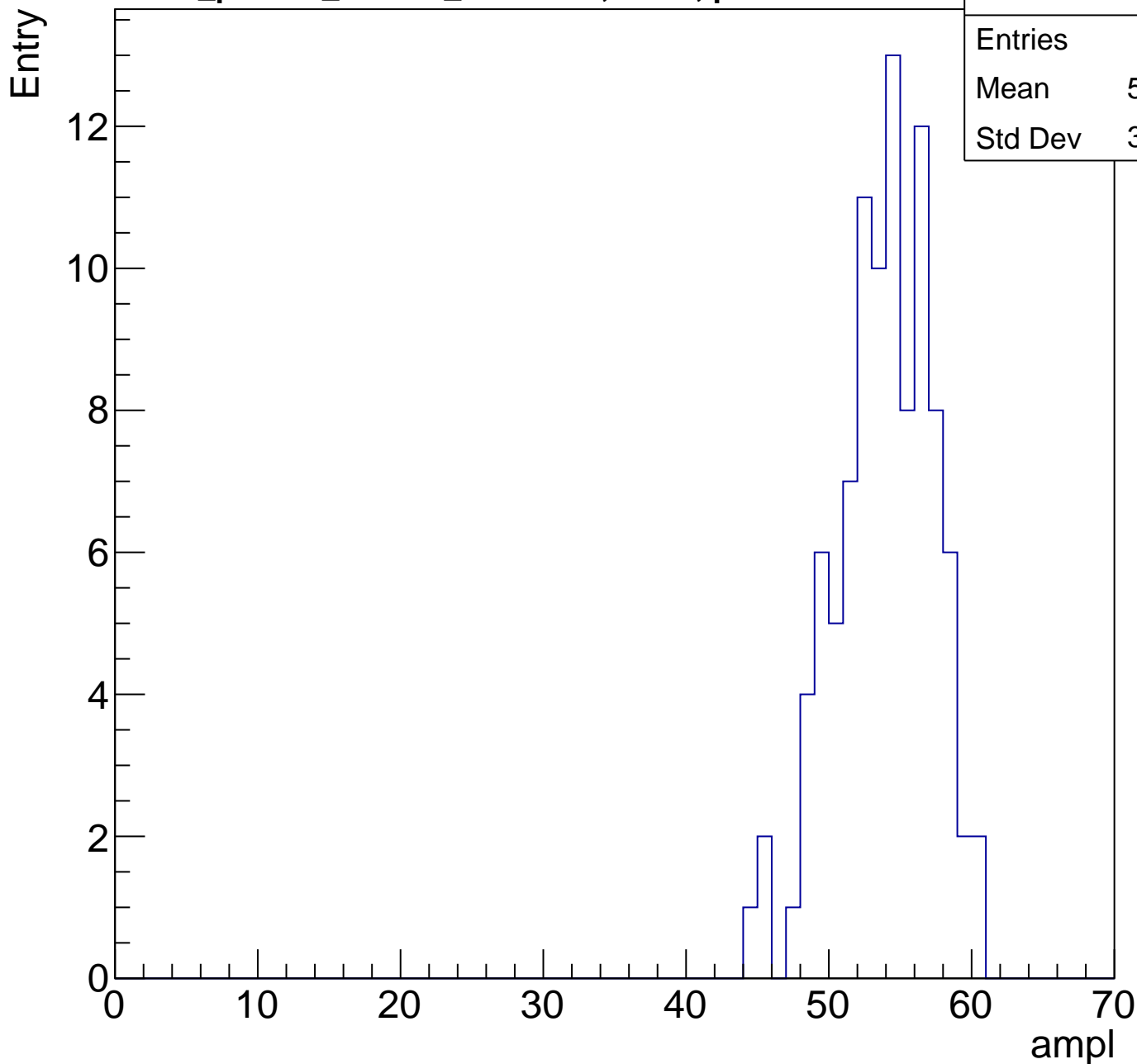
Entries	133
Mean	47.83
Std Dev	3.337



B1L001S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	98
Mean	53.39
Std Dev	3.367



B1L001S, U14-ch96, adc5

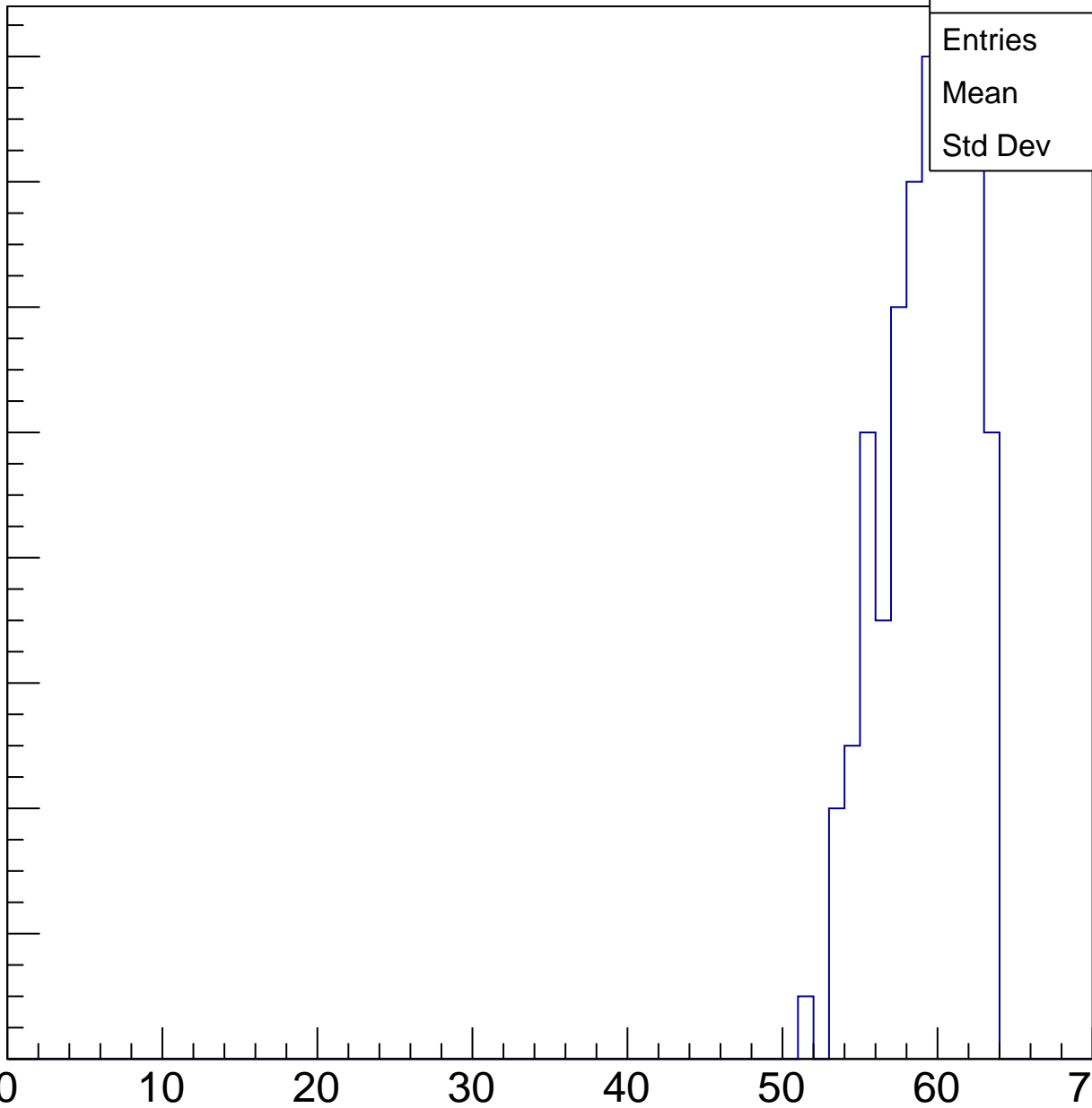
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	58.82
Std Dev	2.801

ampl

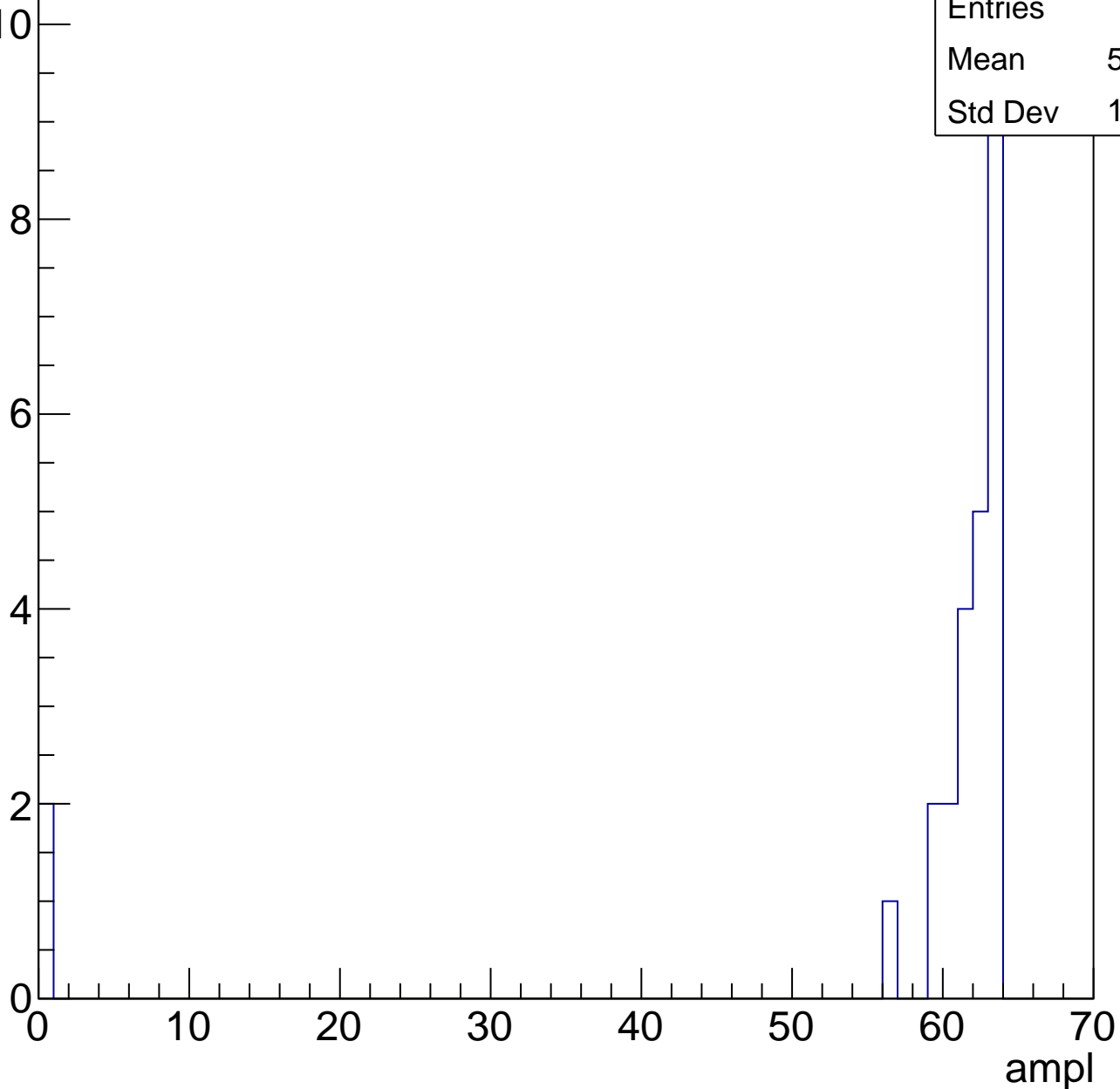


B1L001S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	26
Mean	56.85
Std Dev	16.49



B1L001S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

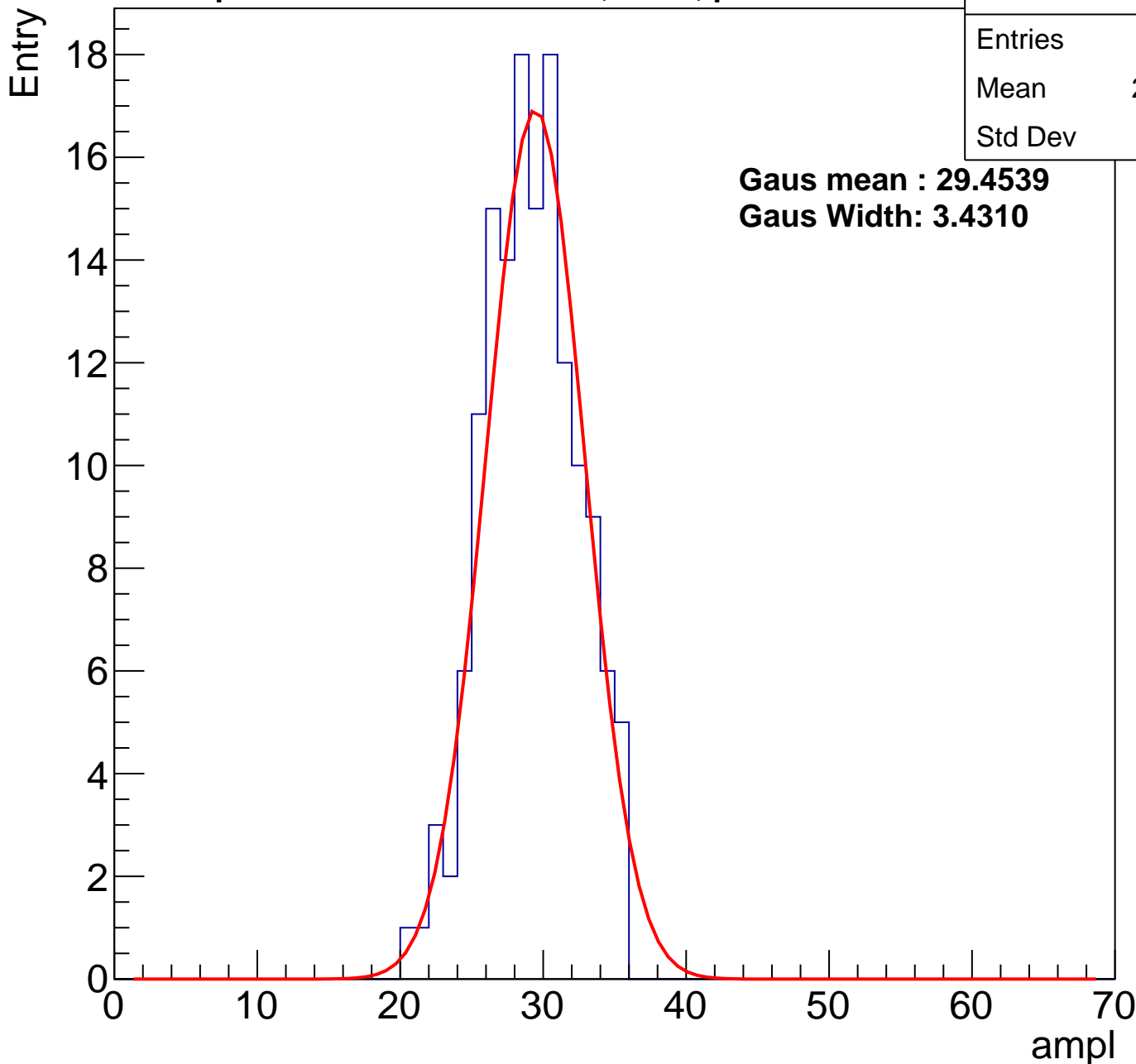
B1L001S, U14-ch97, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	28.68
Std Dev	3.22

Gaus mean : 29.4539

Gaus Width: 3.4310



B1L001S, U14-ch97, adc1

calib_packv5_042523_0143.root, FC#2, port C2

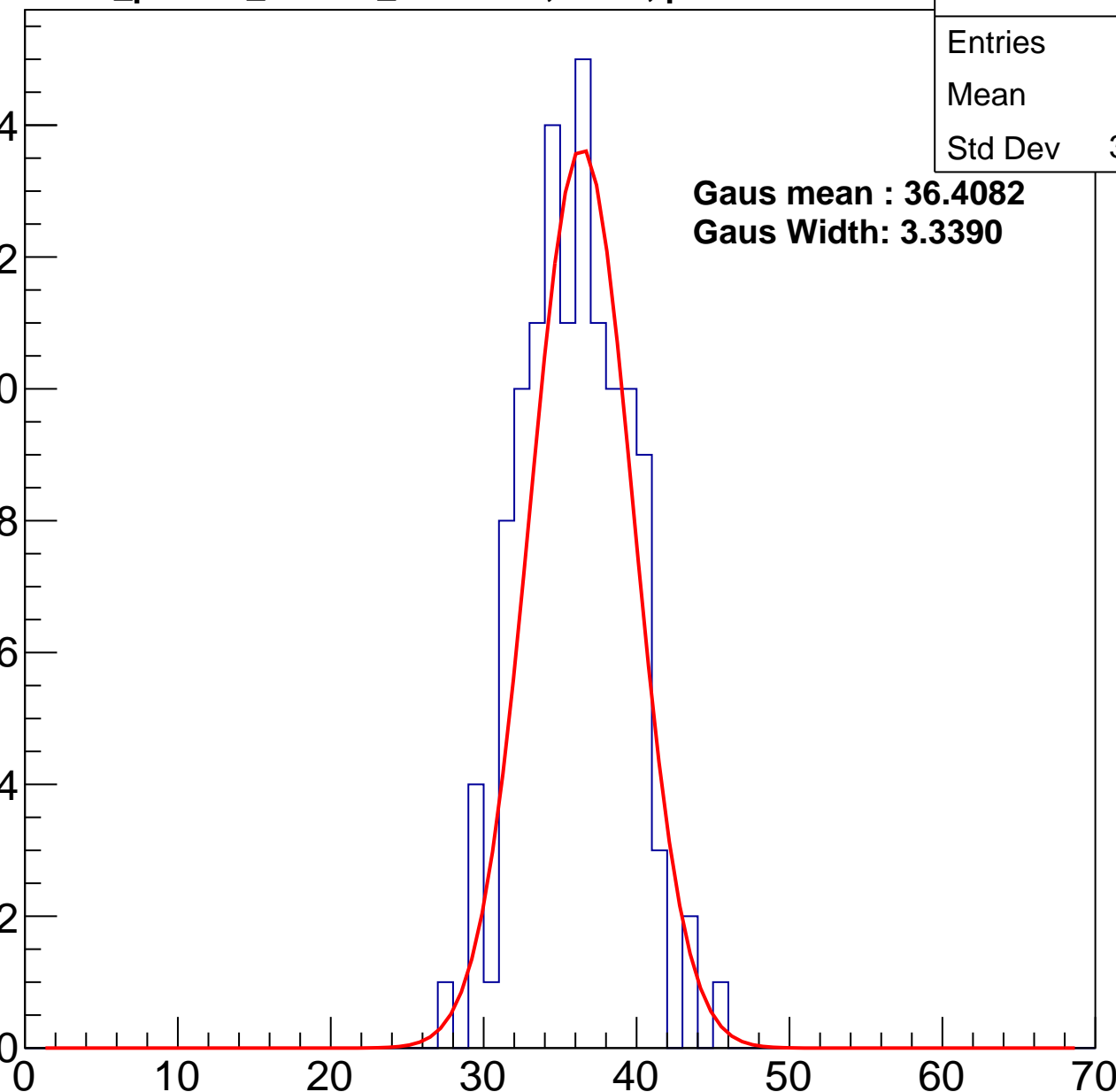
Entry

14
12
10
8
6
4
2
0

Entries	121
Mean	35.5
Std Dev	3.338

Gaus mean : 36.4082

Gaus Width: 3.3390



ampl

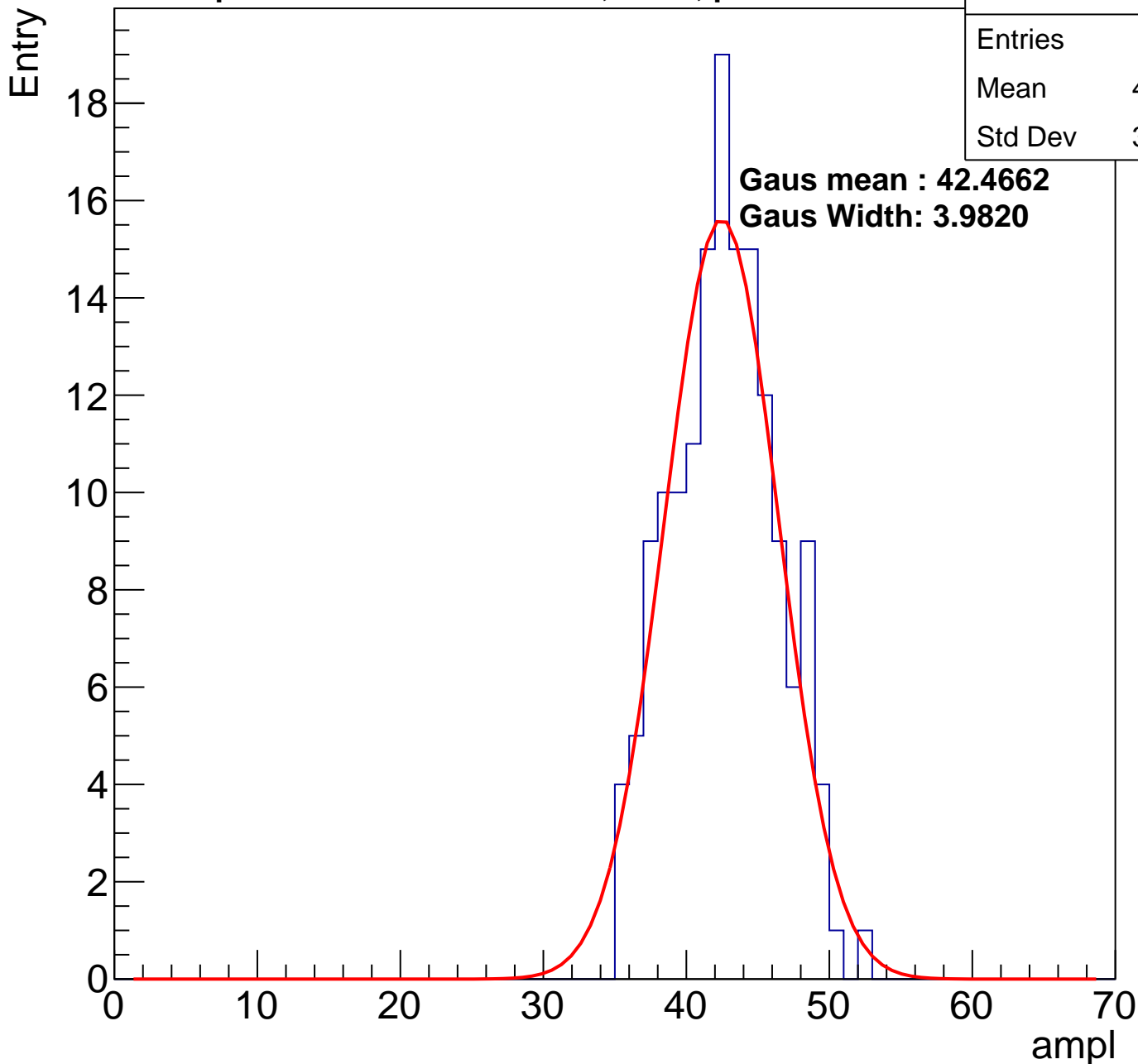
B1L001S, U14-ch97, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	42.24
Std Dev	3.648

Gaus mean : 42.4662

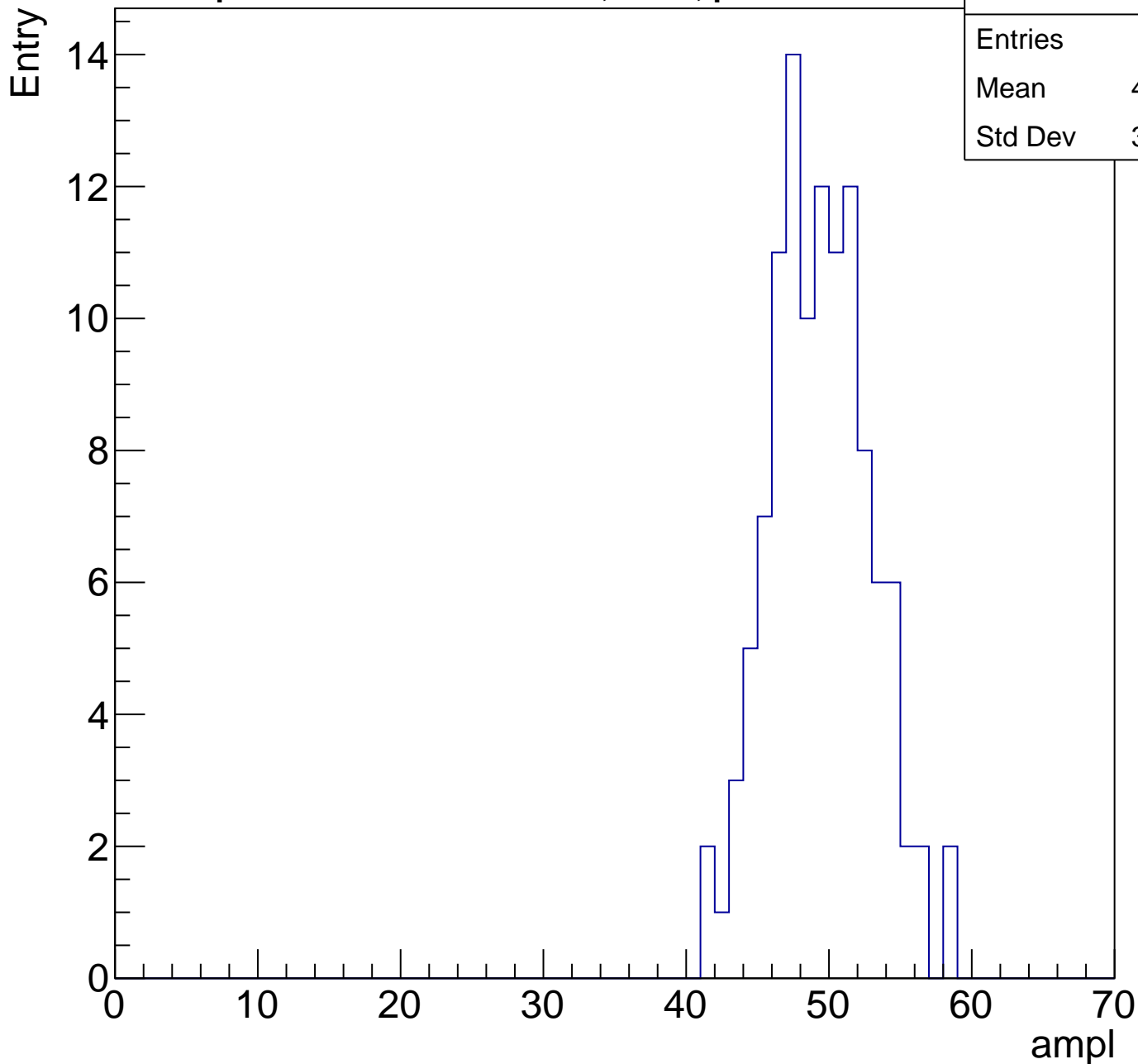
Gaus Width: 3.9820



B1L001S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	48.93
Std Dev	3.499

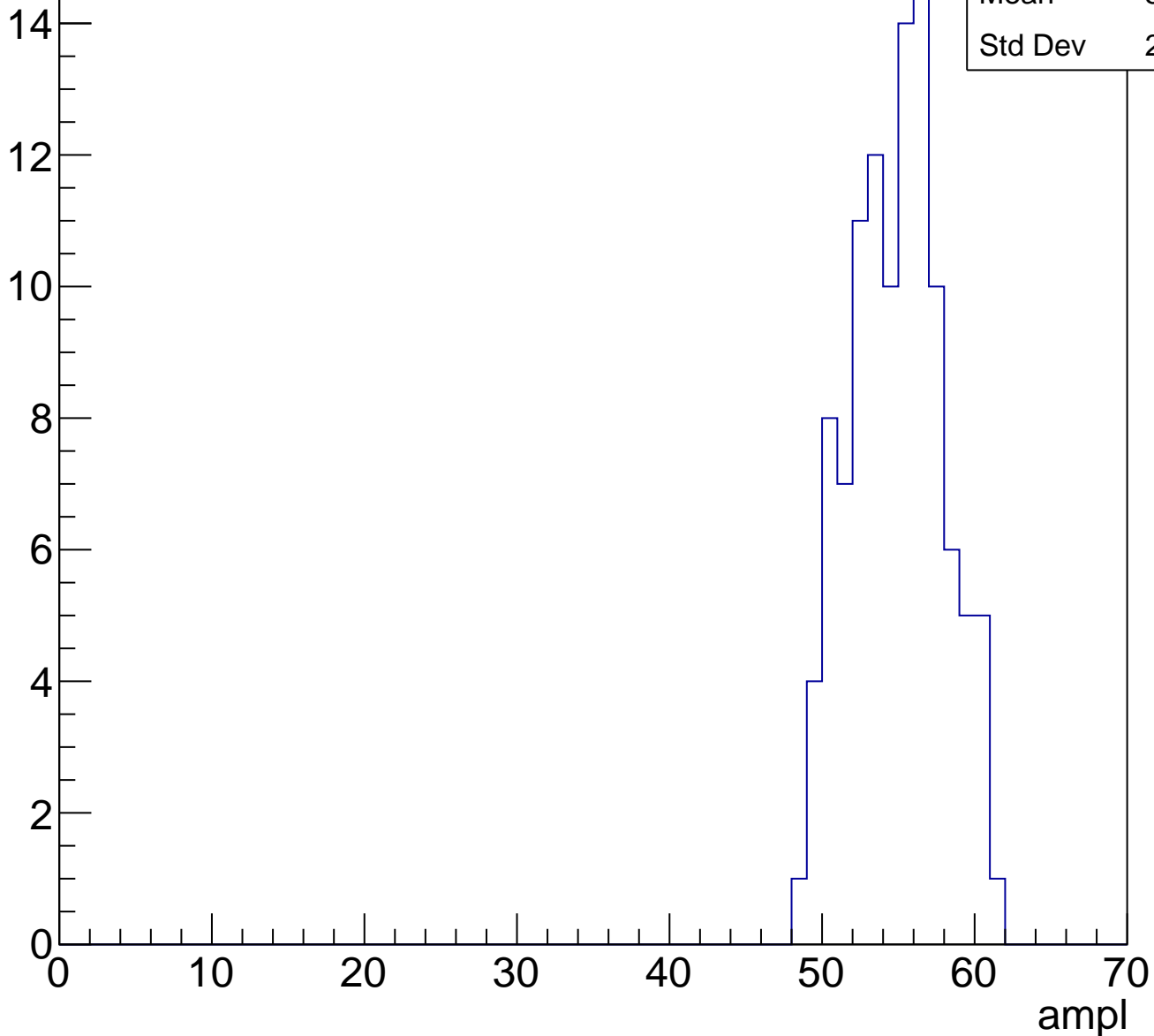


B1L001S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	54.43
Std Dev	2.993

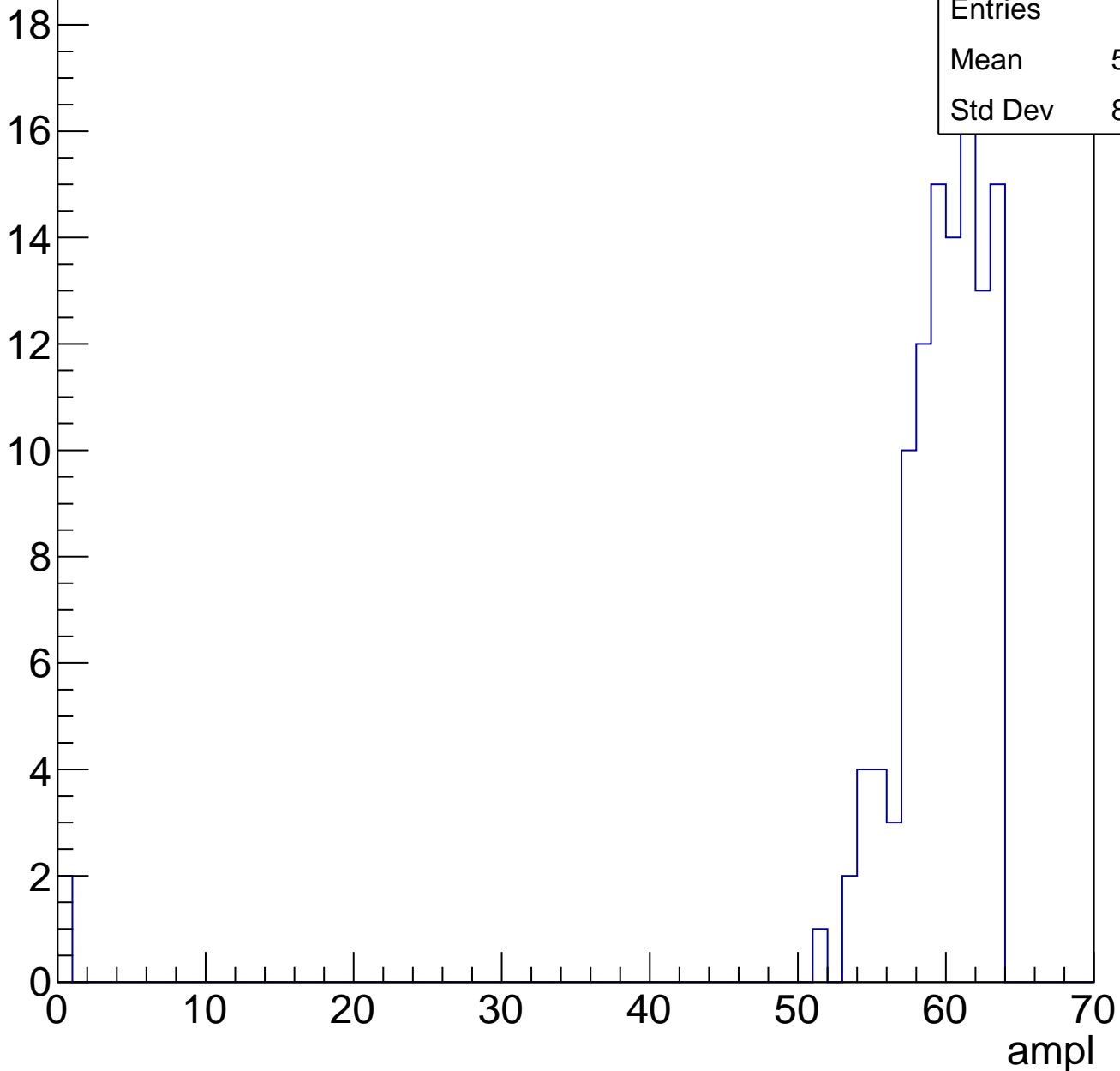
Entry



B1L001S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

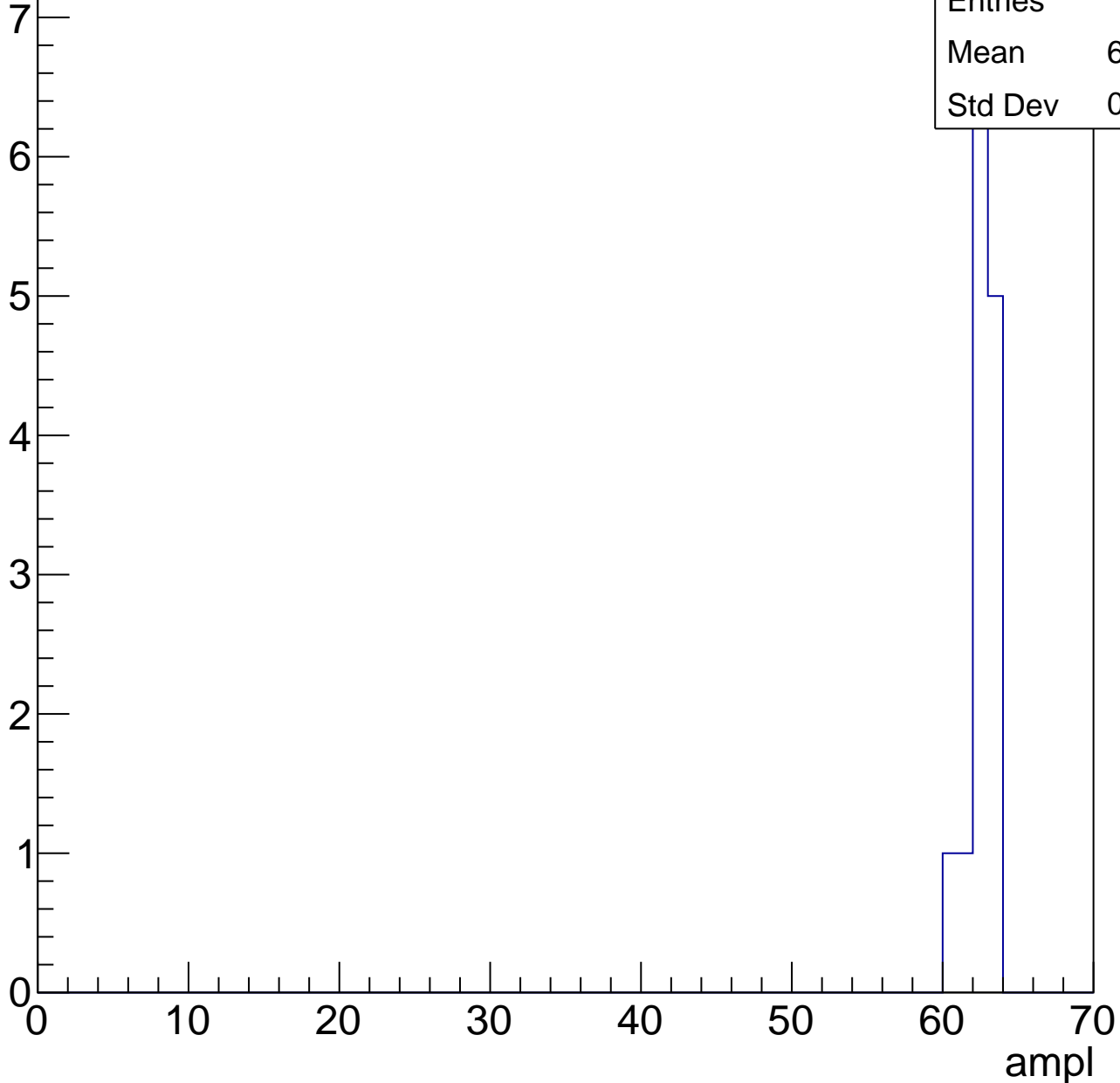


B1L001S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	62.14
Std Dev	0.833



B1L001S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch98, adc0

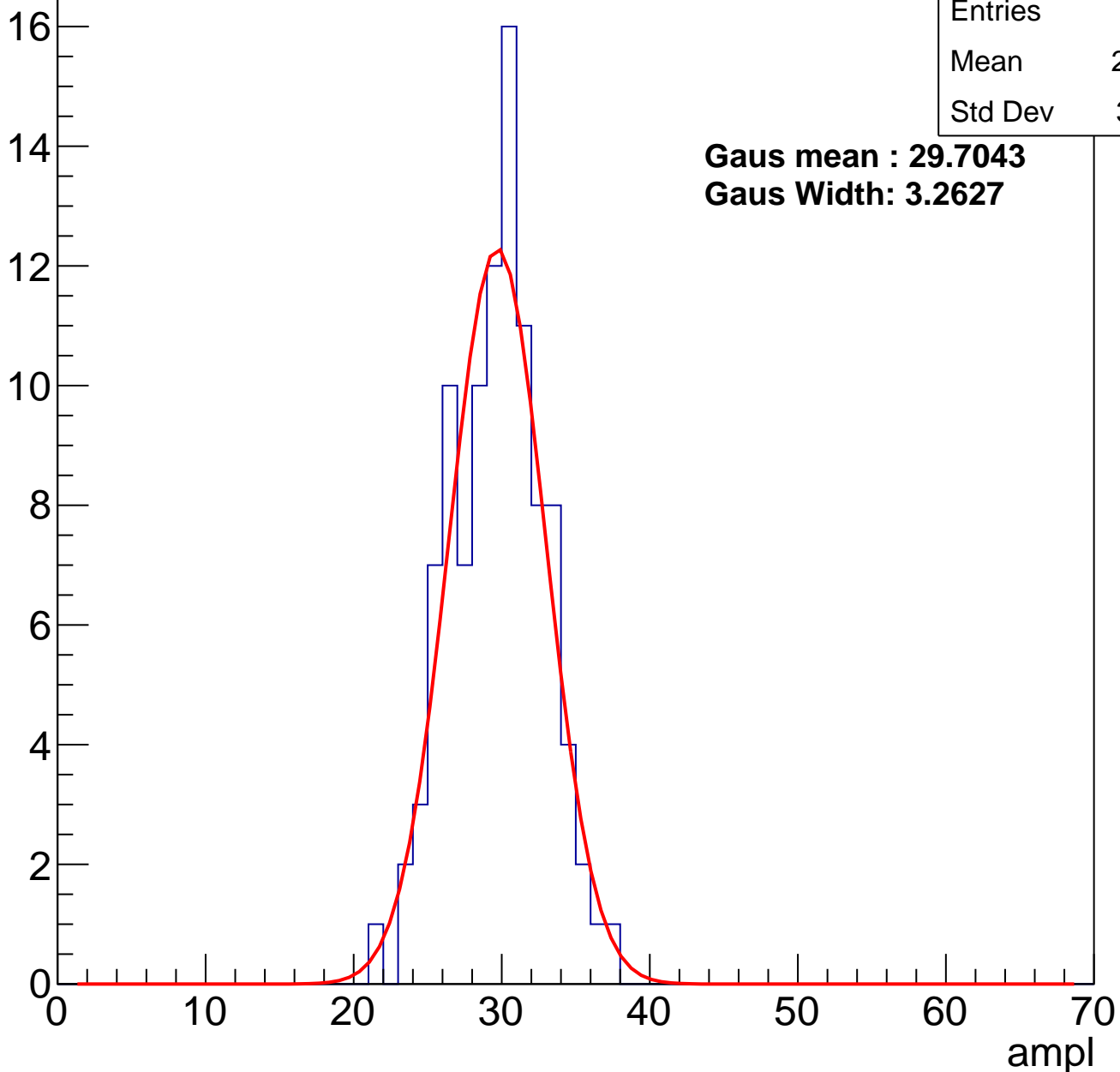
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	29.23
Std Dev	3.101

Gaus mean : 29.7043

Gaus Width: 3.2627

Entry



B1L001S, U14-ch98, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	35.7
Std Dev	3.421

Gaus mean : 36.3596

Gaus Width: 3.3678

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

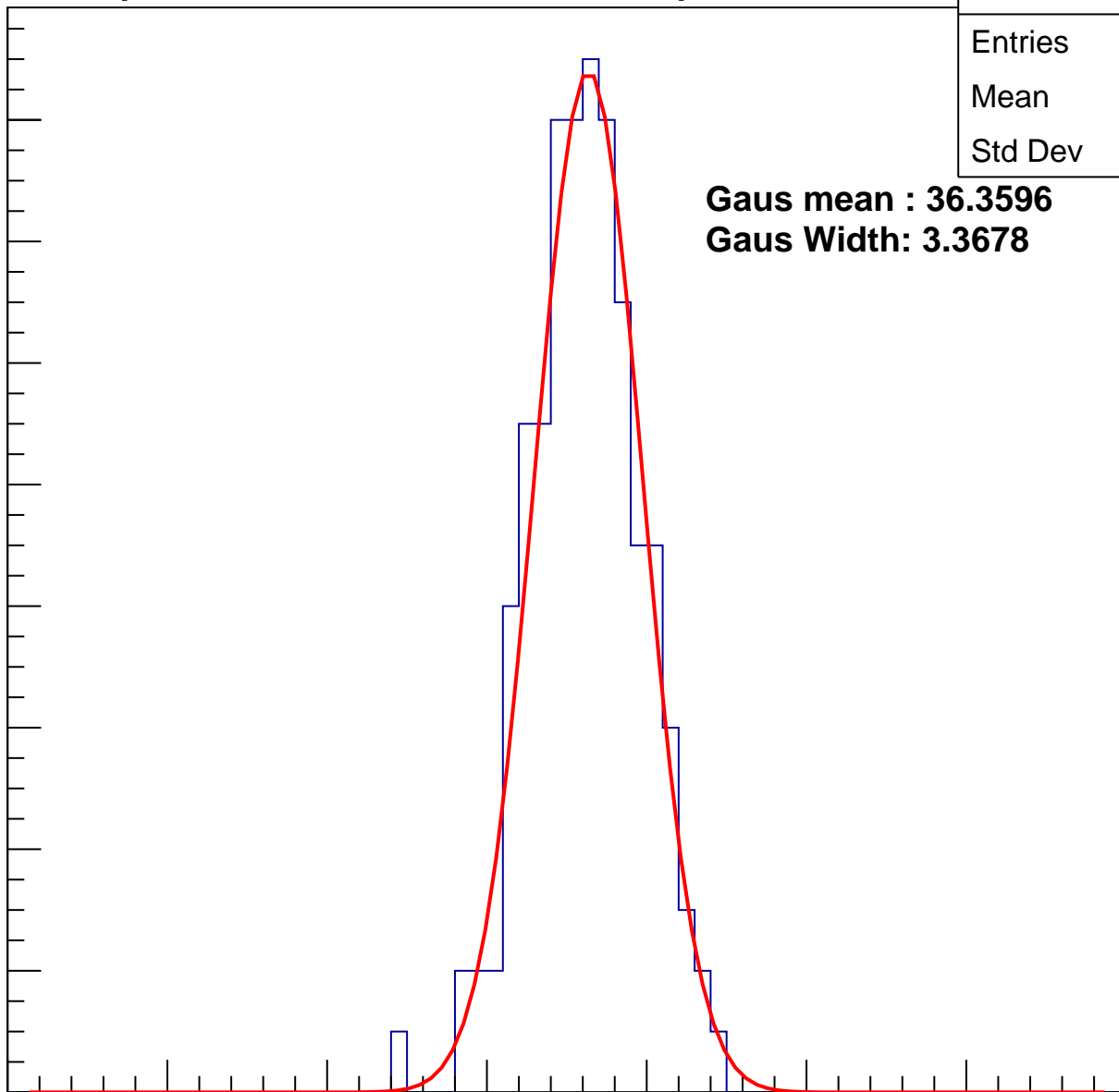
40

50

60

70

ampl



B1L001S, U14-ch98, adc2

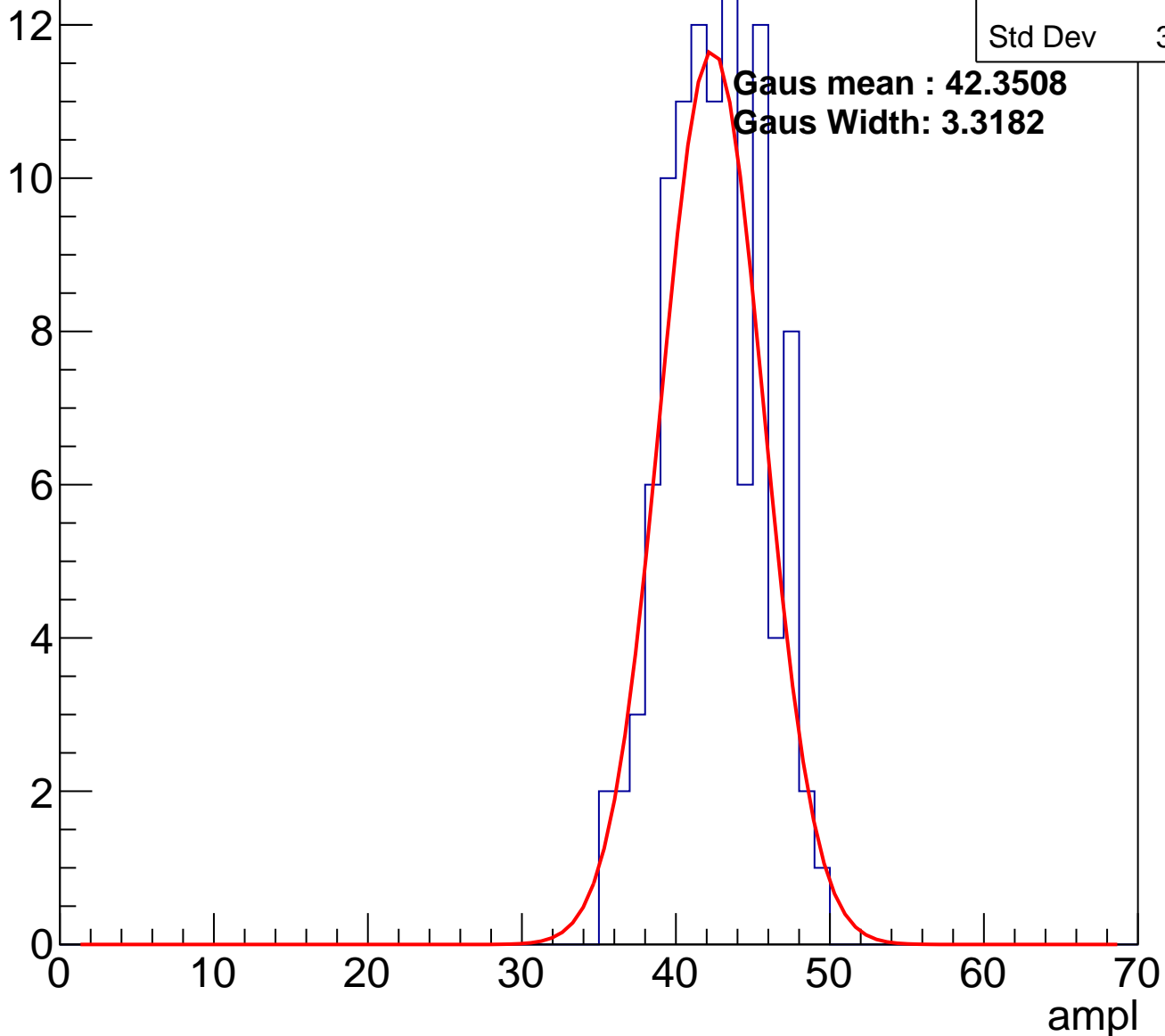
calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	42.07
Std Dev	3.142

Gaus mean : 42.3508

Gaus Width: 3.3182

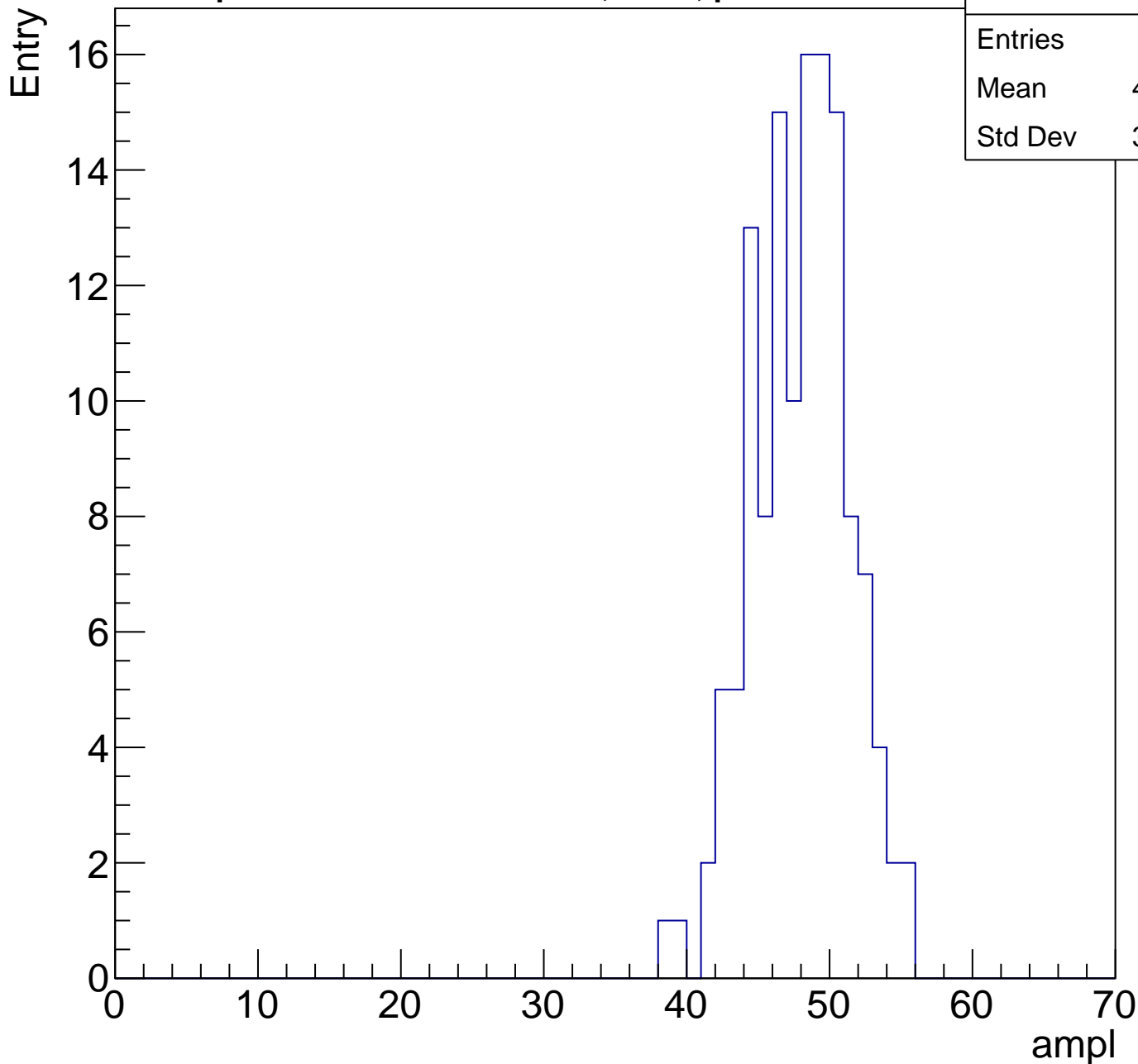
Entry



B1L001S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	47.54
Std Dev	3.324



B1L001S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

0

10

20

30

40

50

60

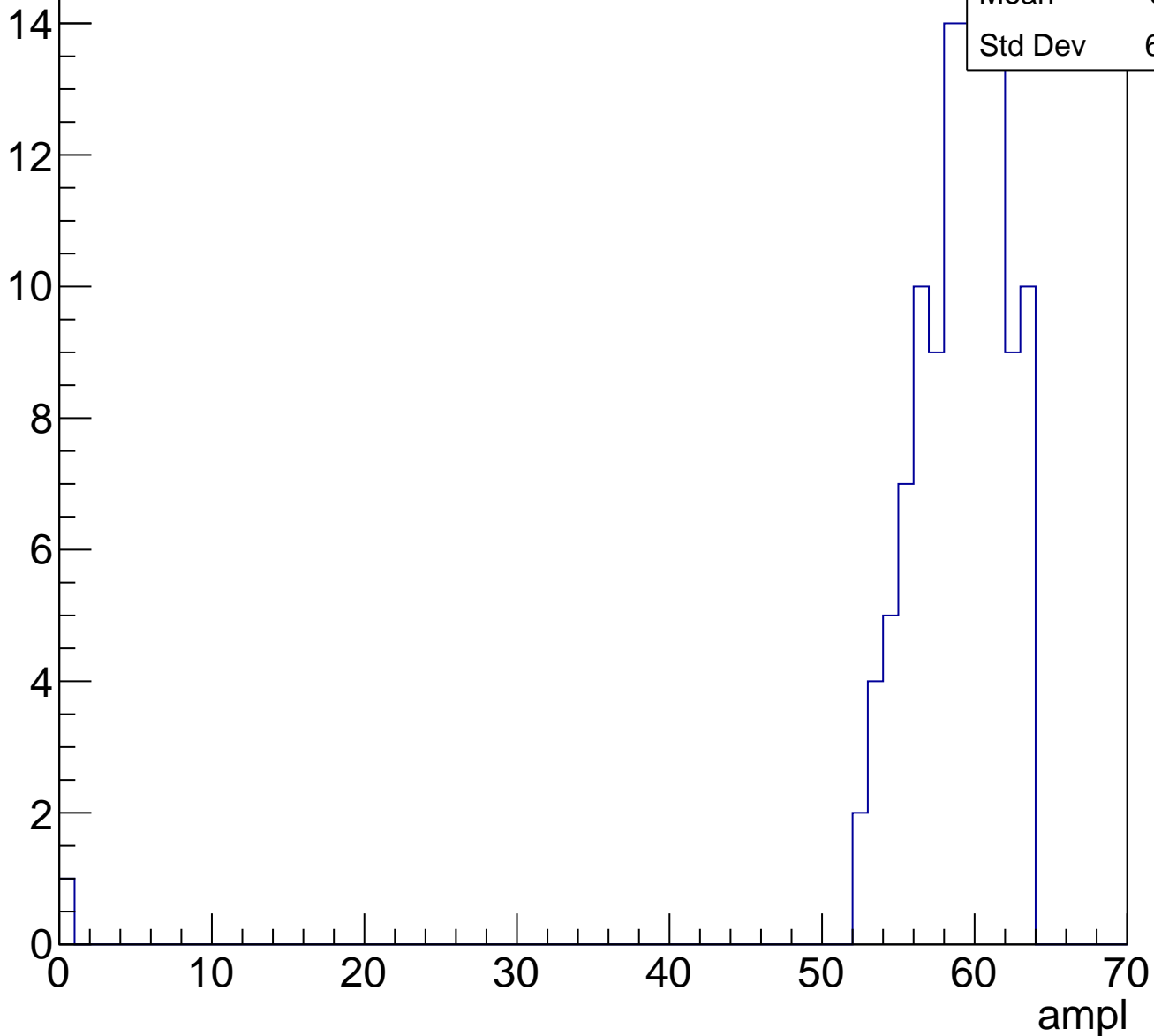
ampl

Entries	101
Mean	53.5
Std Dev	3.126

B1L001S, U14-ch98, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries	38
Mean	59.63
Std Dev	9.972

B1L001S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch99, adc0

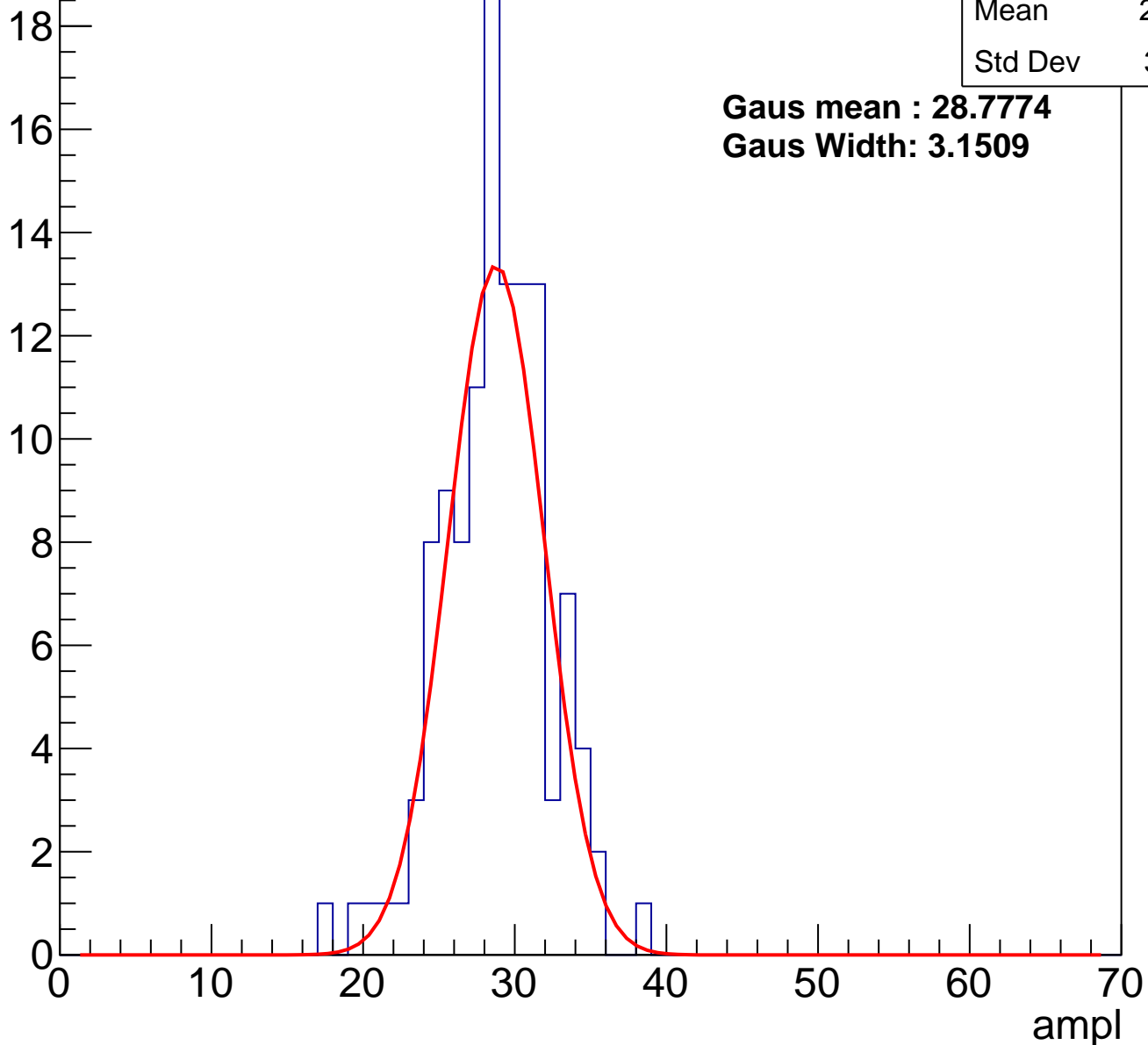
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	28.26
Std Dev	3.451

Gaus mean : 28.7774

Gaus Width: 3.1509

Entry



B1L001S, U14-ch99, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

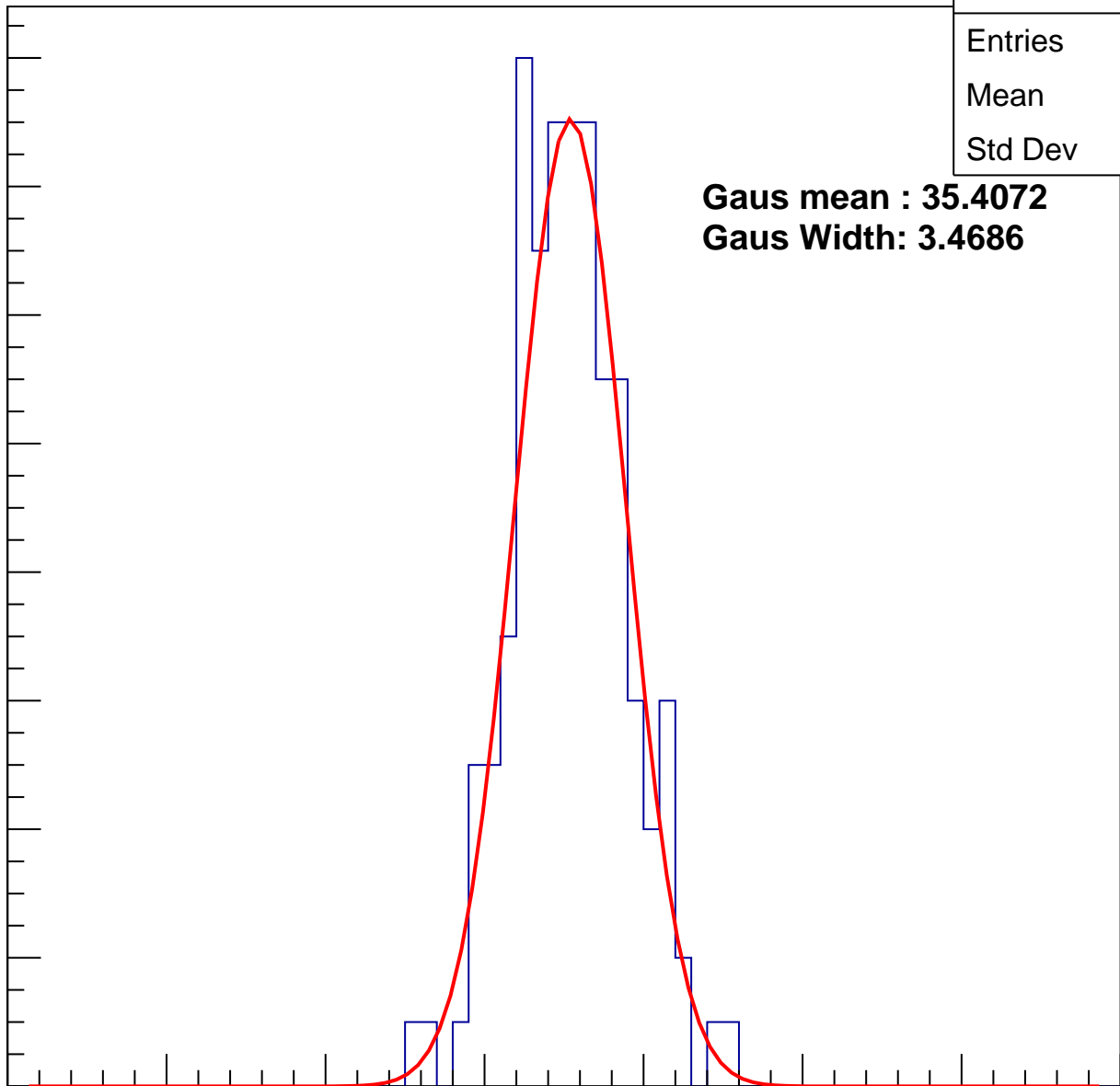
Entries	136
Mean	34.89
Std Dev	3.541

Gaus mean : 35.4072

Gaus Width: 3.4686

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch99, adc2

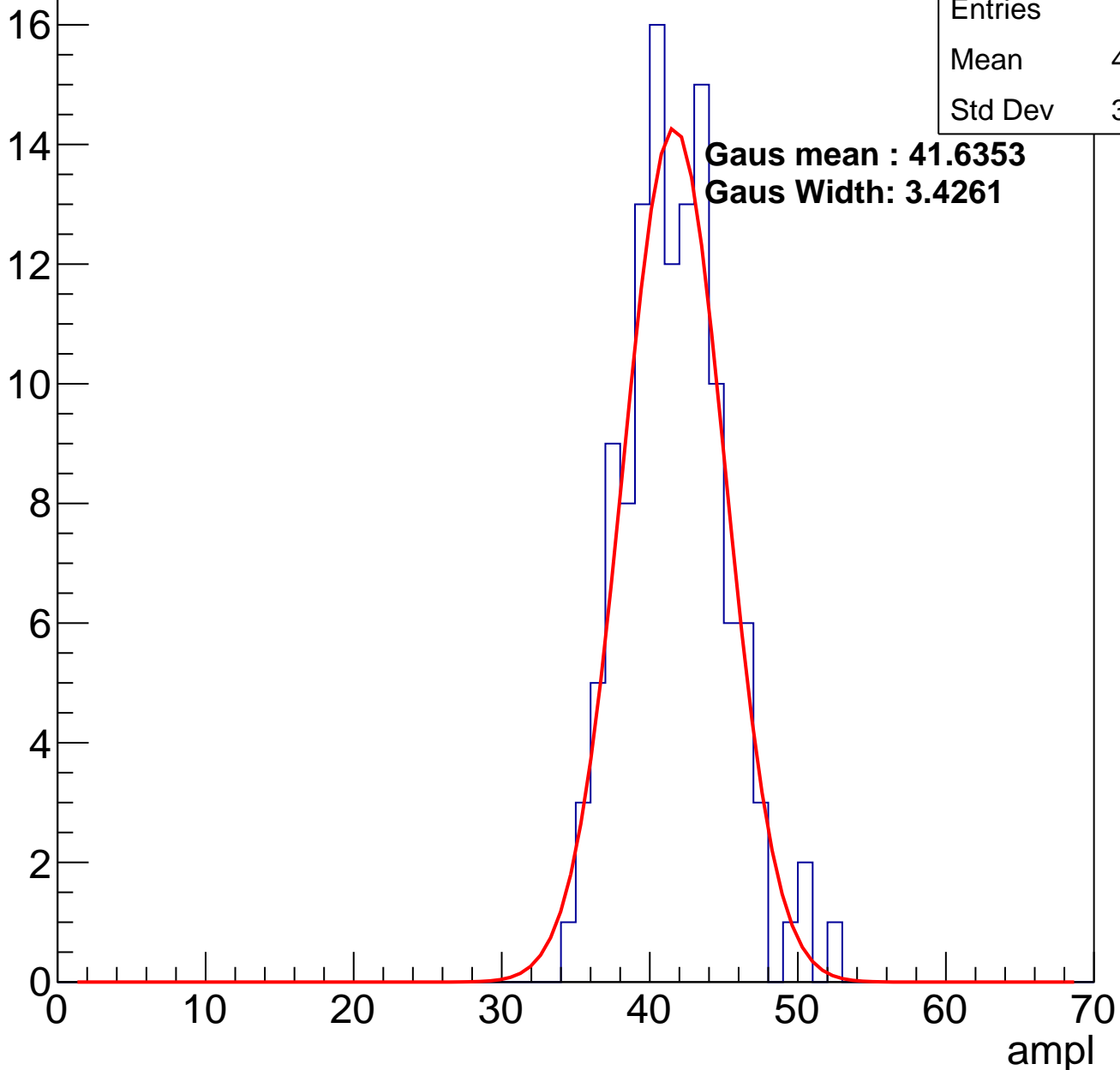
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	41.24
Std Dev	3.387

Gaus mean : 41.6353

Gaus Width: 3.4261

Entry



B1L001S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#2, port C2

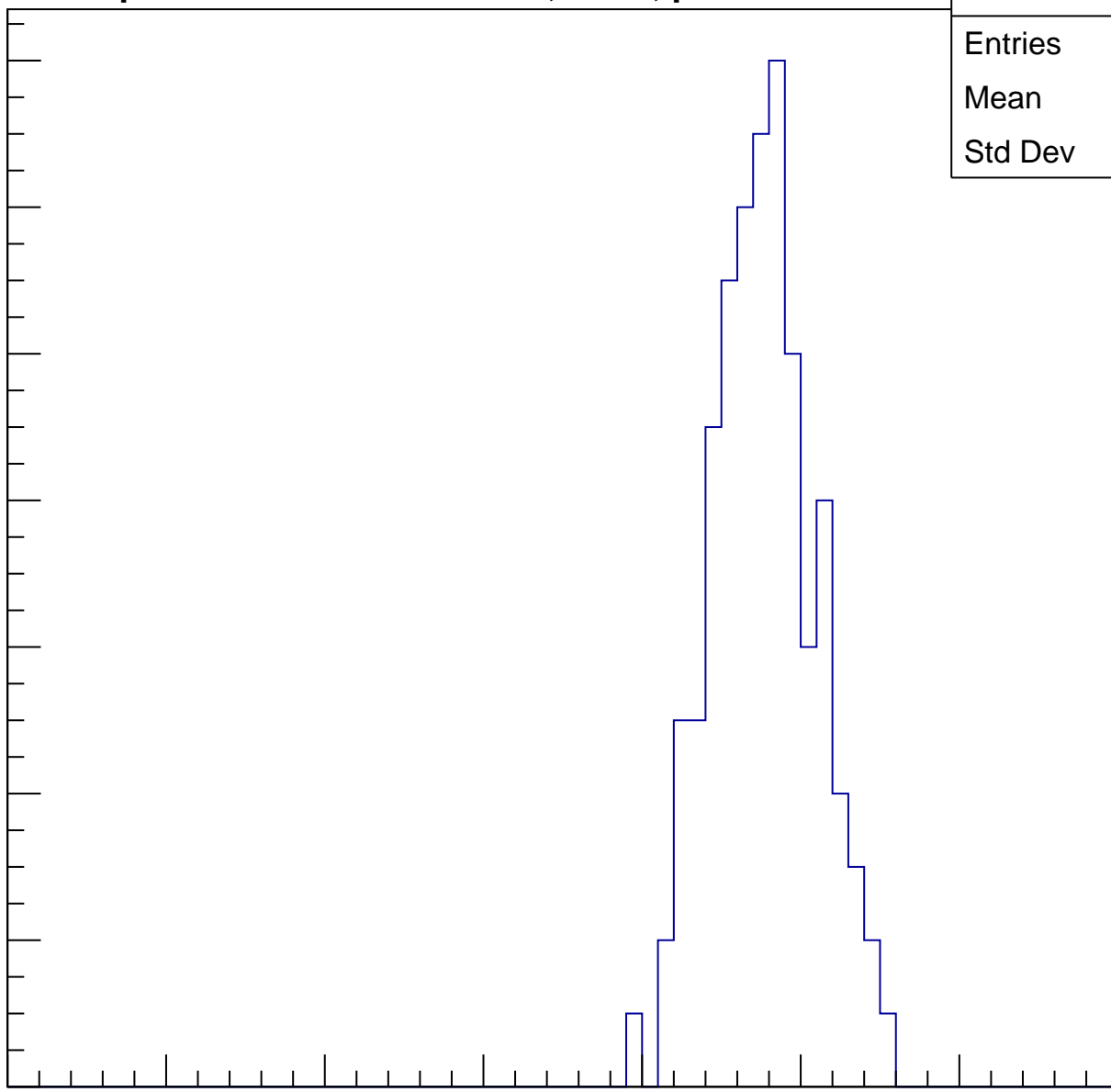
Entries	106
Mean	47.17
Std Dev	3.193

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

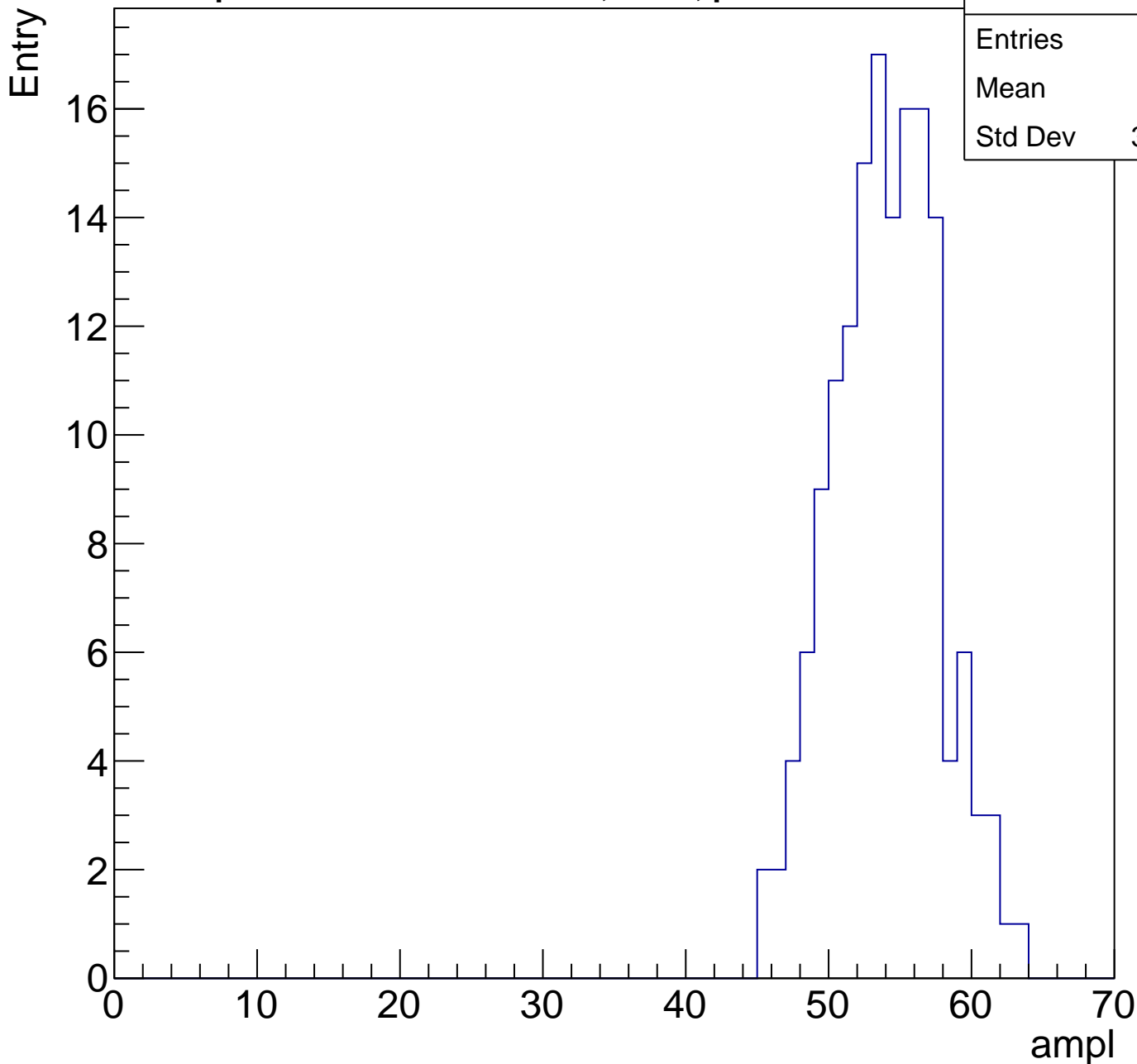
ampl



B1L001S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	156
Mean	53.5
Std Dev	3.642



B1L001S, U14-ch99, adc5

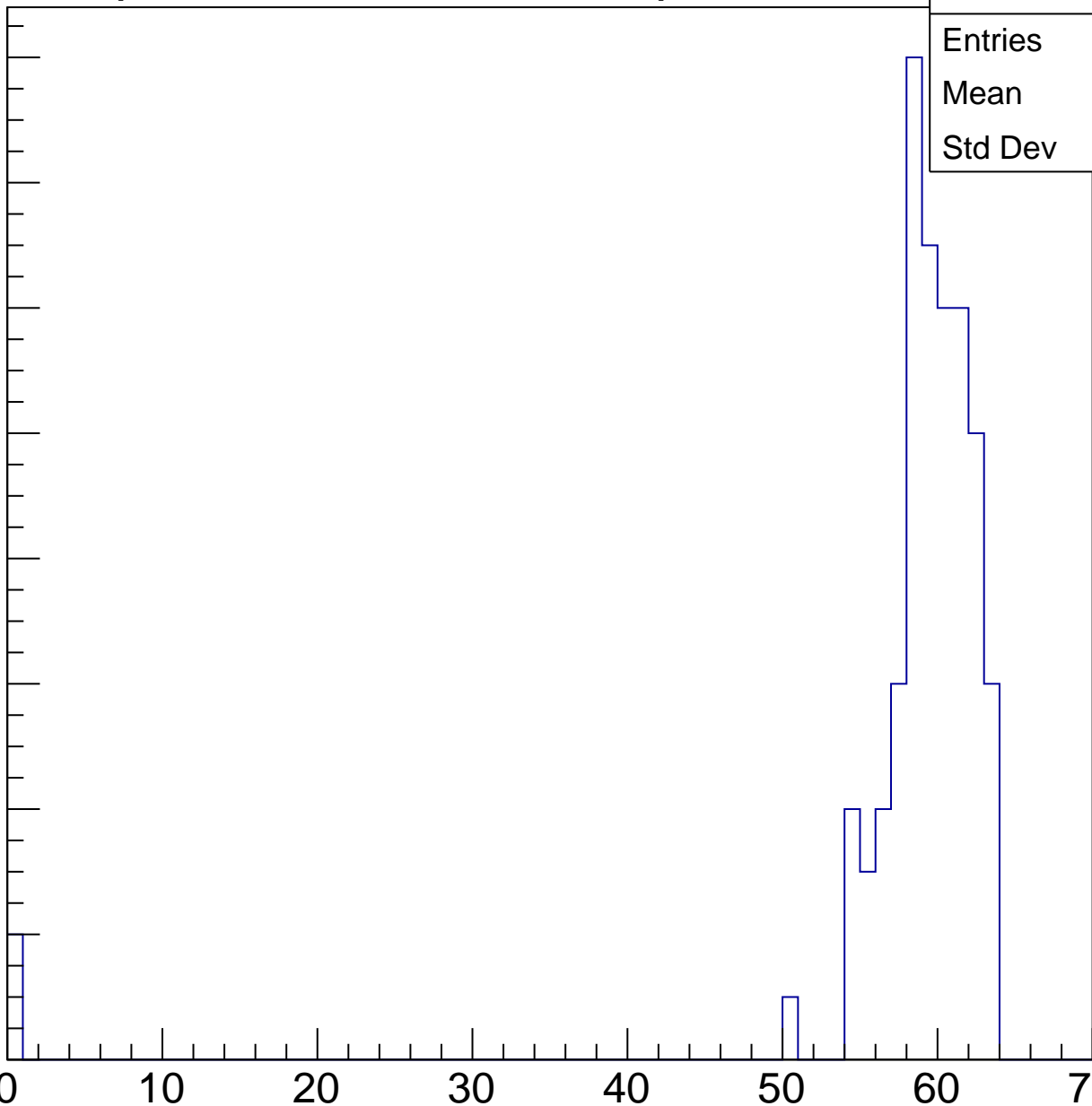
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	89
Mean	57.78
Std Dev	9.106

ampl



B1L001S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

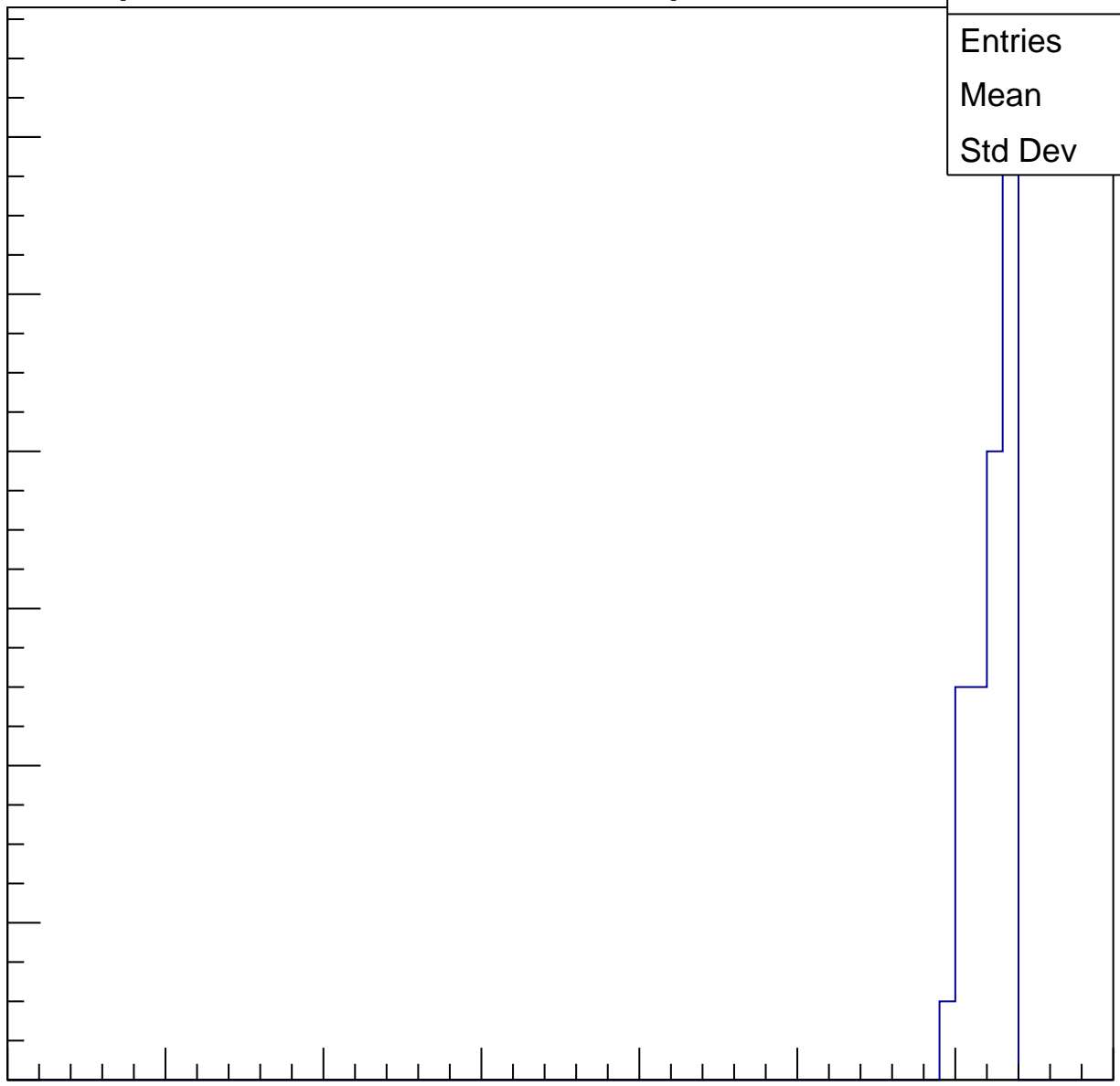
2

0

Entries	32
Mean	61.84
Std Dev	1.202

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch100, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	146
Mean	28.42
Std Dev	4.046

Gaus mean : 29.1915

Gaus Width: 3.4186

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

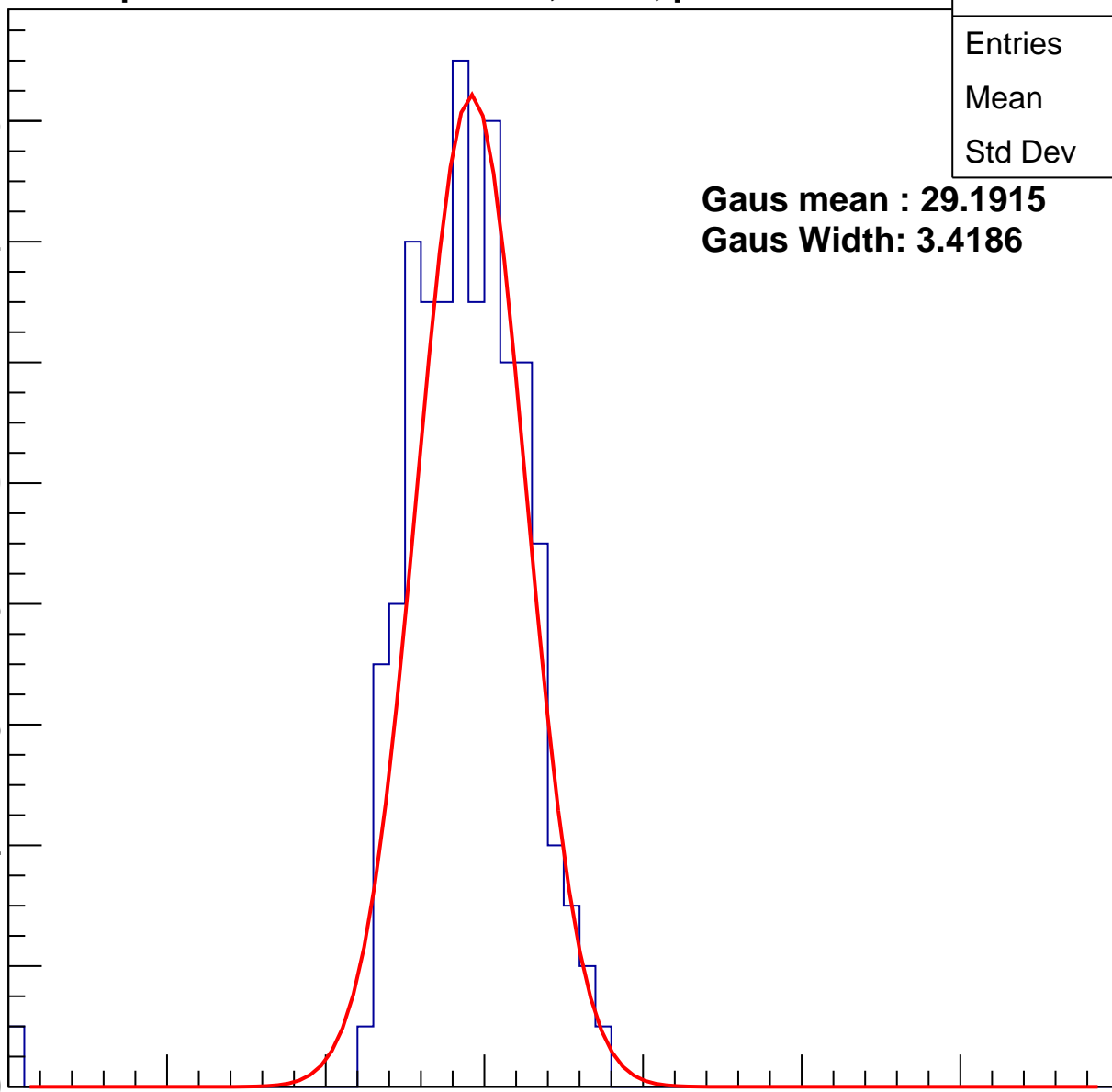
40

50

60

70

ampl



B1L001S, U14-ch100, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	35.13
Std Dev	3.353

Gaus mean : 35.3996

Gaus Width: 3.1579

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch100, adc2

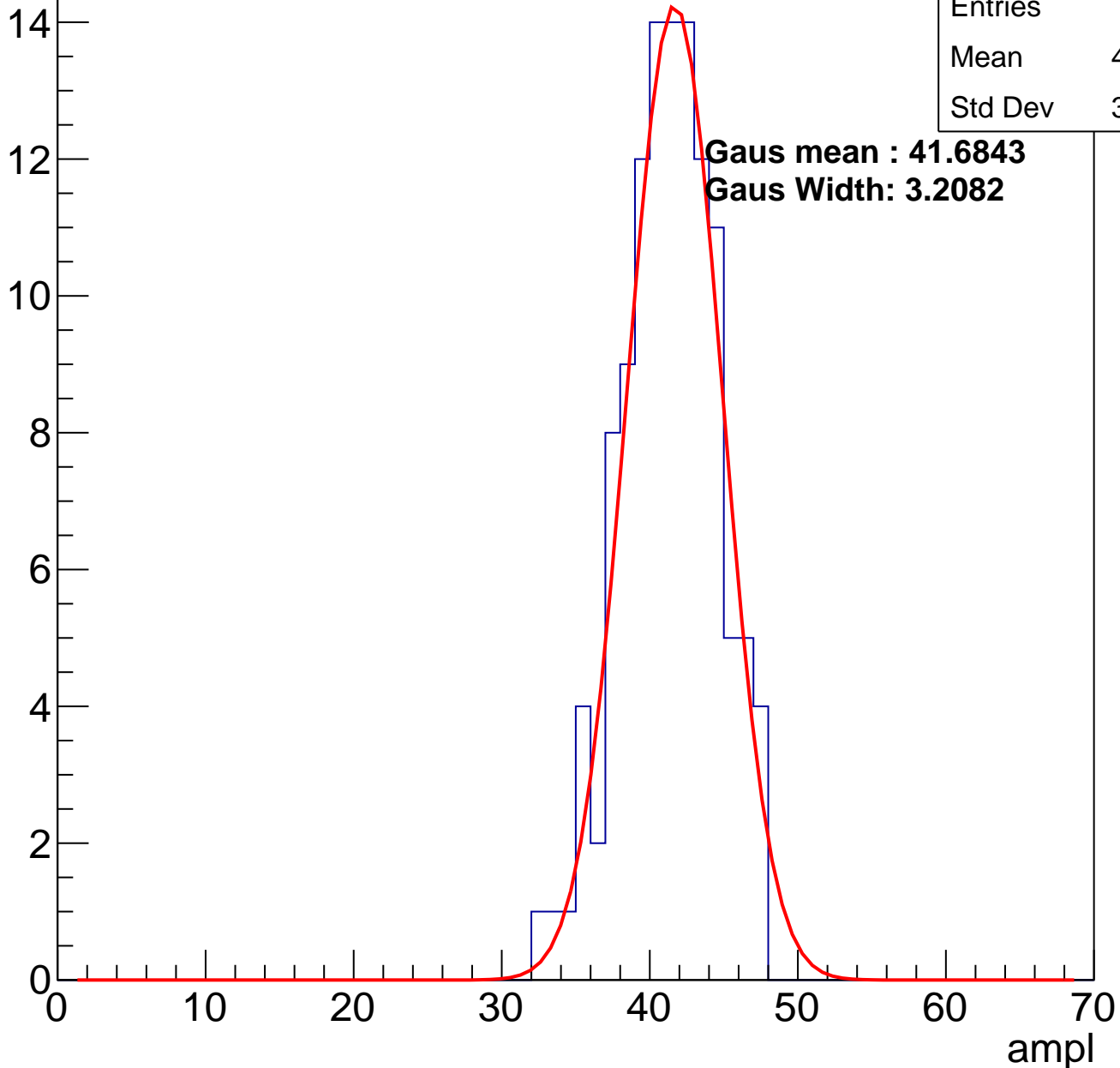
calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	40.87
Std Dev	3.158

Gaus mean : 41.6843

Gaus Width: 3.2082

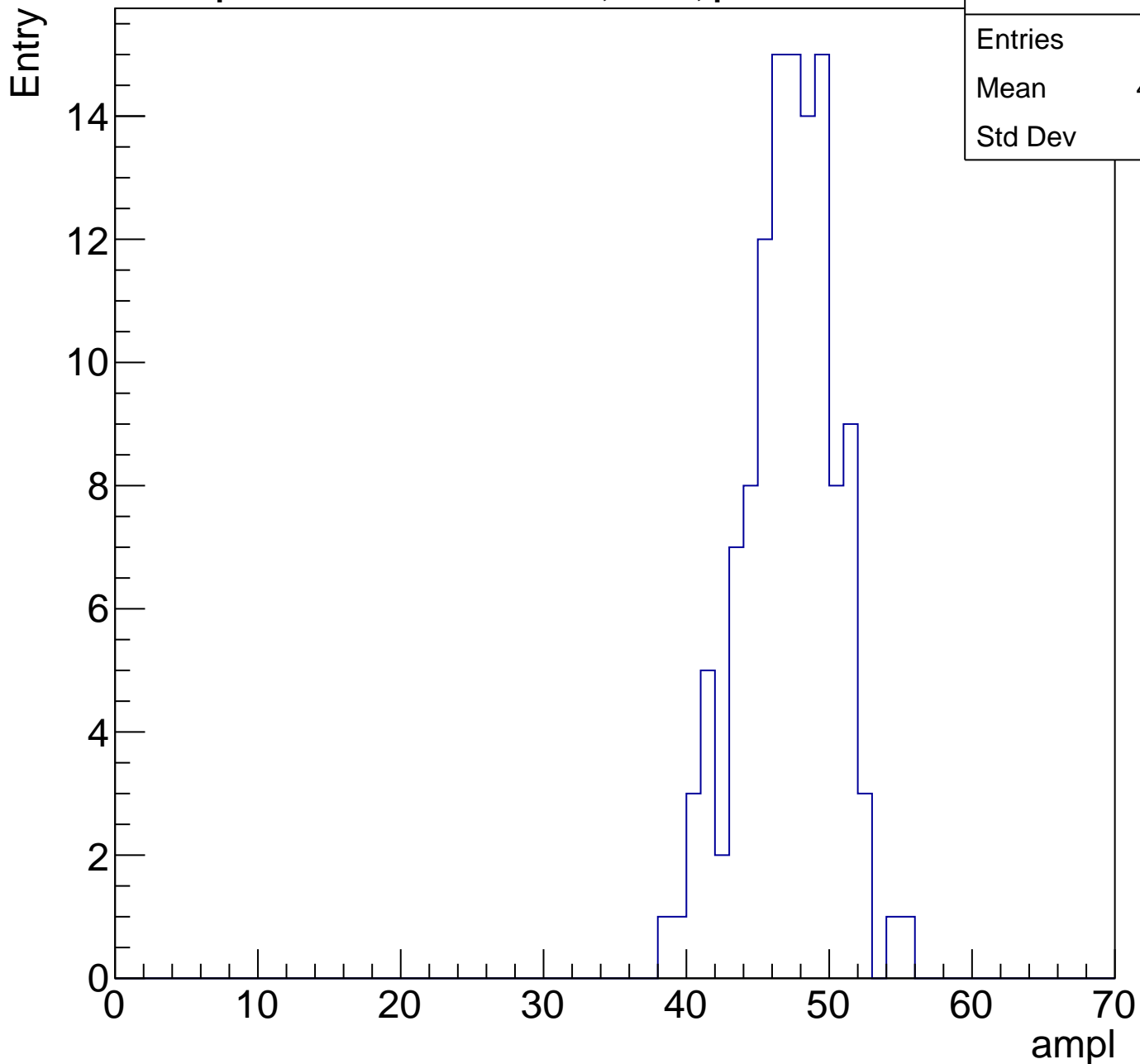
Entry



B1L001S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	46.71
Std Dev	3.21



B1L001S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	52.76
Std Dev	3.139

Entry

16
14
12
10
8
6
4
2
0

0

10

20

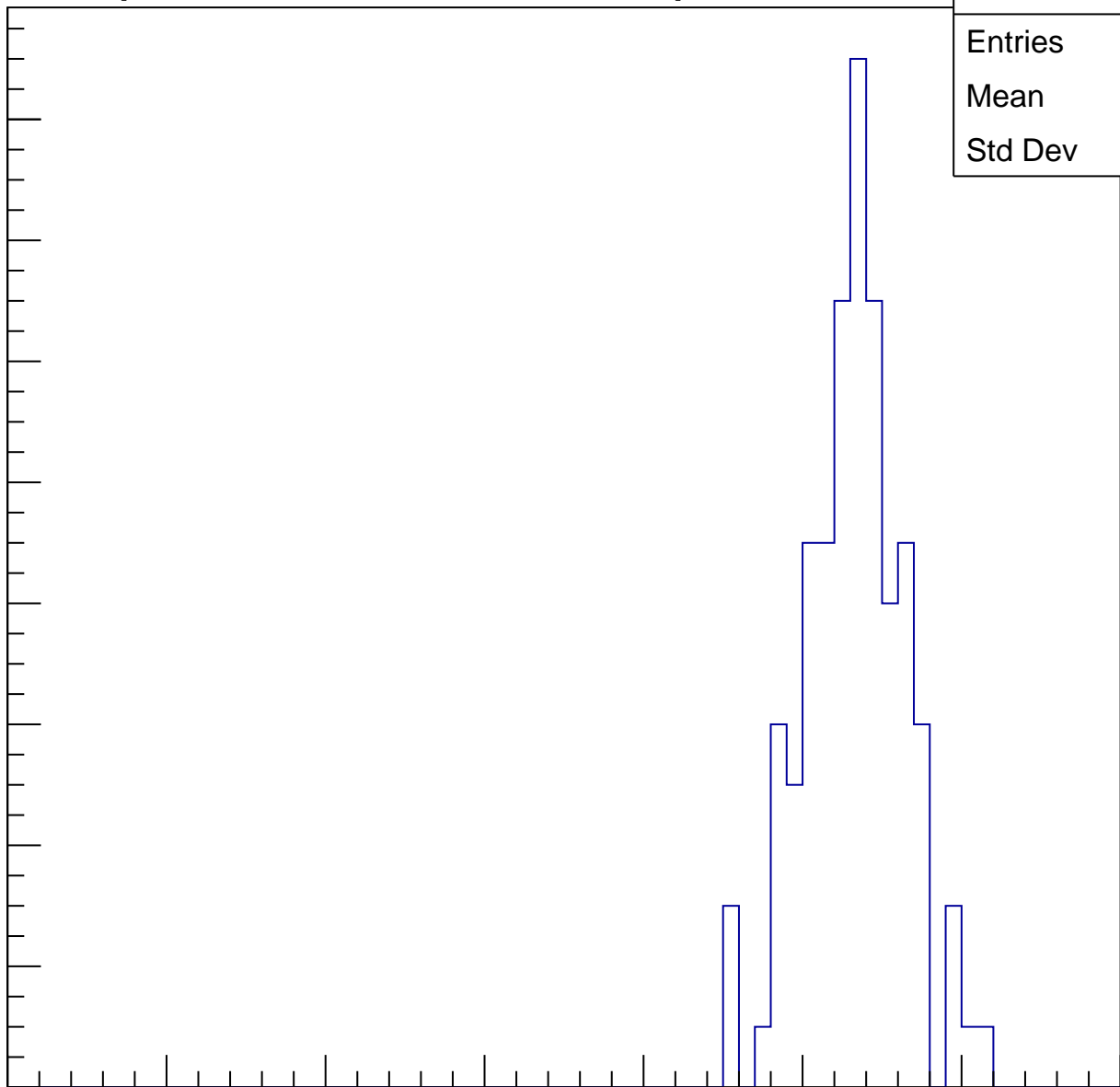
30

40

50

60

ampl



B1L001S, U14-ch100, adc5

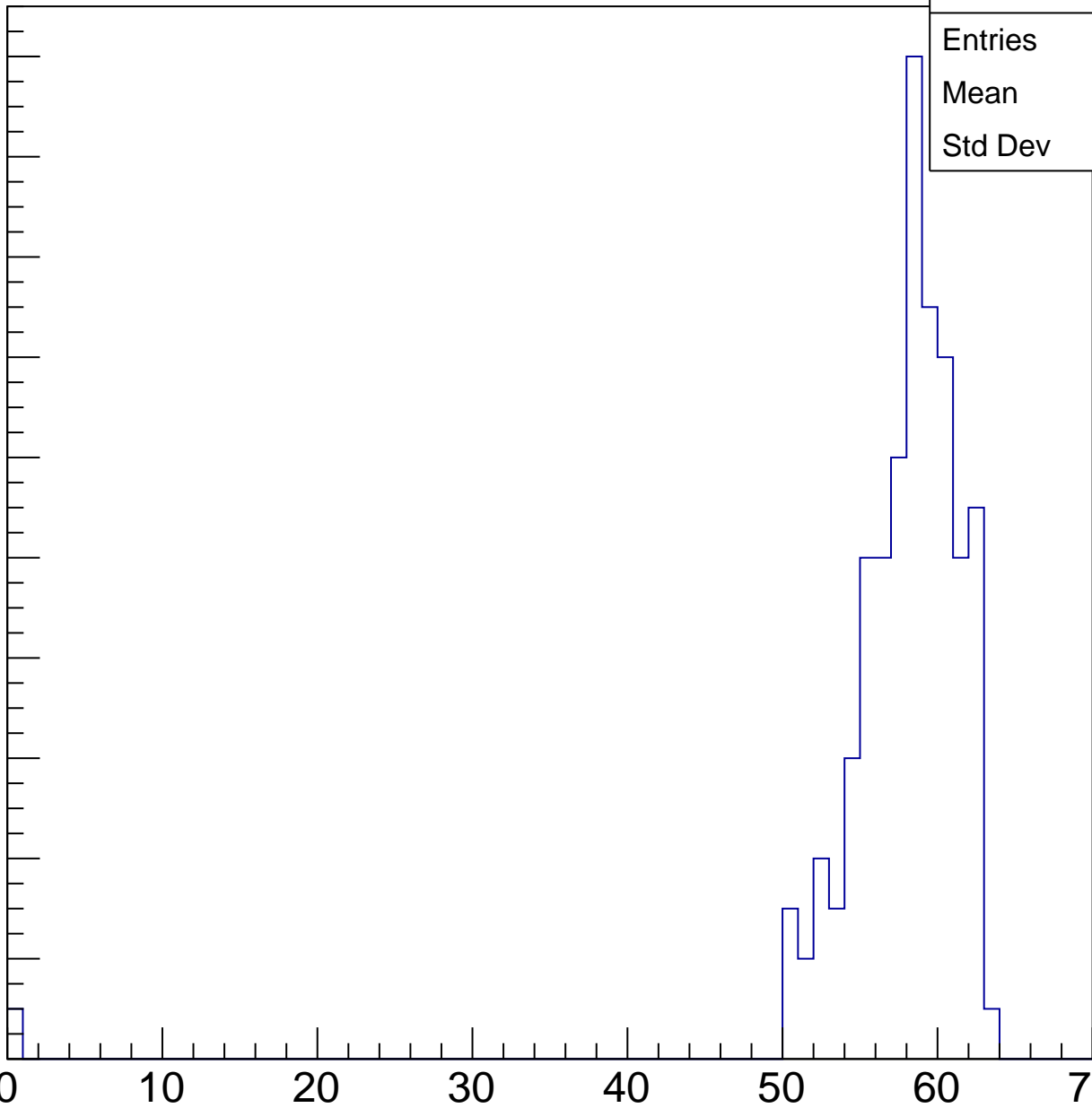
calib_packv5_042523_0143.root, FC#2, port C2

Entry

20
18
16
14
12
10
8
6
4
2
0

Entries	122
Mean	57.19
Std Dev	6

ampl



B1L001S, U14-ch100, adc6

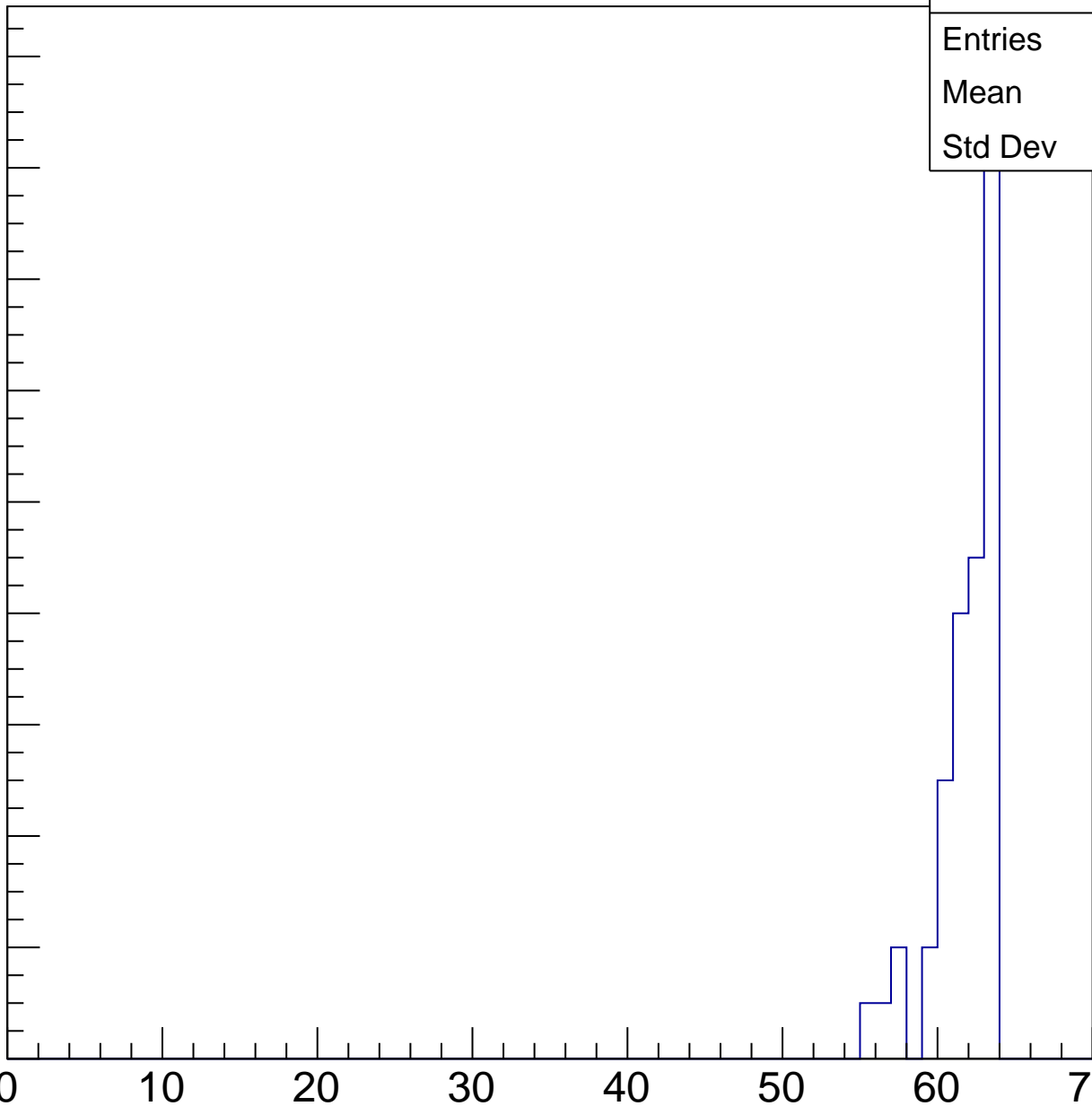
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	46
Mean	61.37
Std Dev	1.982

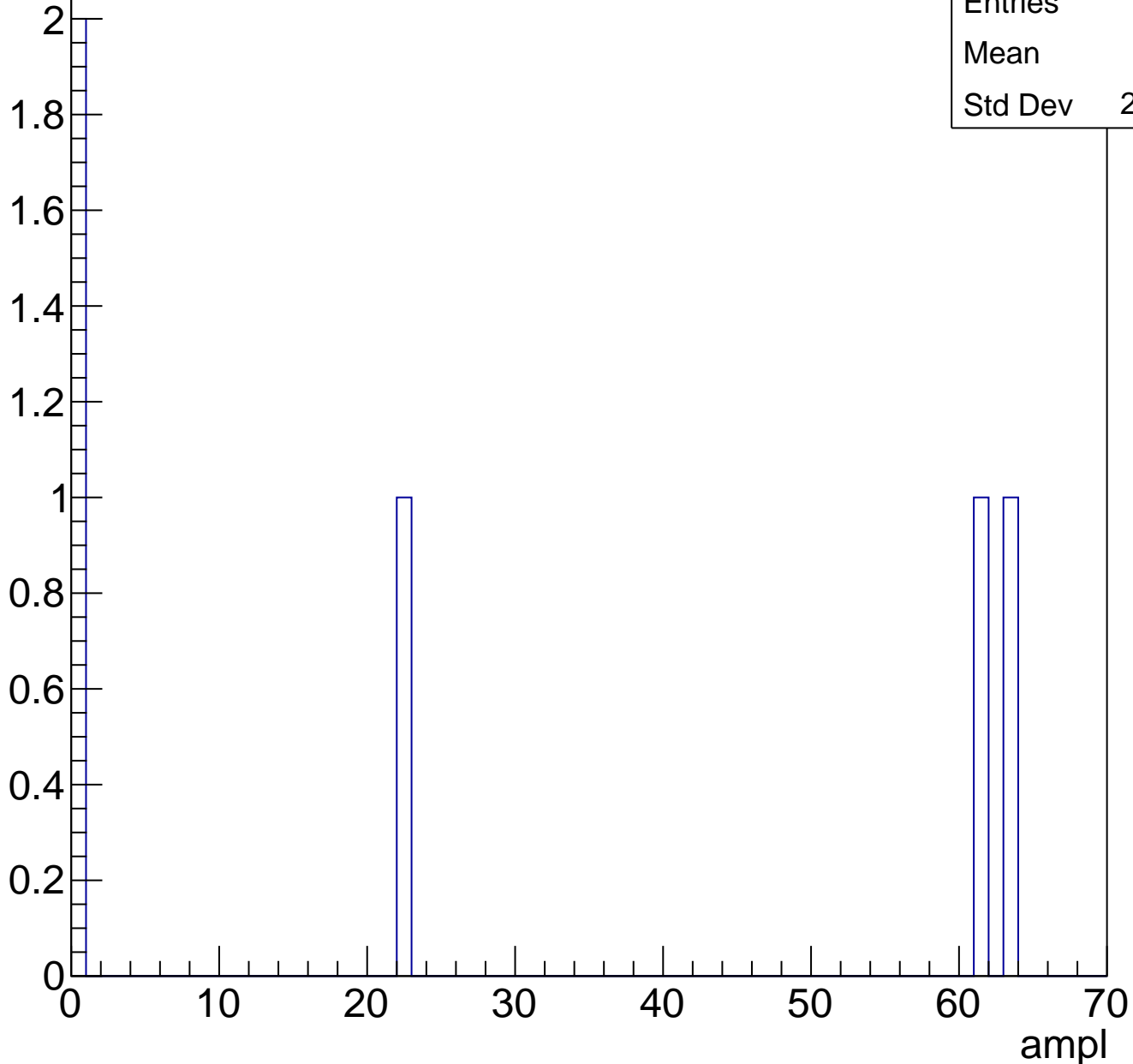
ampl



B1L001S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	5
Mean	29.2
Std Dev	27.97

B1L001S, U14-ch101, adc0

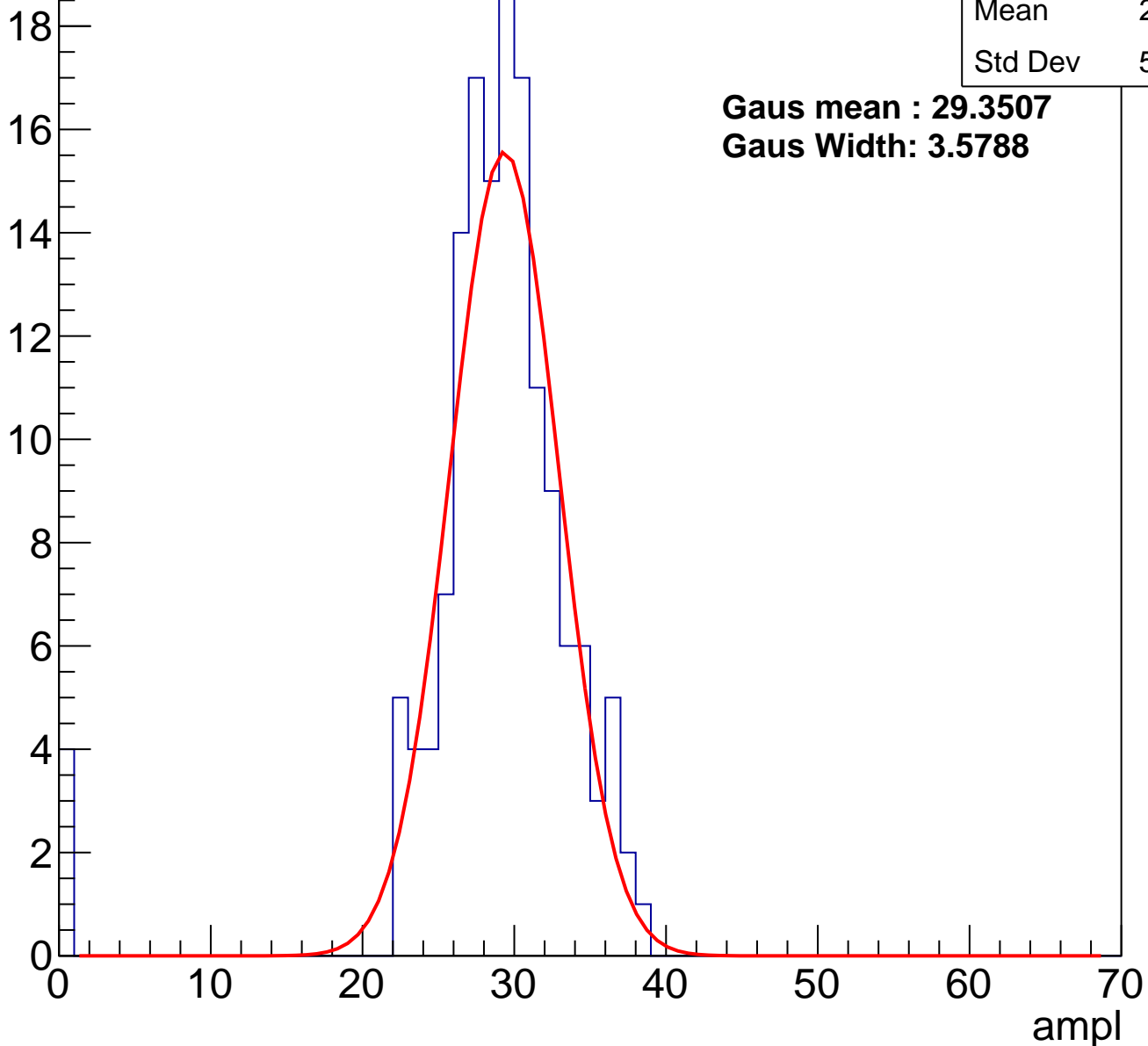
calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	28.22
Std Dev	5.812

Gaus mean : 29.3507

Gaus Width: 3.5788

Entry



B1L001S, U14-ch101, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	137
Mean	35.5
Std Dev	3.289

Gaus mean : 36.1554

Gaus Width: 3.1966

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

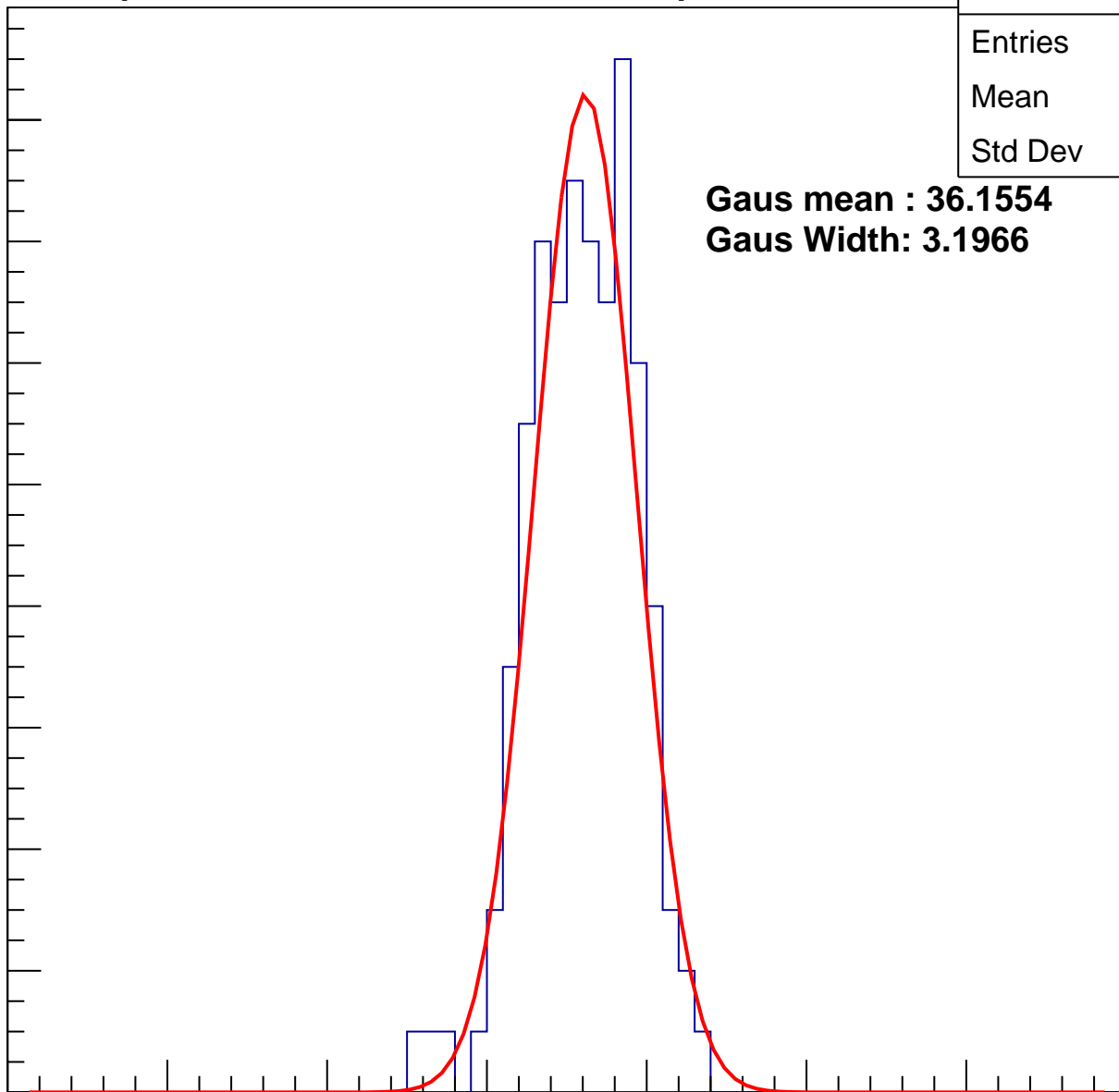
40

50

60

70

ampl



B1L001S, U14-ch101, adc2

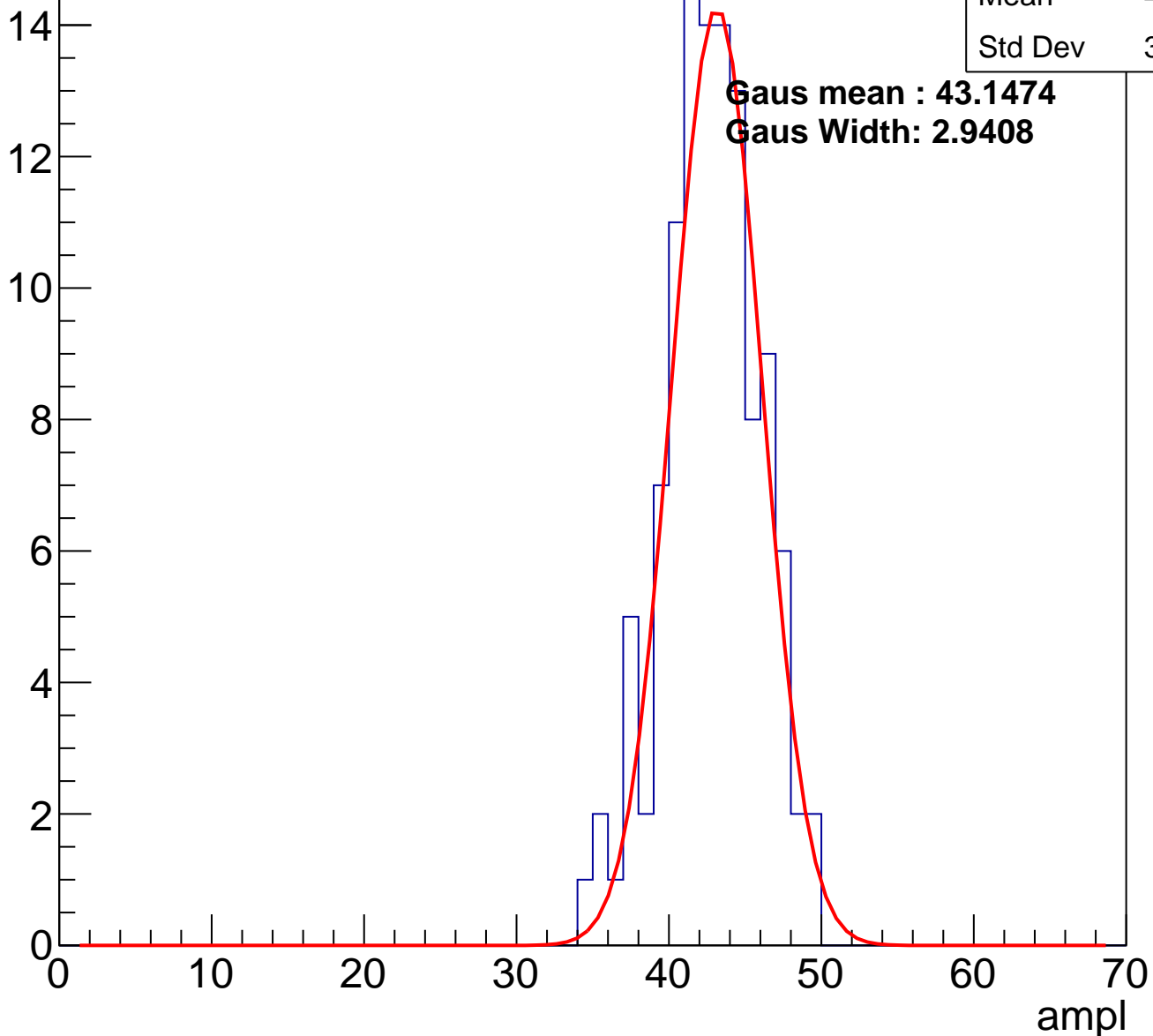
calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	42.33
Std Dev	3.086

Gaus mean : 43.1474

Gaus Width: 2.9408

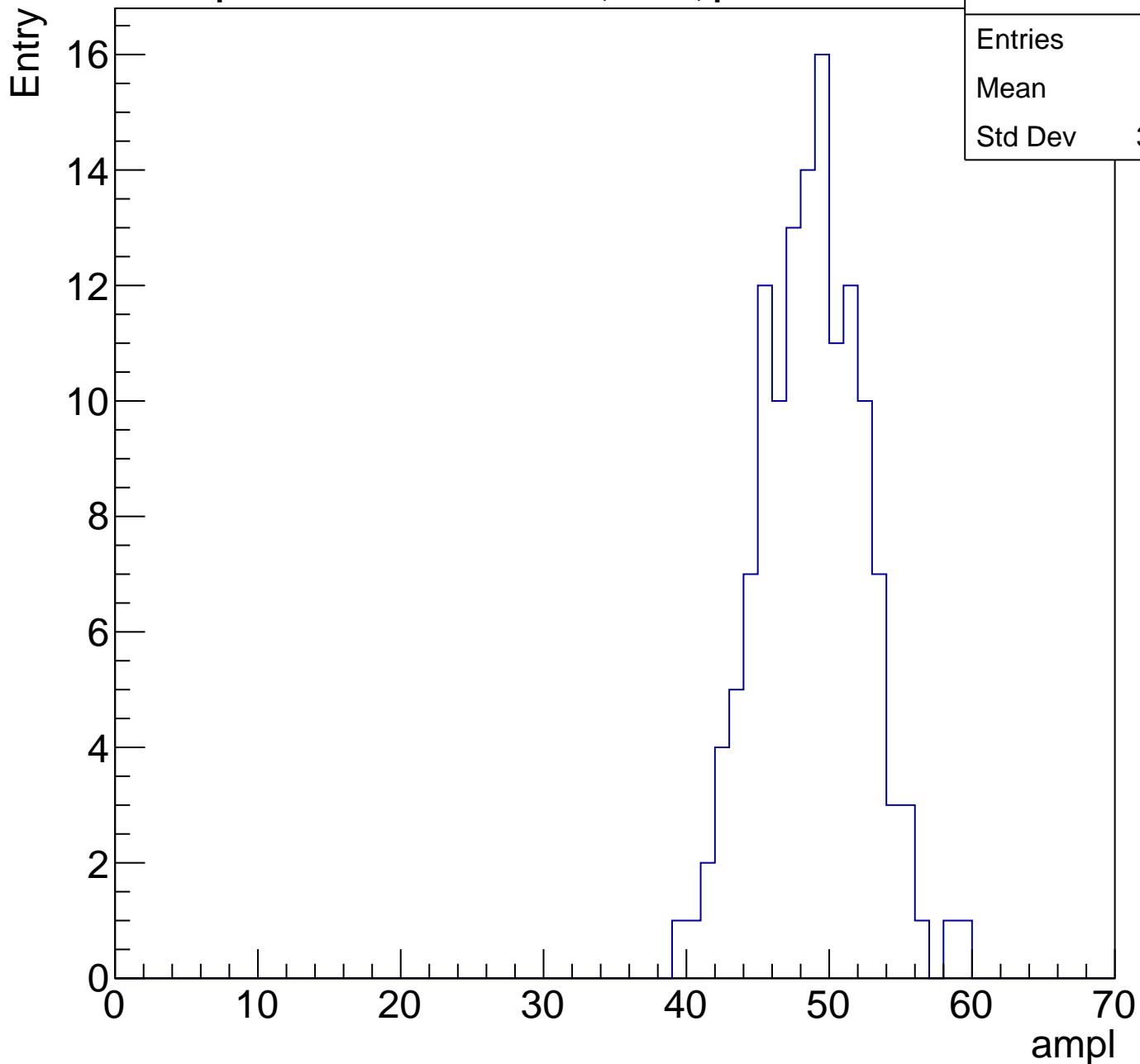
Entry



B1L001S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#2, port C2

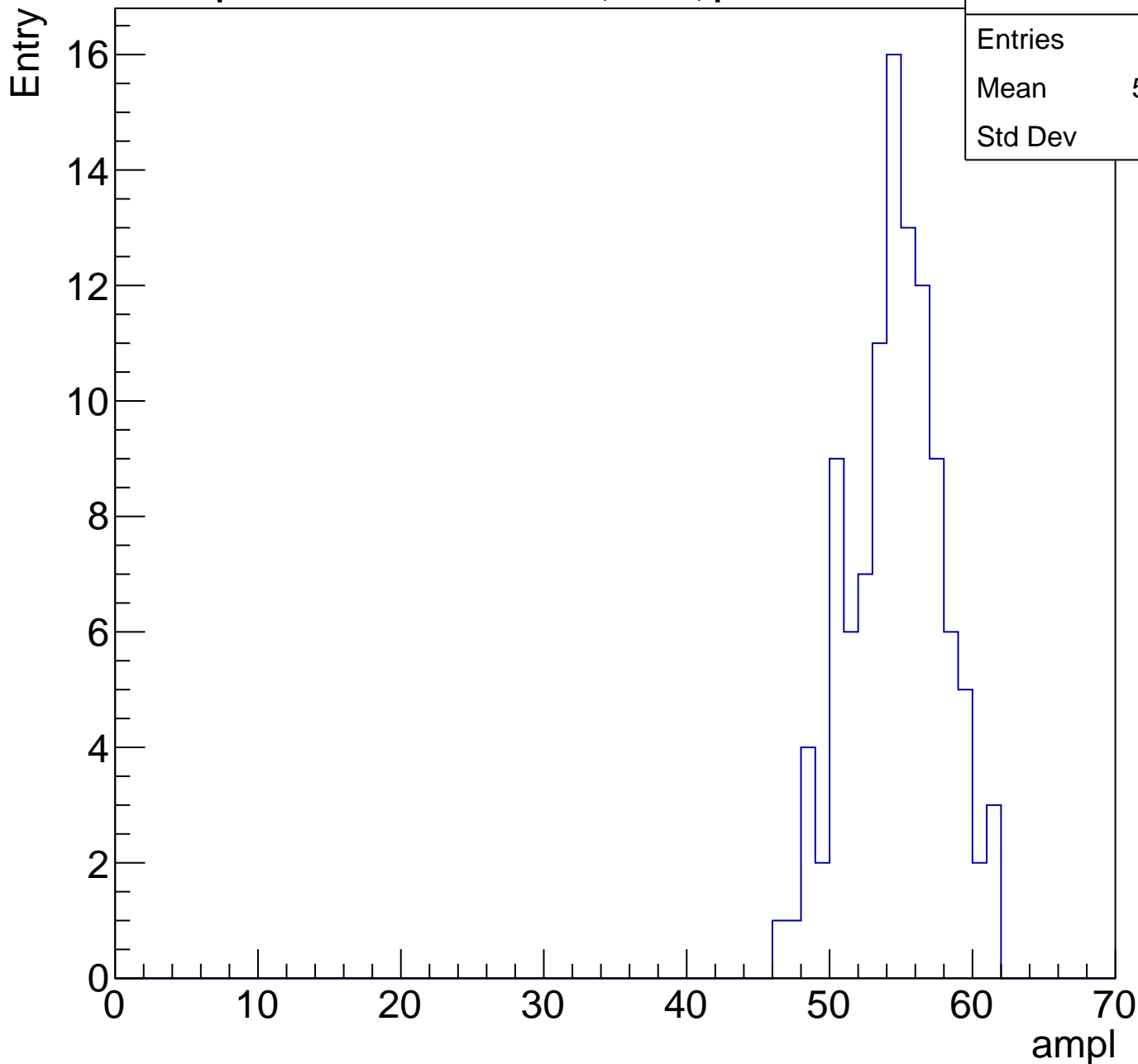
Entries	134
Mean	48.3
Std Dev	3.681



B1L001S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	54.17
Std Dev	3.24



B1L001S, U14-ch101, adc5

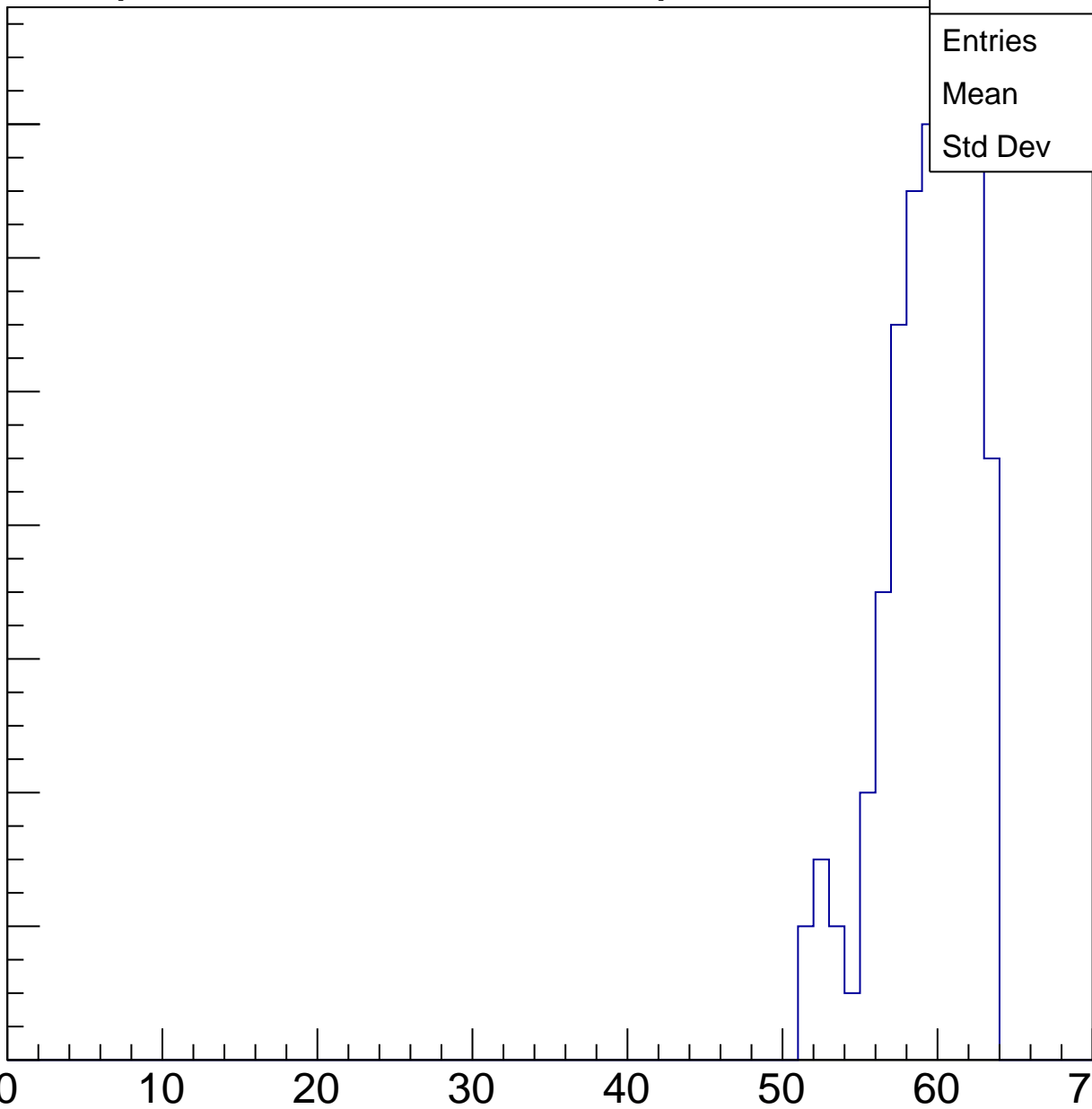
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	109
Mean	58.95
Std Dev	2.868

ampl



B1L001S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	24
Mean	56.88
Std Dev	17.18

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

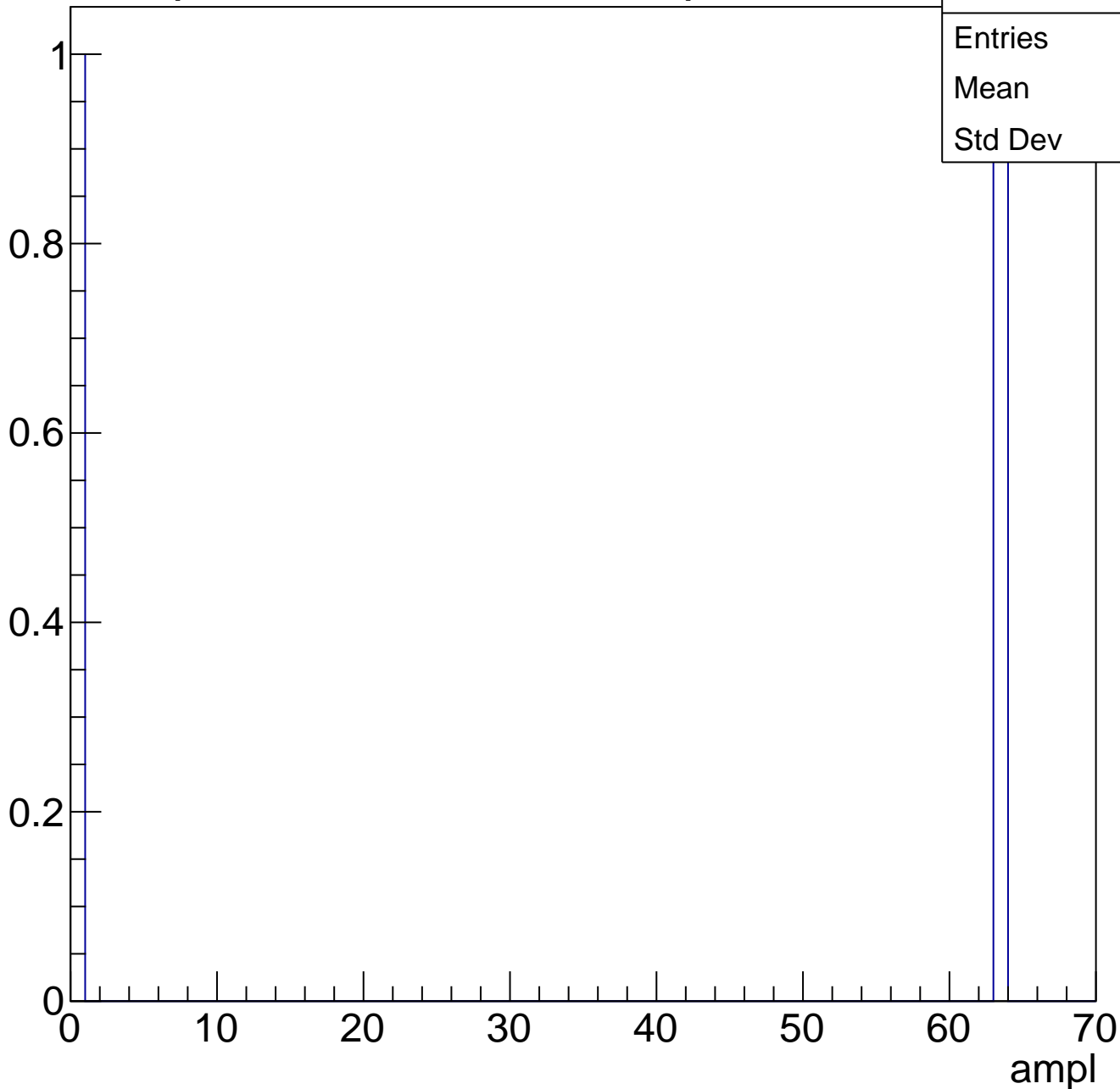
8

10

B1L001S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch102, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.26
Std Dev	4.202

Gaus mean : 28.9909

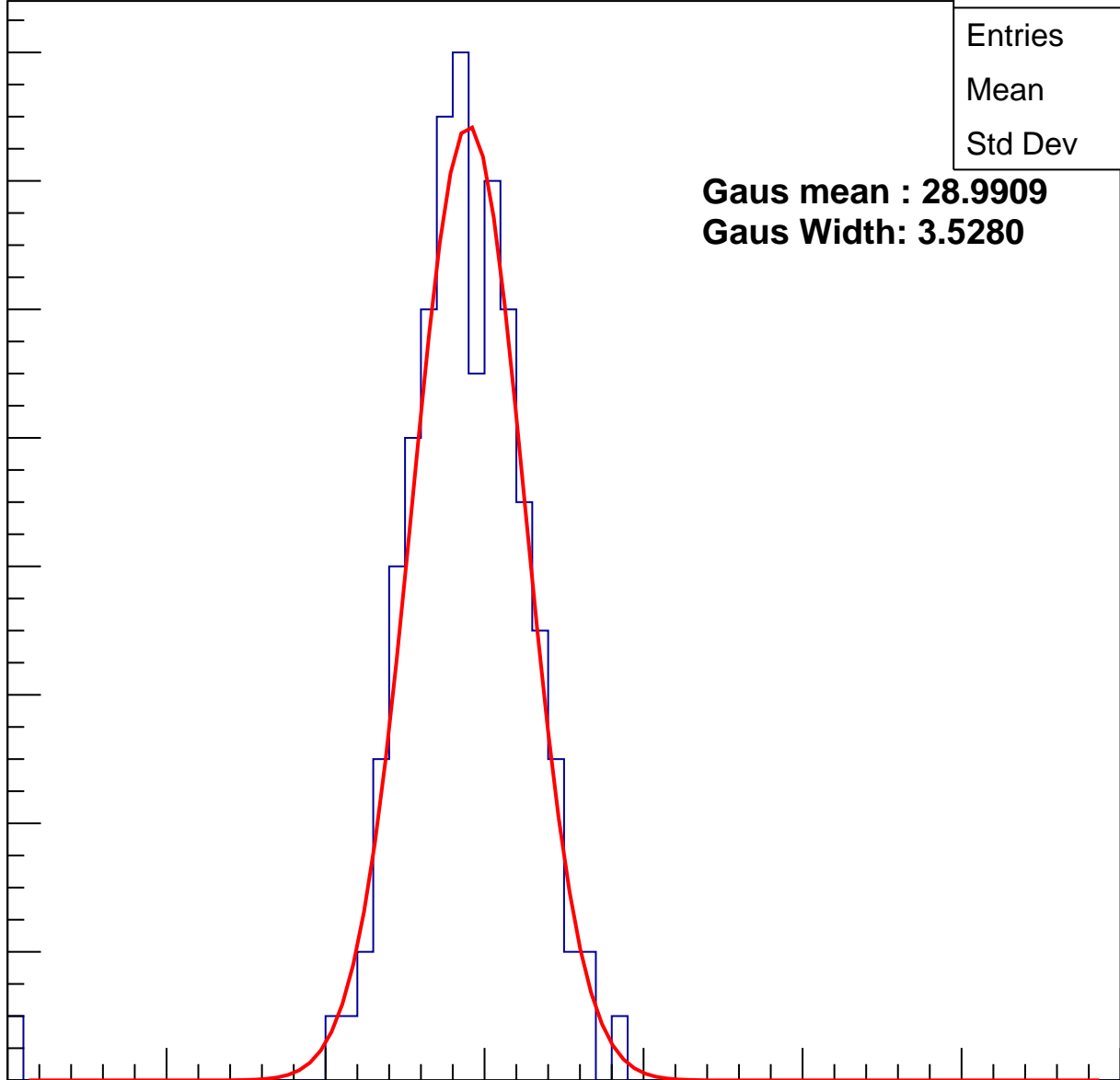
Gaus Width: 3.5280

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch102, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	135
Mean	34.56
Std Dev	3.643

Gaus mean : 34.9940

Gaus Width: 3.4481

Entry

14
12
10
8
6
4
2
0

ampl

0

10

20

50

60

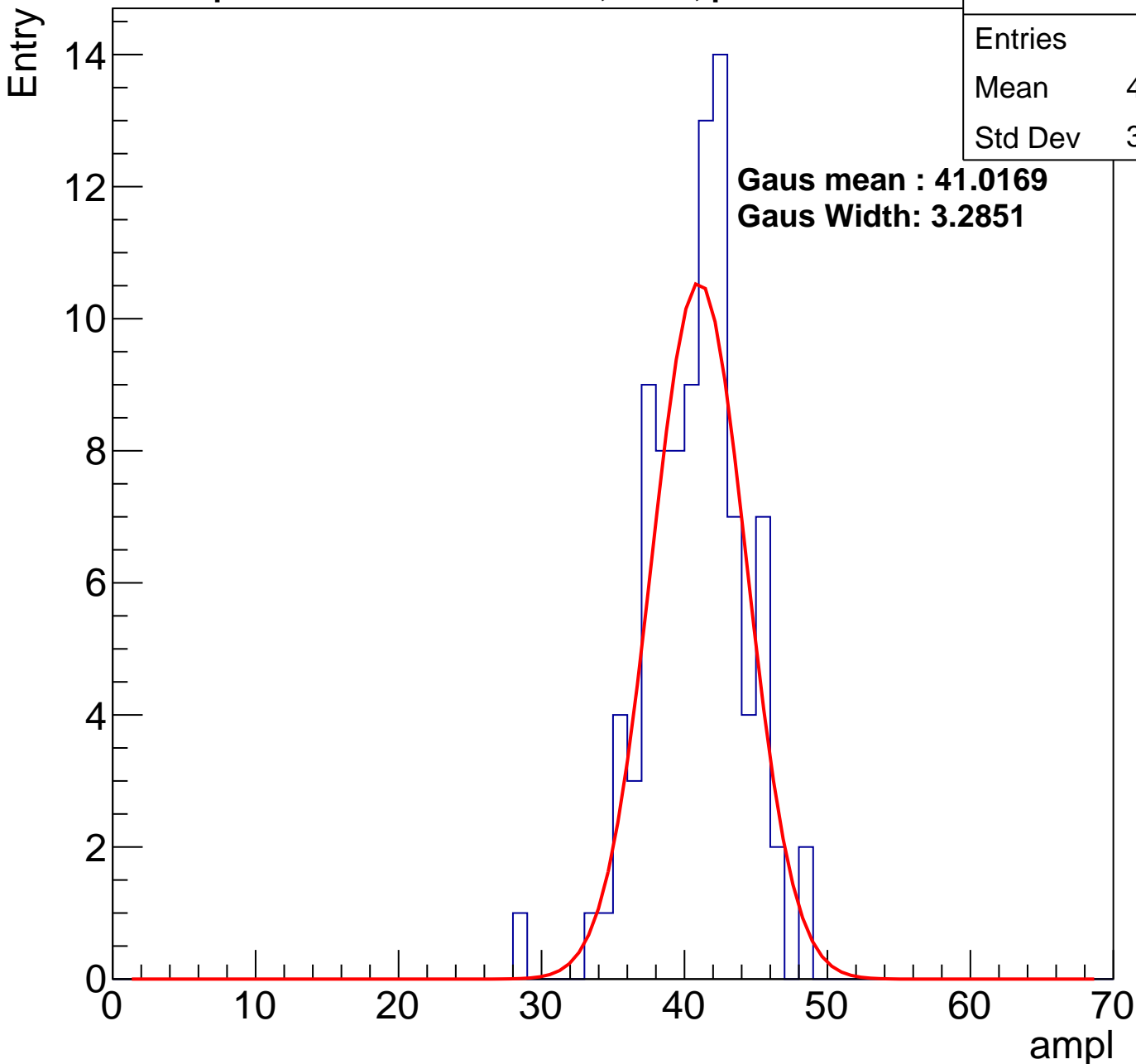
70

B1L001S, U14-ch102, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	93
Mean	40.35
Std Dev	3.372

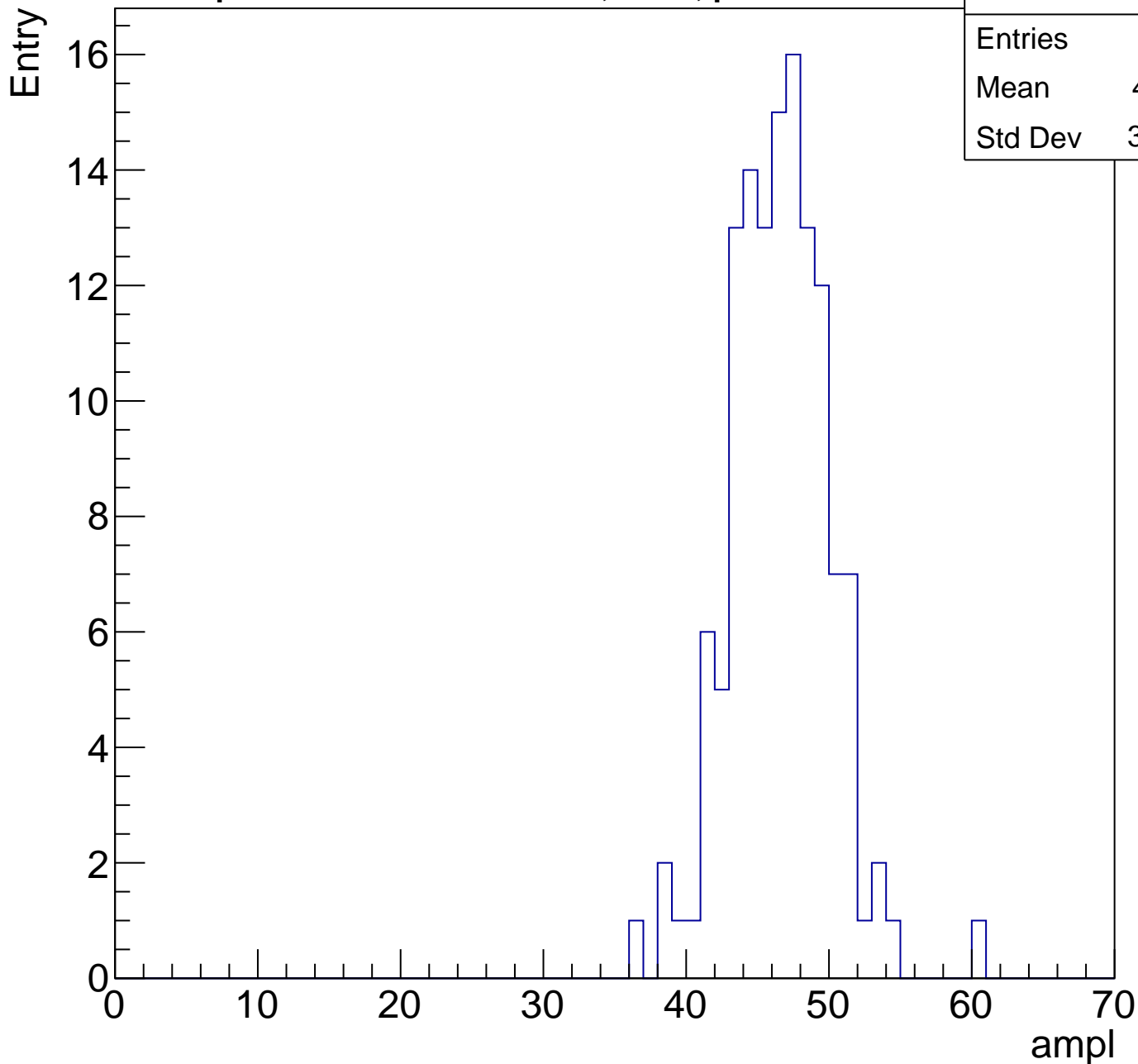
Gaus mean : 41.0169
Gaus Width: 3.2851



B1L001S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#2, port C2

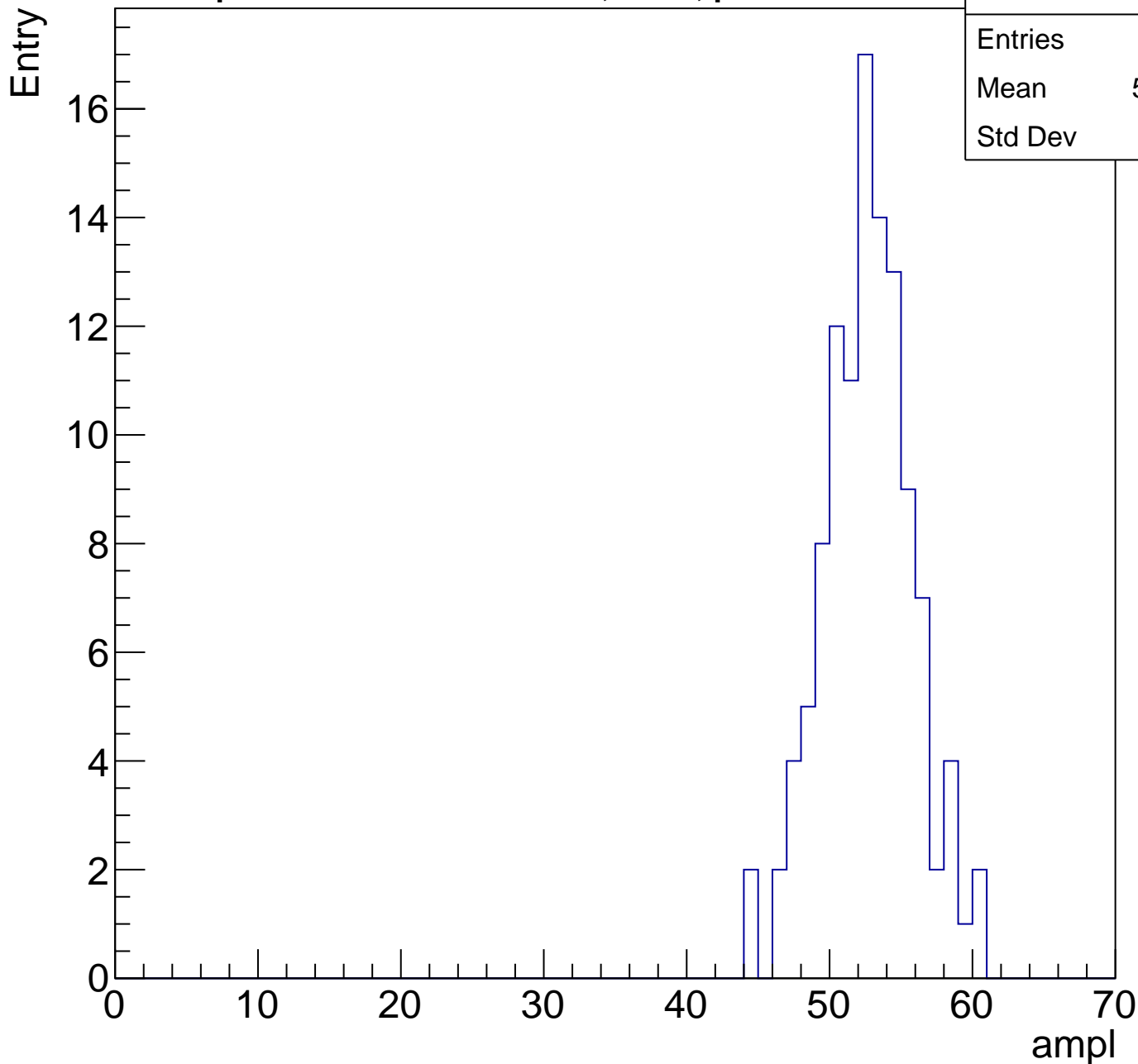
Entries	131
Mean	46.11
Std Dev	3.478



B1L001S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#2, port C2

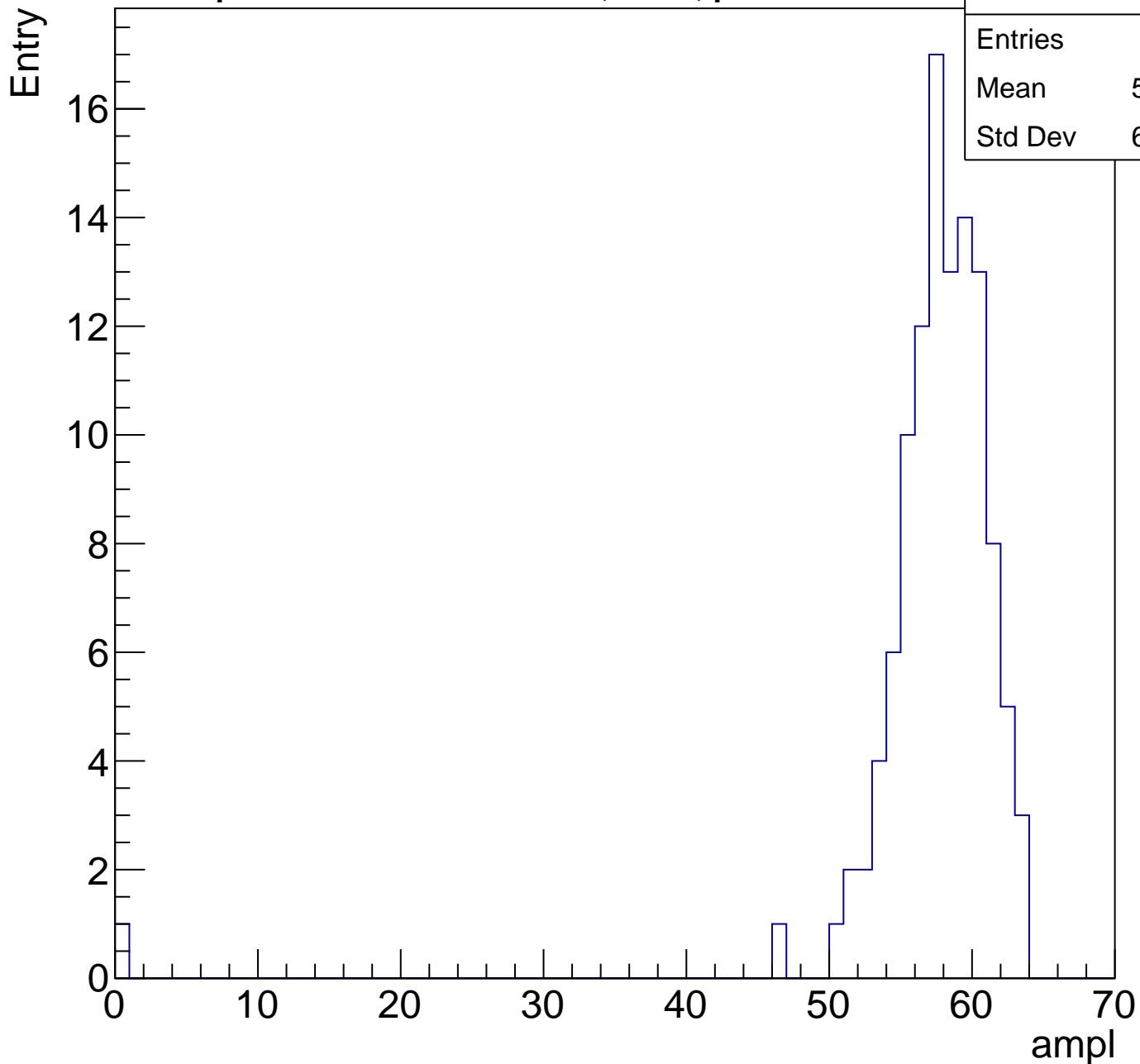
Entries	113
Mean	52.22
Std Dev	3.17



B1L001S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	56.93
Std Dev	6.165



B1L001S, U14-ch102, adc6

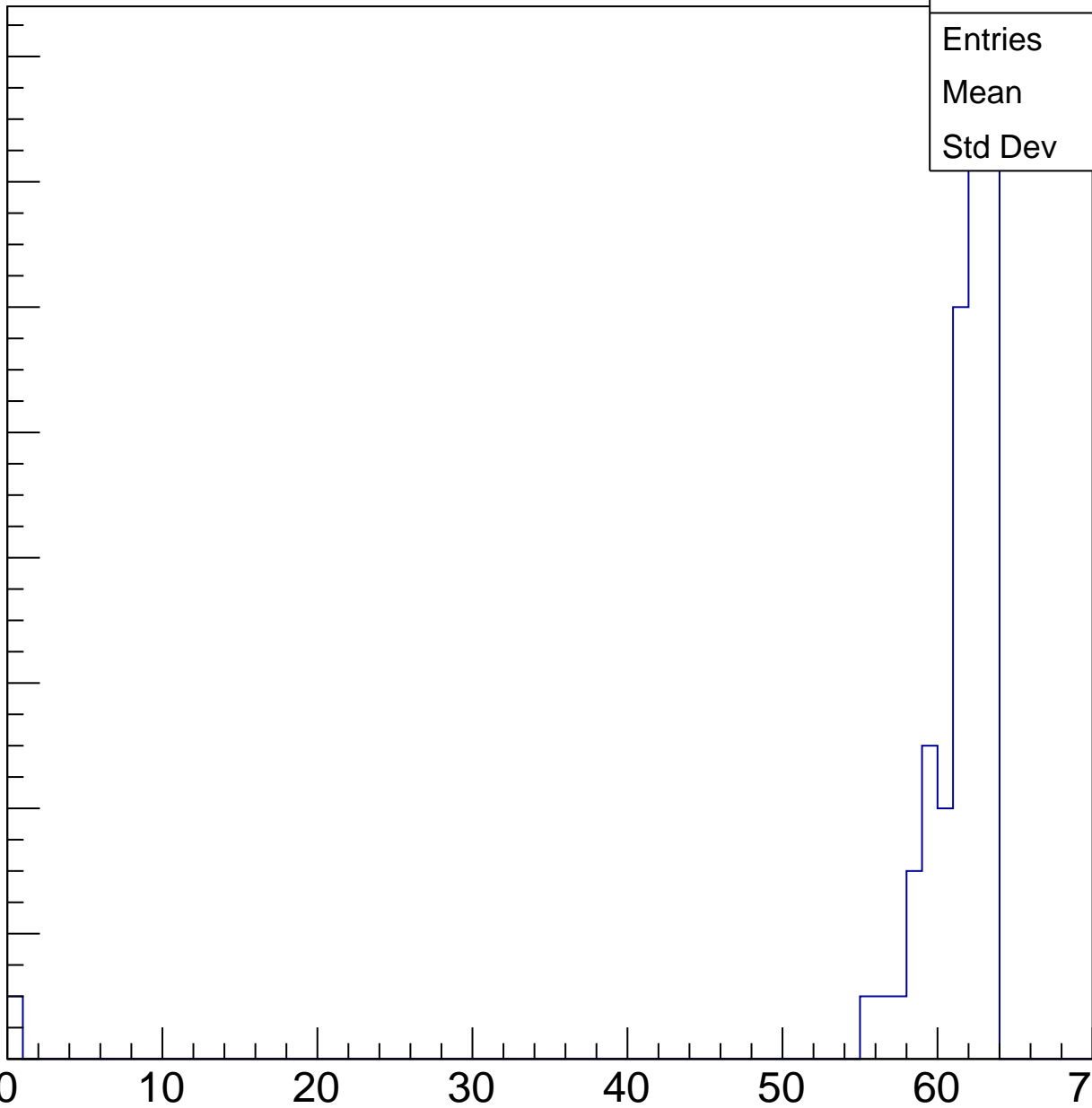
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	59
Mean	60.12
Std Dev	8.112

ampl



B1L001S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

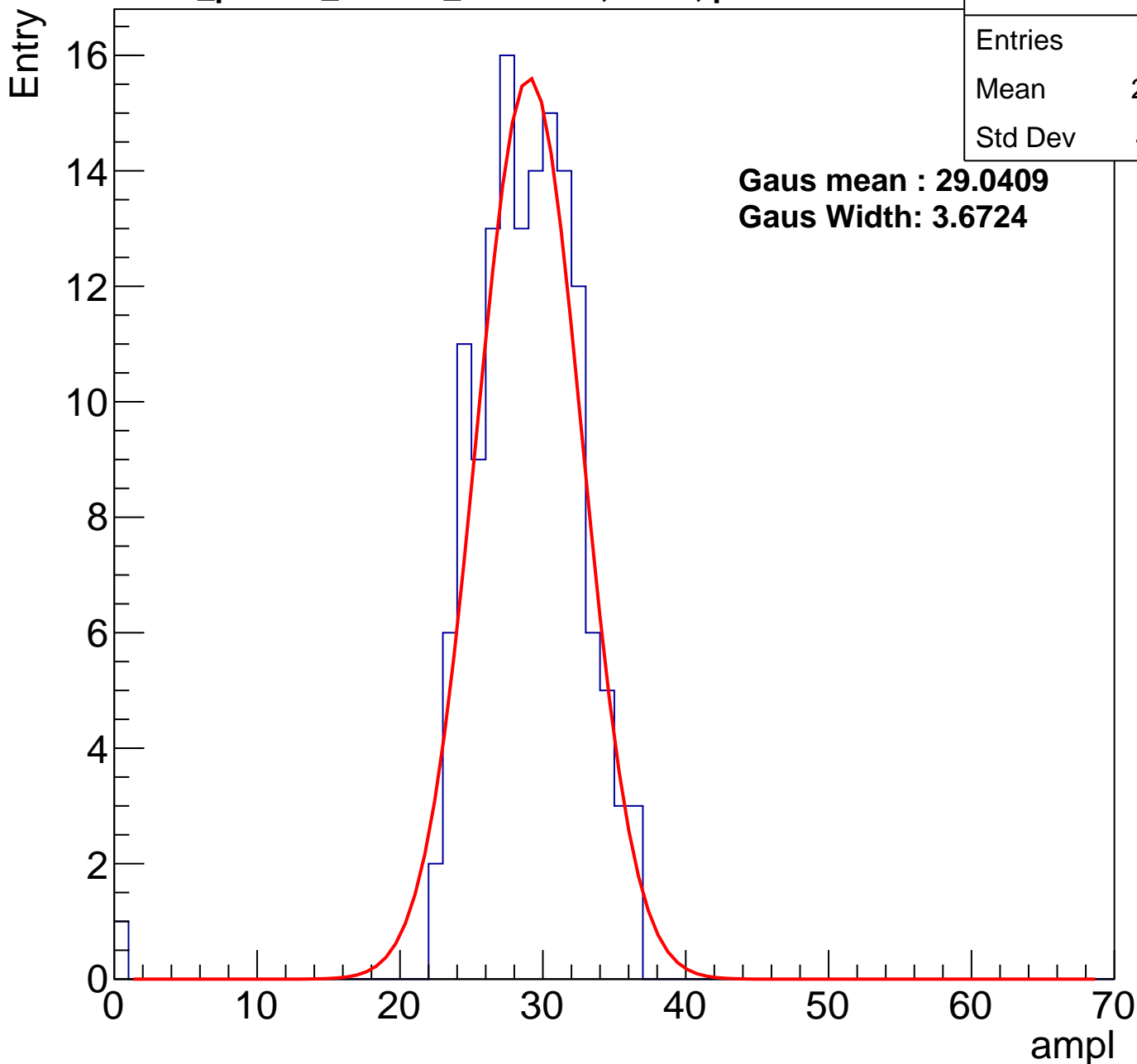
B1L001S, U14-ch103, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	143
Mean	28.39
Std Dev	4.071

Gaus mean : 29.0409

Gaus Width: 3.6724



B1L001S, U14-ch103, adc1

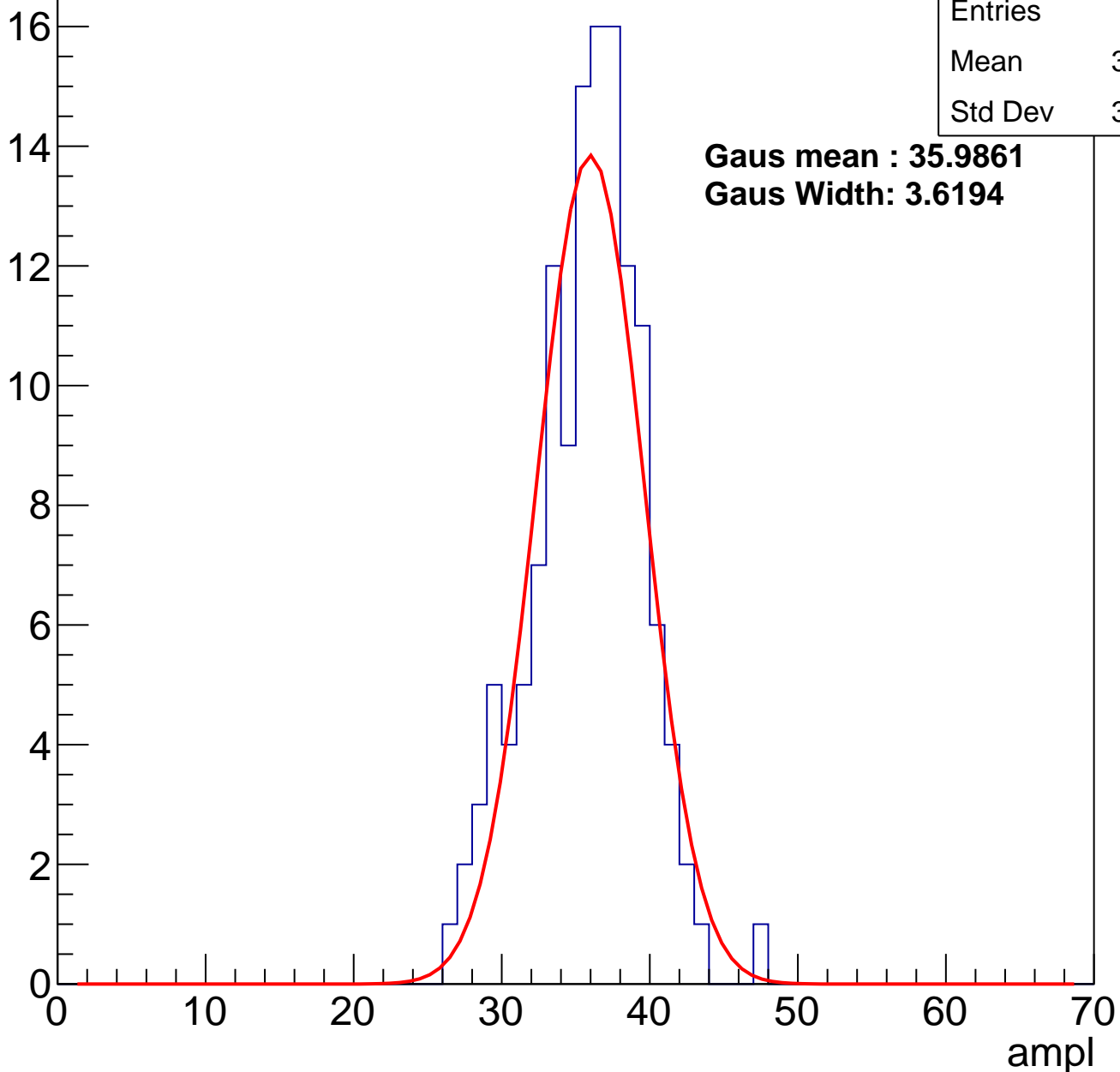
calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	35.35
Std Dev	3.676

Gaus mean : 35.9861

Gaus Width: 3.6194

Entry



B1L001S, U14-ch103, adc2

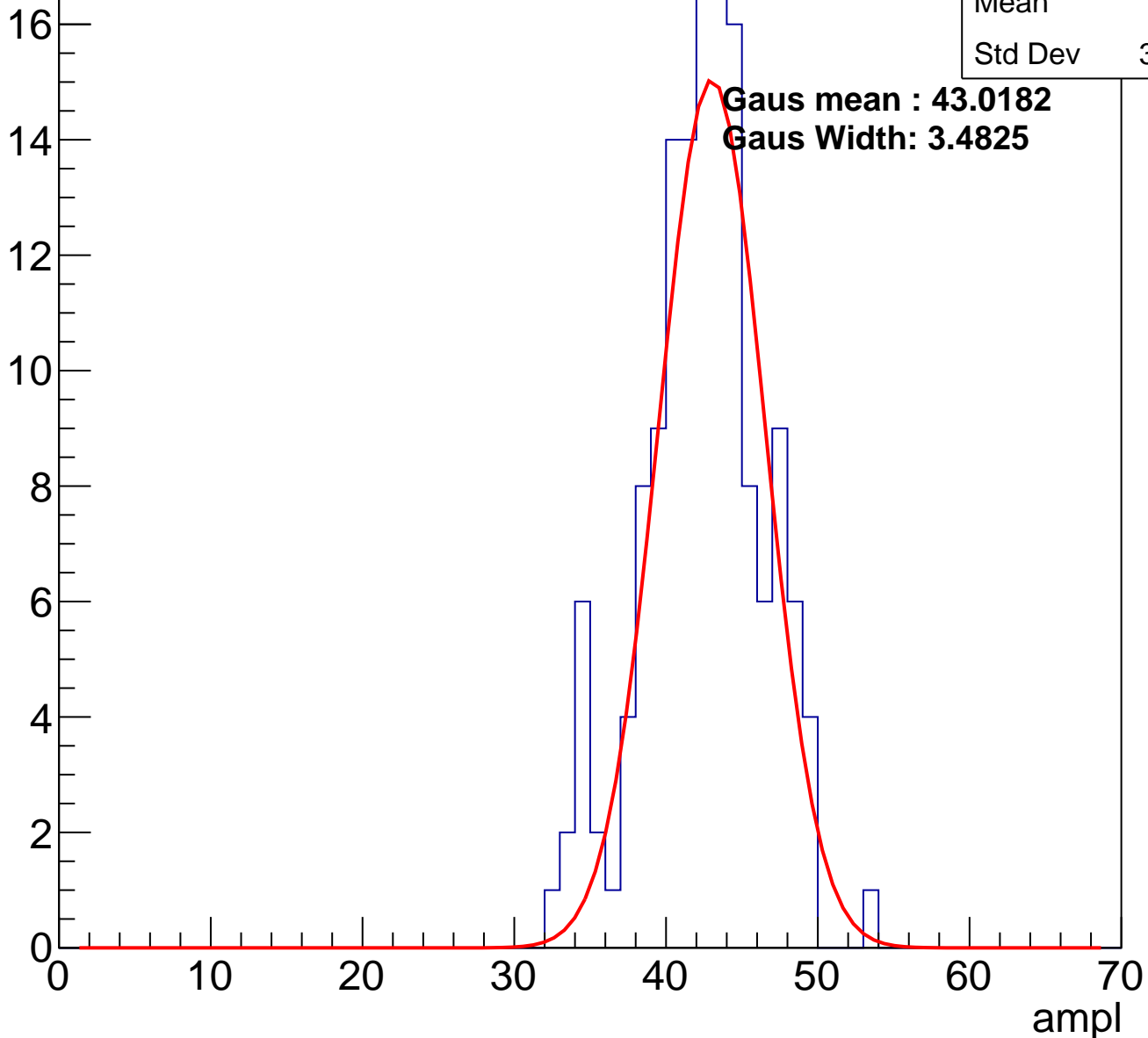
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	145
Mean	42
Std Dev	3.865

Gaus mean : 43.0182

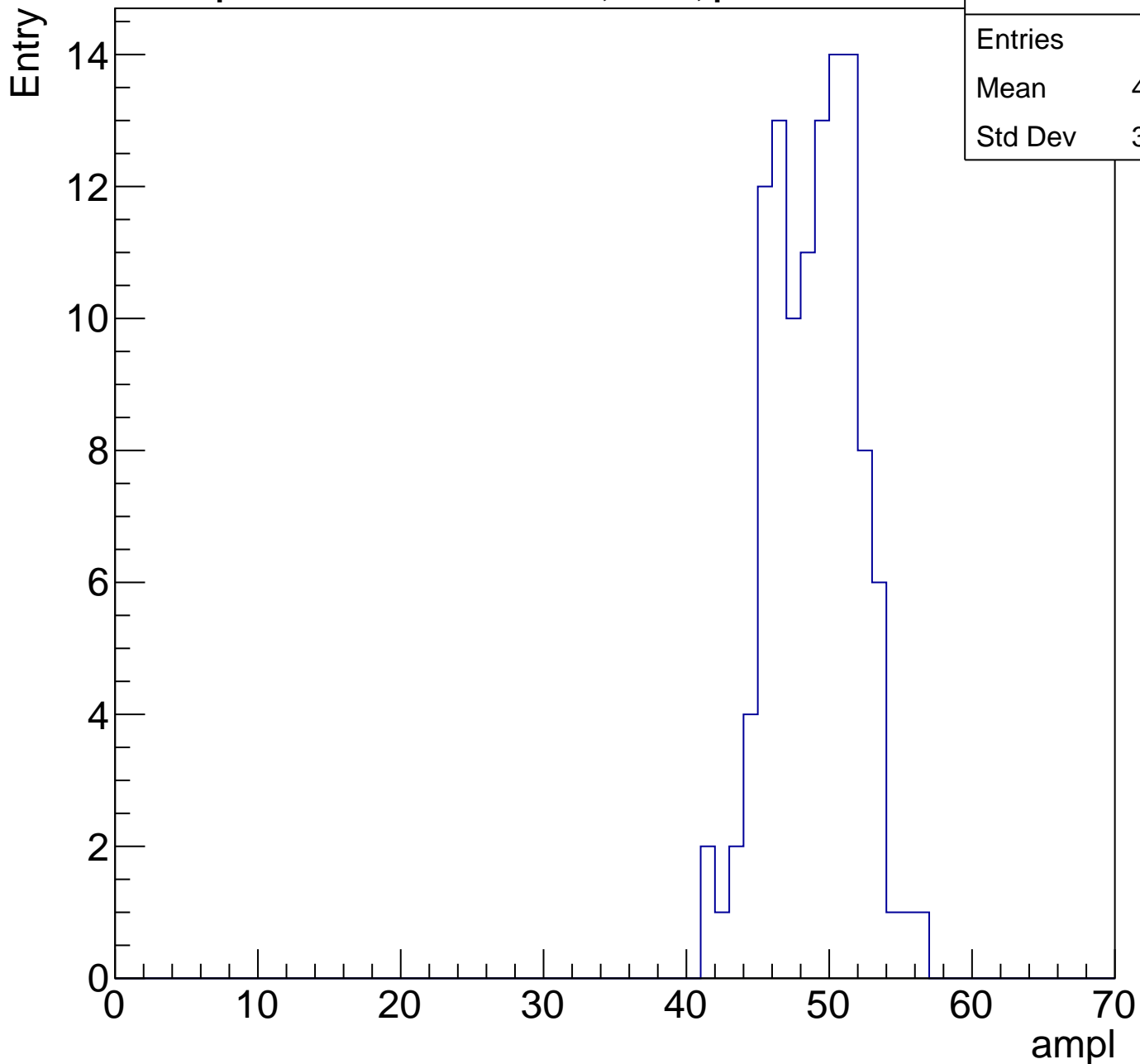
Gaus Width: 3.4825



B1L001S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	48.42
Std Dev	3.004

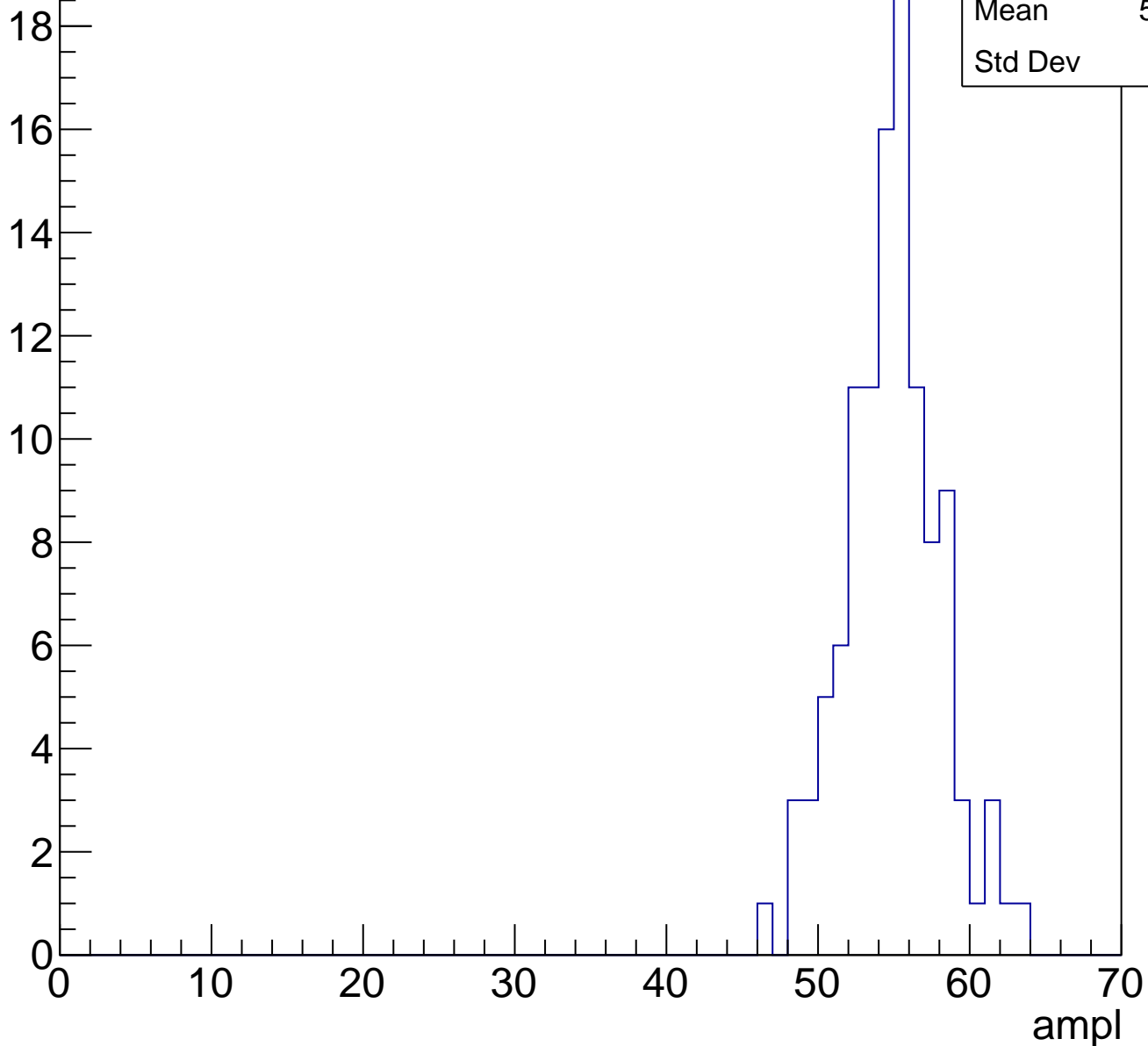


B1L001S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	112
Mean	54.43
Std Dev	3.13

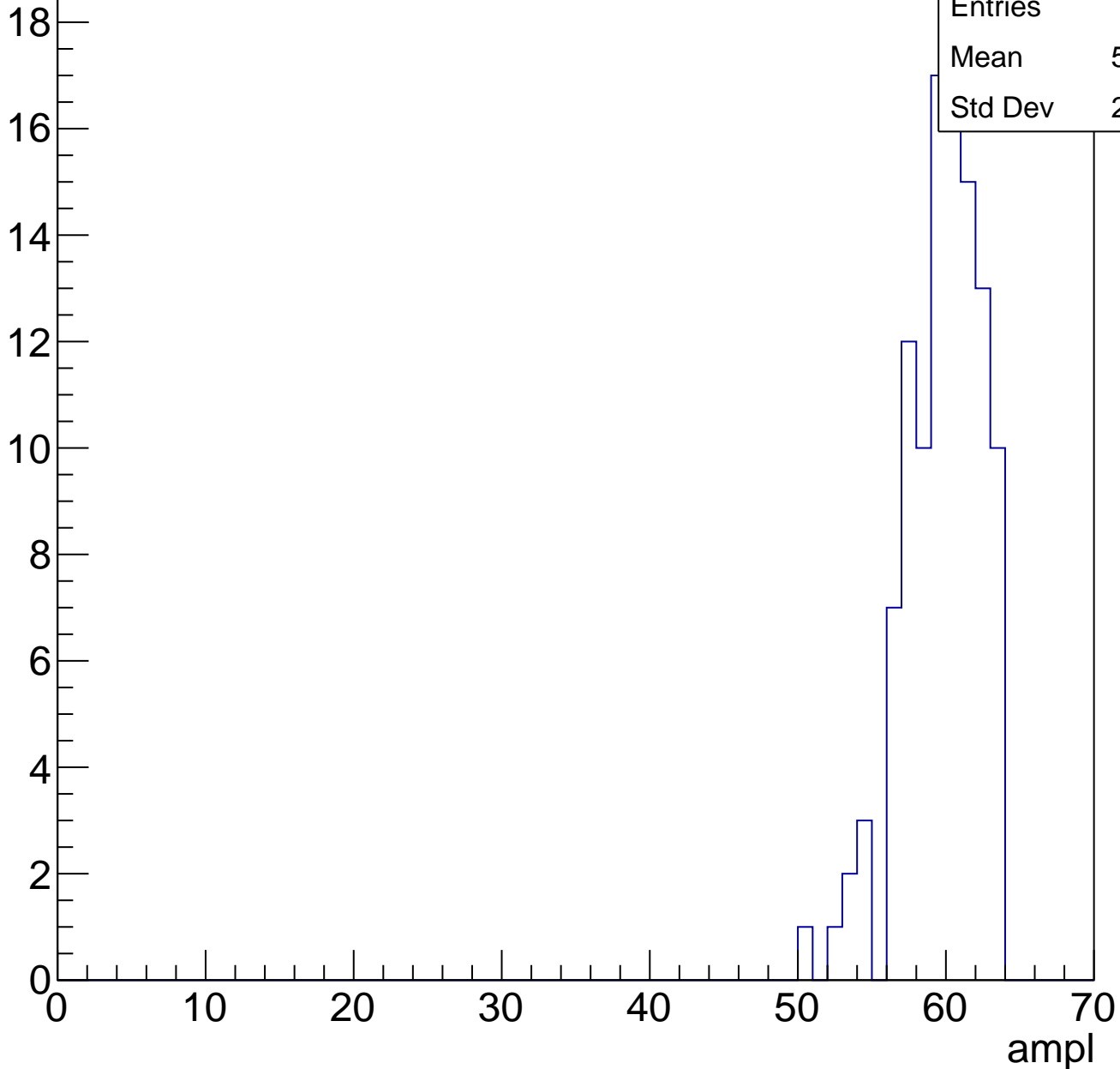
Entry



B1L001S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry

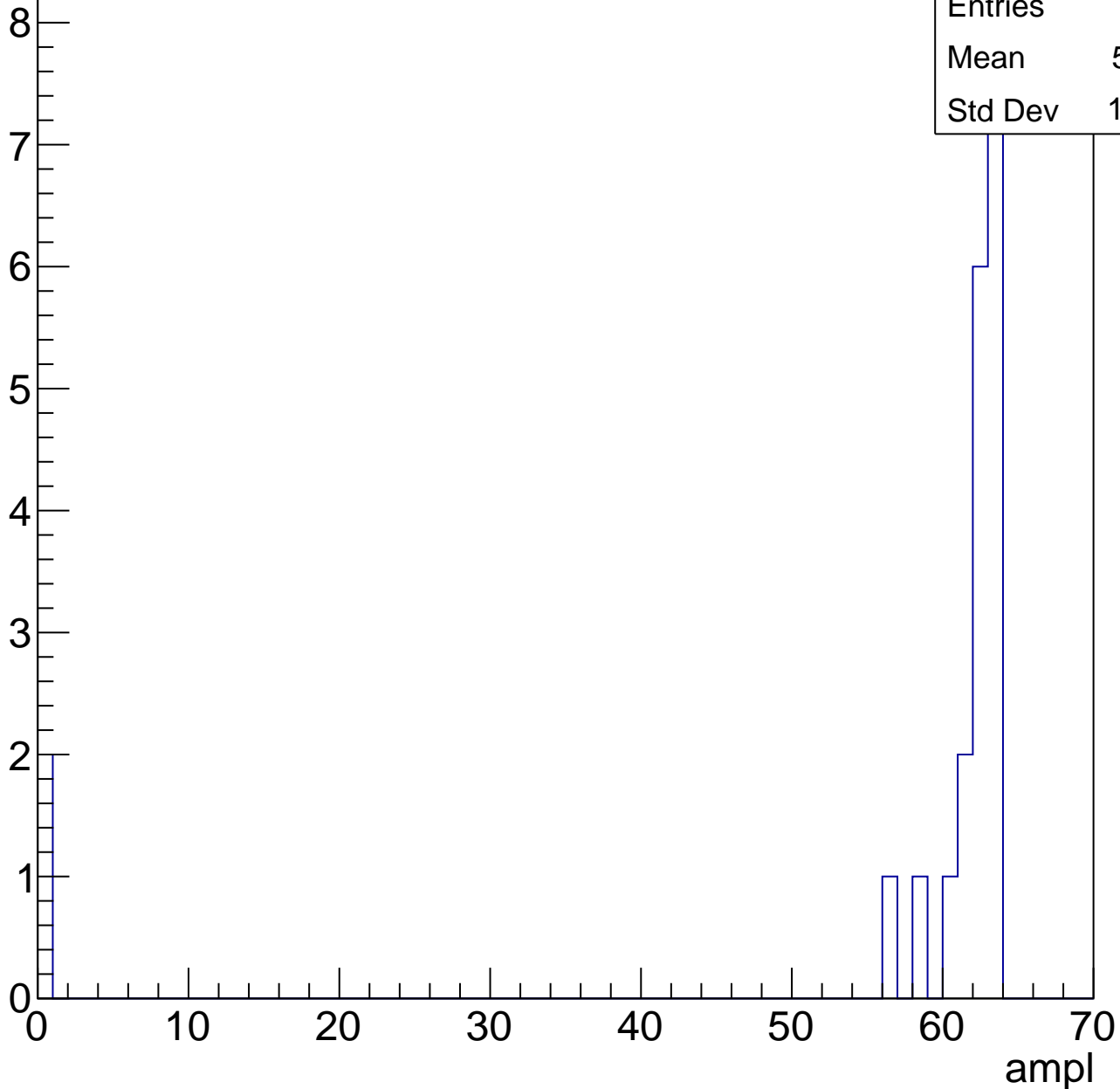


B1L001S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

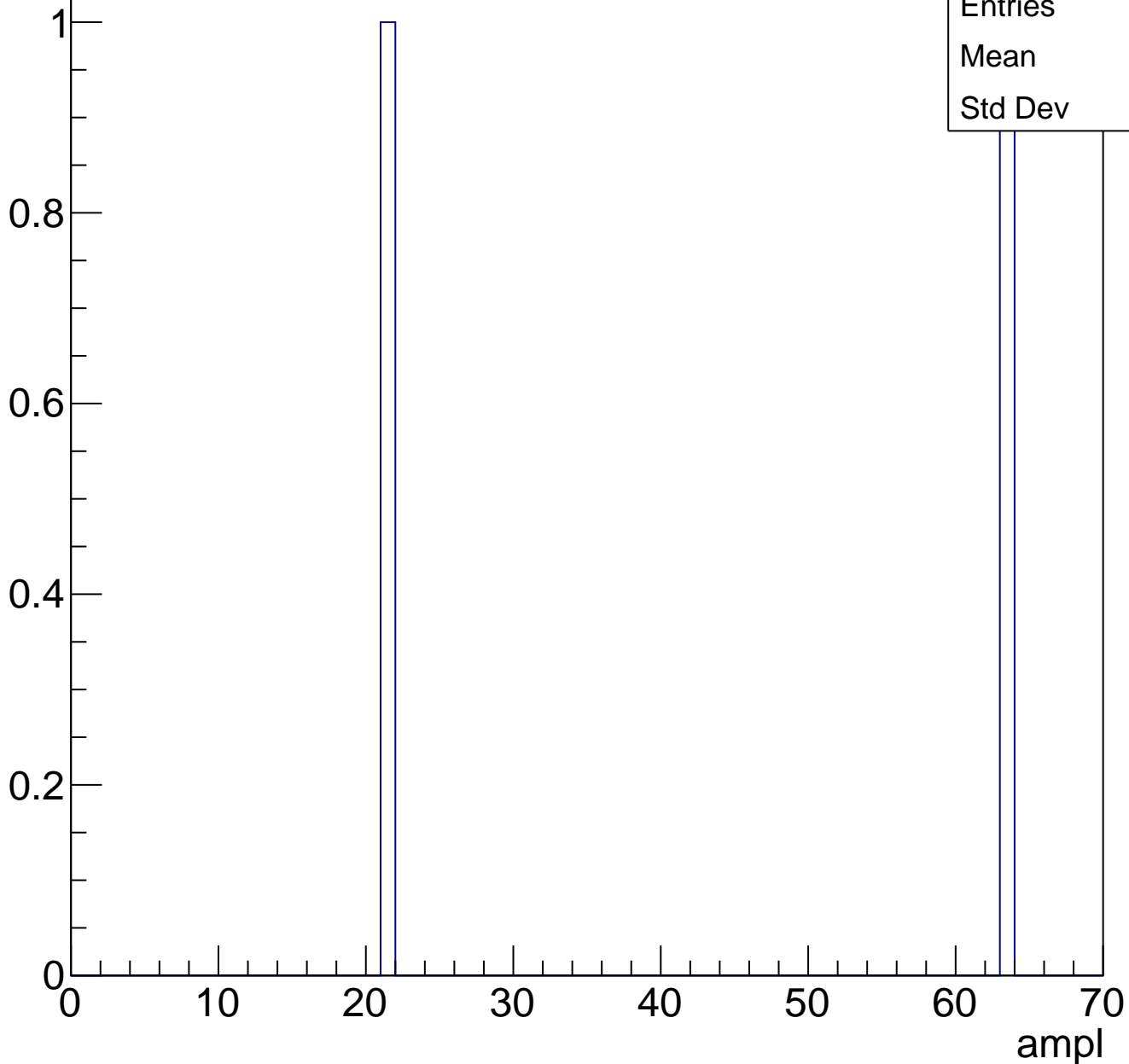
Entries	21
Mean	55.81
Std Dev	18.19



B1L001S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch104, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	145
Mean	27.72
Std Dev	4.122

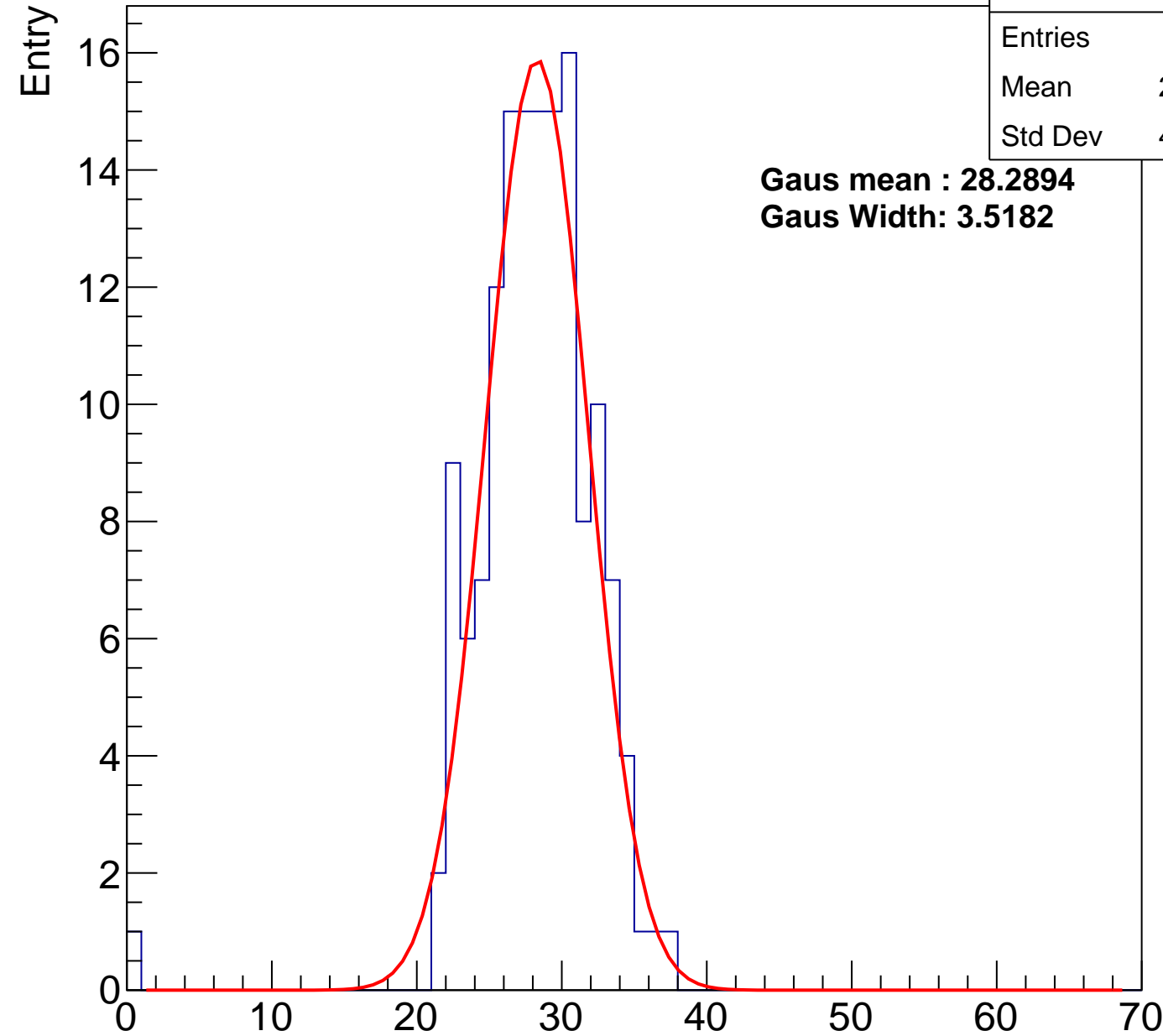
Gaus mean : 28.2894

Gaus Width: 3.5182

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch104, adc1

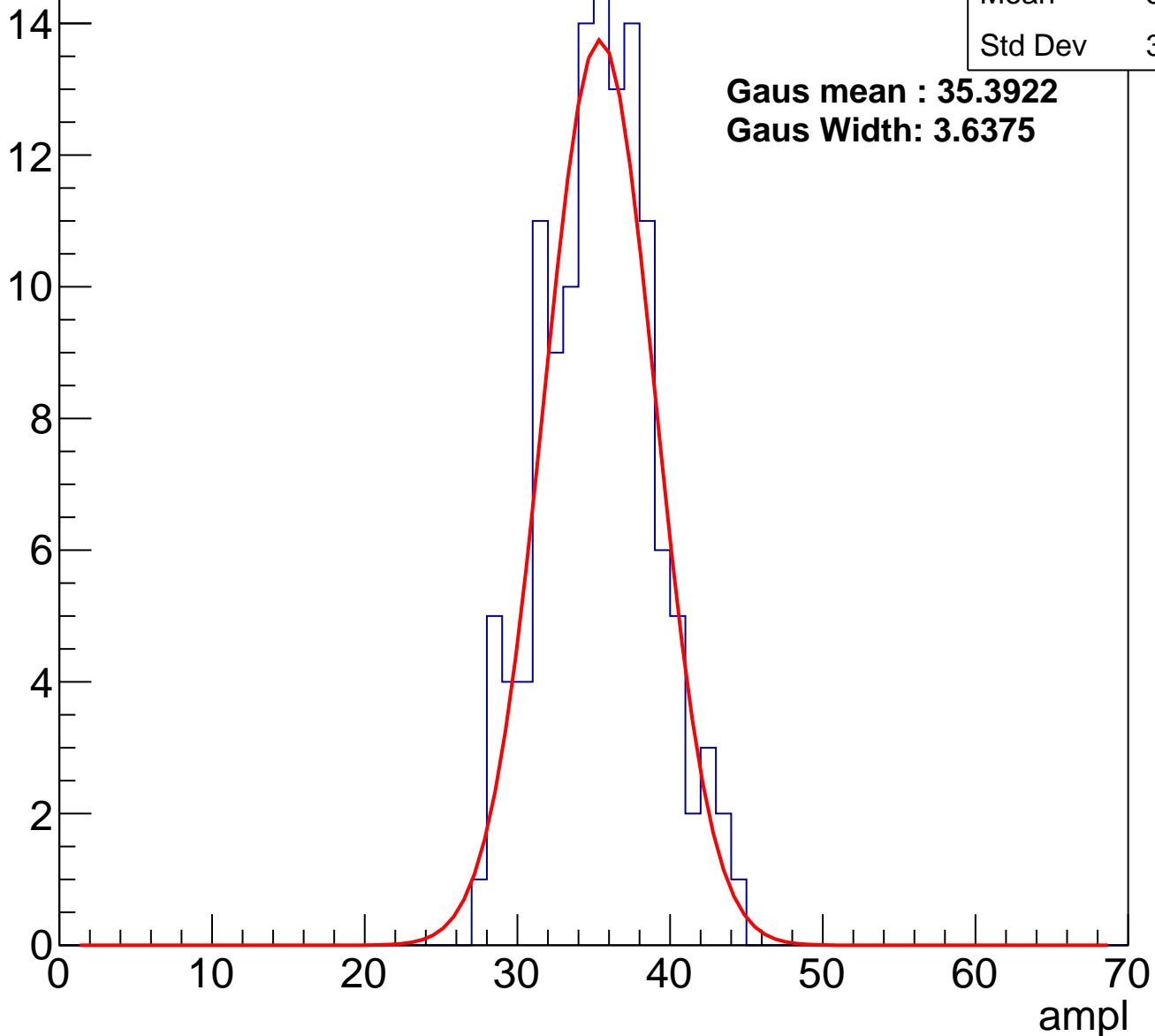
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	34.92
Std Dev	3.595

Gaus mean : 35.3922

Gaus Width: 3.6375

Entry



B1L001S, U14-ch104, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	132
Mean	40.73
Std Dev	3.496

Gaus mean : 41.4053

Gaus Width: 3.3891

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

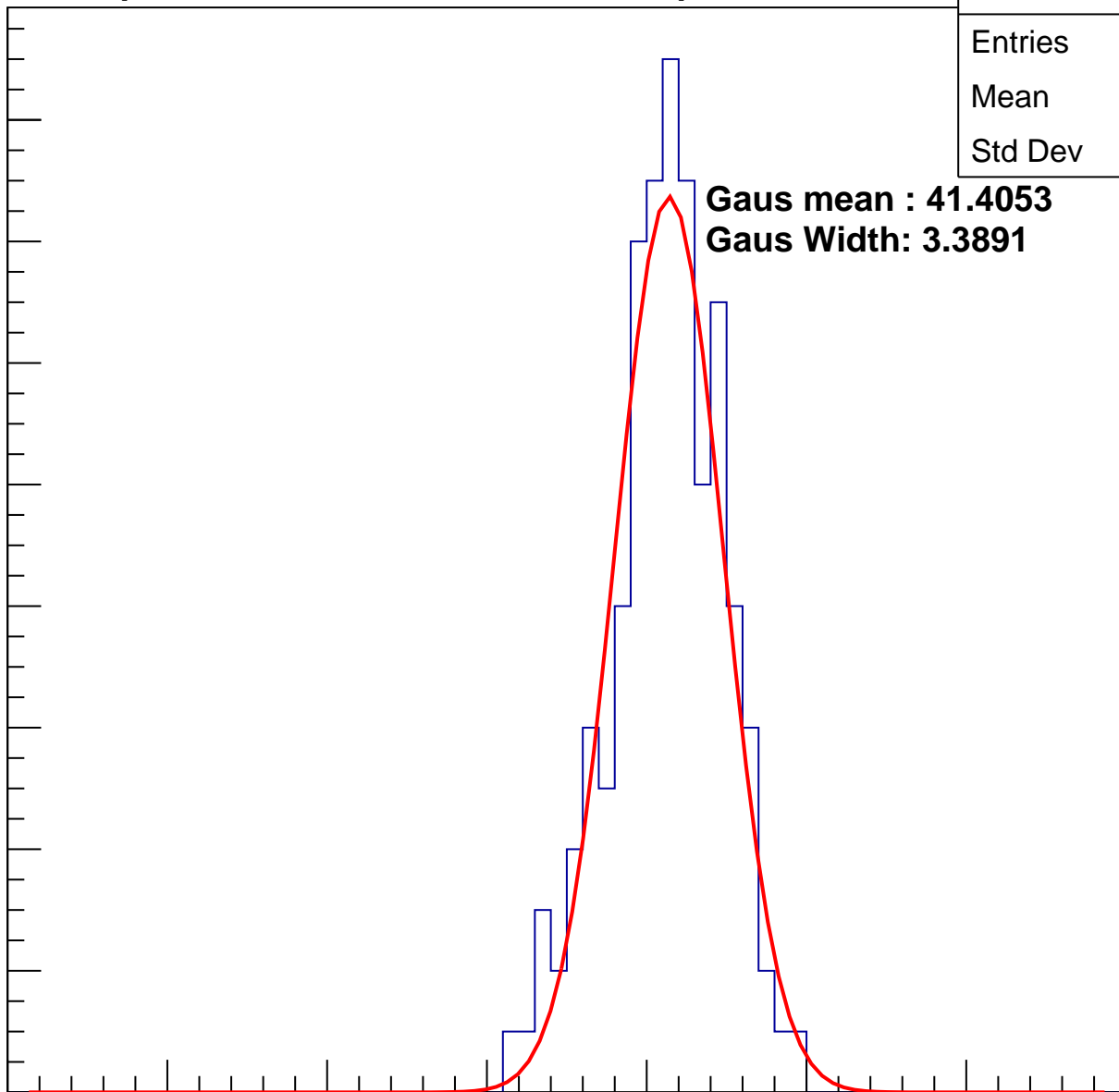
40

50

60

70

ampl

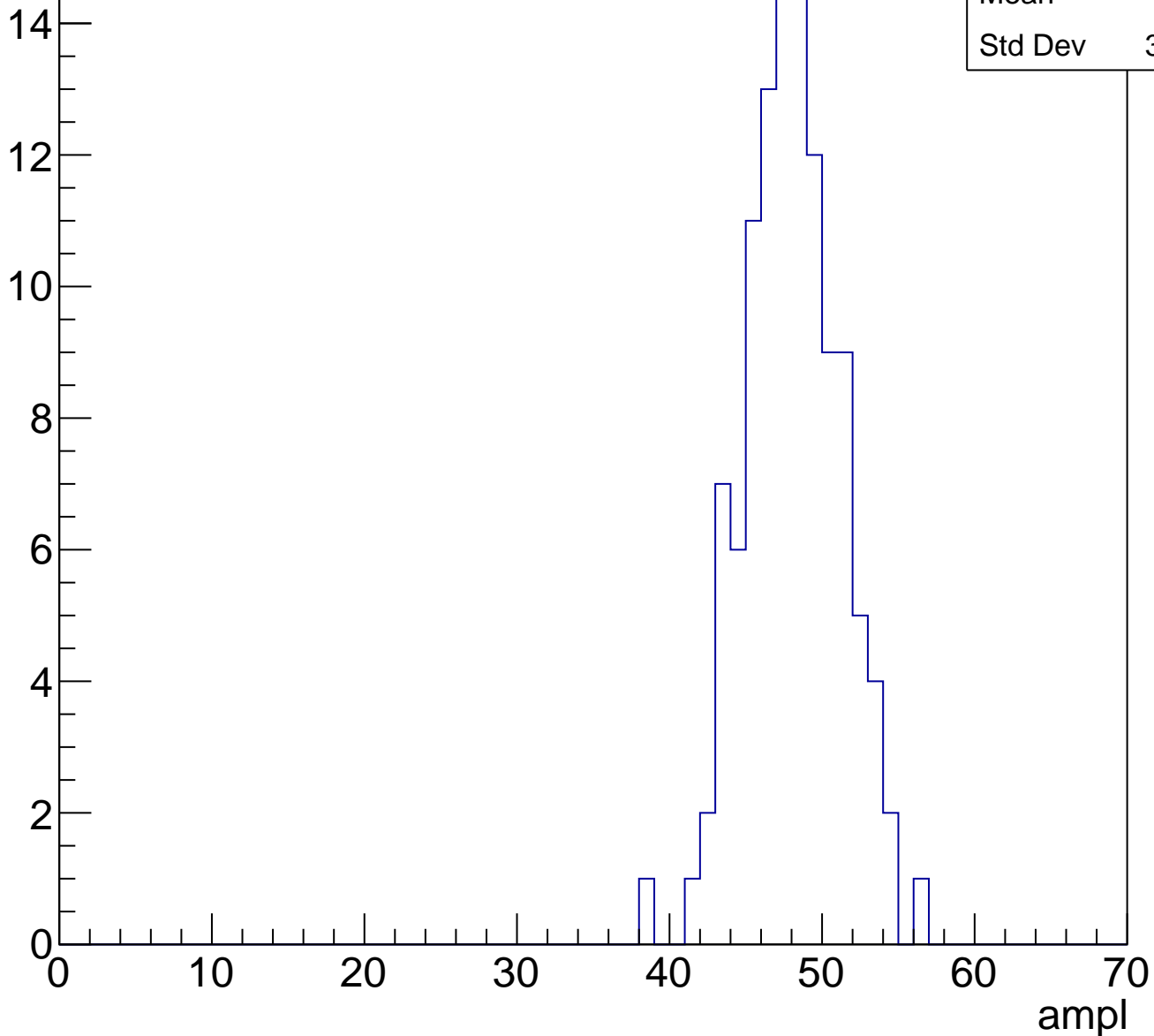


B1L001S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	47.6
Std Dev	3.099

Entry



B1L001S, U14-ch104, adc4

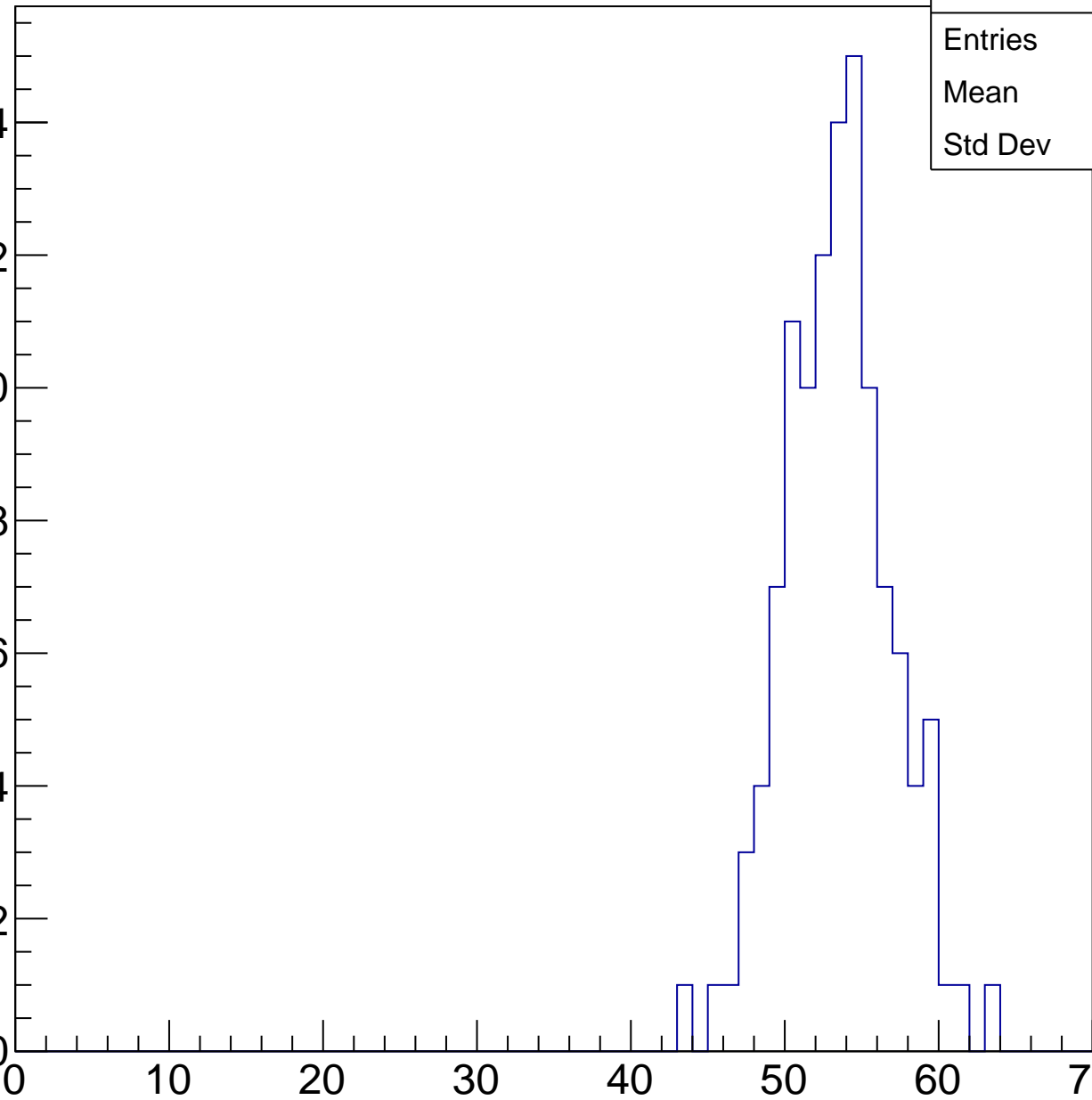
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	52.99
Std Dev	3.493

Entry

14
12
10
8
6
4
2
0

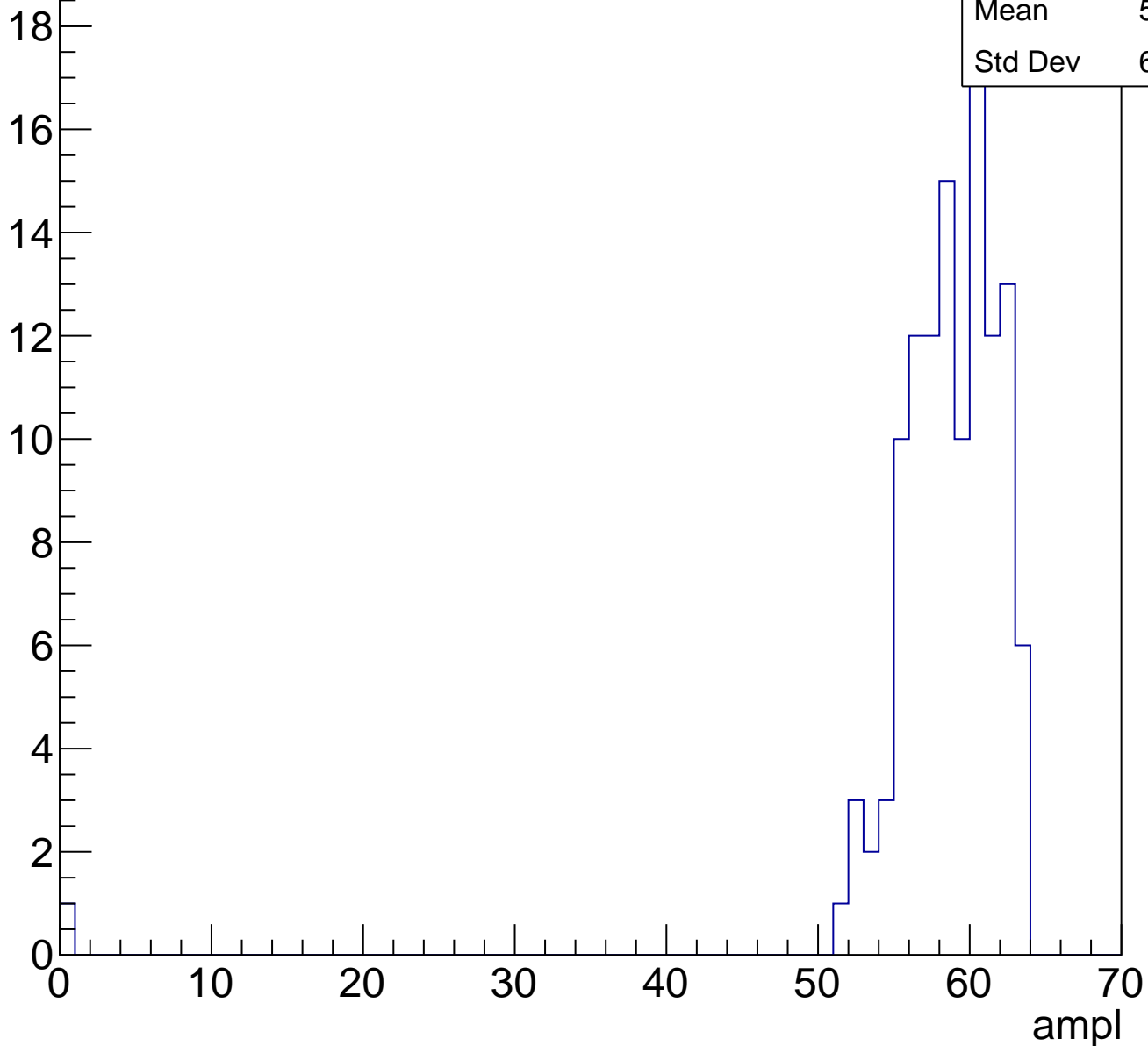
ampl



B1L001S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	33
Mean	59.36
Std Dev	10.65

0

2

4

6

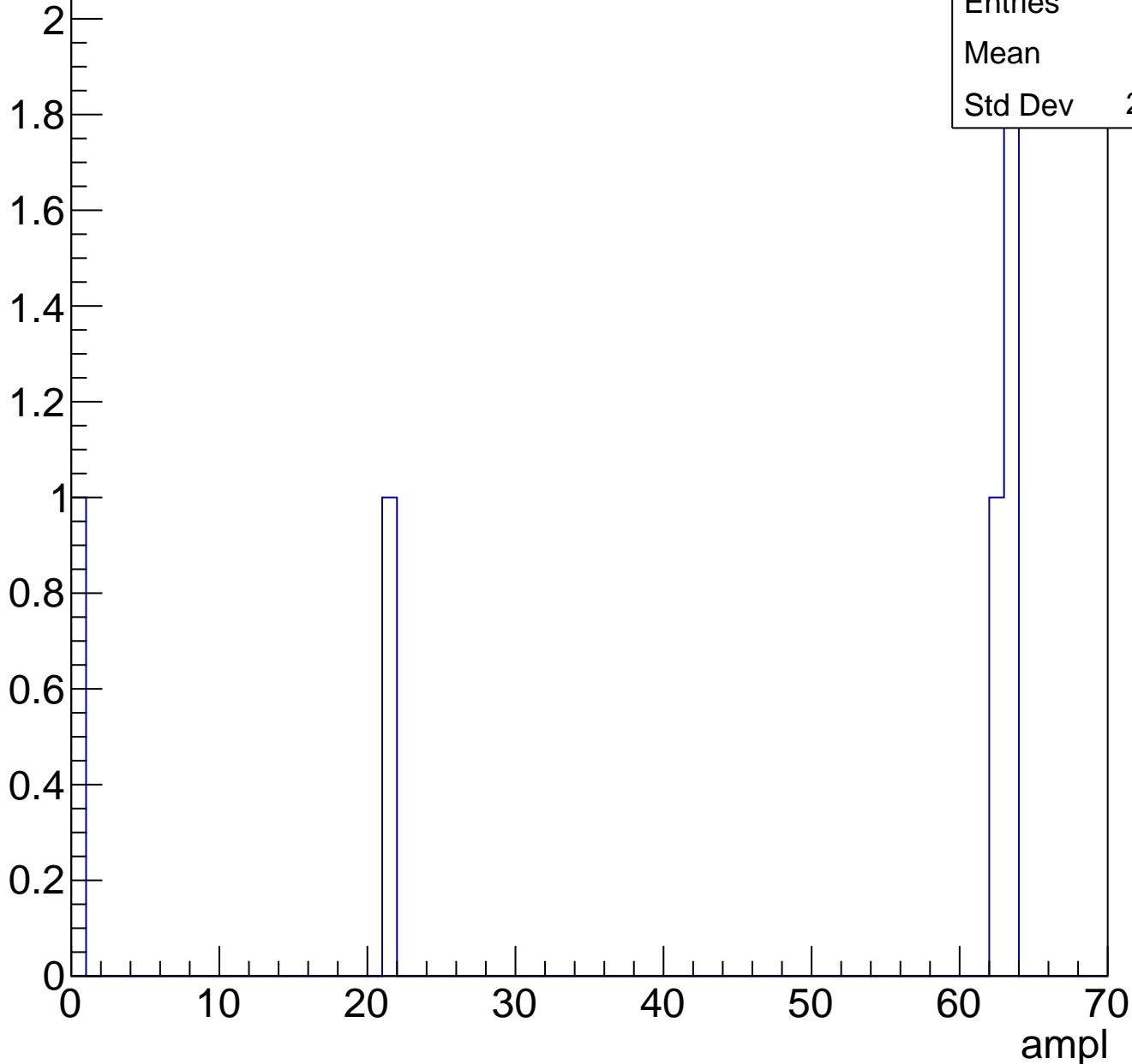
8

10

B1L001S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch105, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	28.92
Std Dev	4.047

Gaus mean : 29.9729

Gaus Width: 3.1779

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

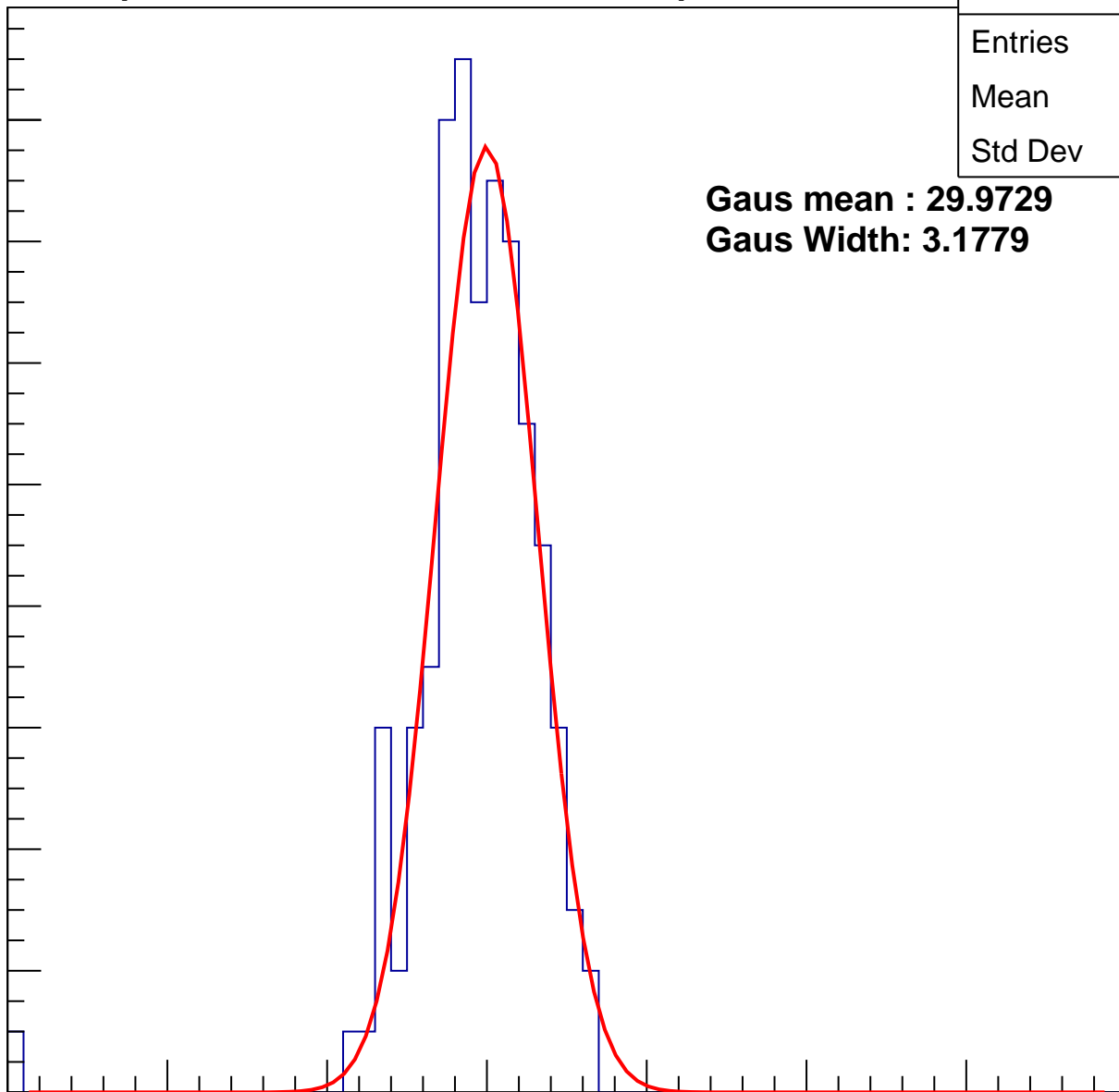
40

50

60

70

ampl



B1L001S, U14-ch105, adc1

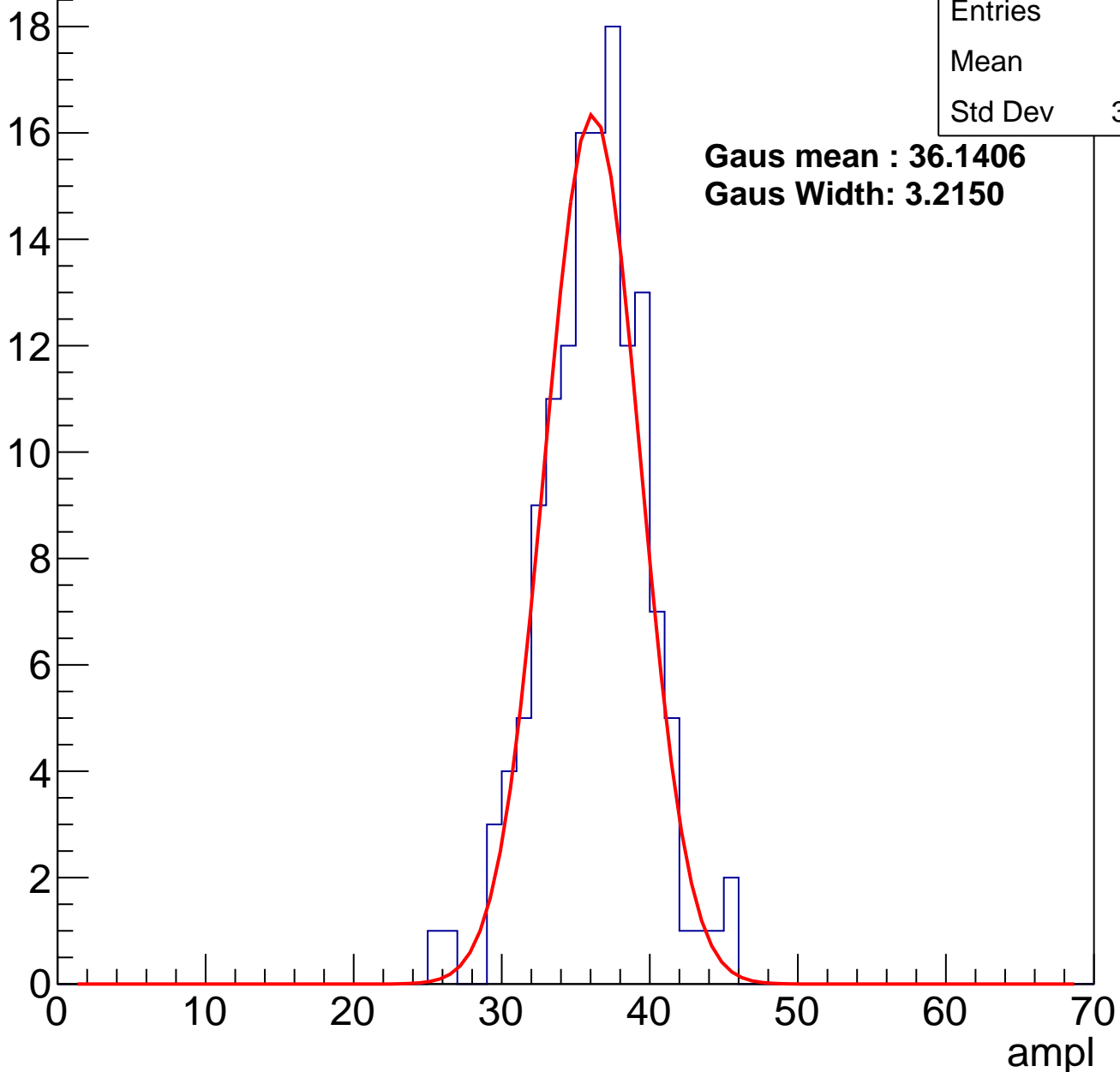
calib_packv5_042523_0143.root, FC#2, port C2

Entries	138
Mean	35.8
Std Dev	3.478

Gaus mean : 36.1406

Gaus Width: 3.2150

Entry



B1L001S, U14-ch105, adc2

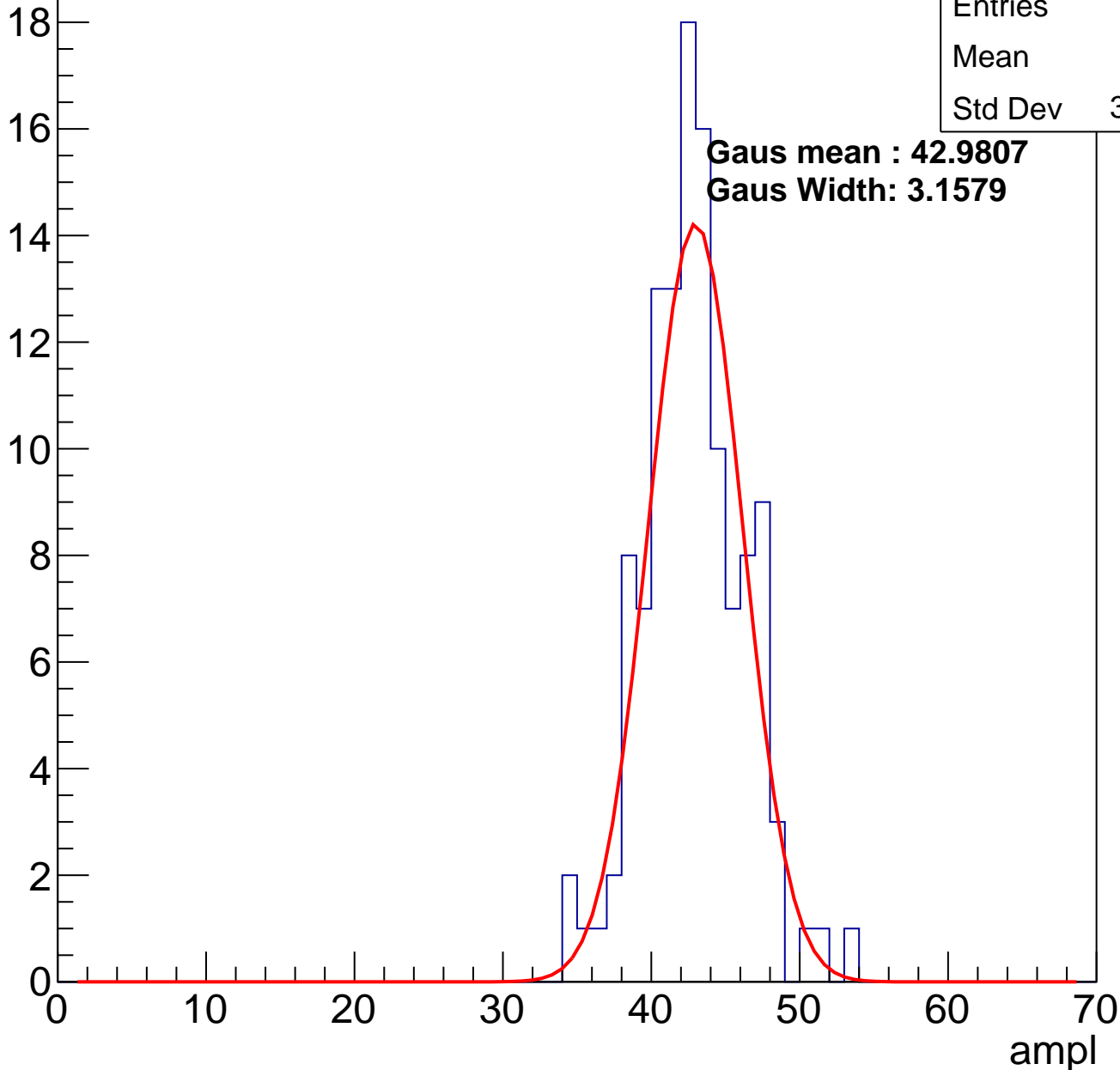
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	42.4
Std Dev	3.334

Gaus mean : 42.9807

Gaus Width: 3.1579

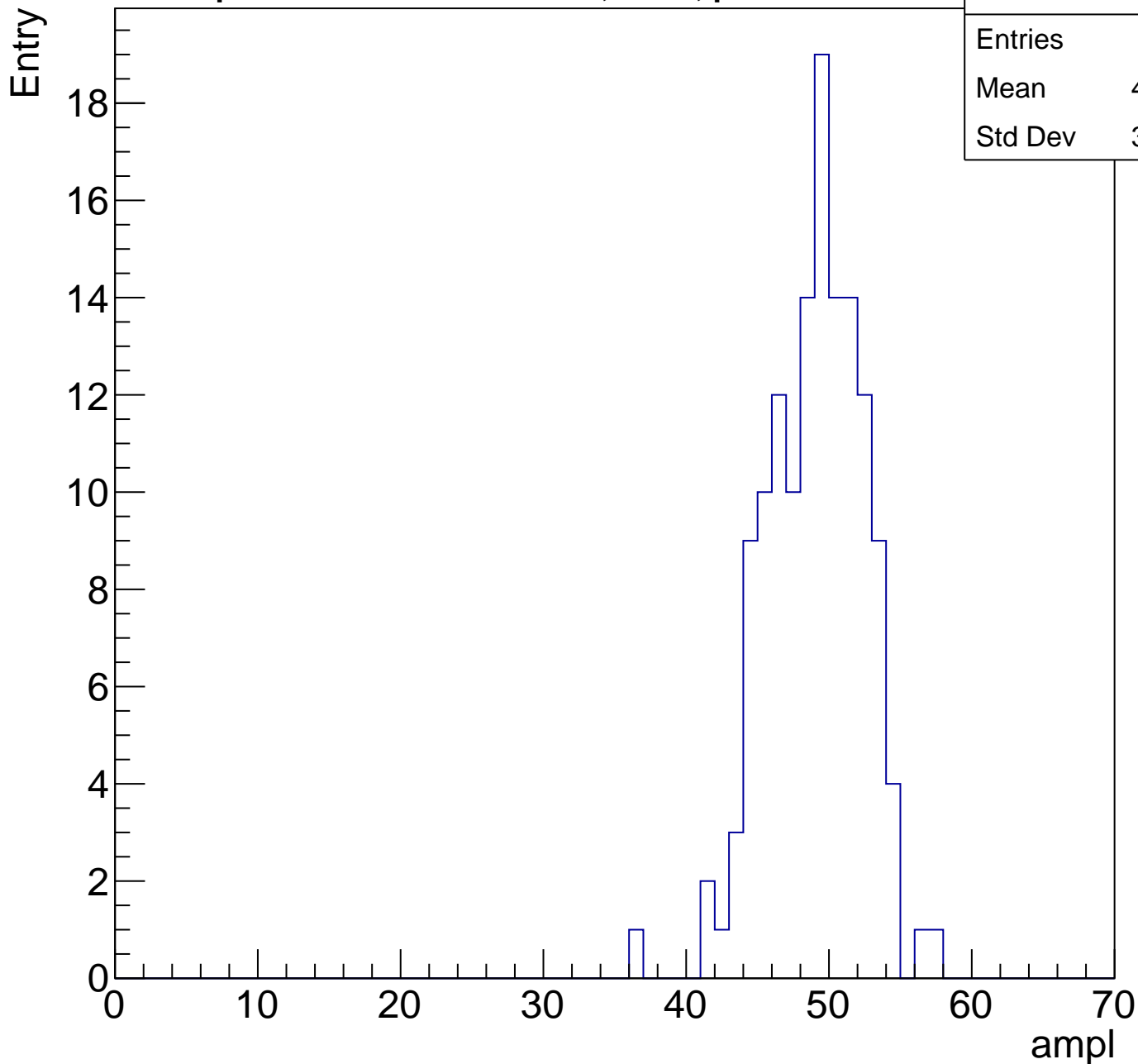
Entry



B1L001S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#2, port C2

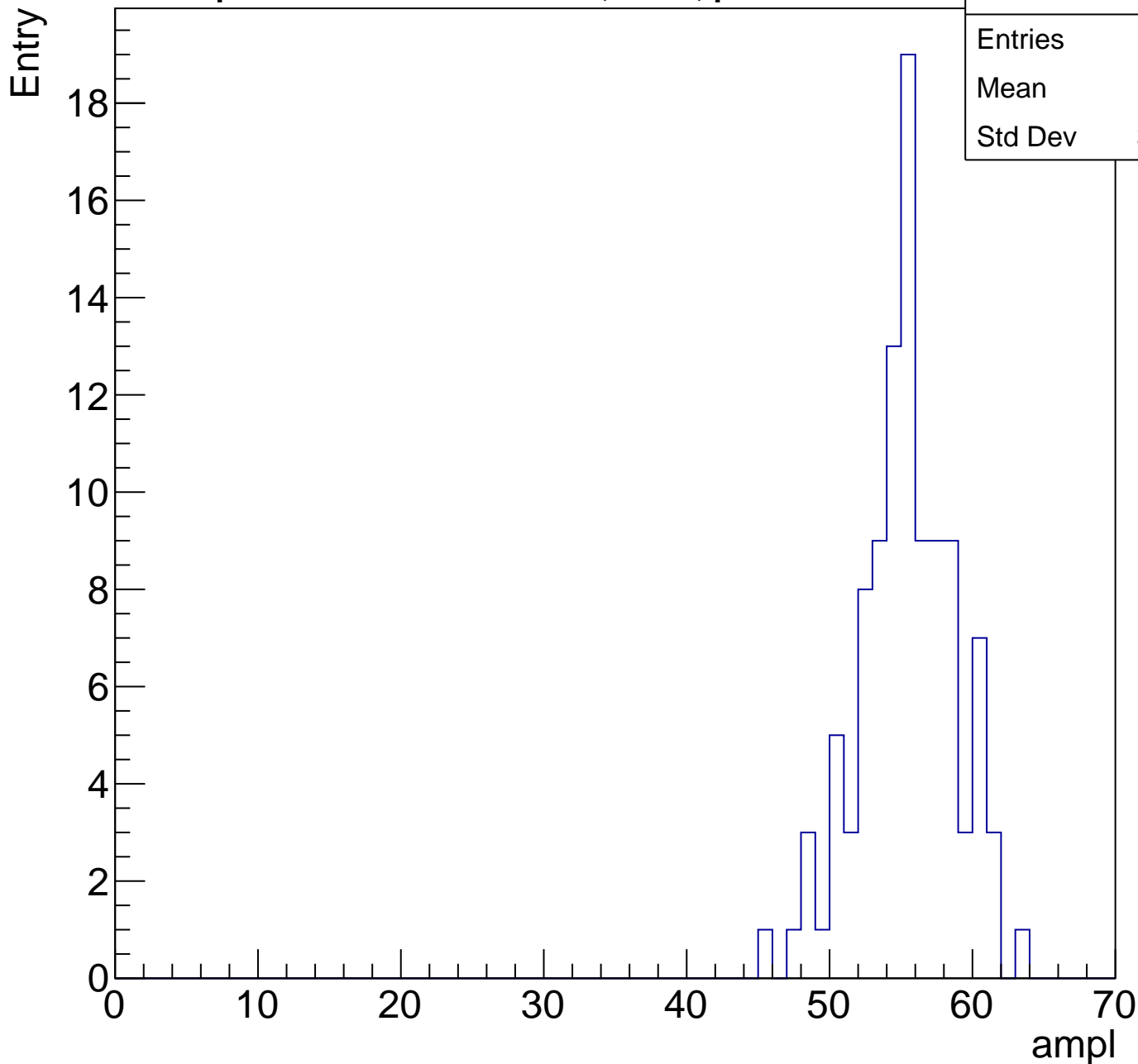
Entries	136
Mean	48.56
Std Dev	3.332



B1L001S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	104
Mean	54.9
Std Dev	3.341



B1L001S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	58.19
Std Dev	8.483

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

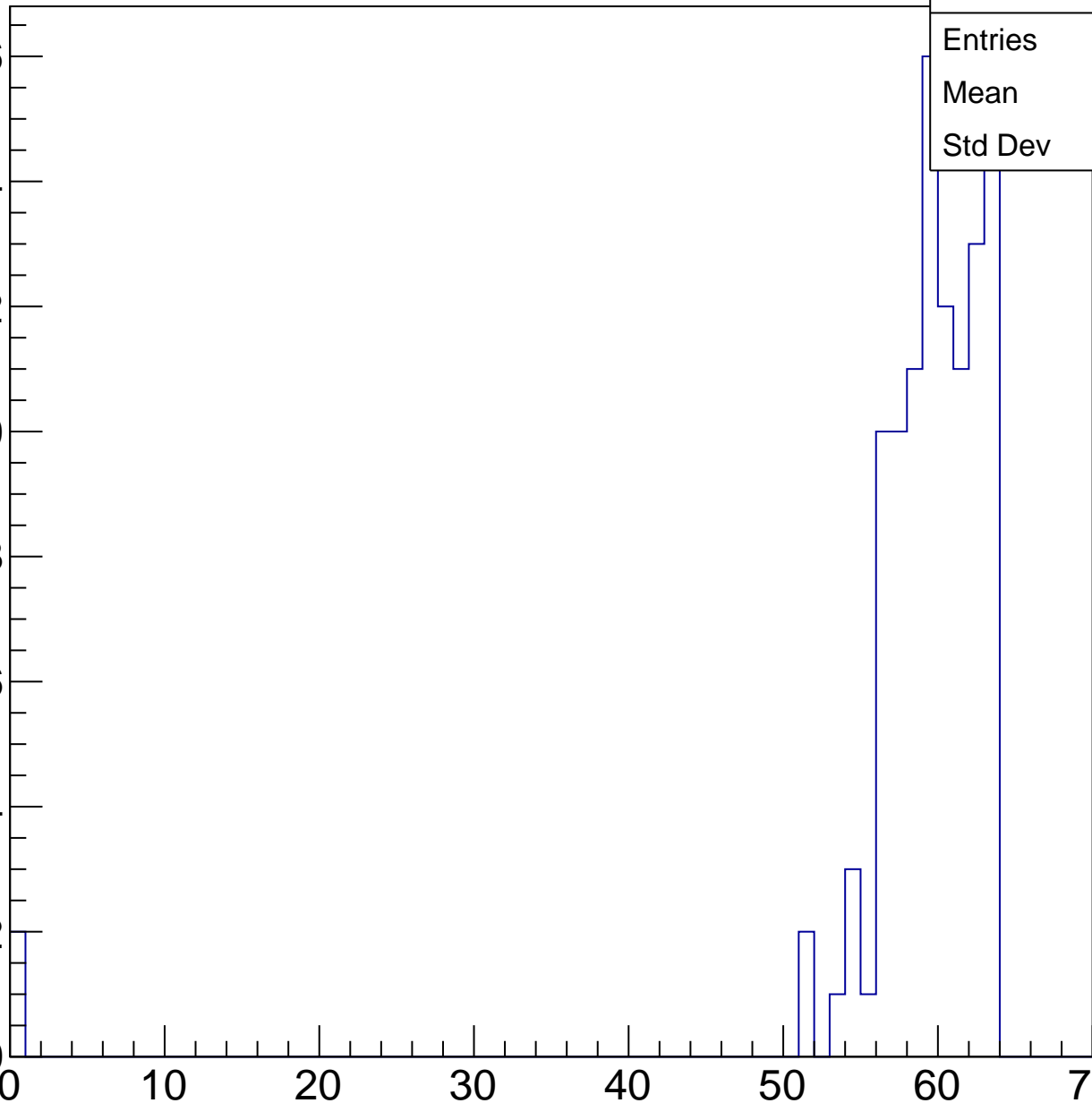
40

50

60

70

ampl

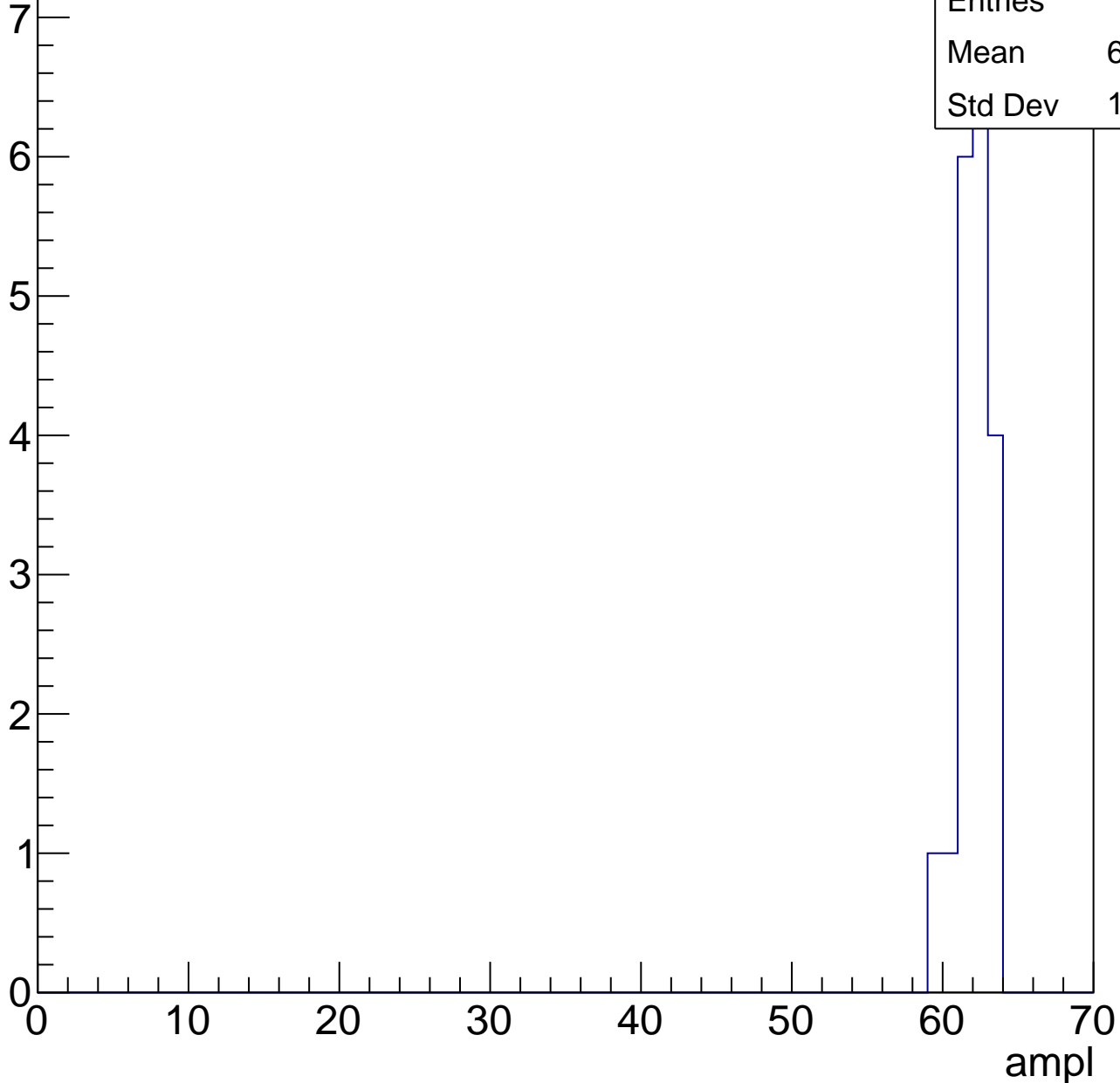


B1L001S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.63
Std Dev	1.037



B1L001S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch106, adc0

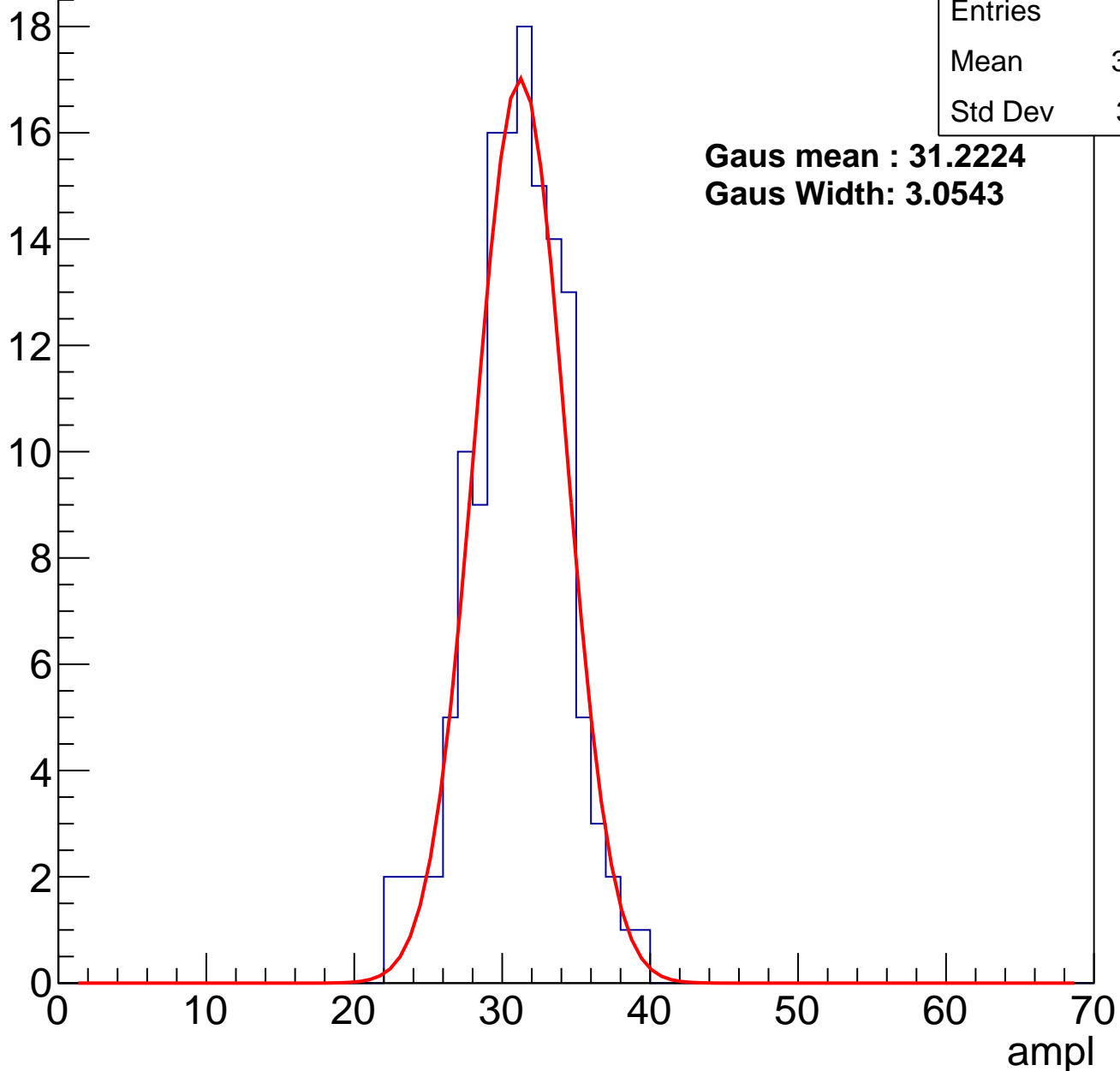
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	30.59
Std Dev	3.221

Gaus mean : 31.2224

Gaus Width: 3.0543

Entry



B1L001S, U14-ch106, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	85
Mean	36.68
Std Dev	2.959

Gaus mean : 37.2539

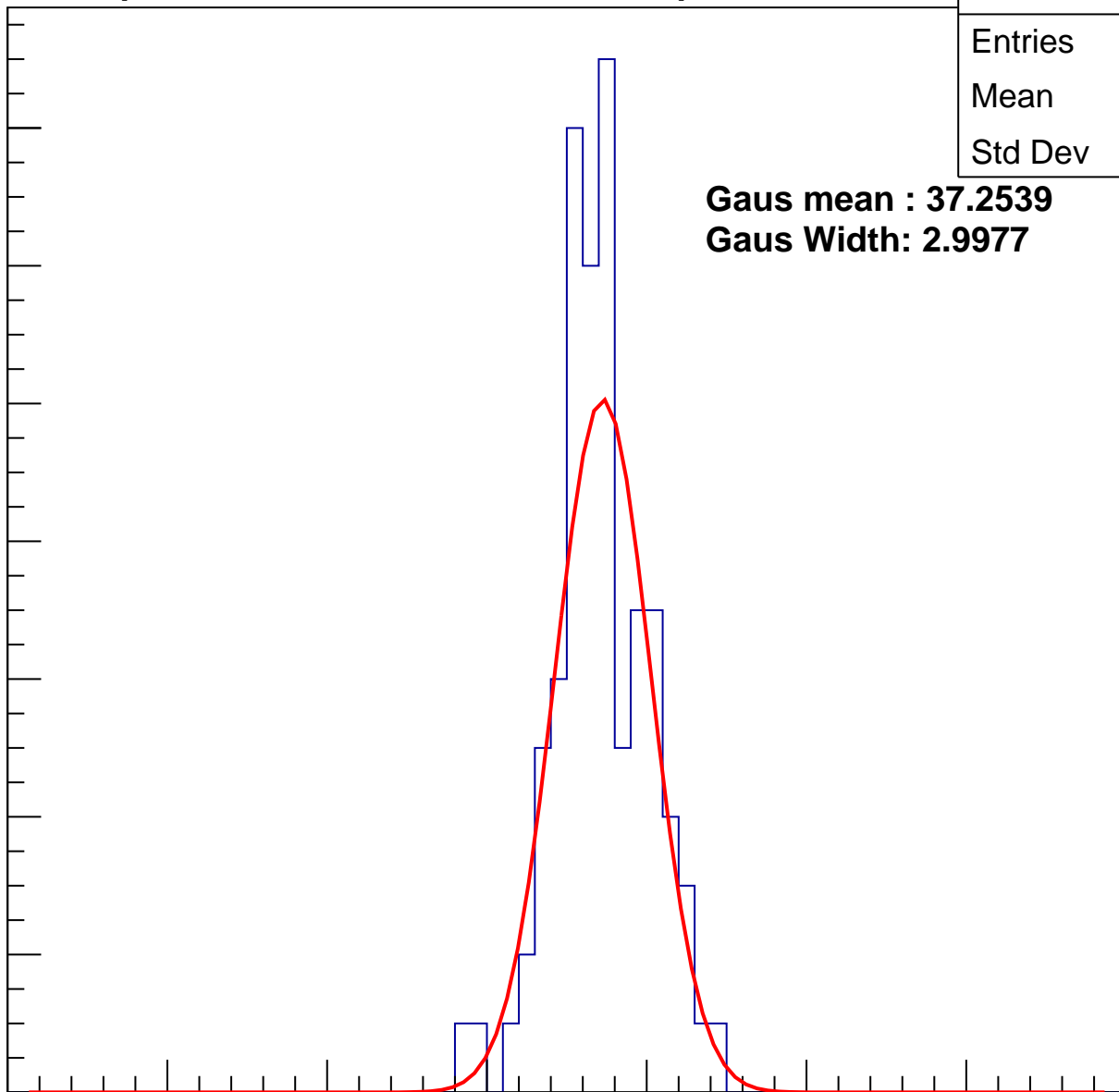
Gaus Width: 2.9977

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch106, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	151
Mean	42.09
Std Dev	3.536

Gaus mean : 42.8848

Gaus Width: 3.4918

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

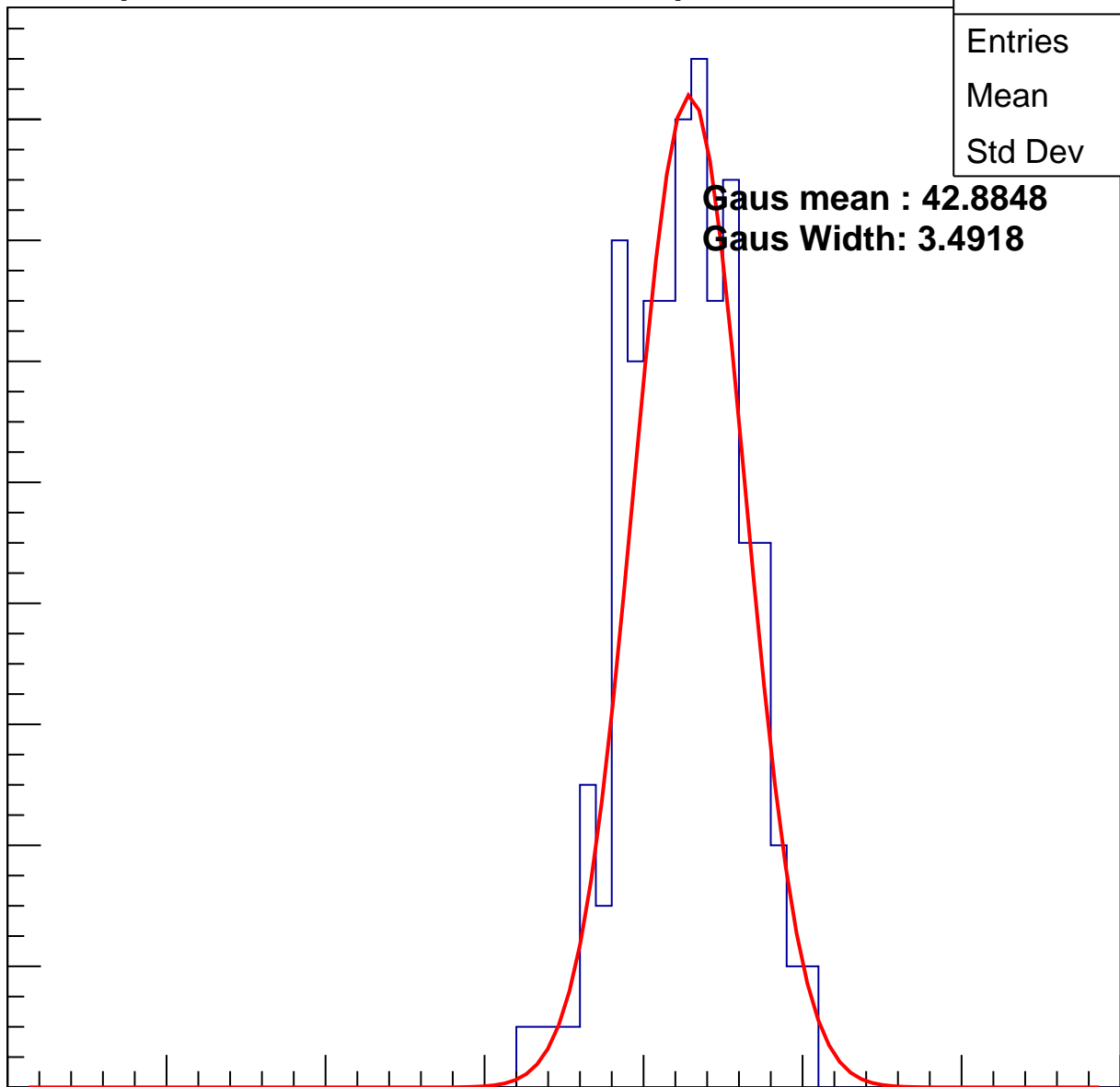
40

50

60

70

ampl



B1L001S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	48.63
Std Dev	3.14

Entry

12

10

8

6

4

2

0

0

10

20

30

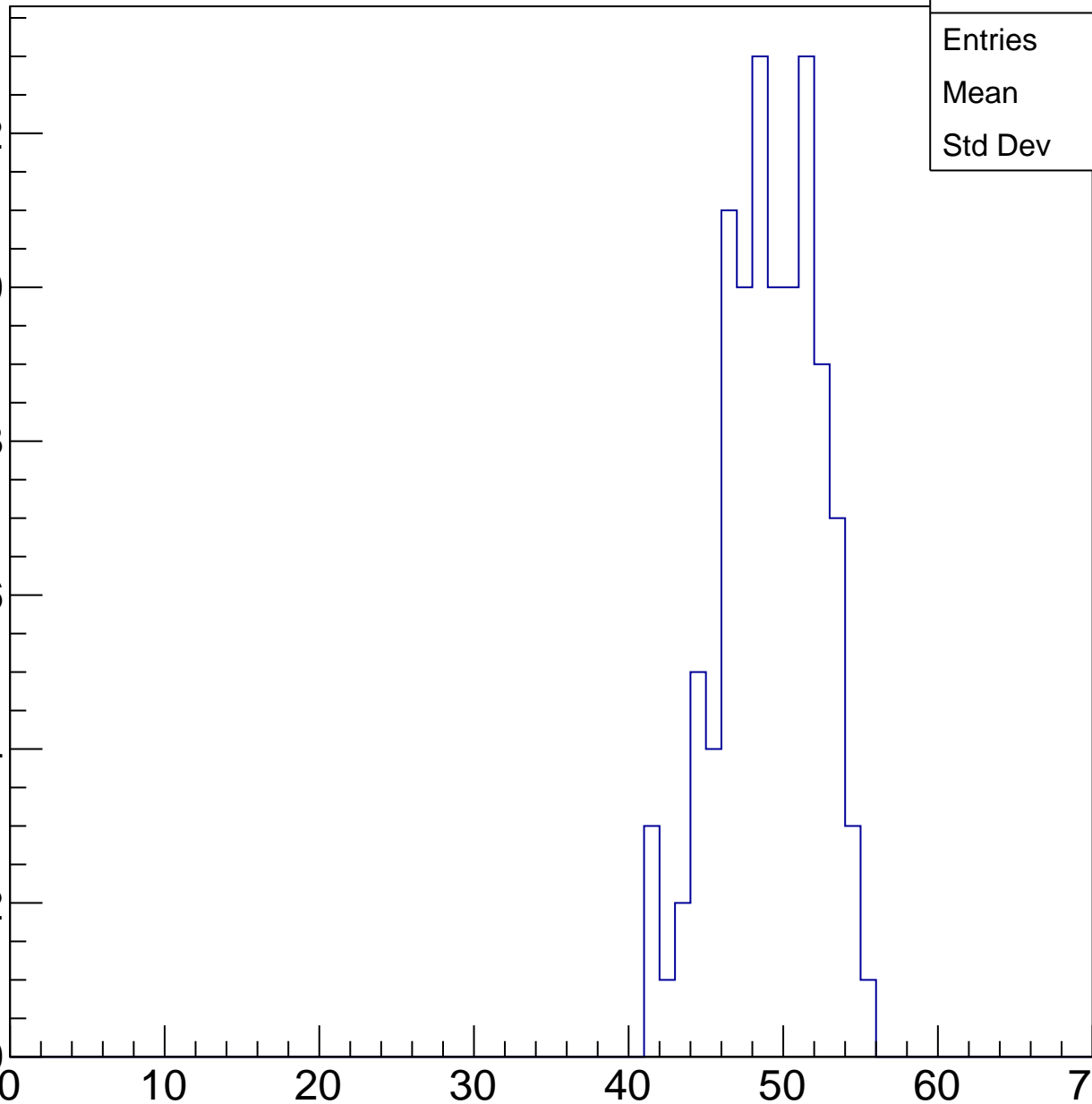
40

50

60

70

ampl



B1L001S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	53.62
Std Dev	3.37

Entry

14

12

10

8

6

4

2

0

0

10

20

30

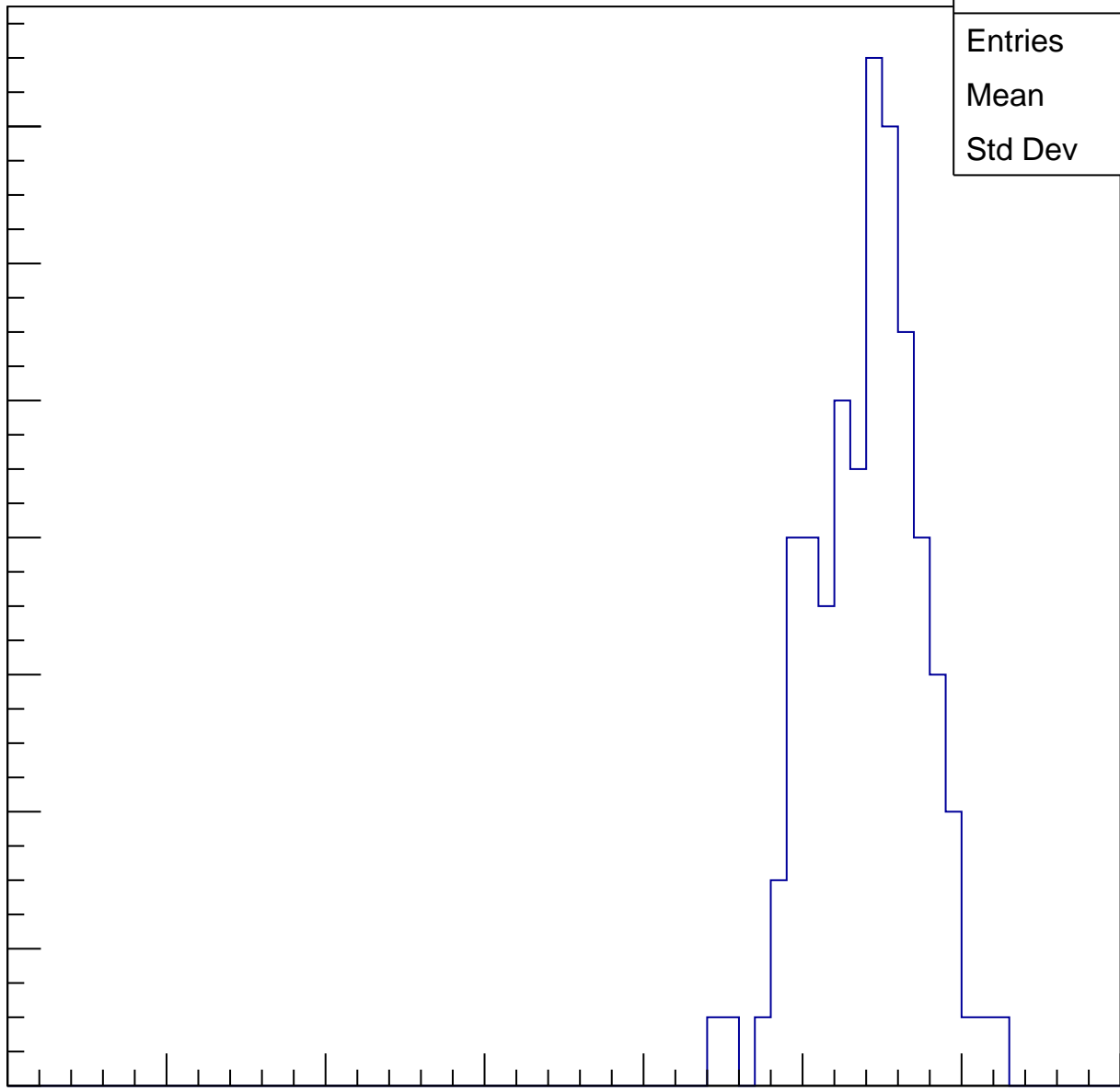
40

50

60

70

ampl



B1L001S, U14-ch106, adc5

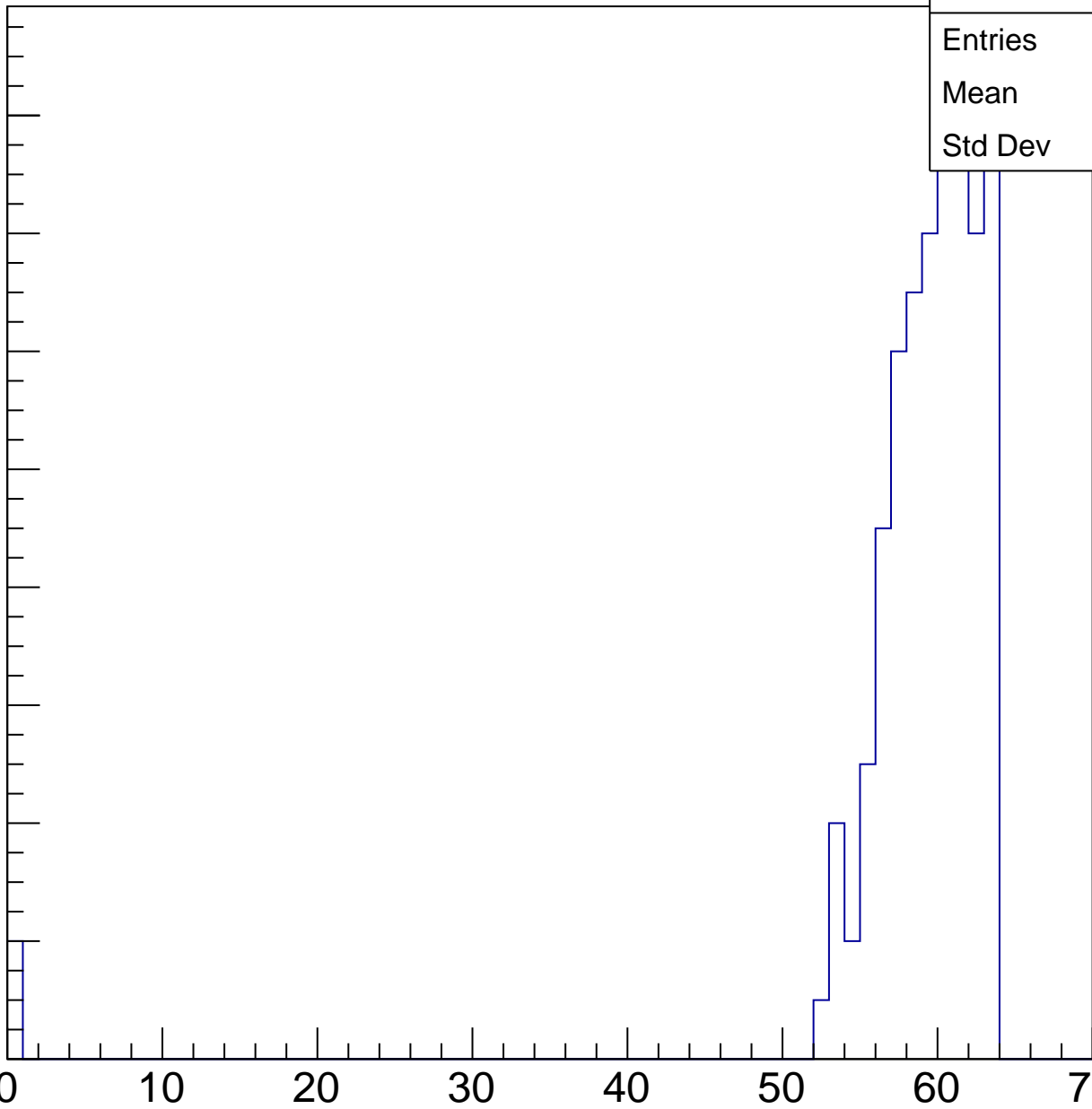
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	126
Mean	58.34
Std Dev	7.895

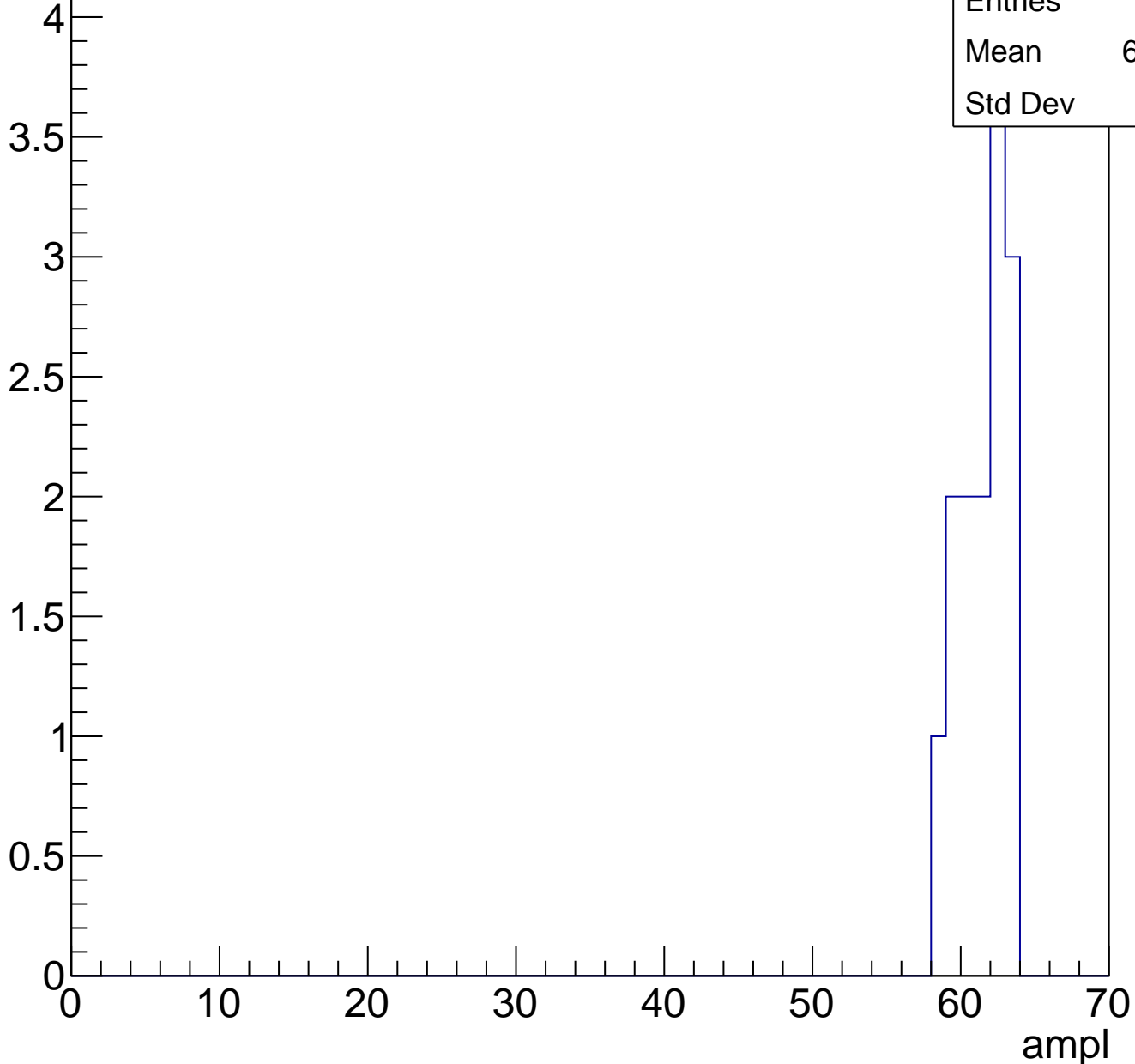
ampl



B1L001S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

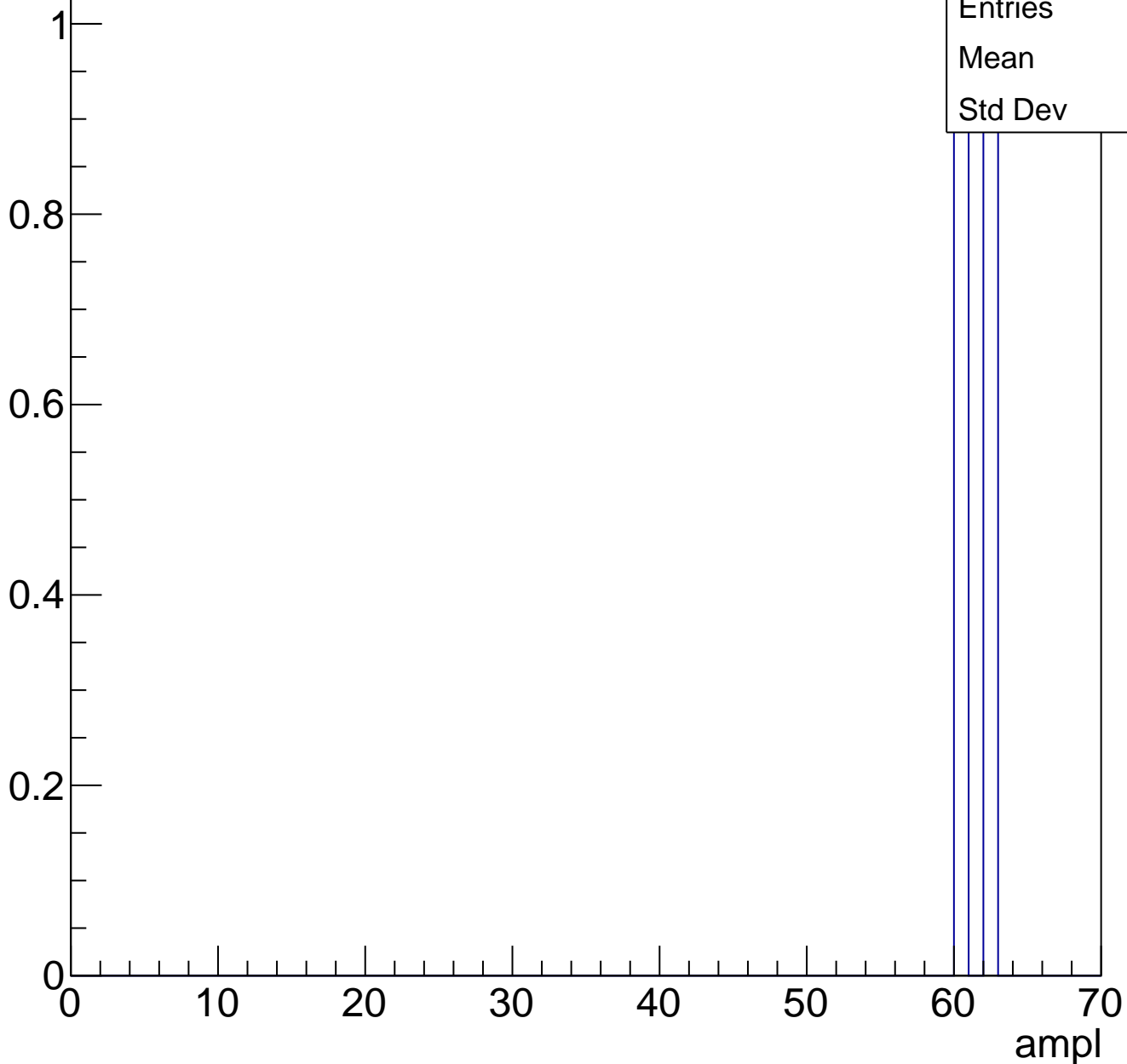


Entries	14
Mean	61.07
Std Dev	1.58

B1L001S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch107, adc0

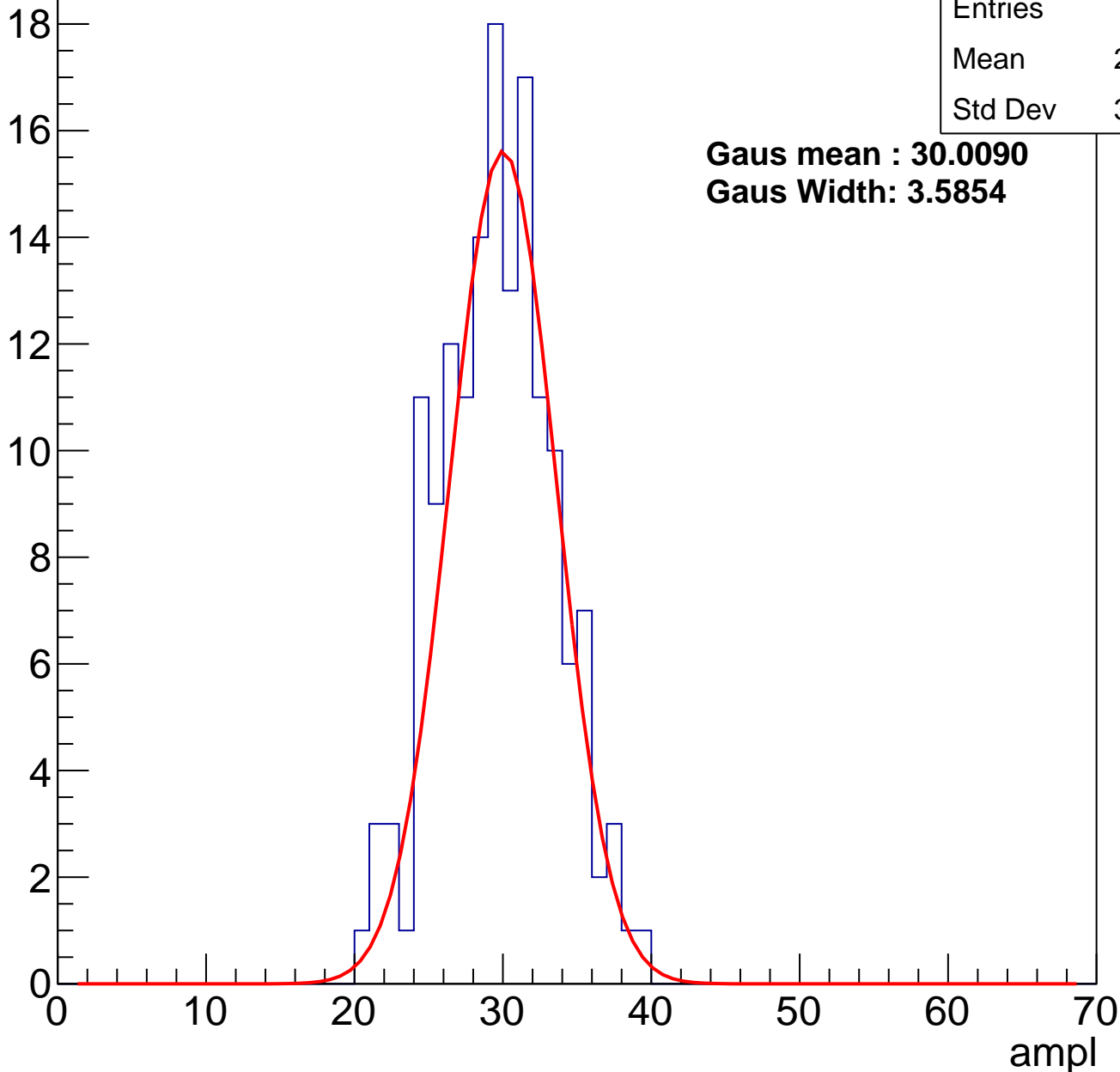
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	29.17
Std Dev	3.822

Gaus mean : 30.0090

Gaus Width: 3.5854

Entry



B1L001S, U14-ch107, adc1

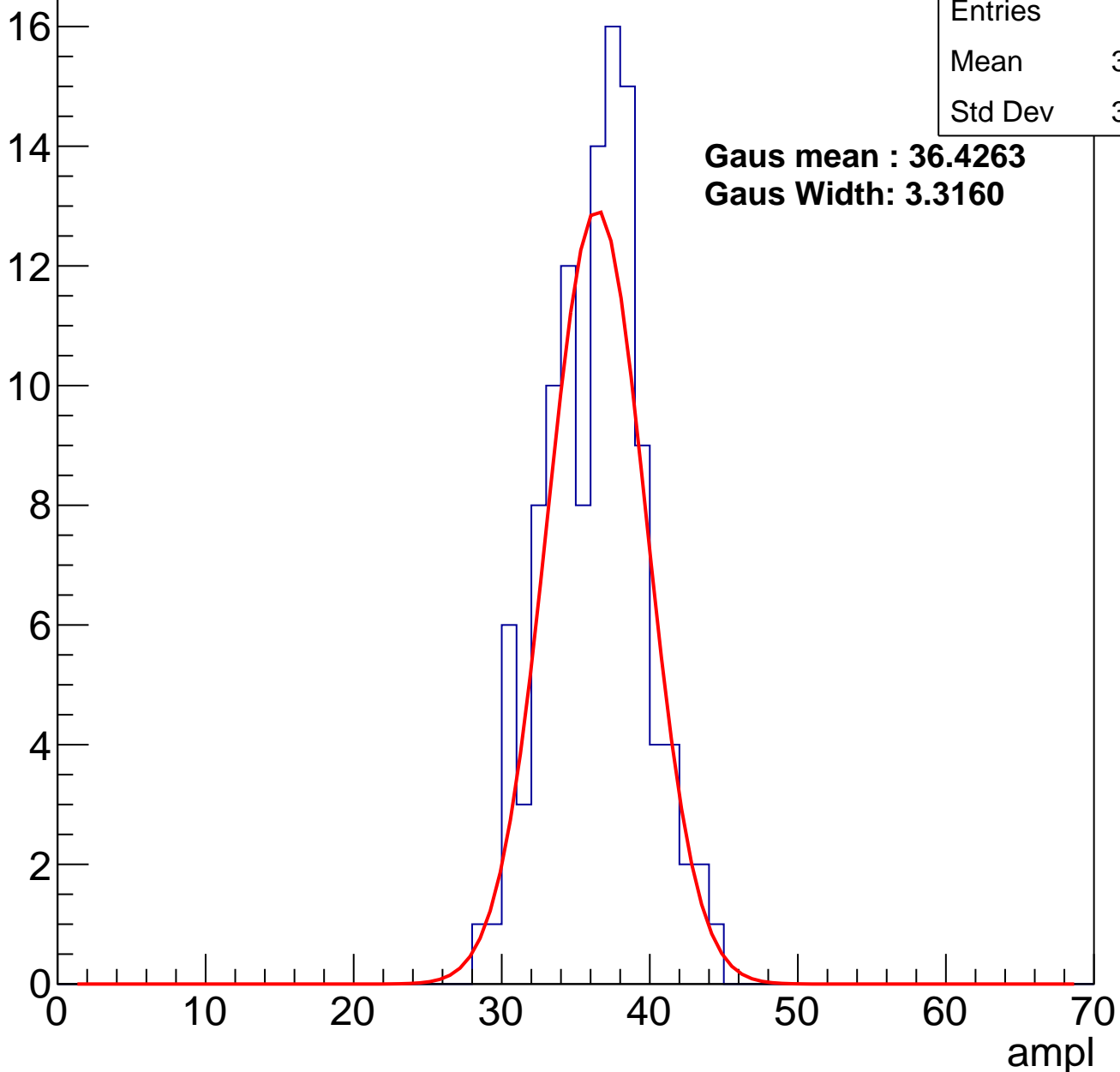
calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	35.85
Std Dev	3.257

Gaus mean : 36.4263

Gaus Width: 3.3160

Entry



B1L001S, U14-ch107, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	149
Mean	42.63
Std Dev	3.783

Gaus mean : 43.2314

Gaus Width: 2.9799

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

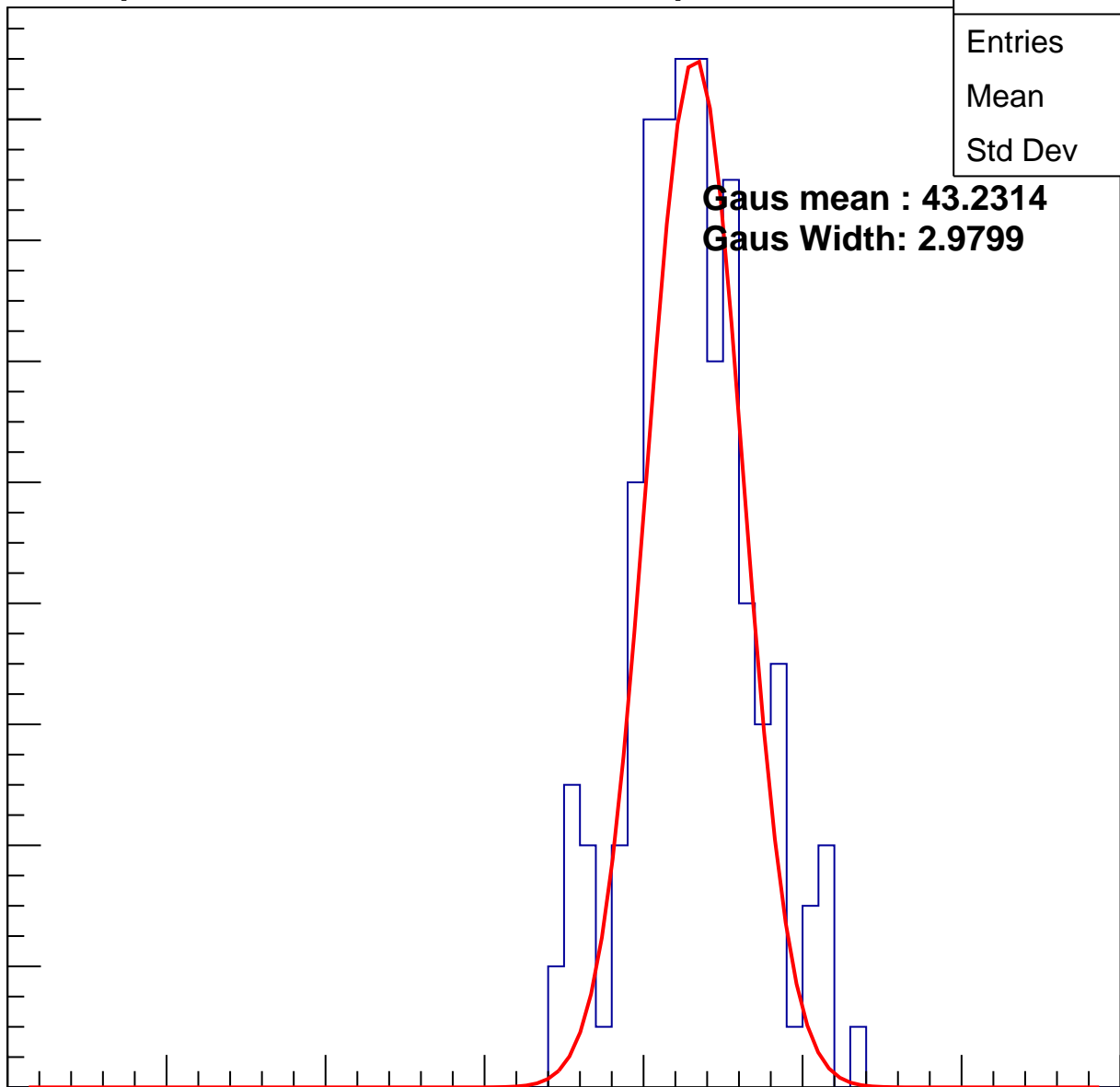
40

50

60

70

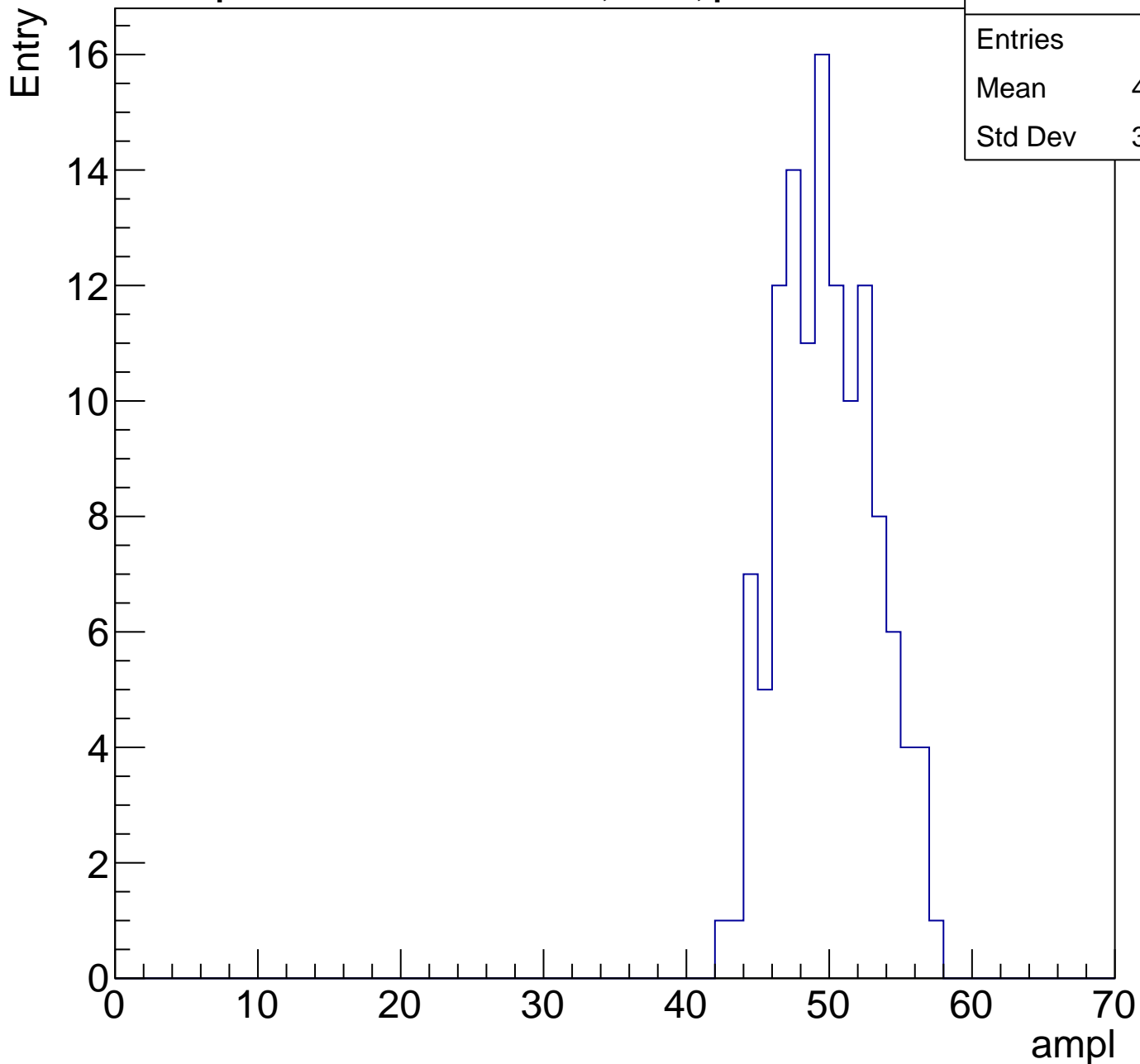
ampl



B1L001S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#2, port C2

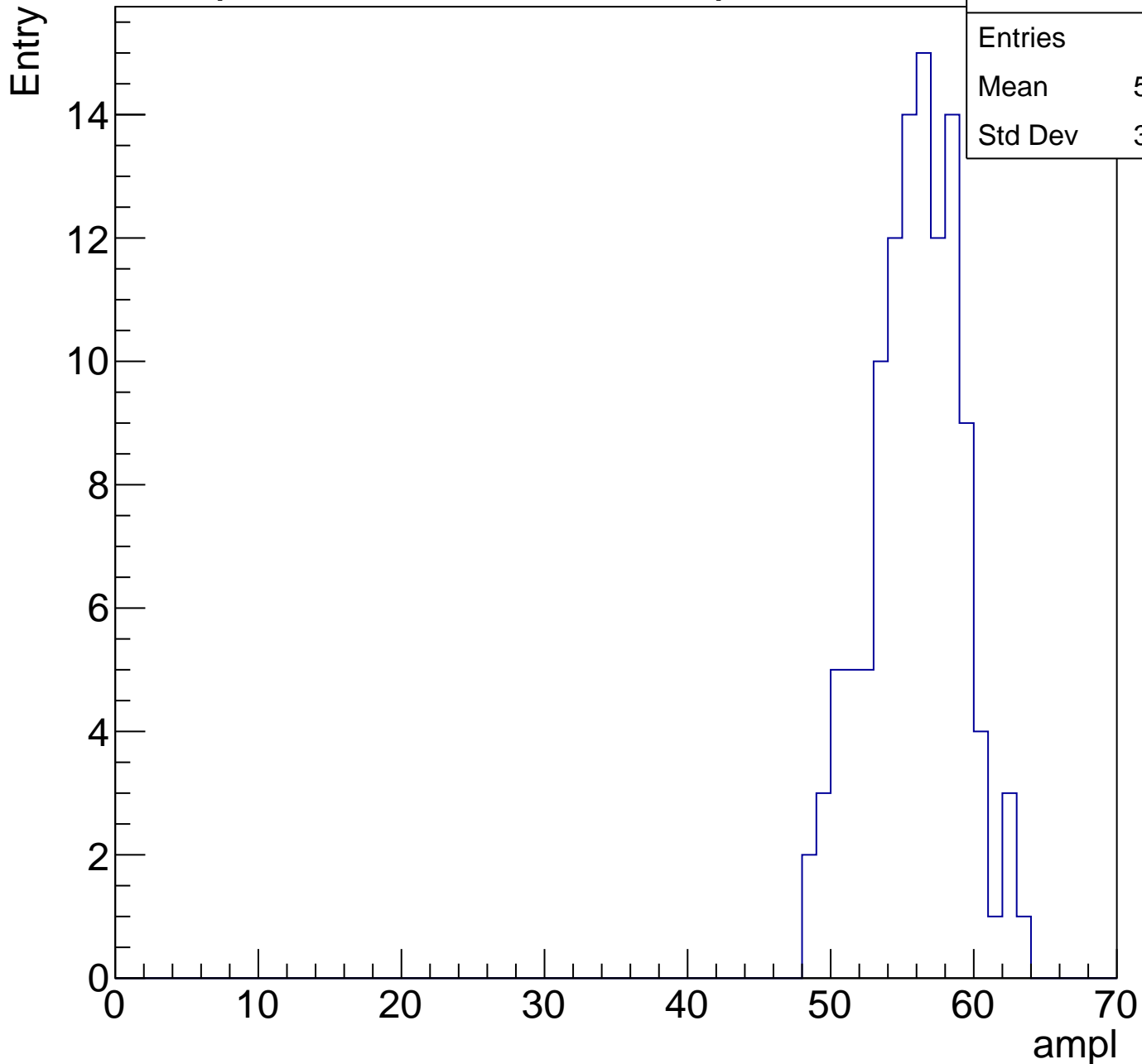
Entries	124
Mean	49.38
Std Dev	3.286



B1L001S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	55.42
Std Dev	3.187

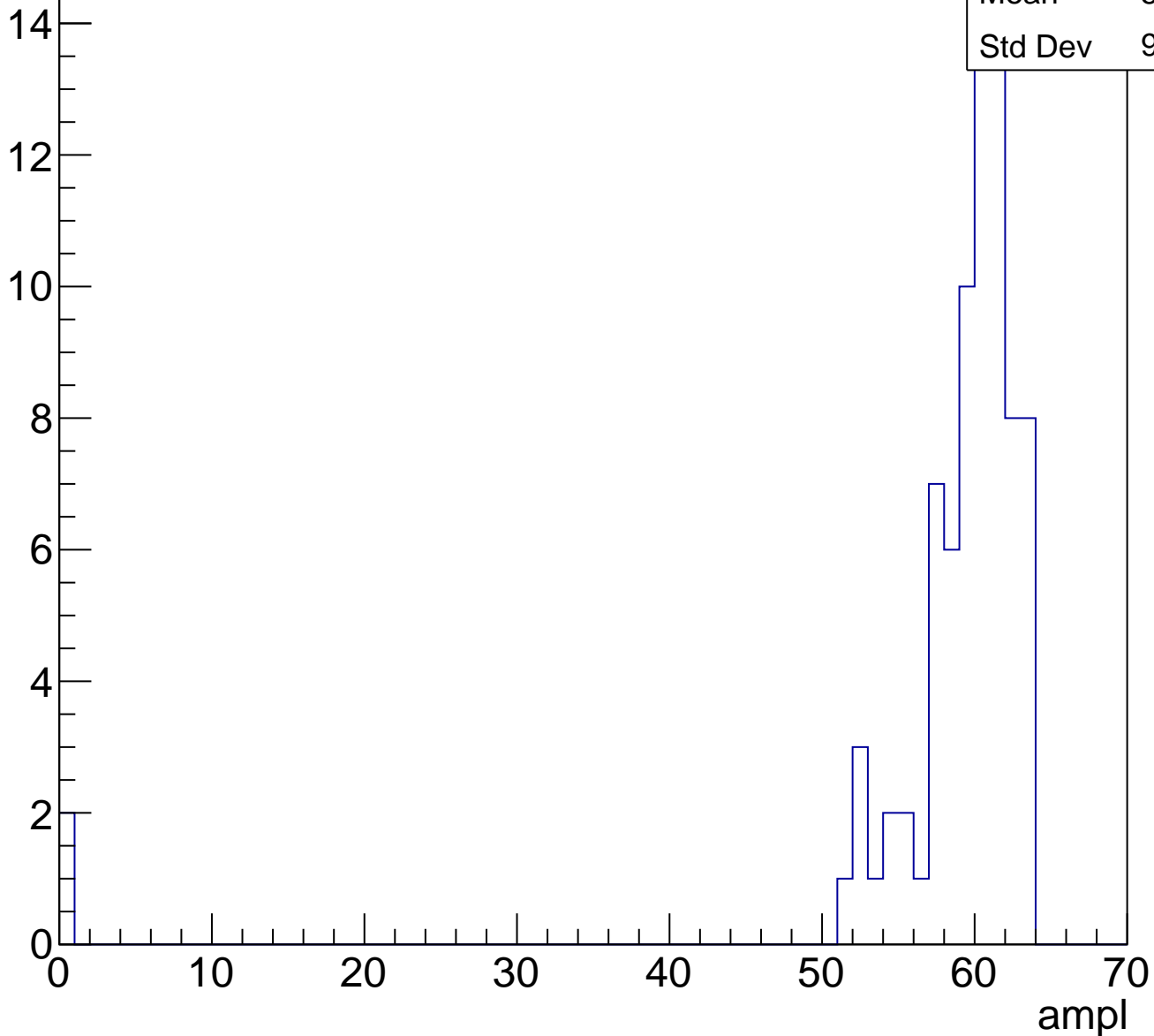


B1L001S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	81
Mean	57.85
Std Dev	9.624

Entry

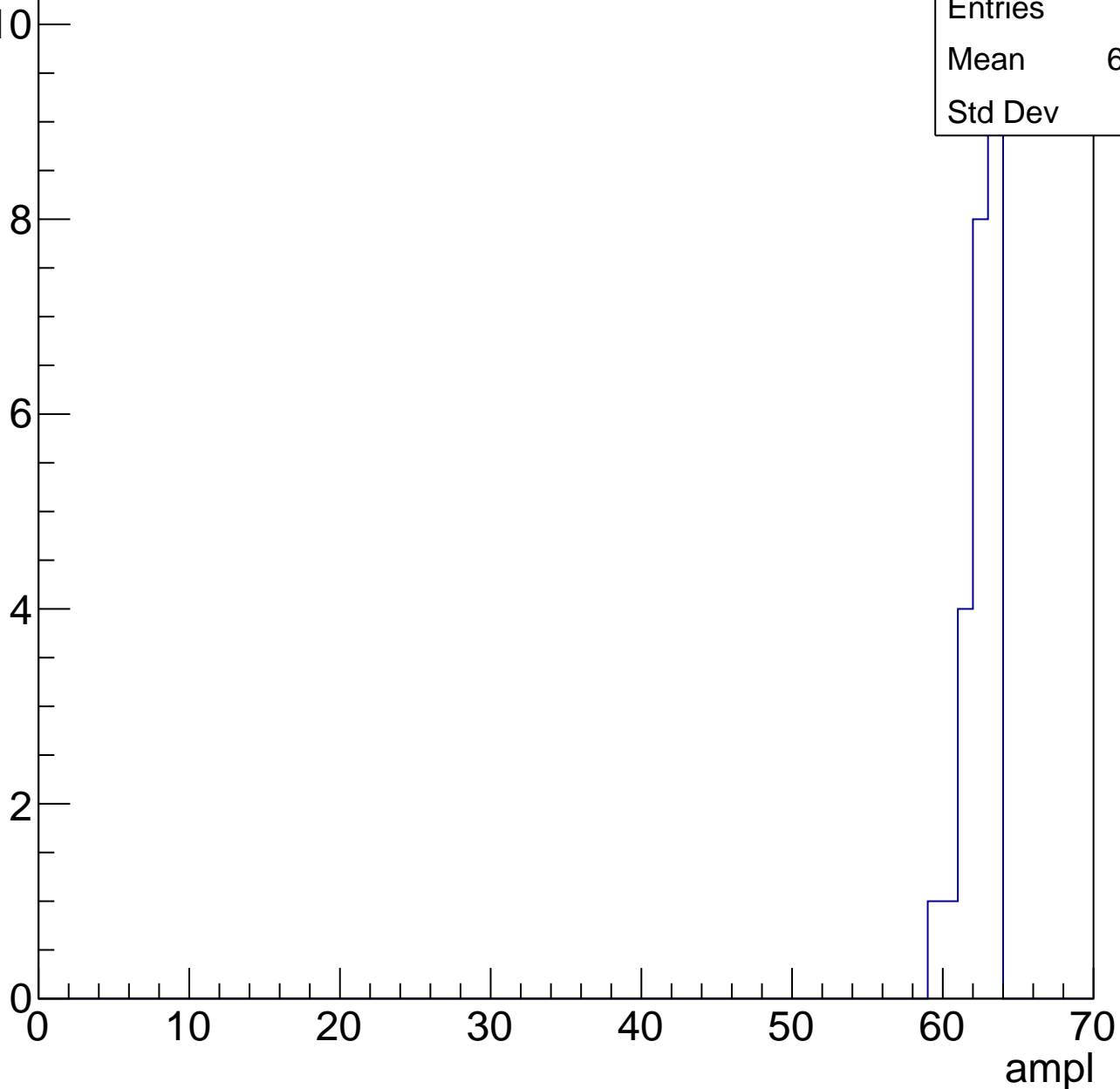


B1L001S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	24
Mean	62.04
Std Dev	1.06



B1L001S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch108, adc0

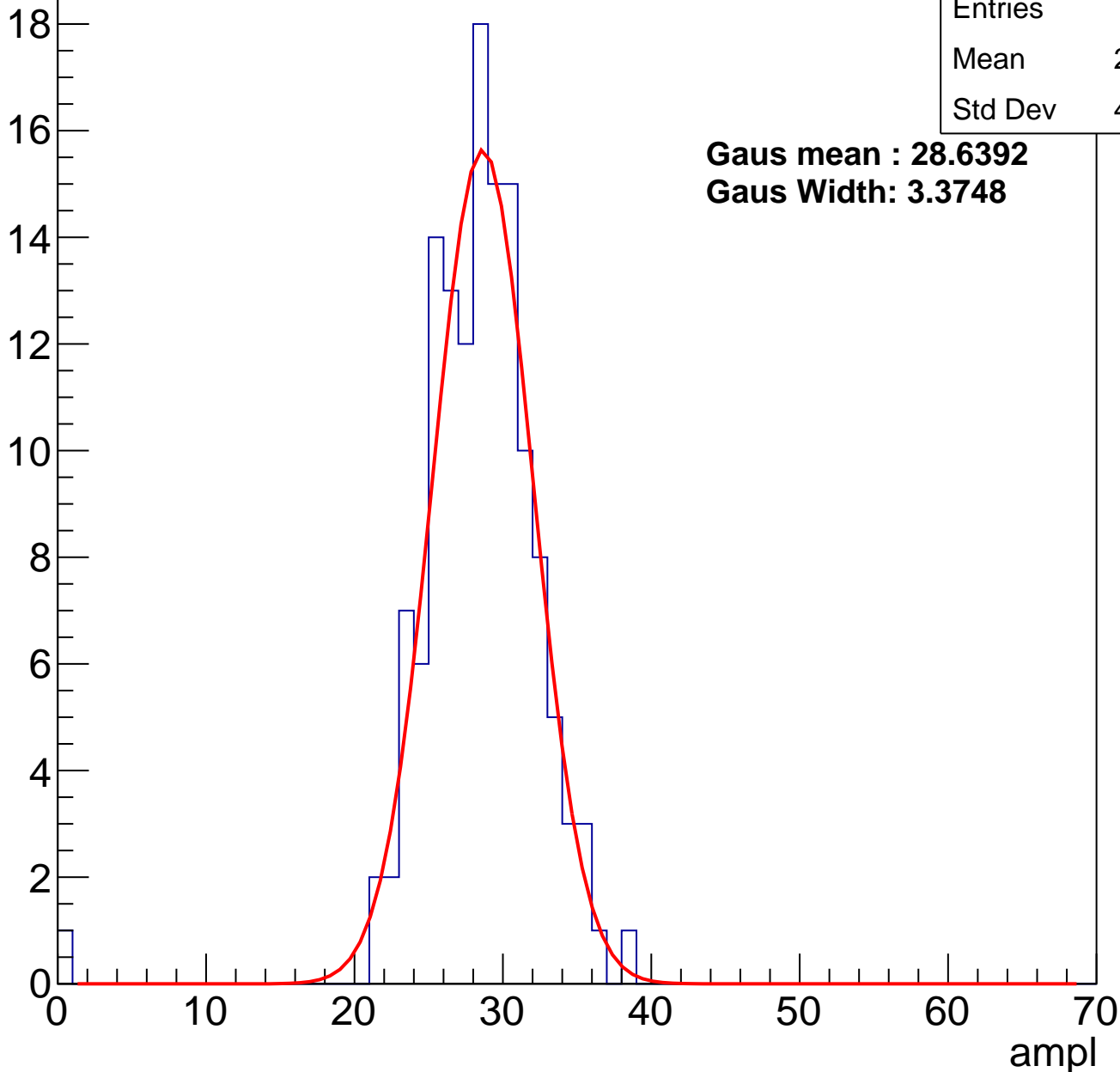
calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	27.97
Std Dev	4.069

Gaus mean : 28.6392

Gaus Width: 3.3748

Entry



B1L001S, U14-ch108, adc1

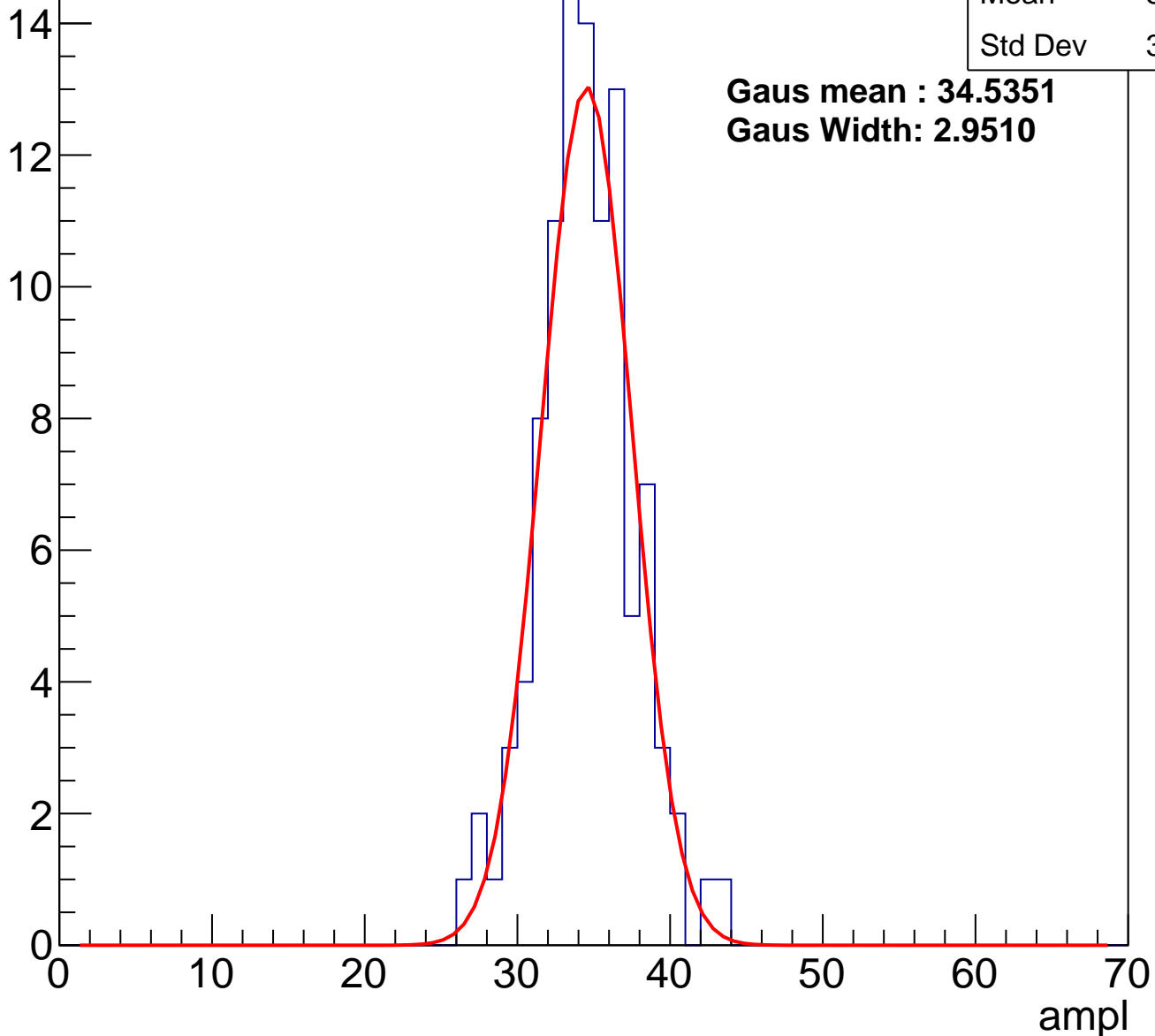
calib_packv5_042523_0143.root, FC#2, port C2

Entries	102
Mean	34.04
Std Dev	3.099

Gaus mean : 34.5351

Gaus Width: 2.9510

Entry



B1L001S, U14-ch108, adc2

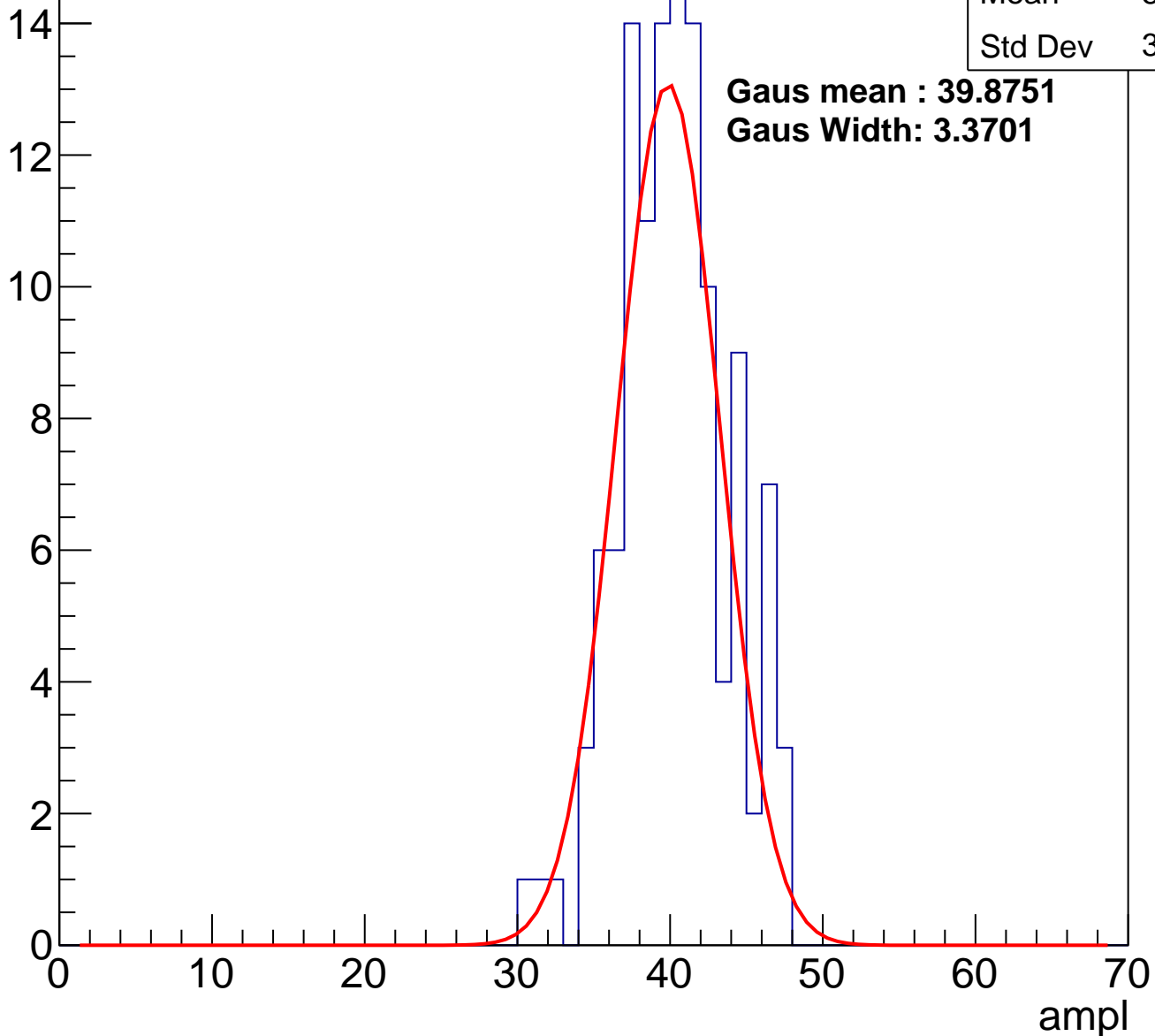
calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	39.82
Std Dev	3.476

Gaus mean : 39.8751

Gaus Width: 3.3701

Entry

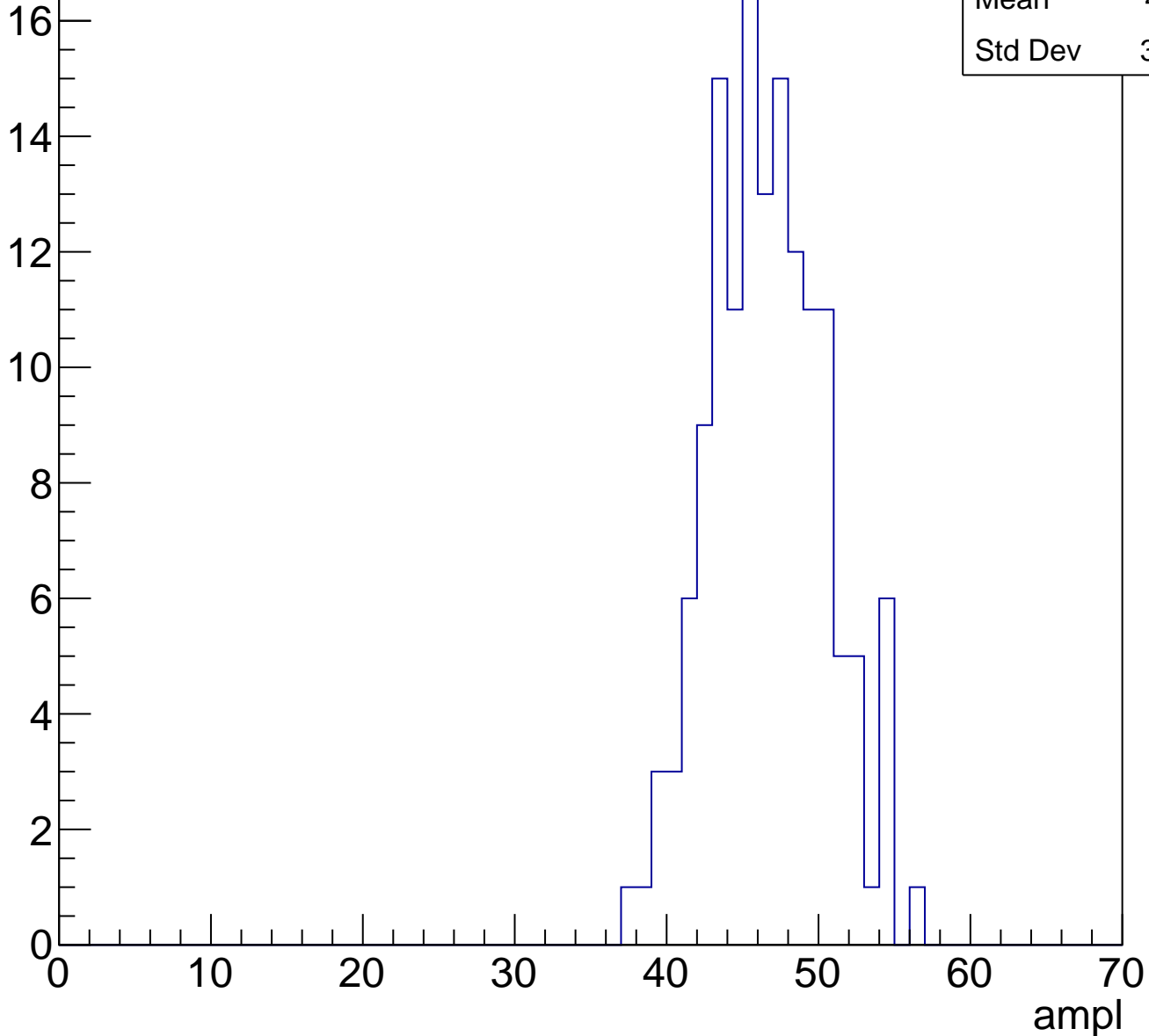


B1L001S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

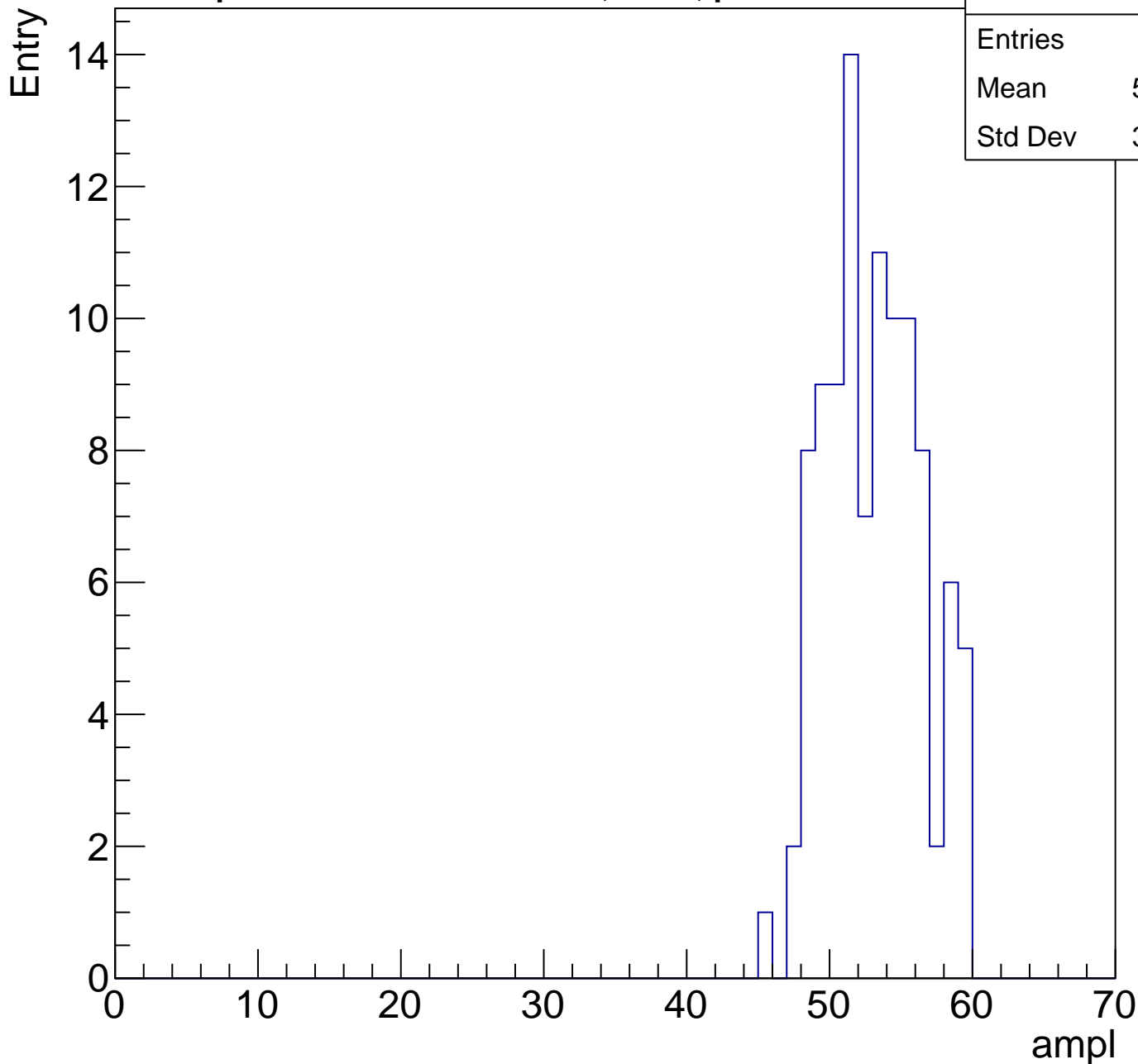
Entries	146
Mean	46.21
Std Dev	3.756



B1L001S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#2, port C2

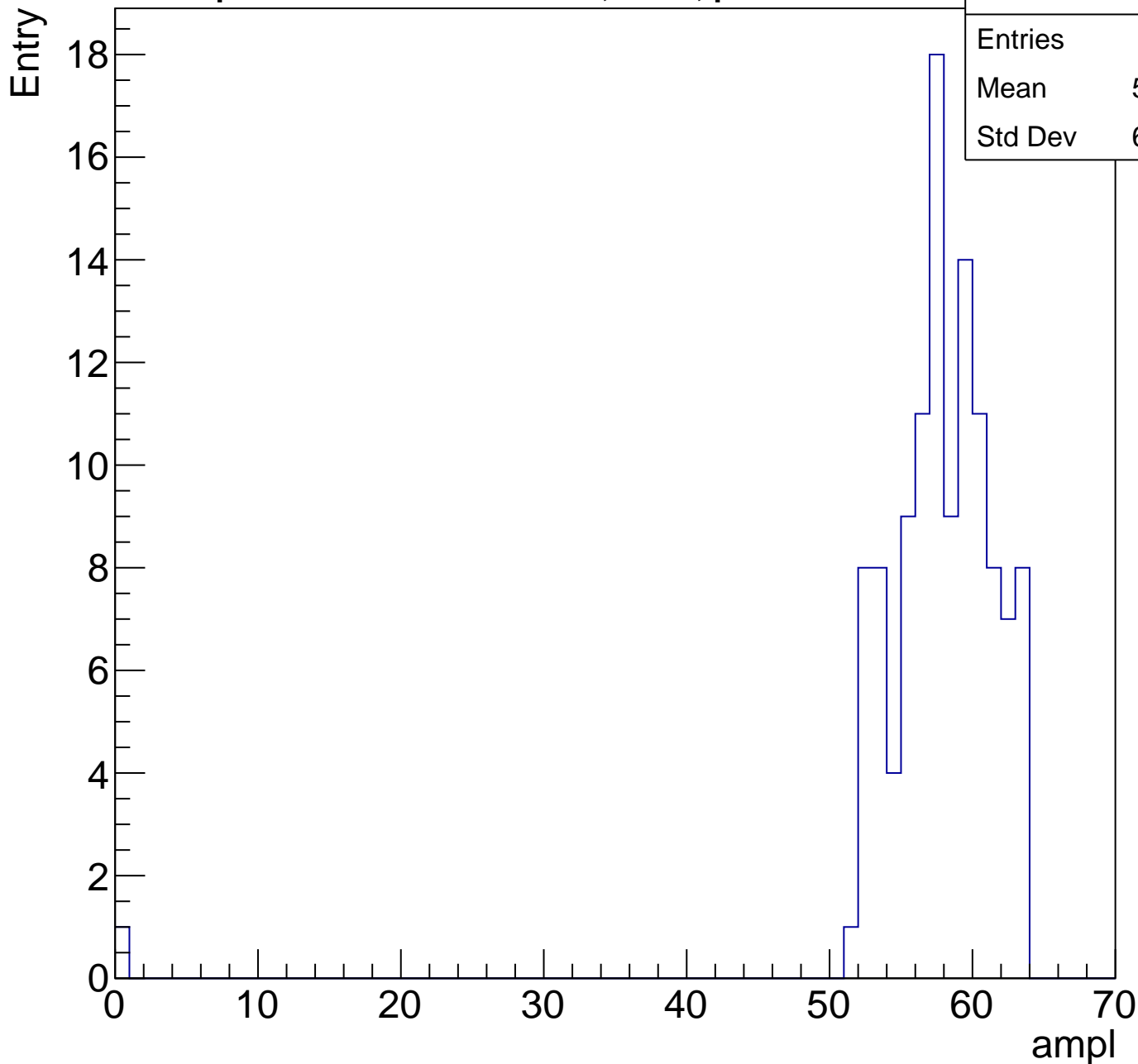
Entries	102
Mean	52.65
Std Dev	3.277



B1L001S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	117
Mean	57.08
Std Dev	6.167



B1L001S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

51

Mean

59.8

Std Dev

8.625

ampl

0

10

20

30

40

50

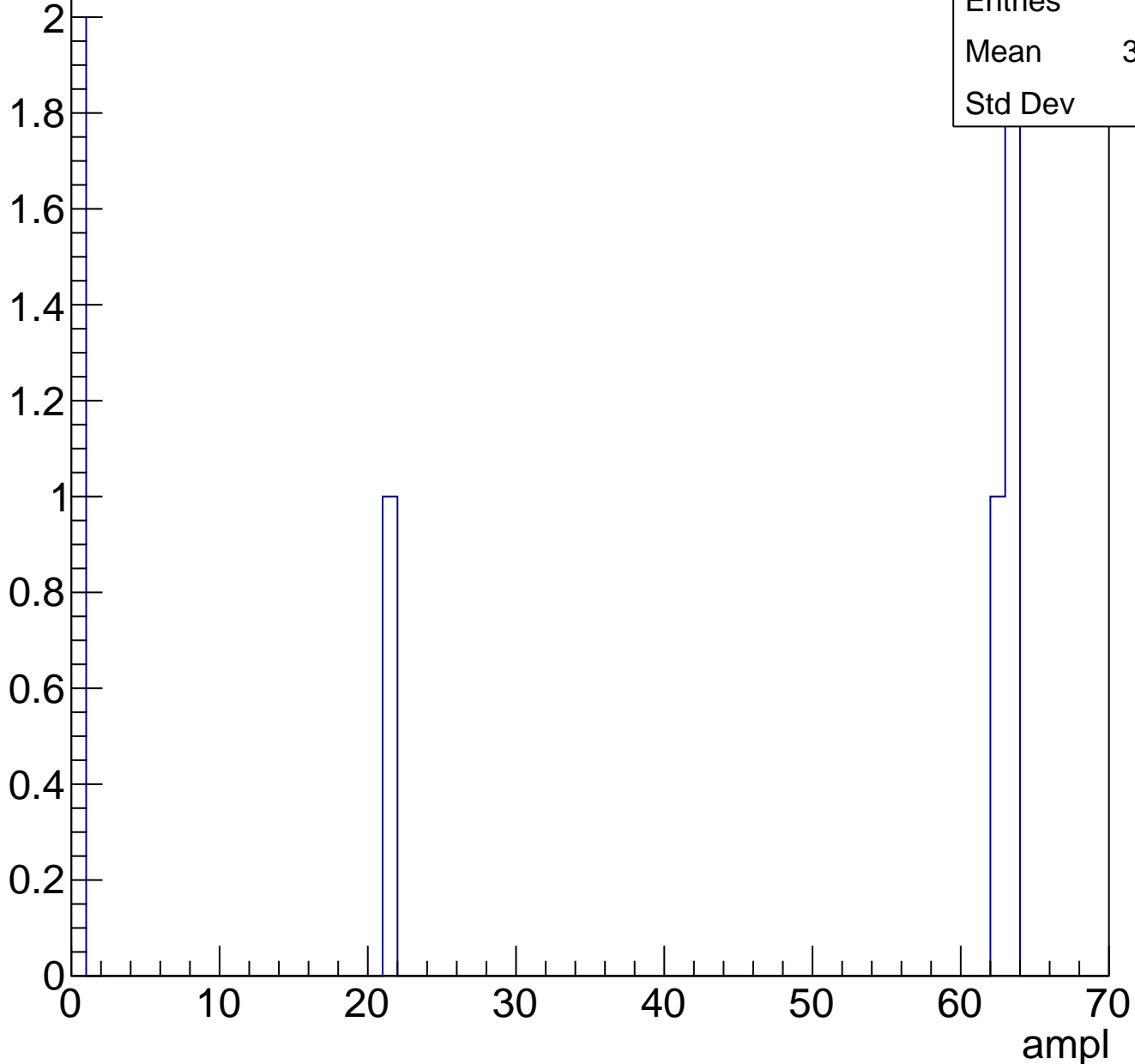
60

70

B1L001S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	6
Mean	34.83
Std Dev	28.7

B1L001S, U14-ch109, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	139
Mean	28.87
Std Dev	3.63

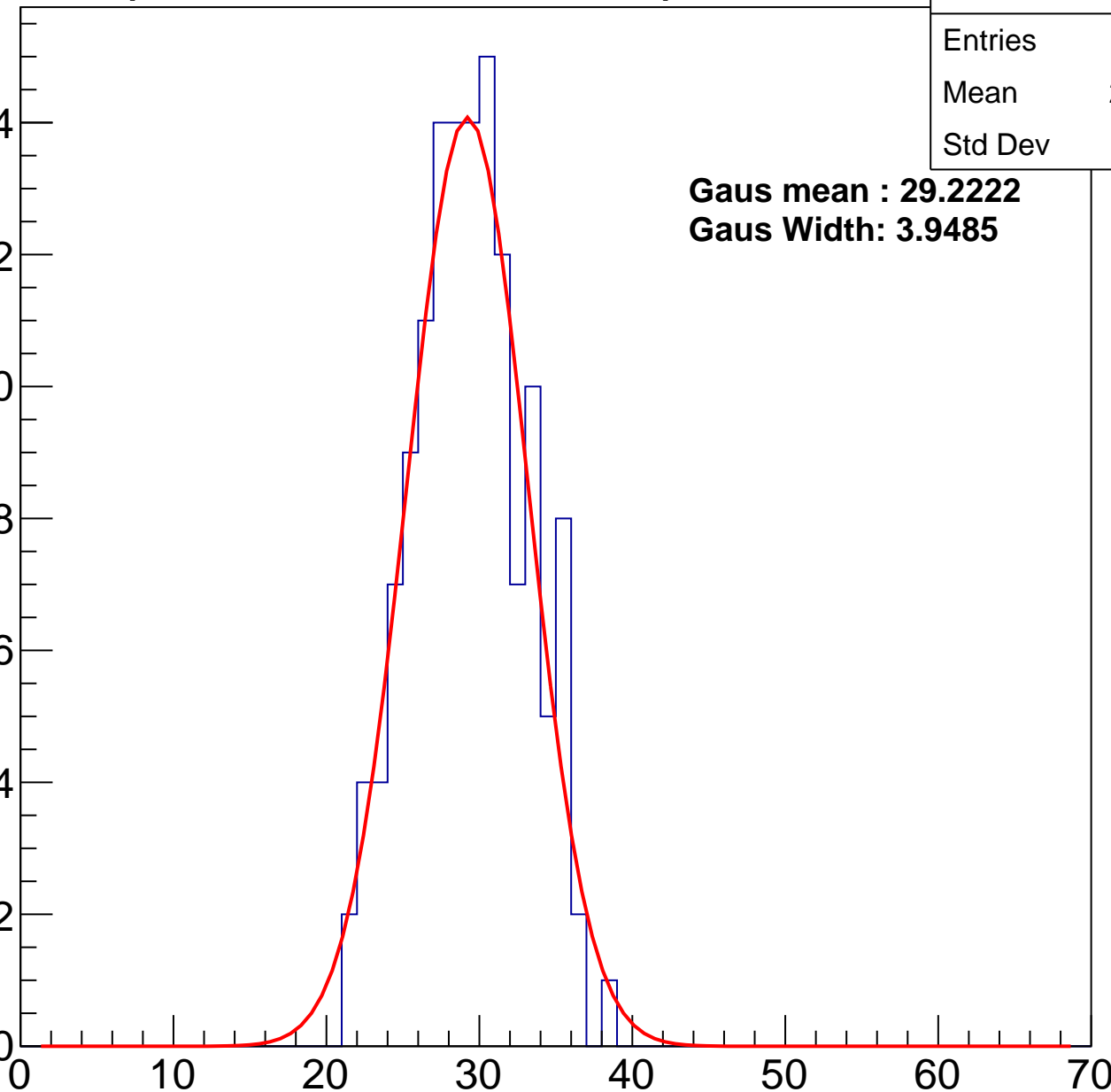
Gaus mean : 29.2222

Gaus Width: 3.9485

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch109, adc1

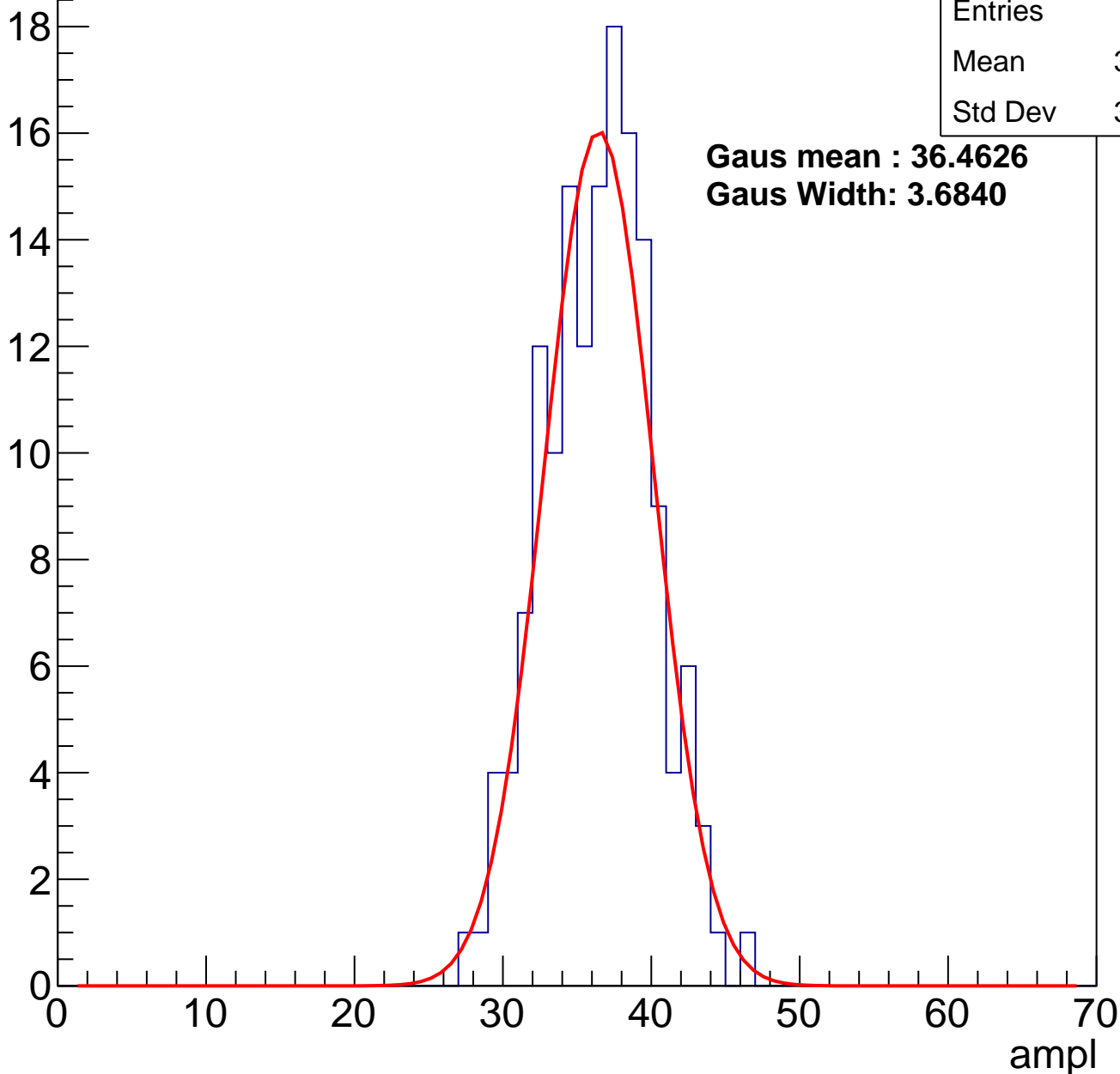
calib_packv5_042523_0143.root, FC#2, port C2

Entries	153
Mean	35.99
Std Dev	3.609

Gaus mean : 36.4626

Gaus Width: 3.6840

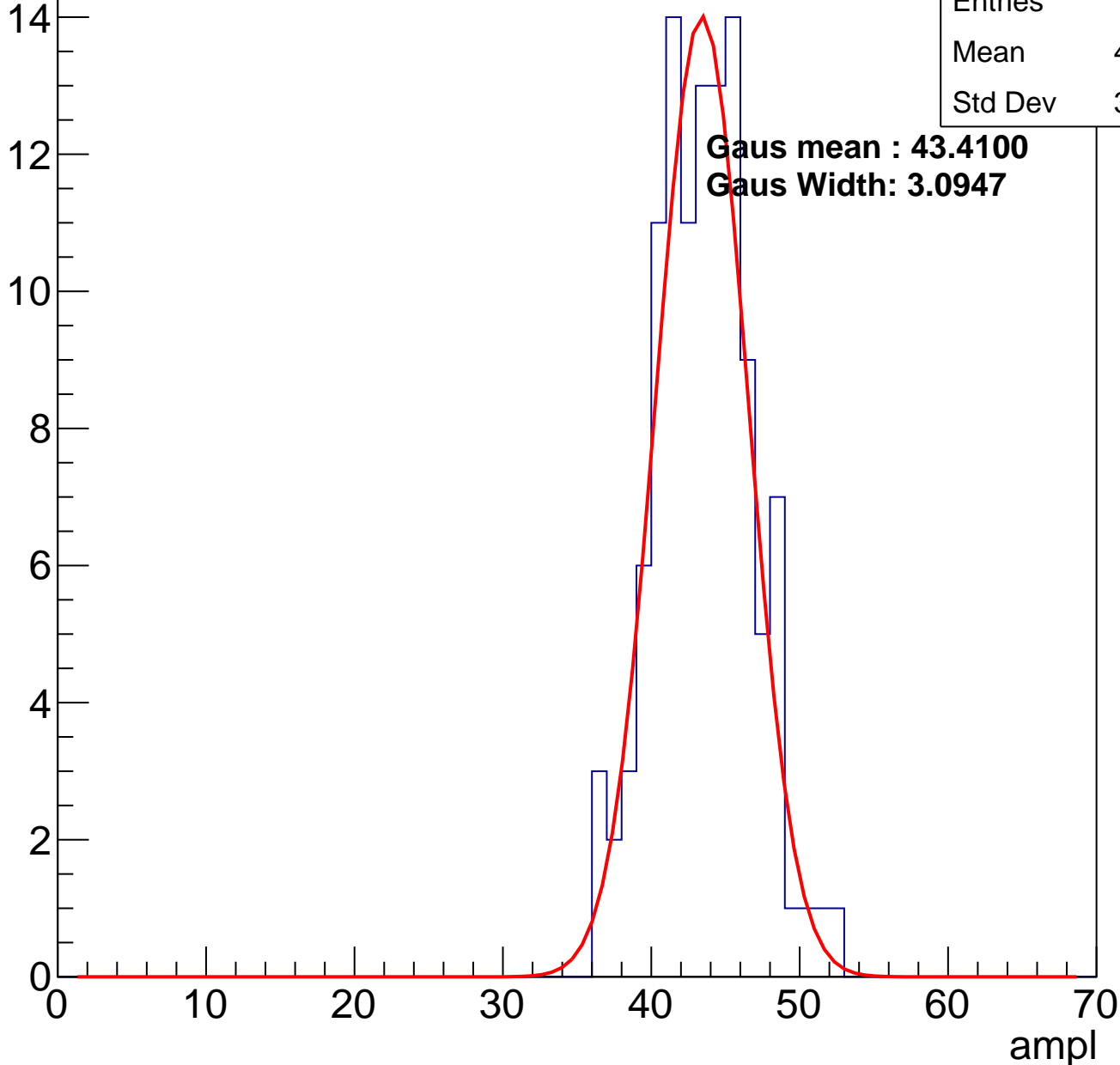
Entry



B1L001S, U14-ch109, adc2

calib_packv5_042523_0143.root, FC#2, port C2

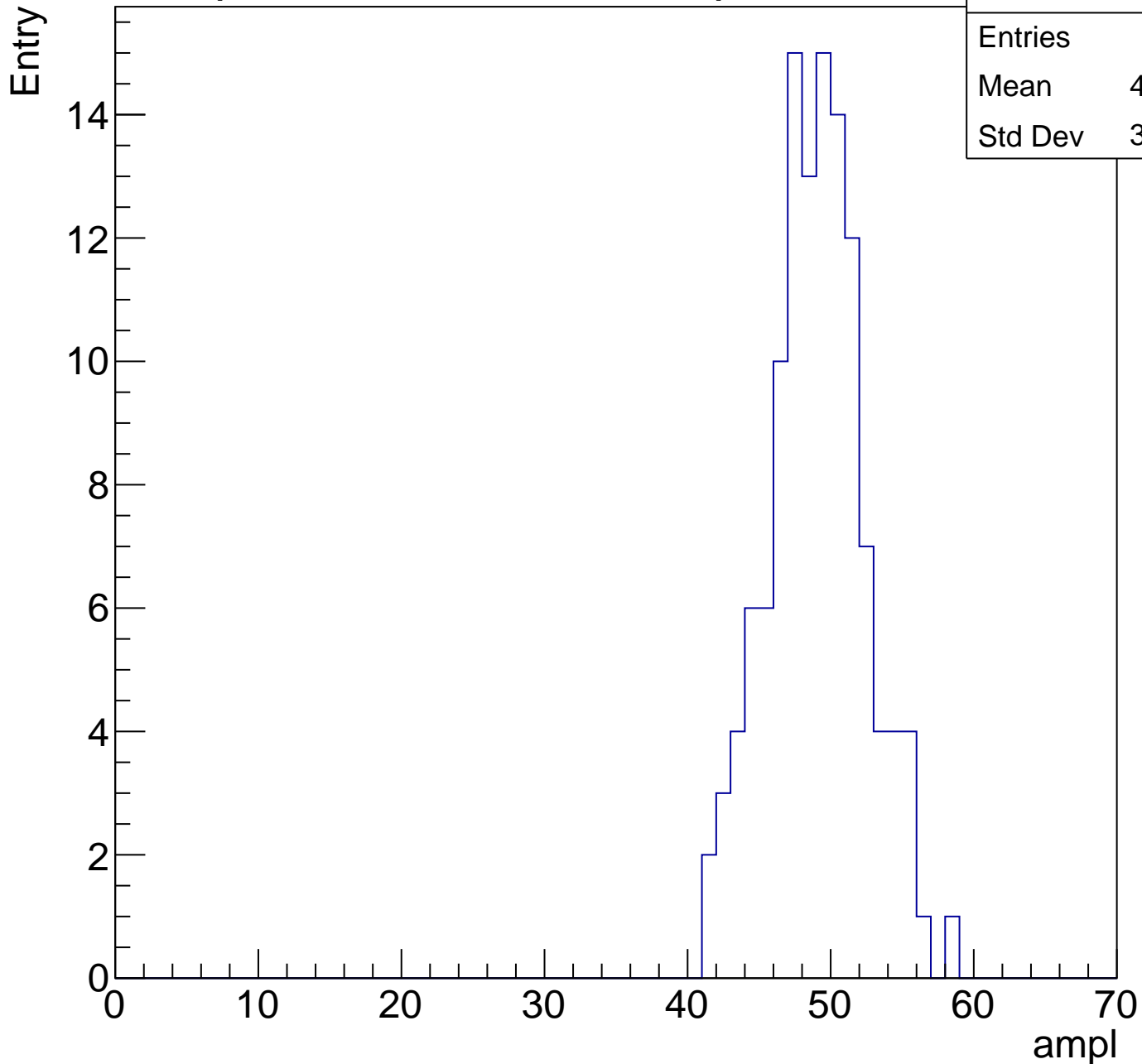
Entry



B1L001S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#2, port C2

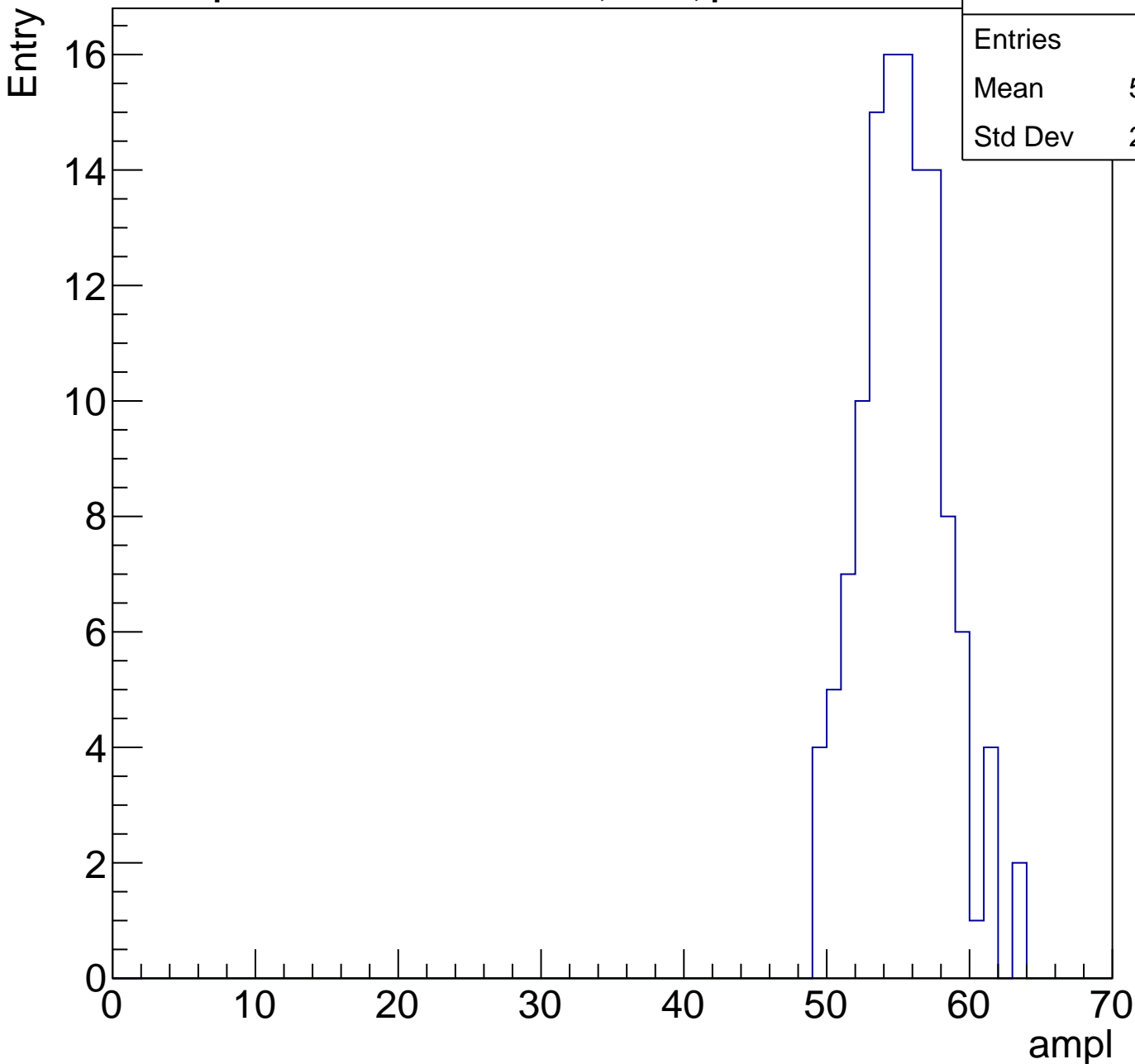
Entries	121
Mean	48.56
Std Dev	3.362



B1L001S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	54.85
Std Dev	2.974



B1L001S, U14-ch109, adc5

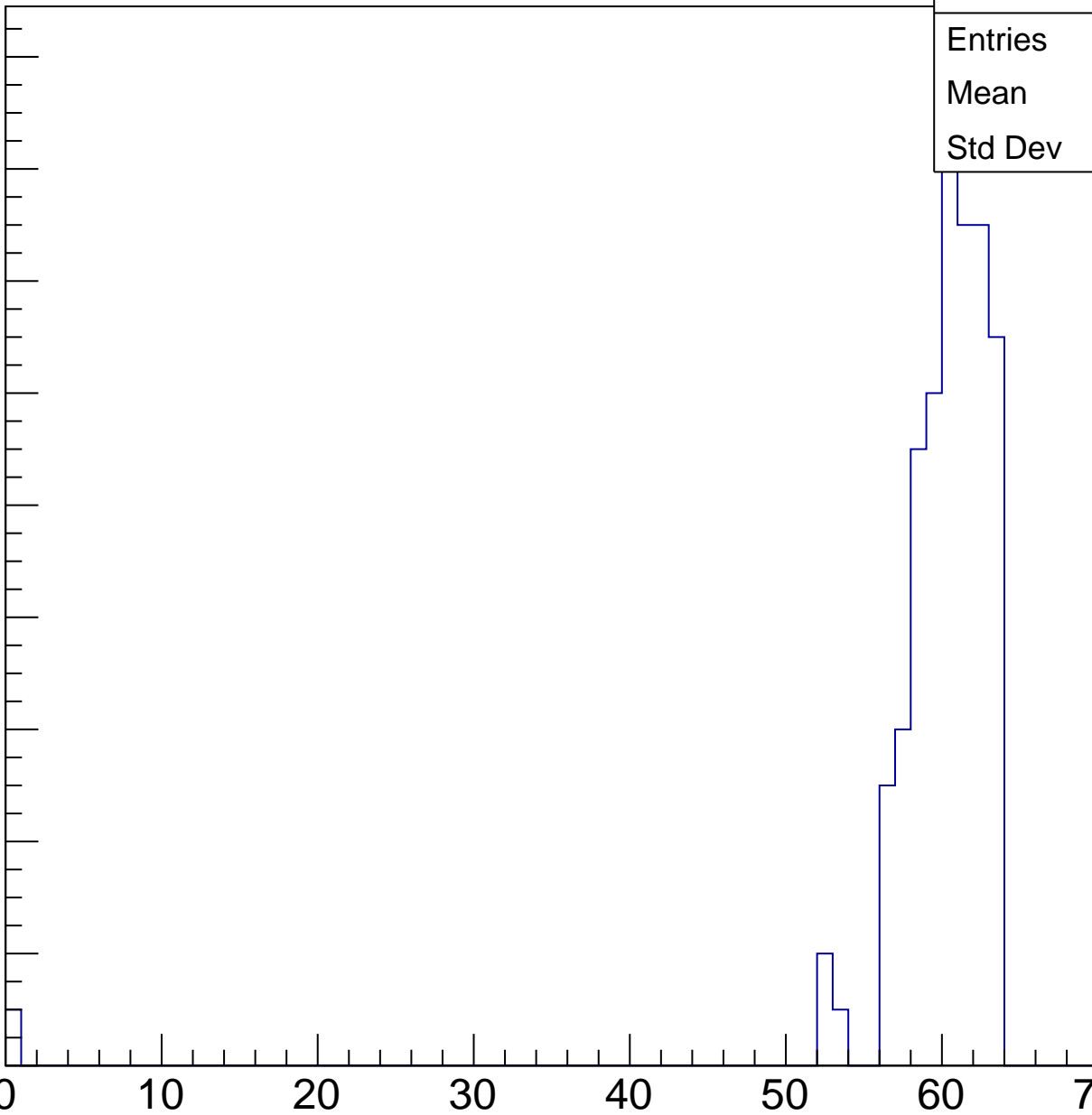
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	99
Mean	59.28
Std Dev	6.442

ampl

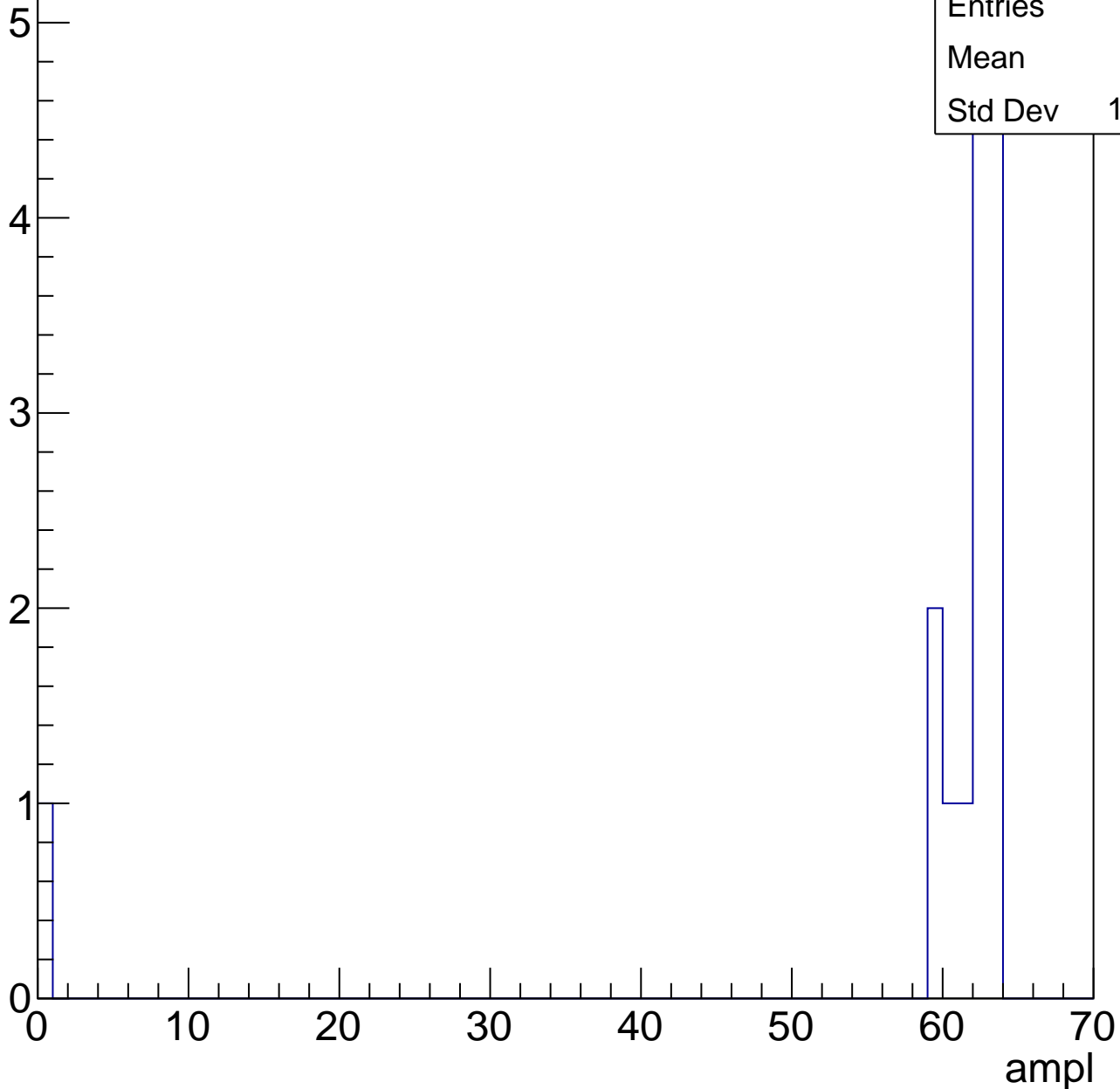


B1L001S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	15
Mean	57.6
Std Dev	15.45



B1L001S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L001S, U14-ch110, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.06
Std Dev	4.207

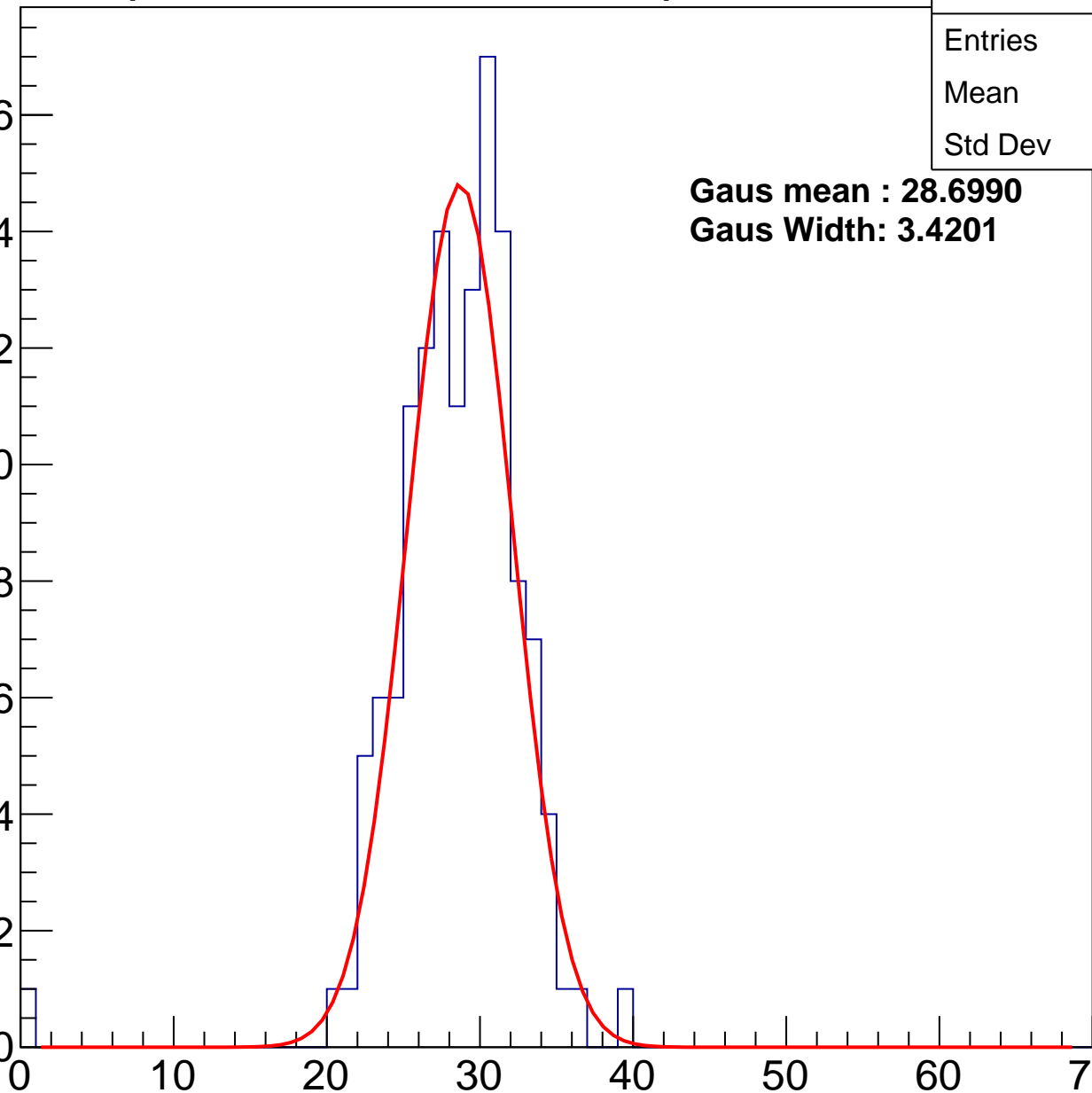
Gaus mean : 28.6990

Gaus Width: 3.4201

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch110, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	34.95
Std Dev	3.391

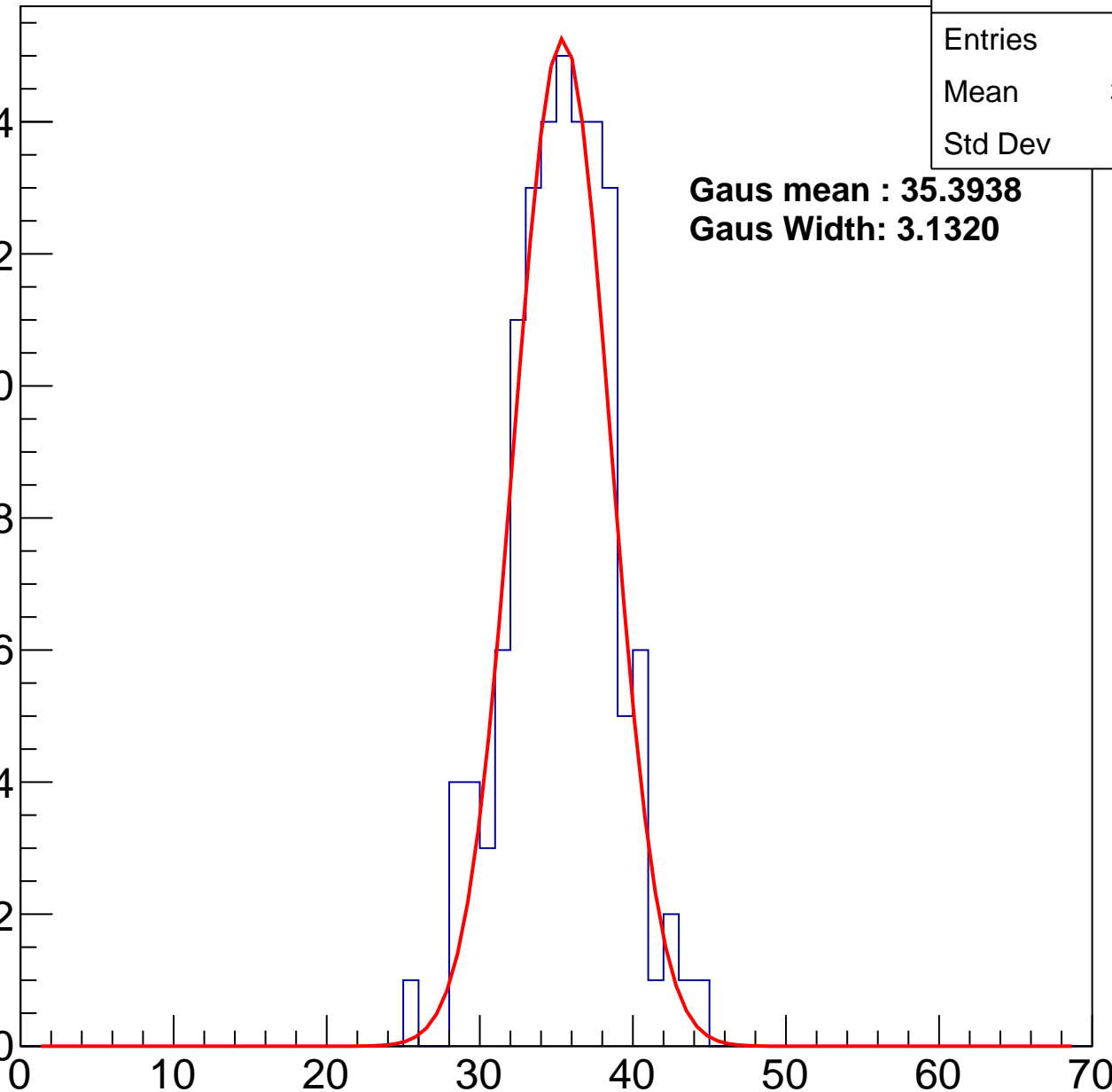
Gaus mean : 35.3938

Gaus Width: 3.1320

Entry

14
12
10
8
6
4
2
0

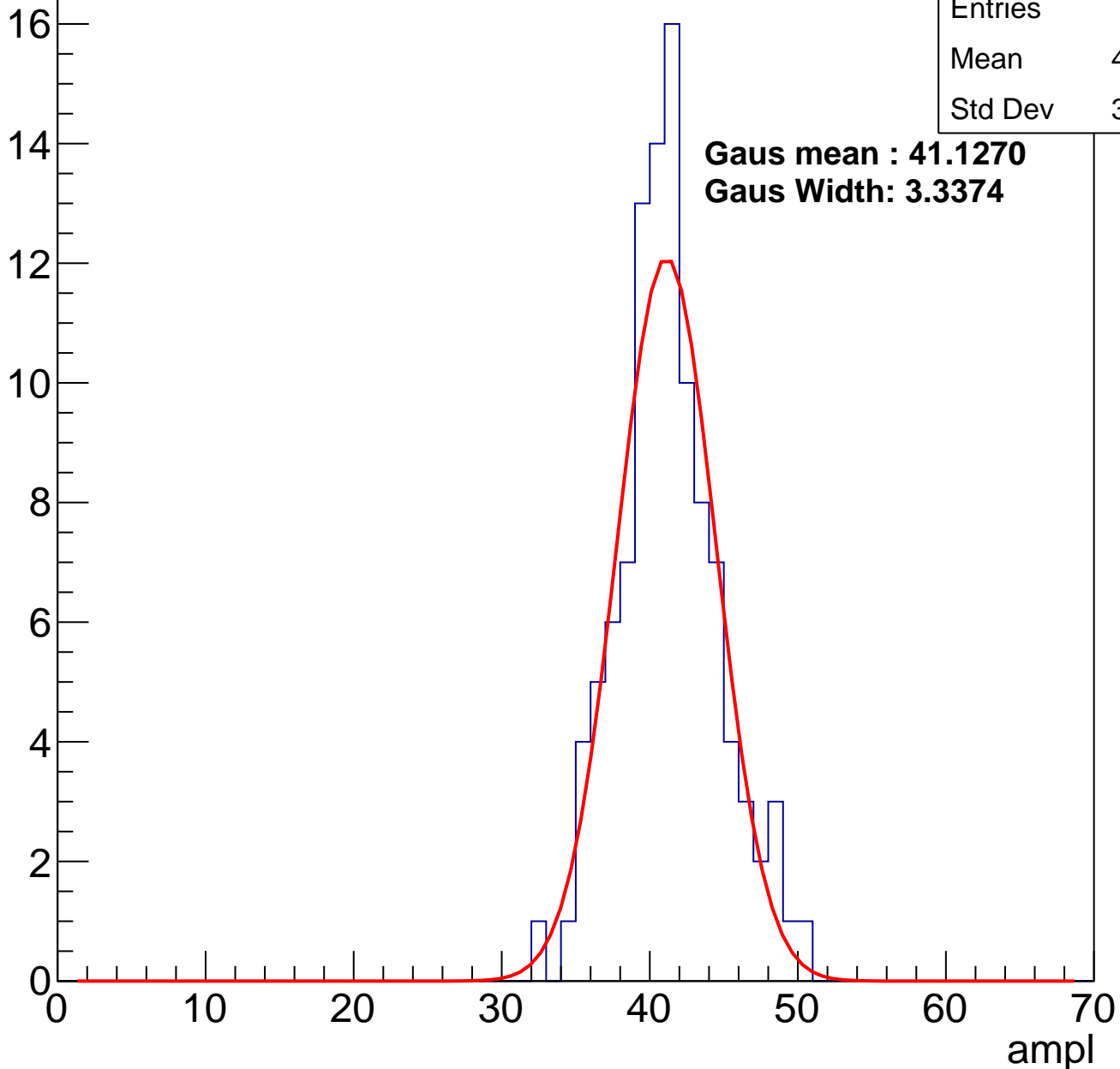
ampl



B1L001S, U14-ch110, adc2

calib_packv5_042523_0143.root, FC#2, port C2

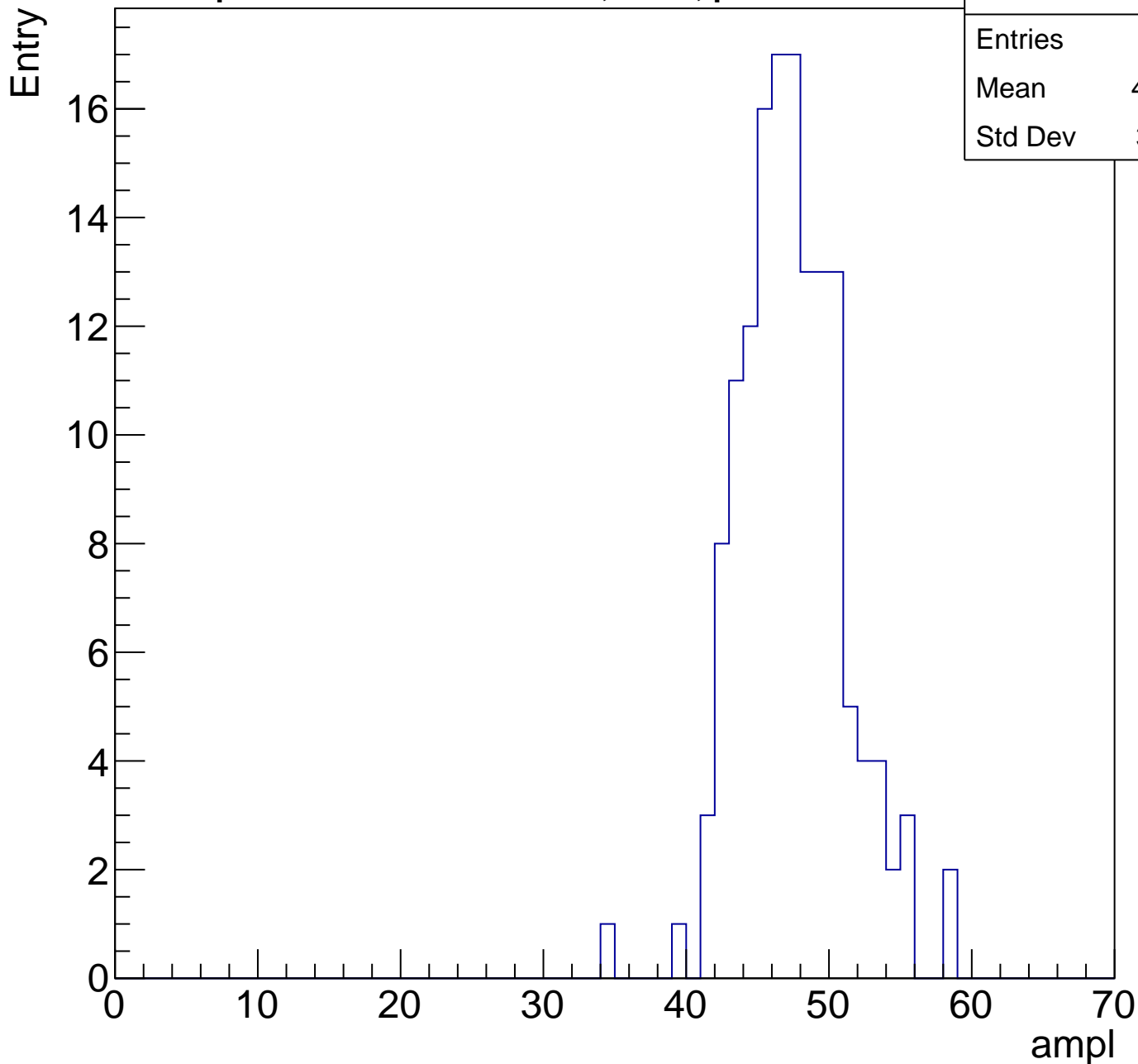
Entry



B1L001S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#2, port C2

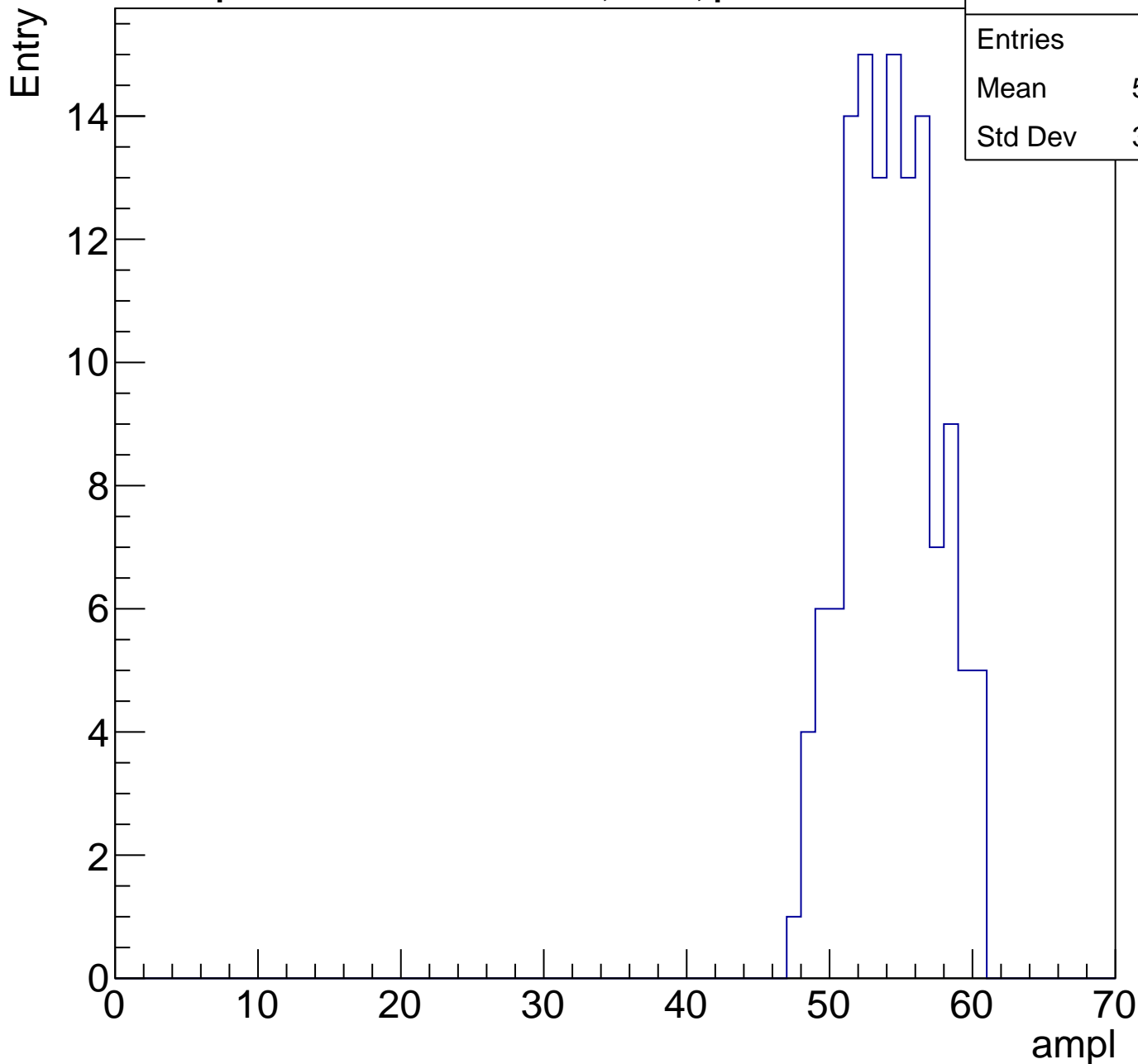
Entries	145
Mean	46.96
Std Dev	3.661



B1L001S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	53.87
Std Dev	3.095



B1L001S, U14-ch110, adc5

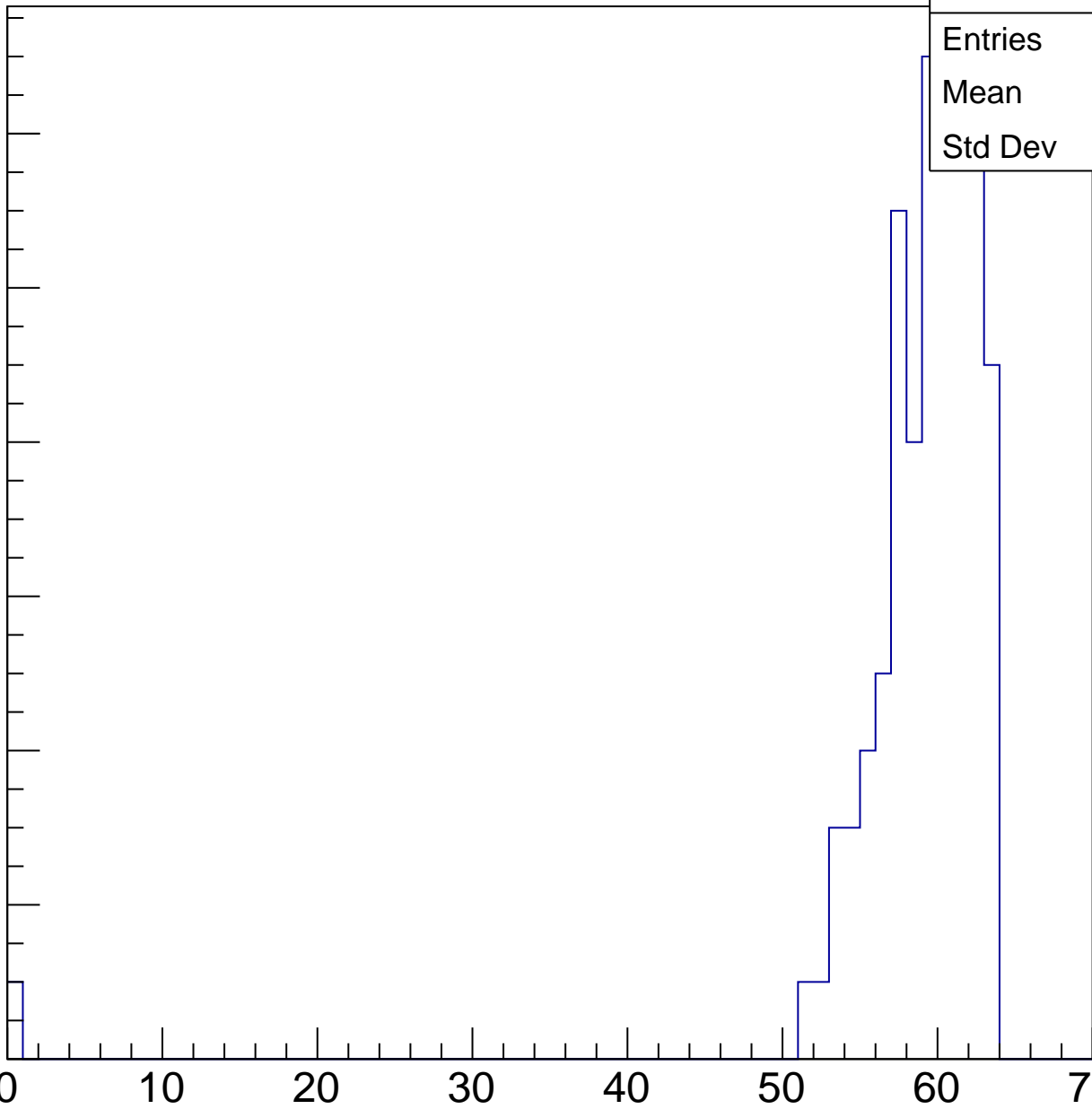
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	97
Mean	58.41
Std Dev	6.597

ampl

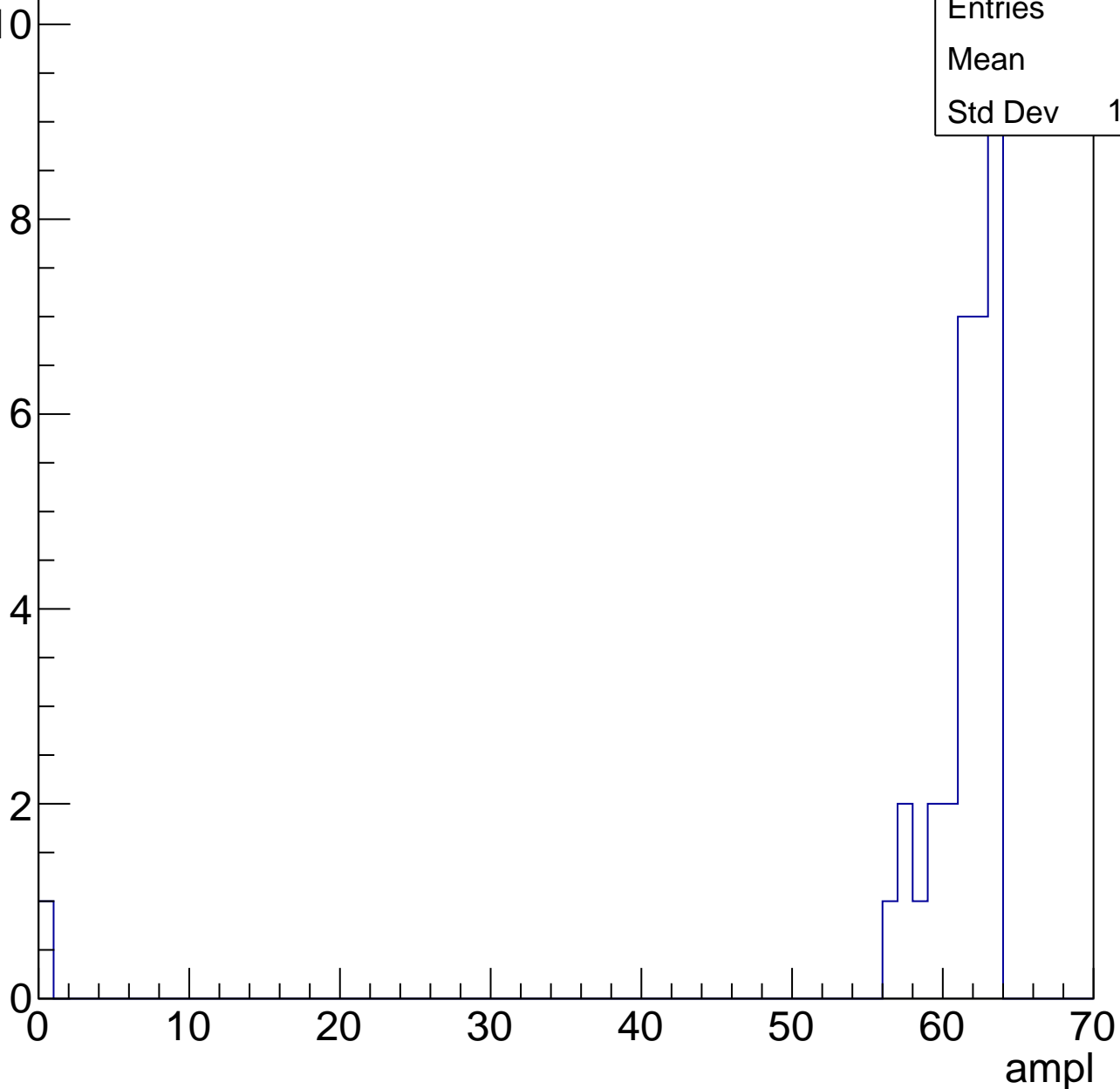


B1L001S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	33
Mean	59.3
Std Dev	10.66



B1L001S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch111, adc0

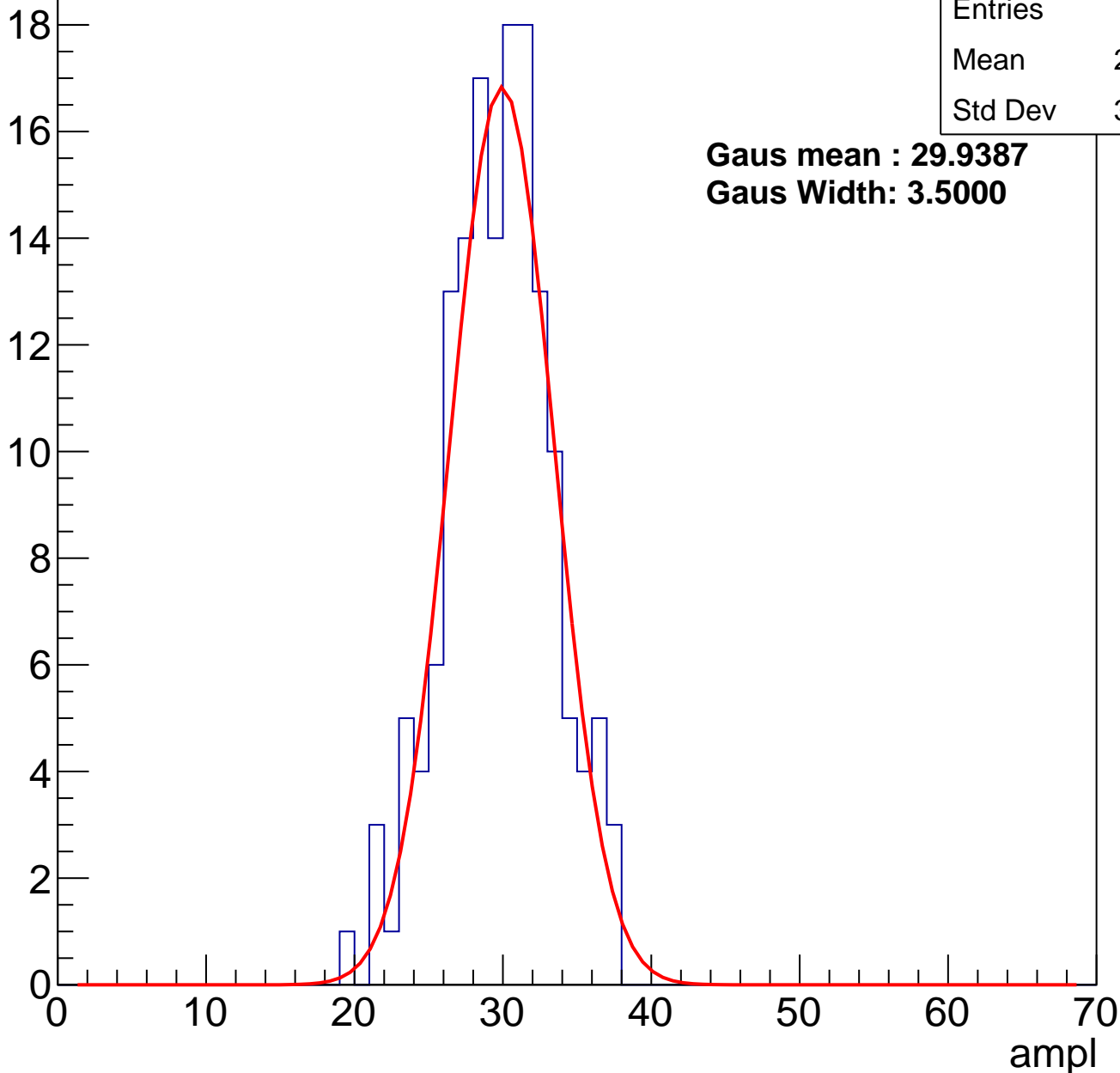
calib_packv5_042523_0143.root, FC#2, port C2

Entries	154
Mean	29.27
Std Dev	3.575

Gaus mean : 29.9387

Gaus Width: 3.5000

Entry



B1L001S, U14-ch111, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	36.55
Std Dev	2.78

Gaus mean : 36.5509

Gaus Width: 2.6379

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

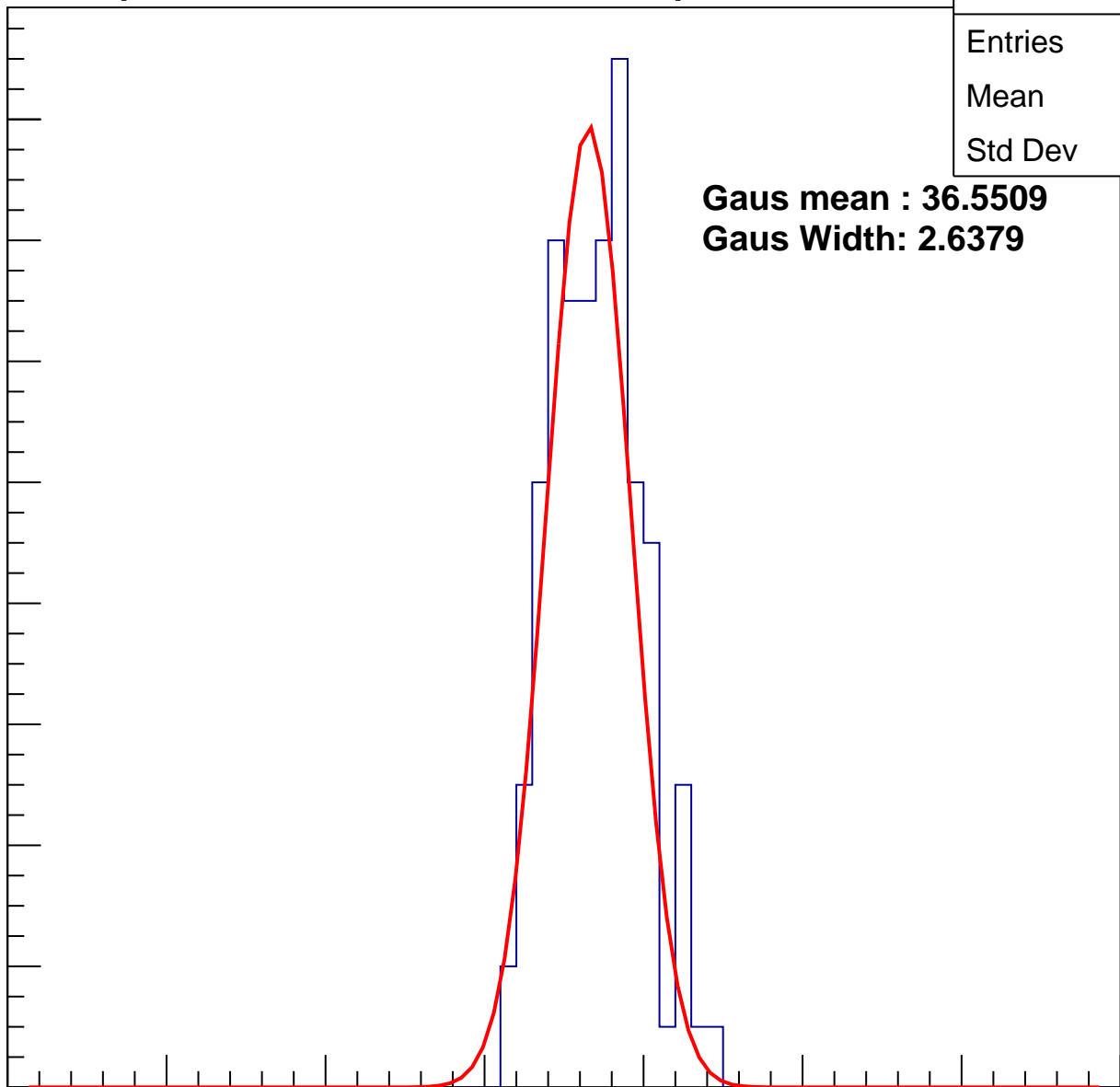
40

50

60

70

ampl



B1L001S, U14-ch111, adc2

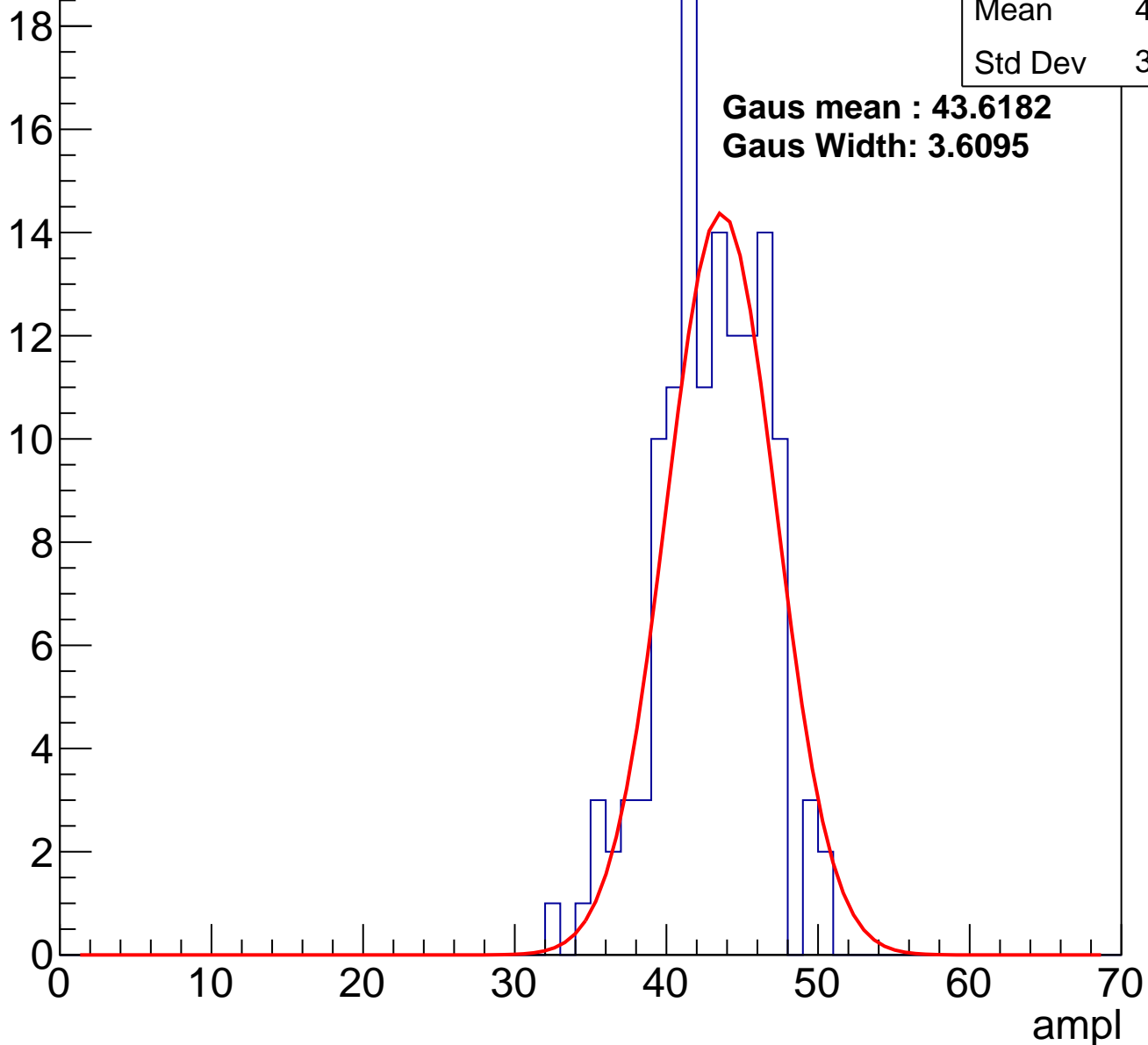
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	42.52
Std Dev	3.432

Gaus mean : 43.6182

Gaus Width: 3.6095

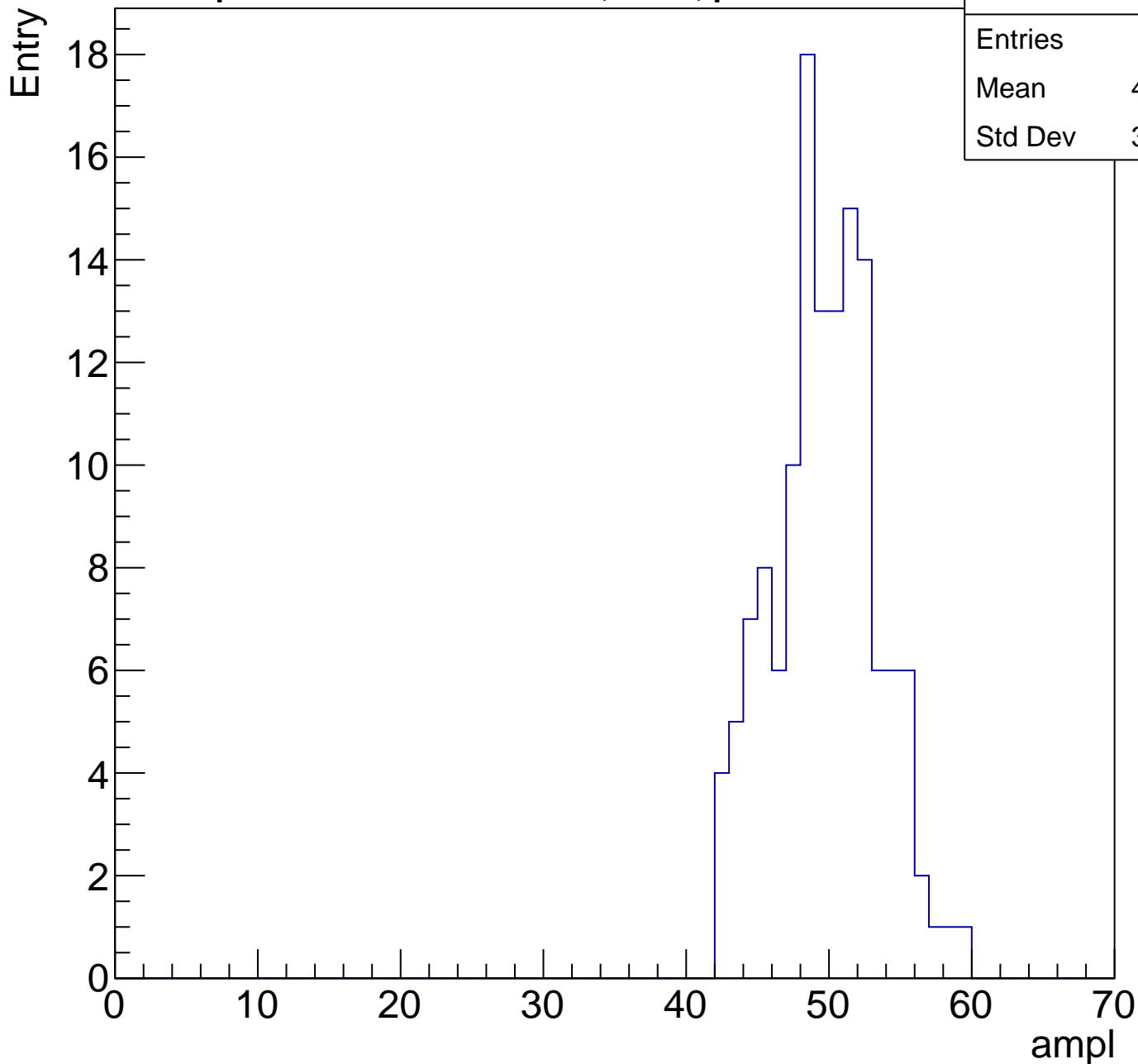
Entry



B1L001S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	49.26
Std Dev	3.624



B1L001S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	105
Mean	54.83
Std Dev	3.081

Entry

12

10

8

6

4

2

0

0

10

20

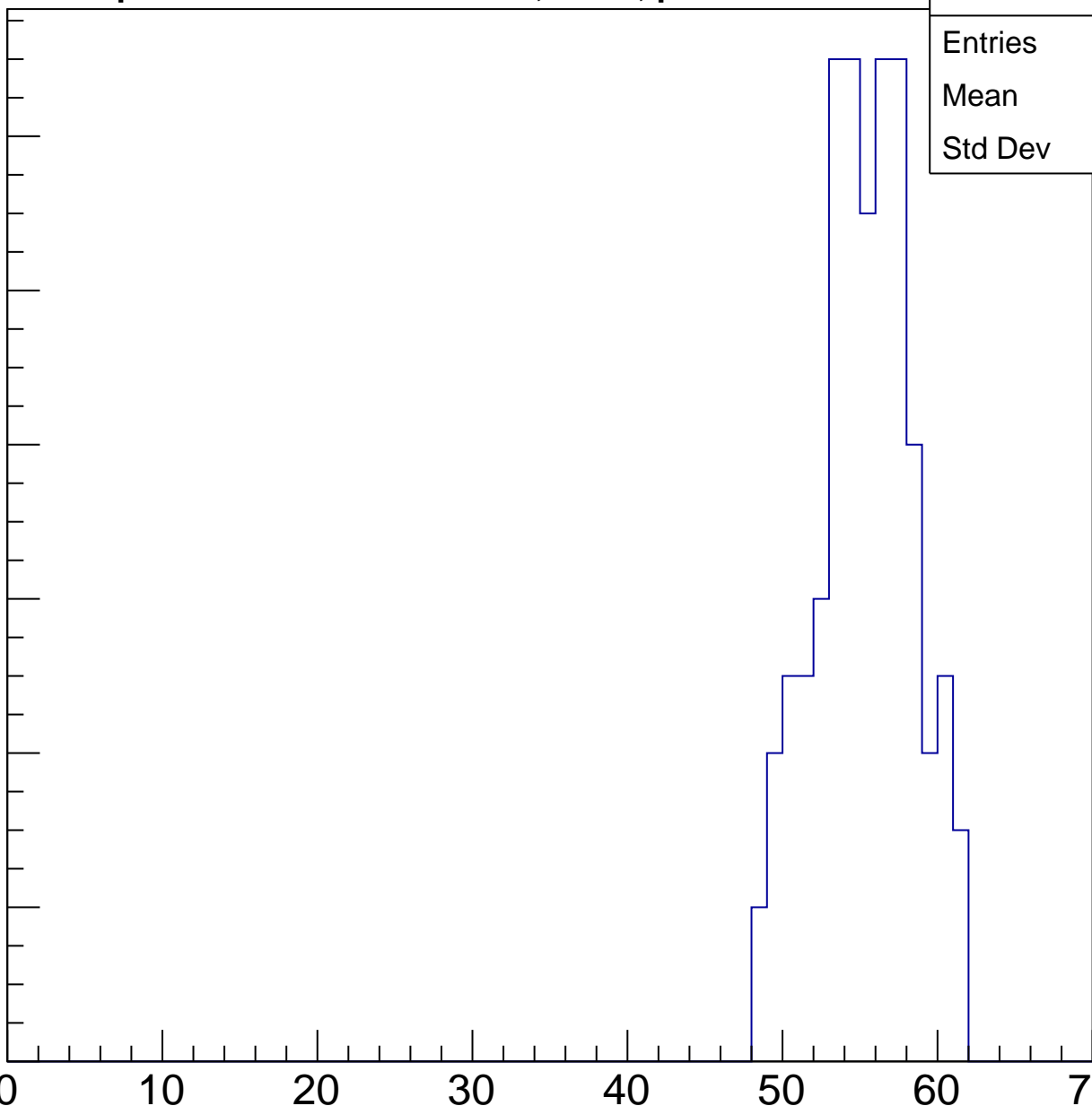
30

40

50

60

ampl



B1L001S, U14-ch111, adc5

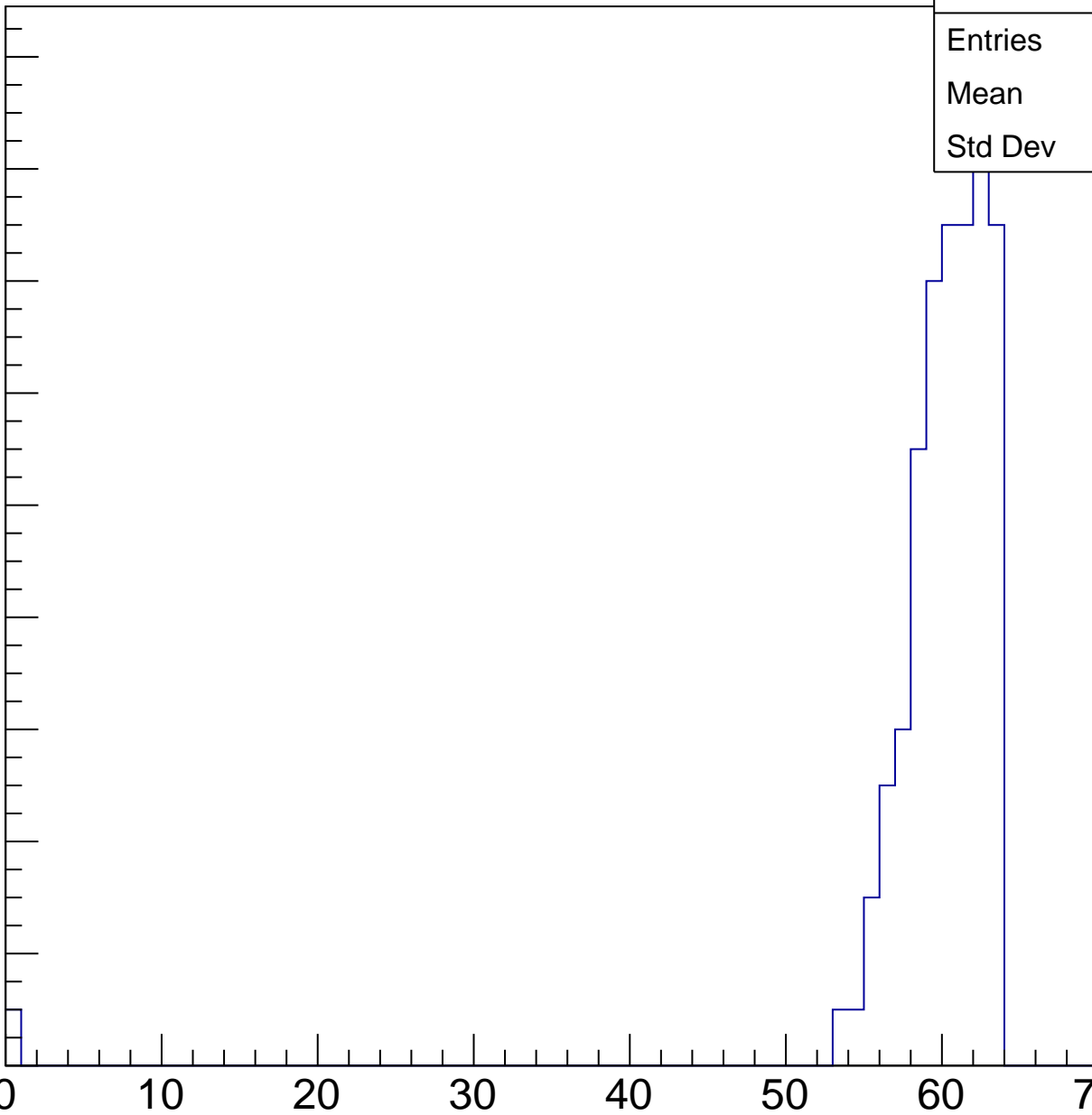
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	105
Mean	59.37
Std Dev	6.274

ampl

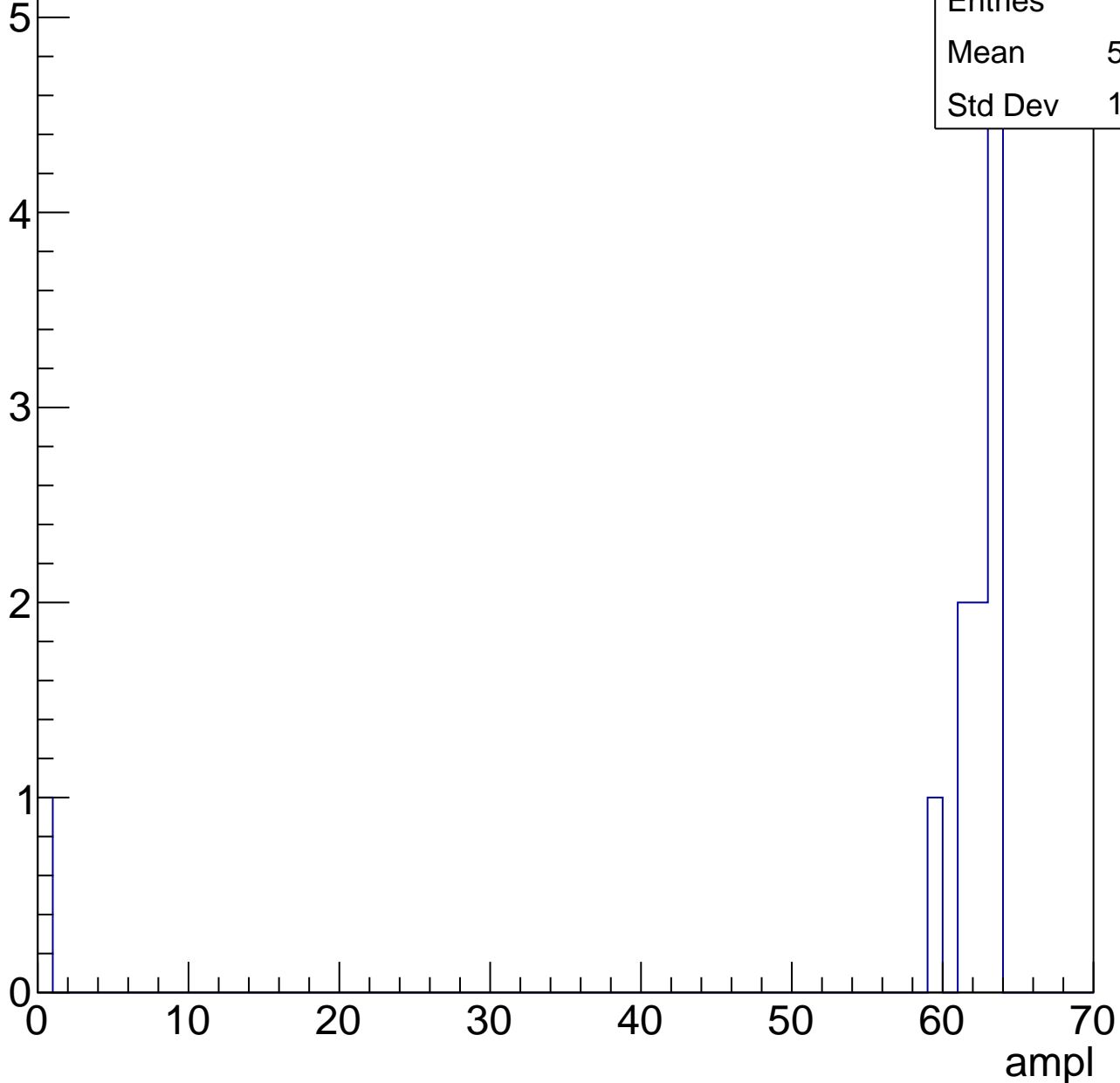


B1L001S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	11
Mean	56.36
Std Dev	17.86



B1L001S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	20
Std Dev	0

ampl

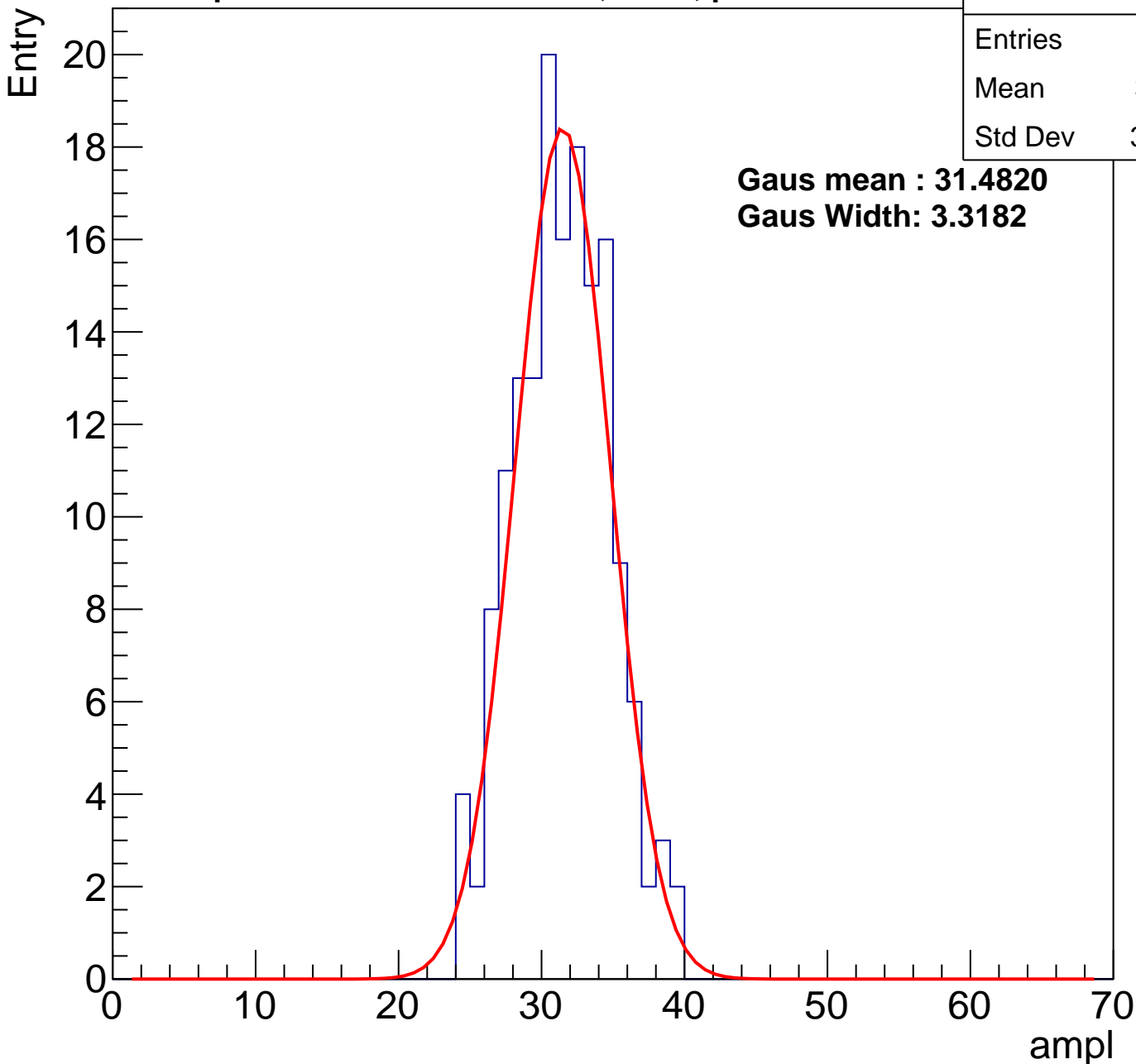
B1L001S, U14-ch112, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	158
Mean	31.01
Std Dev	3.276

Gaus mean : 31.4820

Gaus Width: 3.3182



B1L001S, U14-ch112, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	37.52
Std Dev	3.137

Gaus mean : 38.0382

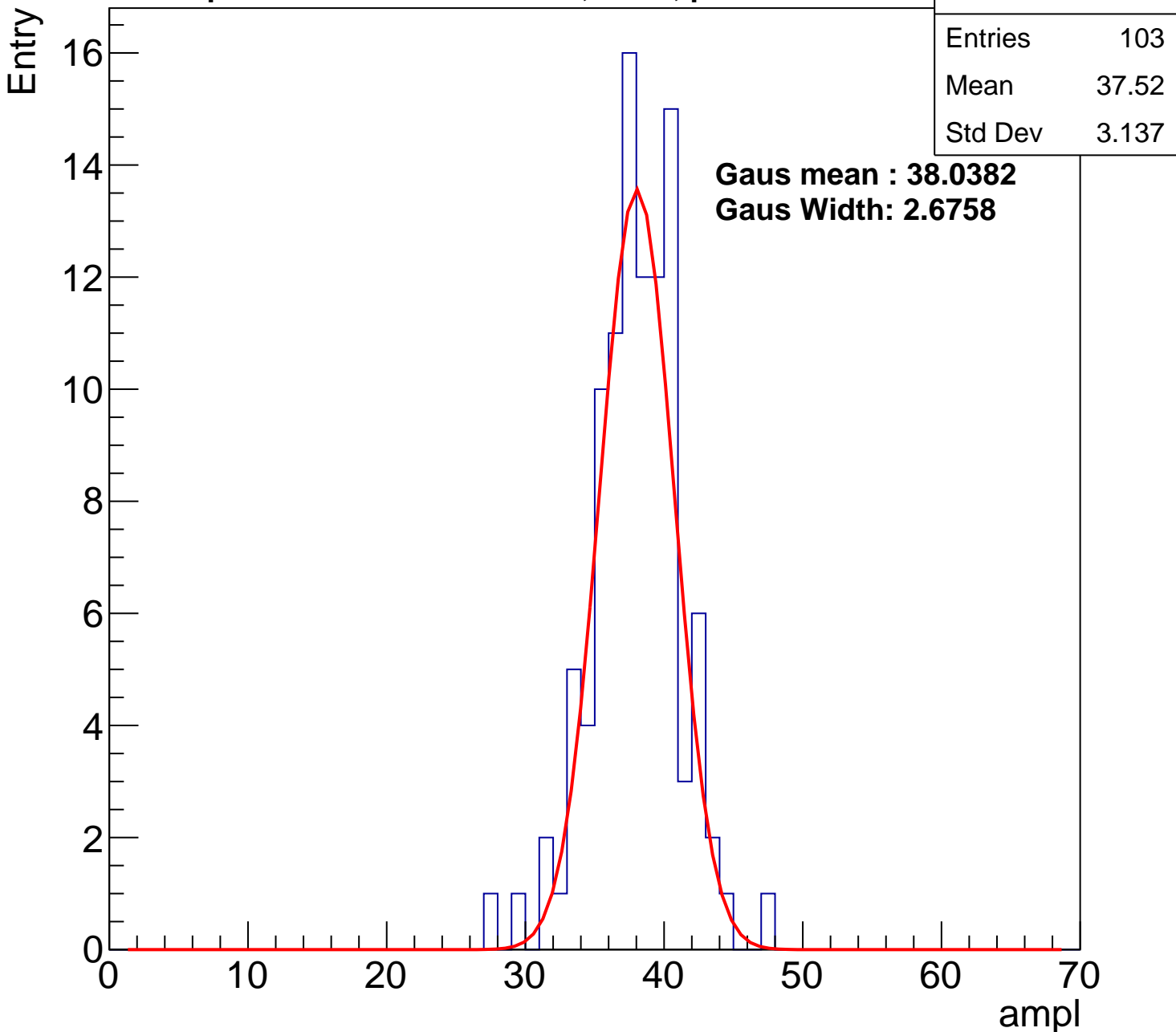
Gaus Width: 2.6758

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch112, adc2

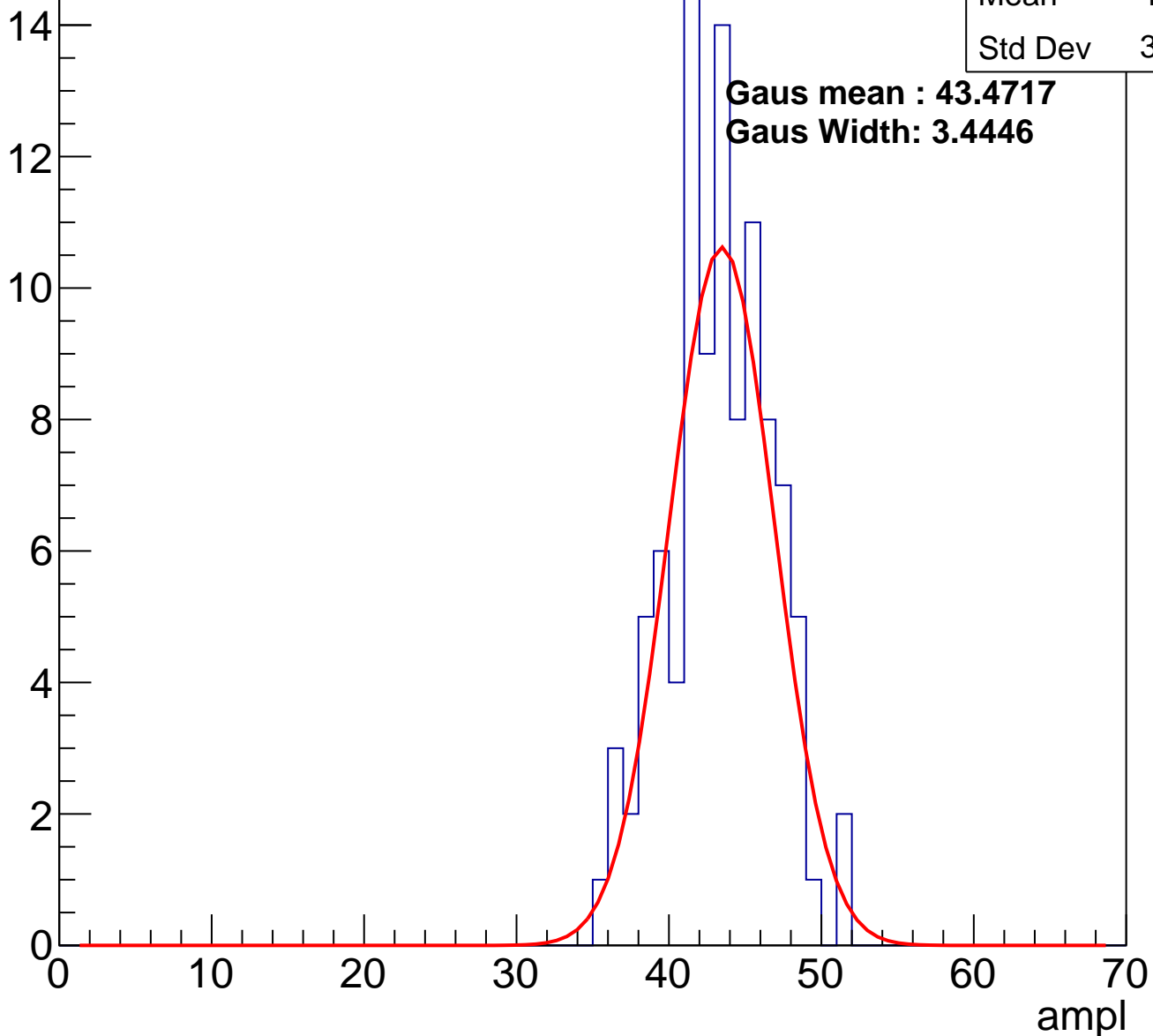
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	101
Mean	42.88
Std Dev	3.349

Gaus mean : 43.4717

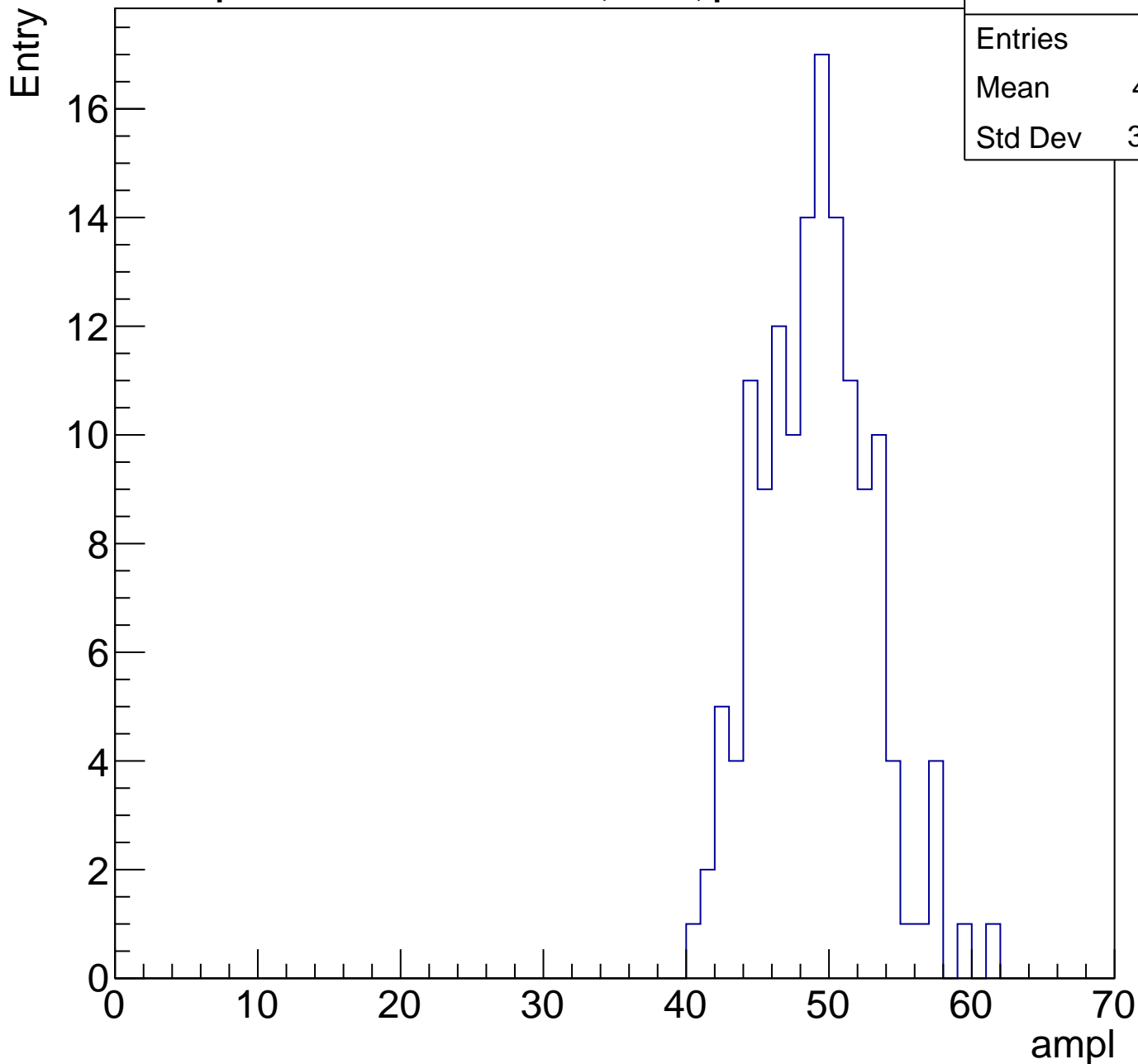
Gaus Width: 3.4446



B1L001S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#2, port C2

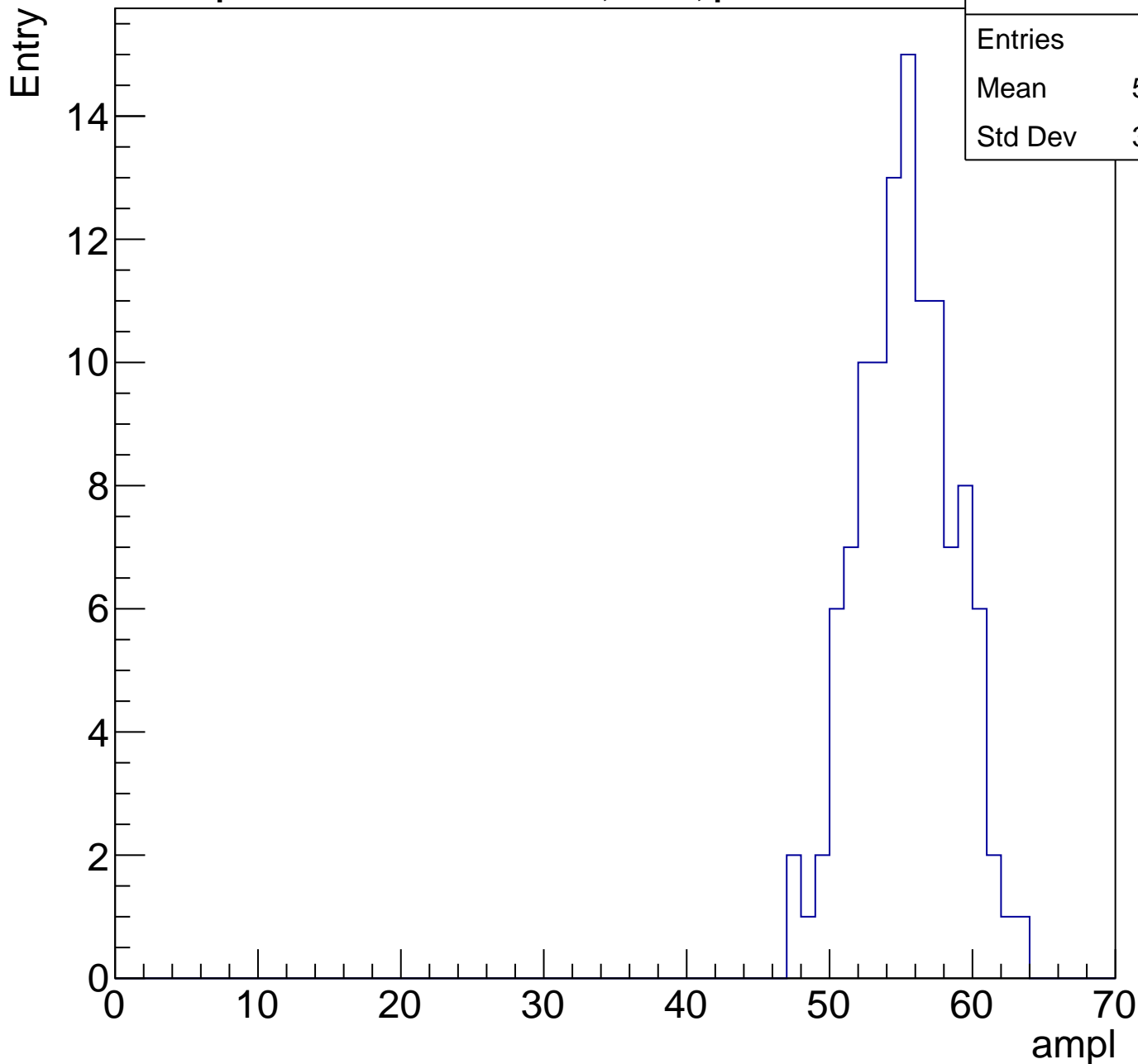
Entries	141
Mean	48.61
Std Dev	3.897



B1L001S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	54.88
Std Dev	3.304



B1L001S, U14-ch112, adc5

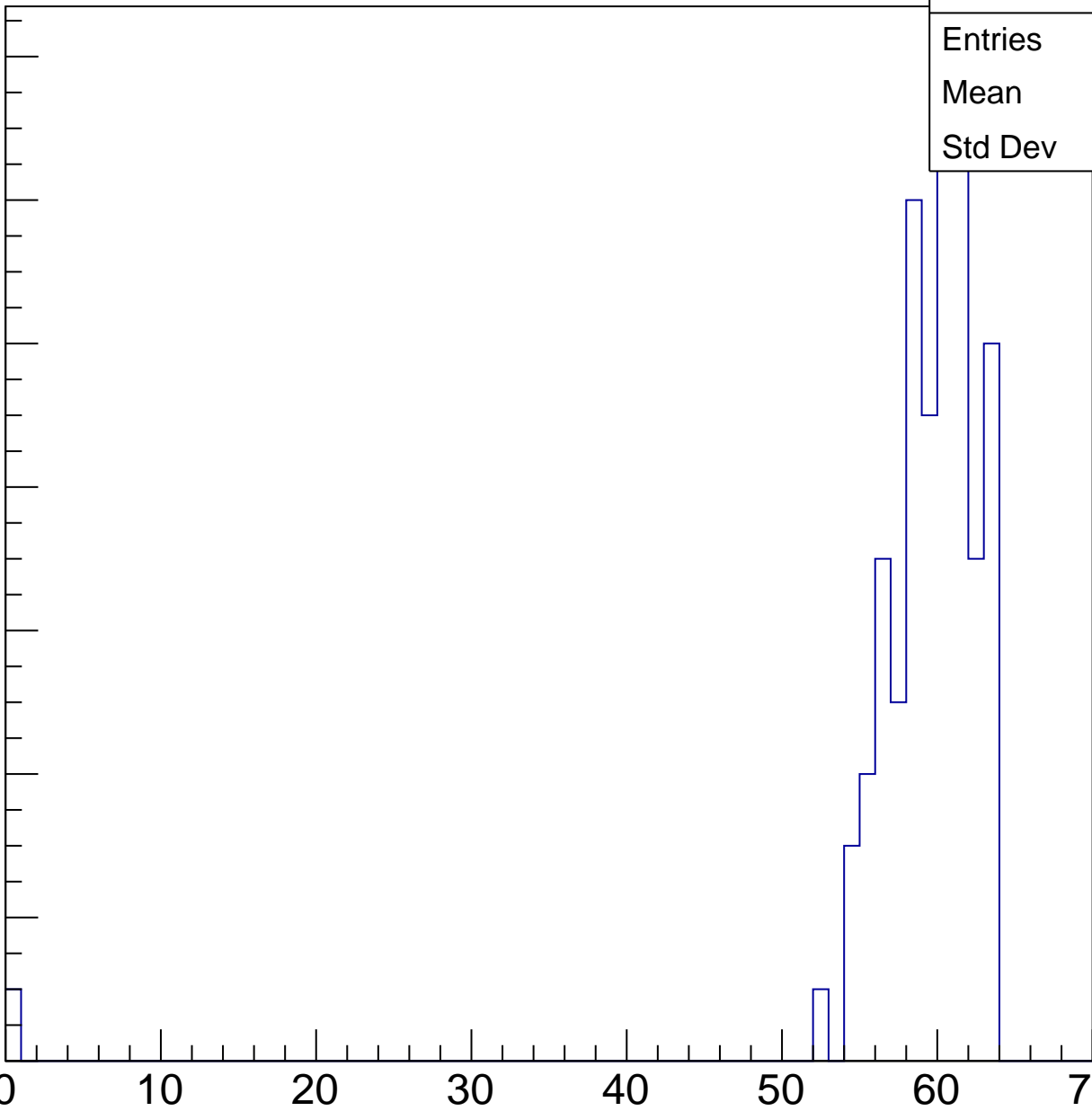
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	86
Mean	58.55
Std Dev	6.85

ampl

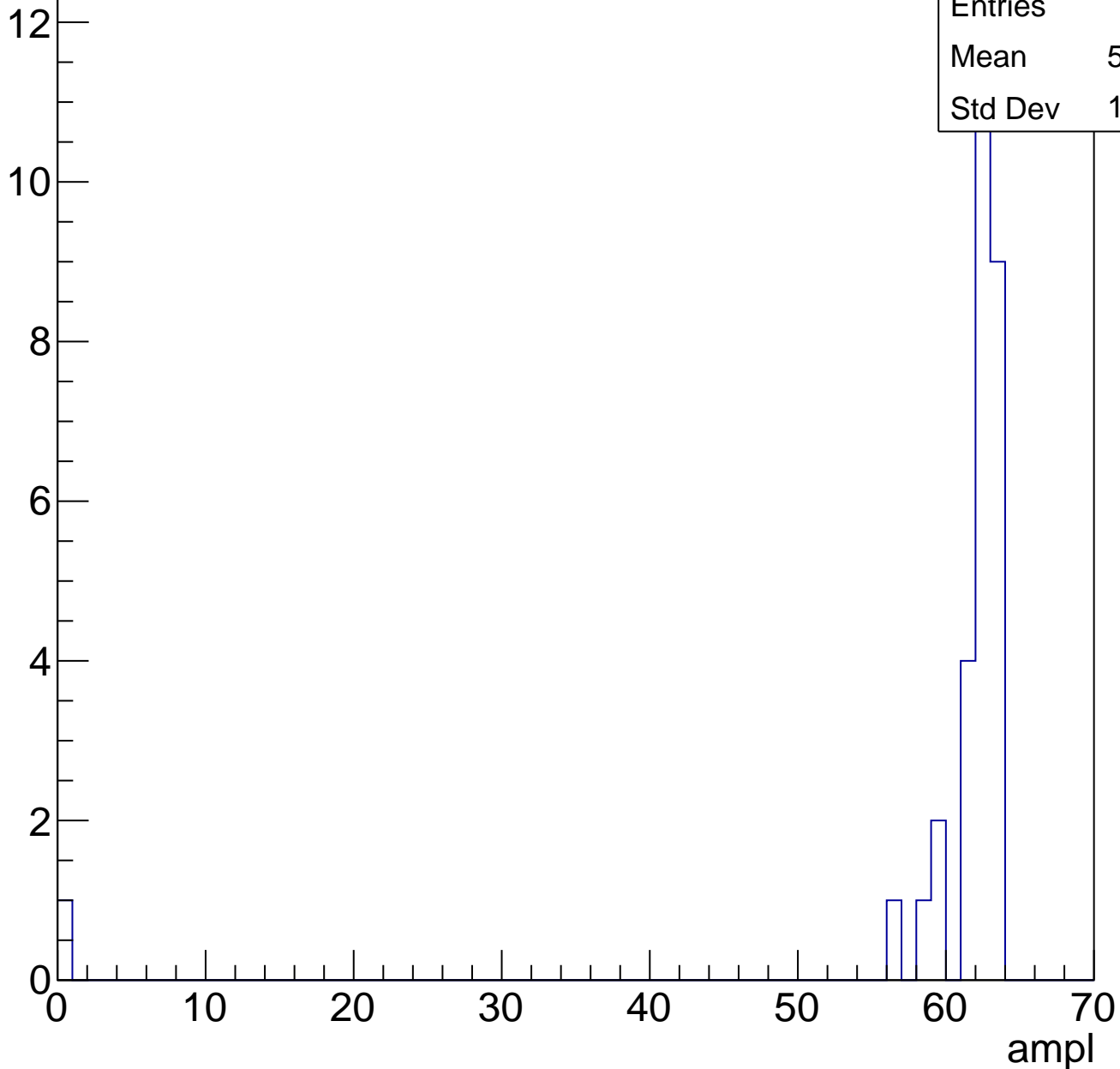


B1L001S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	30
Mean	59.57
Std Dev	11.18



B1L001S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L001S, U14-ch113, adc0

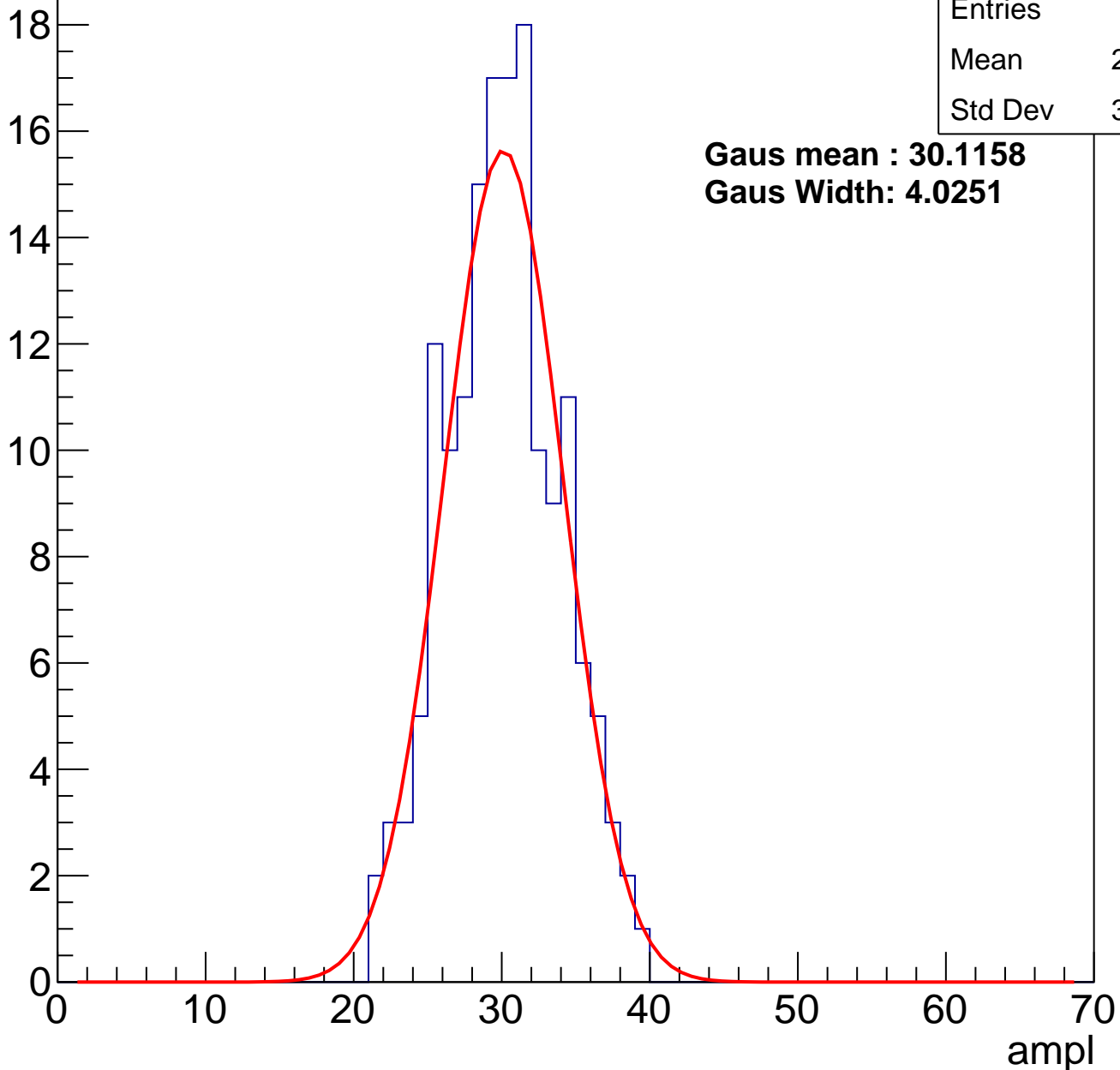
calib_packv5_042523_0143.root, FC#2, port C2

Entries	160
Mean	29.64
Std Dev	3.789

Gaus mean : 30.1158

Gaus Width: 4.0251

Entry



B1L001S, U14-ch113, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.67
Std Dev	3.29

Gaus mean : 37.0654

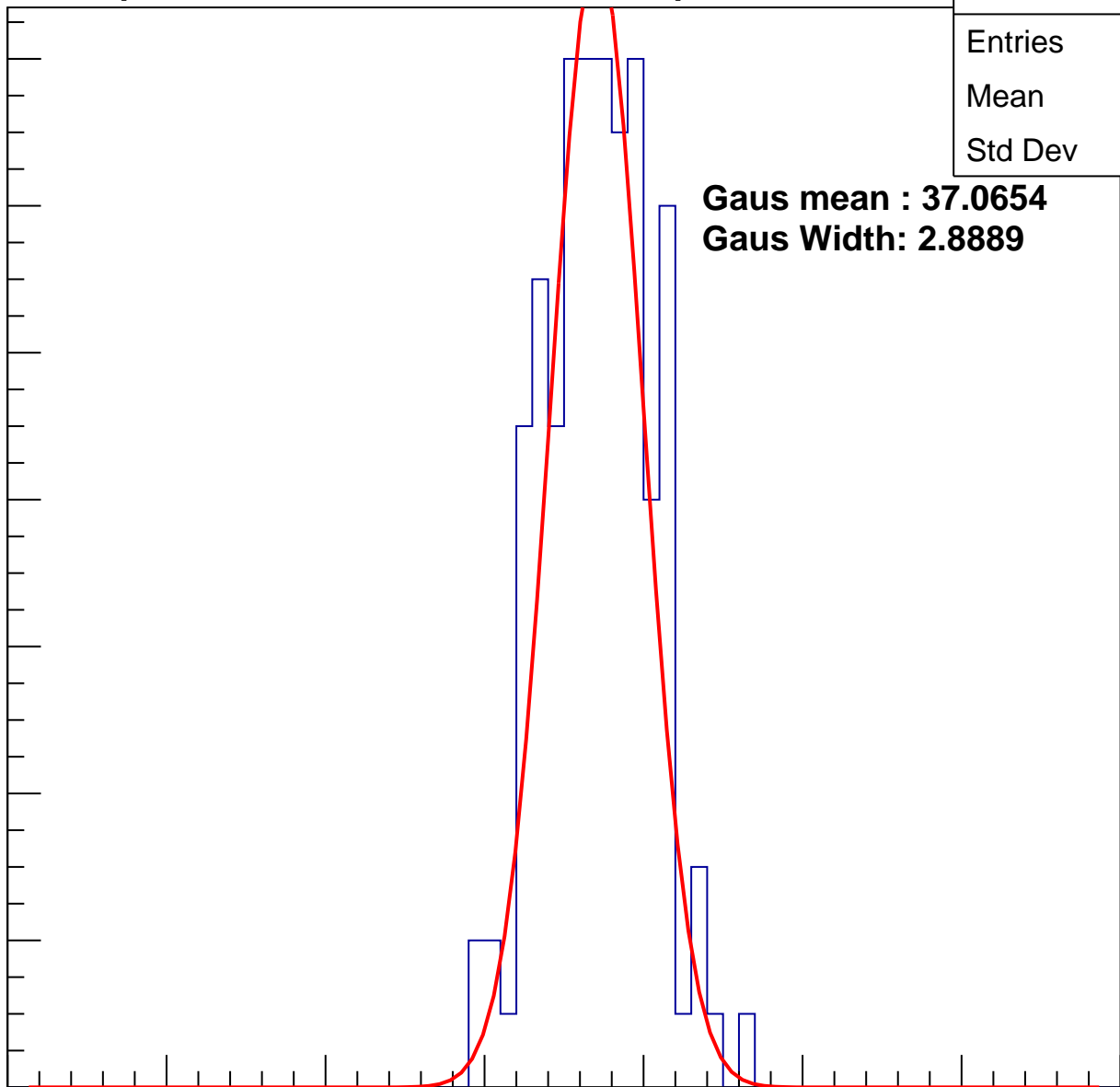
Gaus Width: 2.8889

Entry

14
12
10
8
6
4
2
0

ampl

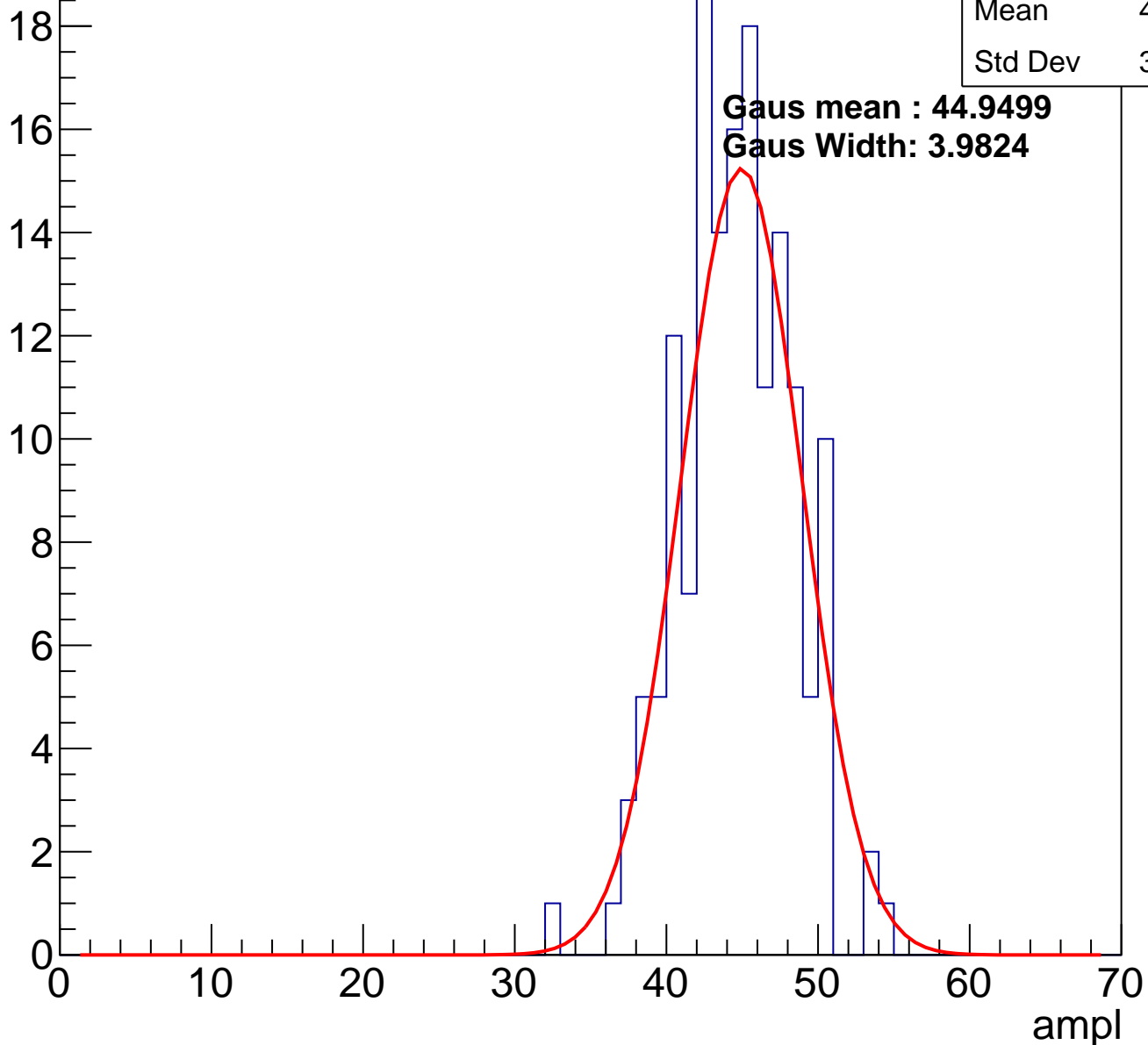
0 10 20 30 40 50 60 70



B1L001S, U14-ch113, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#2, port C2

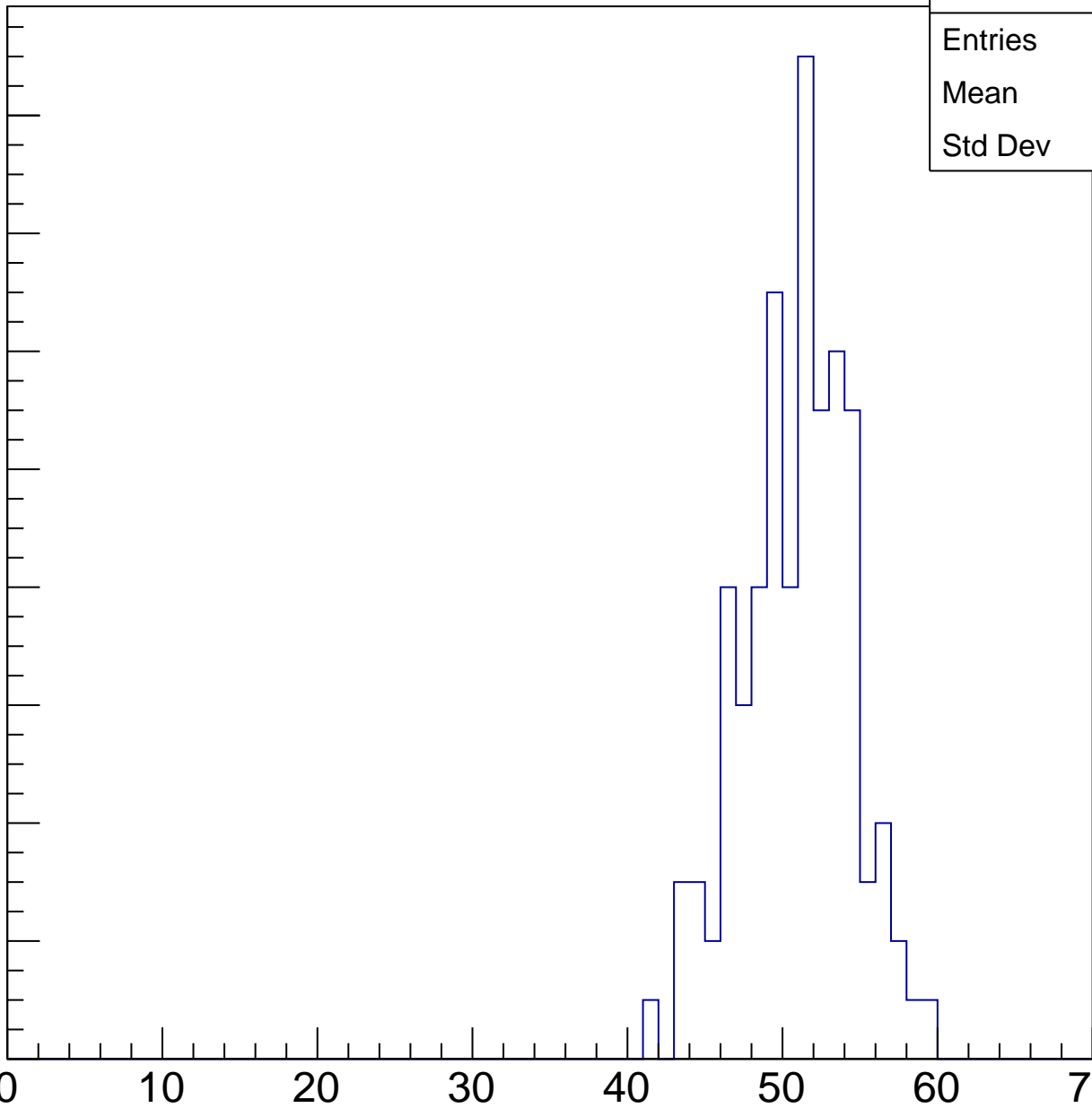
Entries	114
Mean	50.46
Std Dev	3.5

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L001S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	103
Mean	55.5
Std Dev	6.372

Entry

12

10

8

6

4

2

0

0

10

20

30

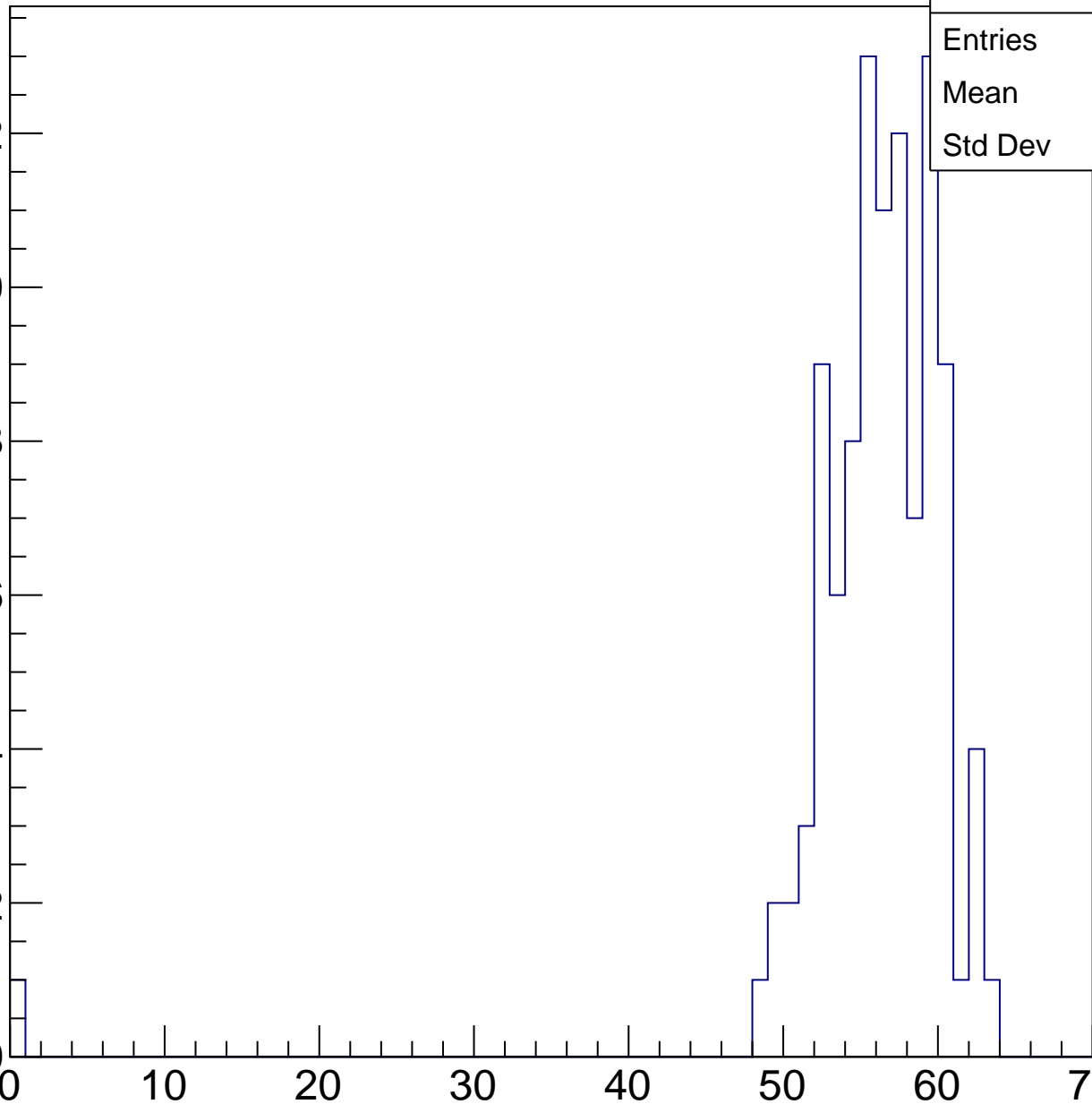
40

50

60

70

ampl



B1L001S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	82
Mean	59.12
Std Dev	6.975

Entry

16
14
12
10
8
6
4
2
0

0

10

20

30

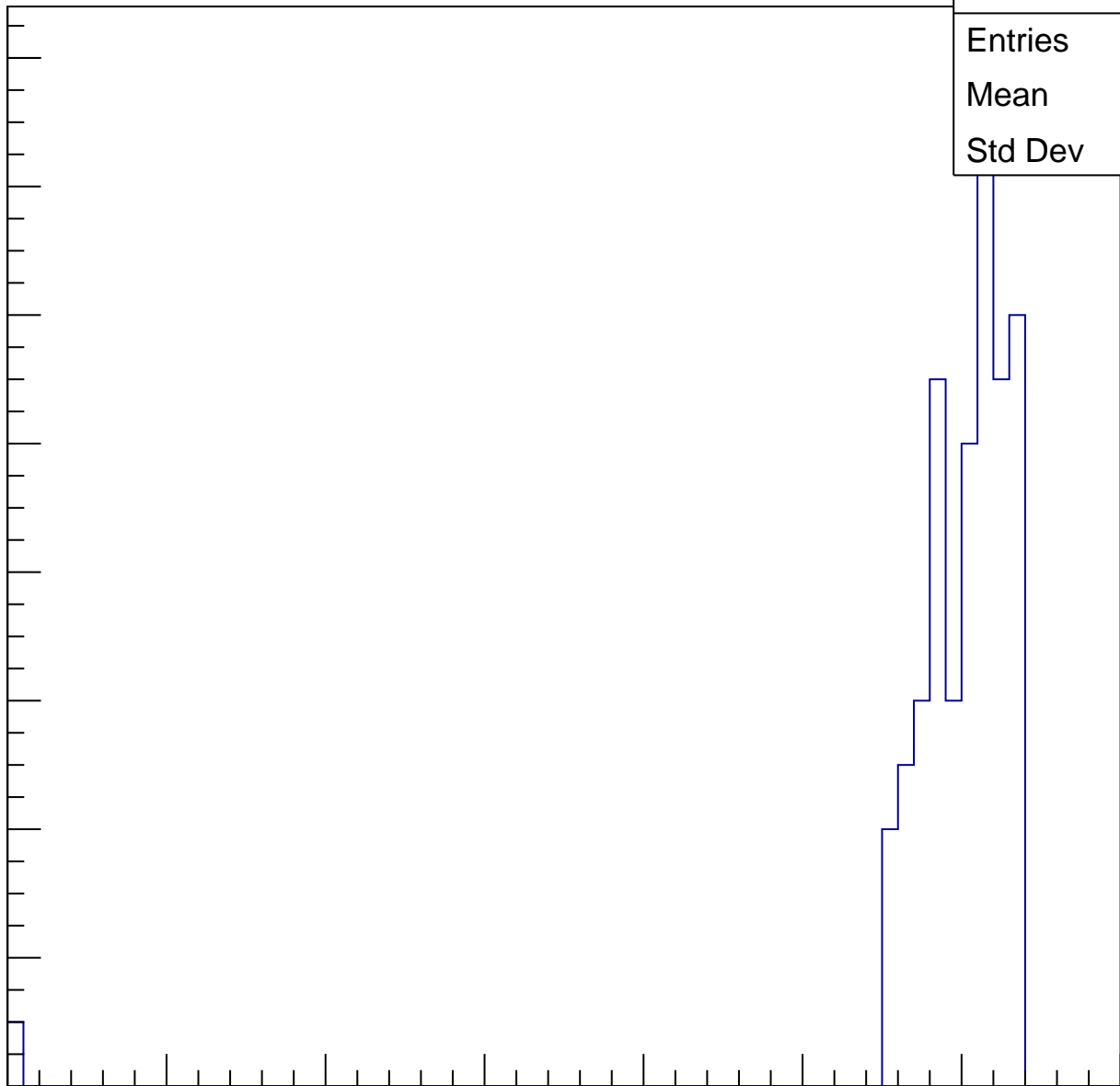
40

50

60

70

ampl

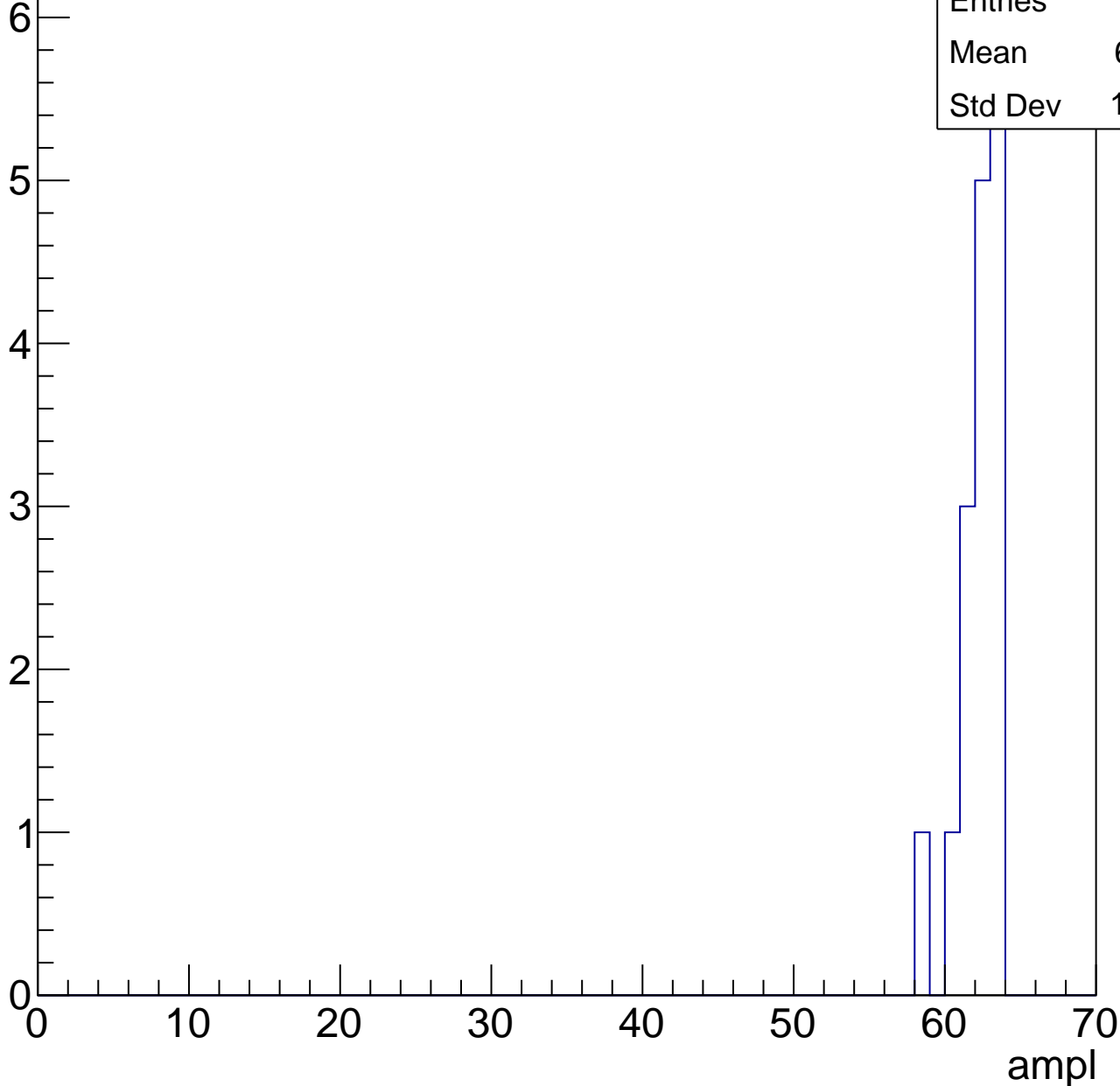


B1L001S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

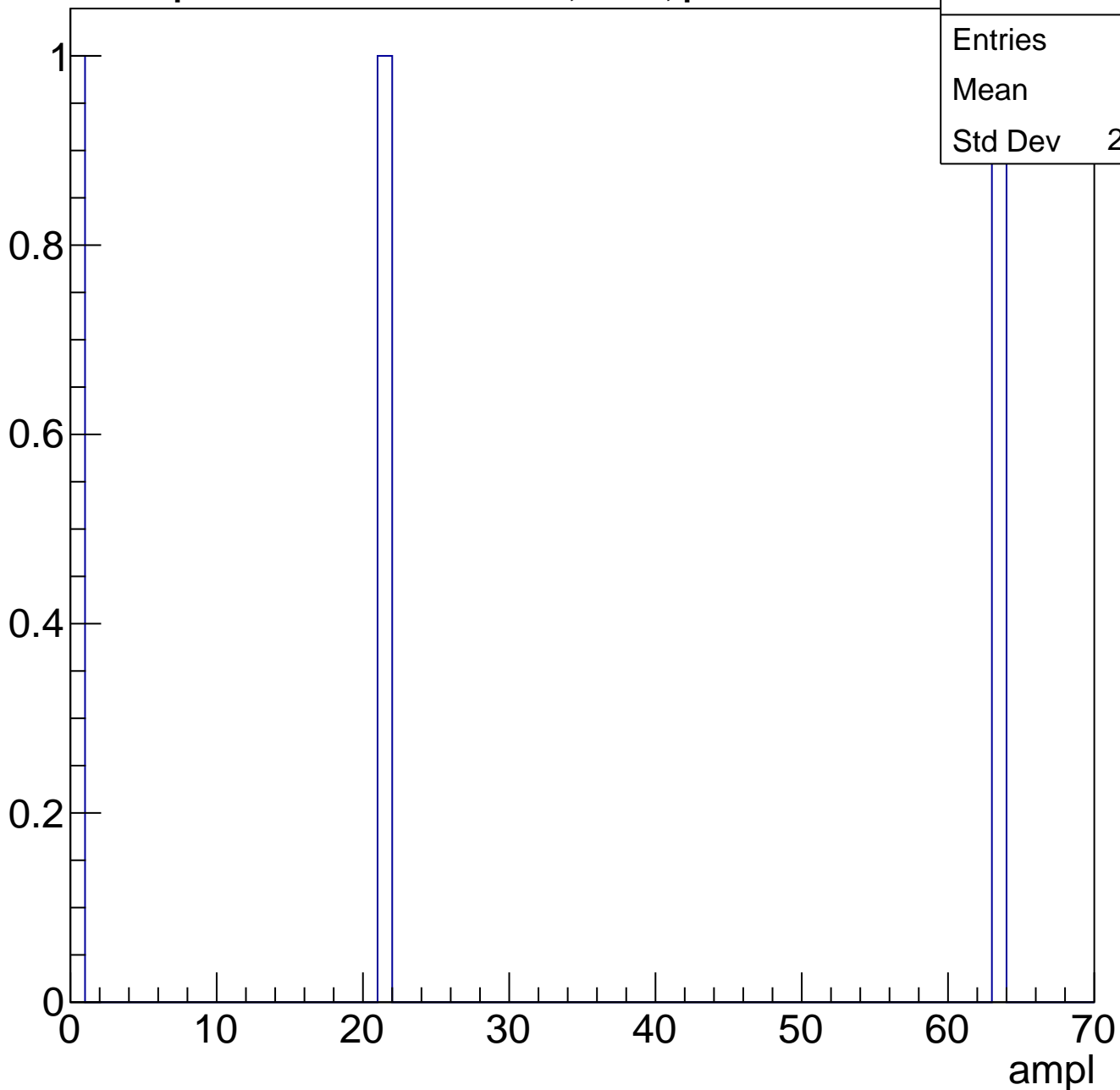
Entries	16
Mean	61.81
Std Dev	1.333



B1L001S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	28
Std Dev	26.19

B1L001S, U14-ch114, adc0

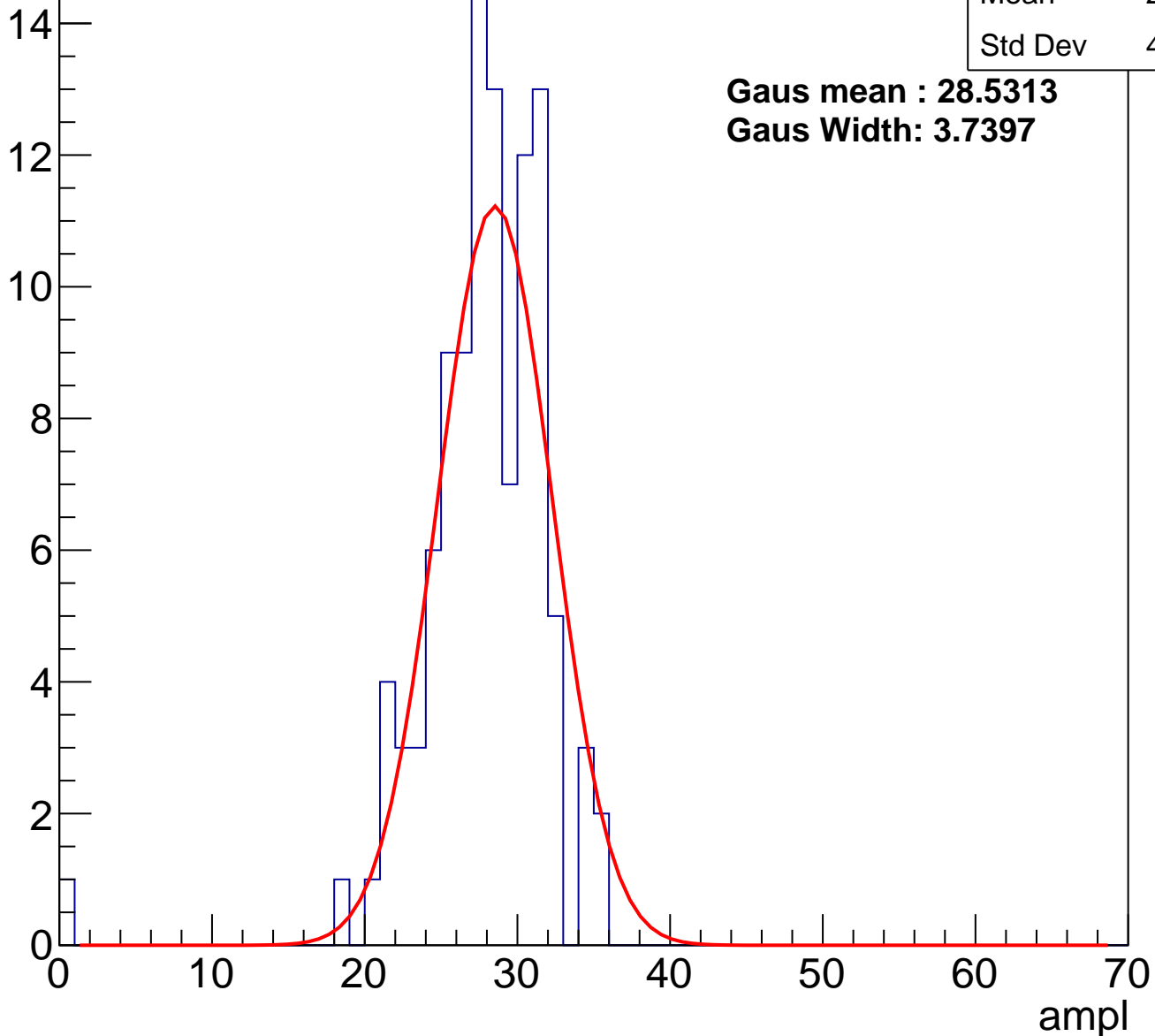
calib_packv5_042523_0143.root, FC#2, port C2

Entries	107
Mean	27.36
Std Dev	4.289

Gaus mean : 28.5313

Gaus Width: 3.7397

Entry



B1L001S, U14-ch114, adc1

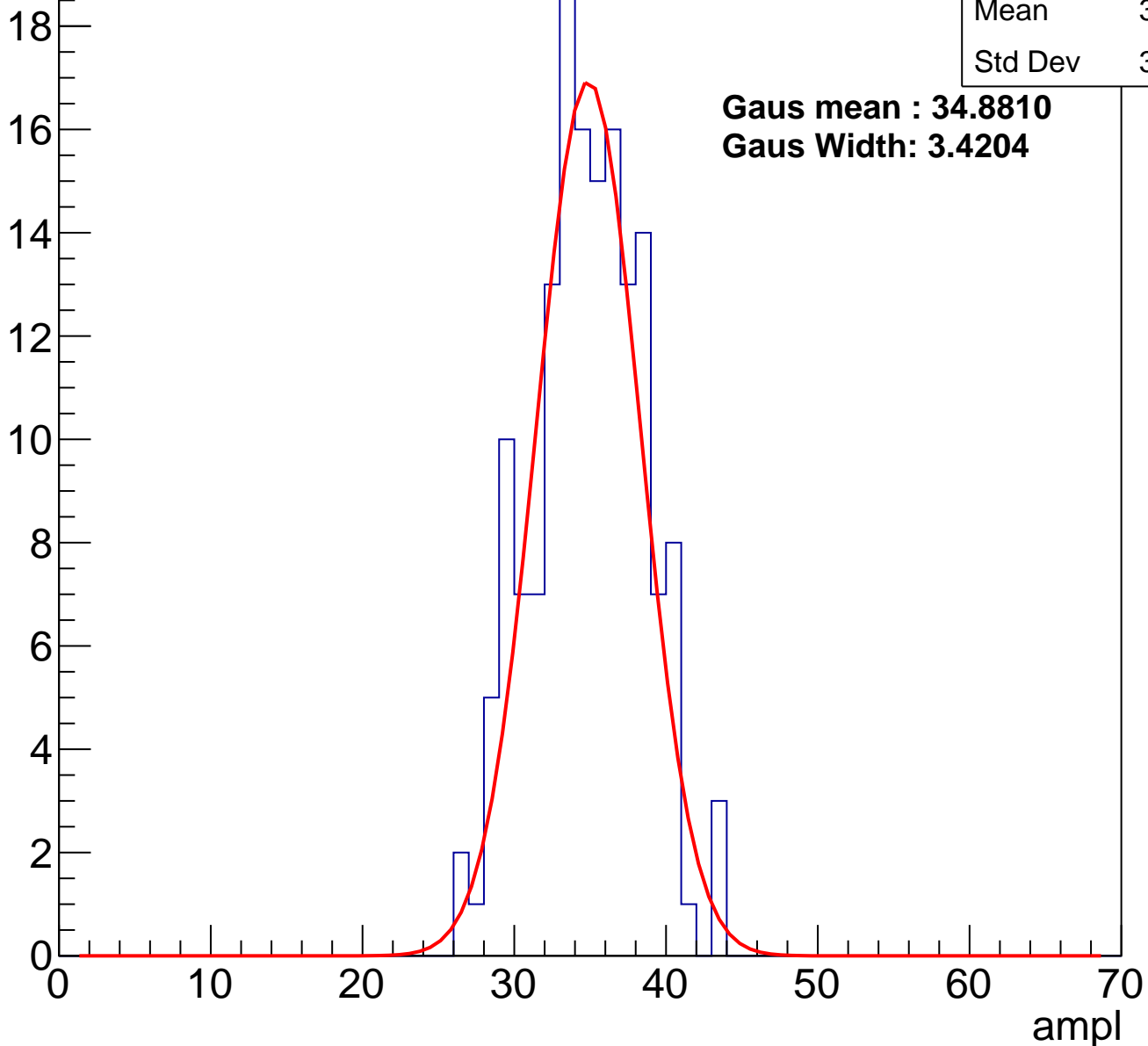
calib_packv5_042523_0143.root, FC#2, port C2

Entries	157
Mean	34.39
Std Dev	3.572

Gaus mean : 34.8810

Gaus Width: 3.4204

Entry



B1L001S, U14-ch114, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entry

14

12

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

106

Mean

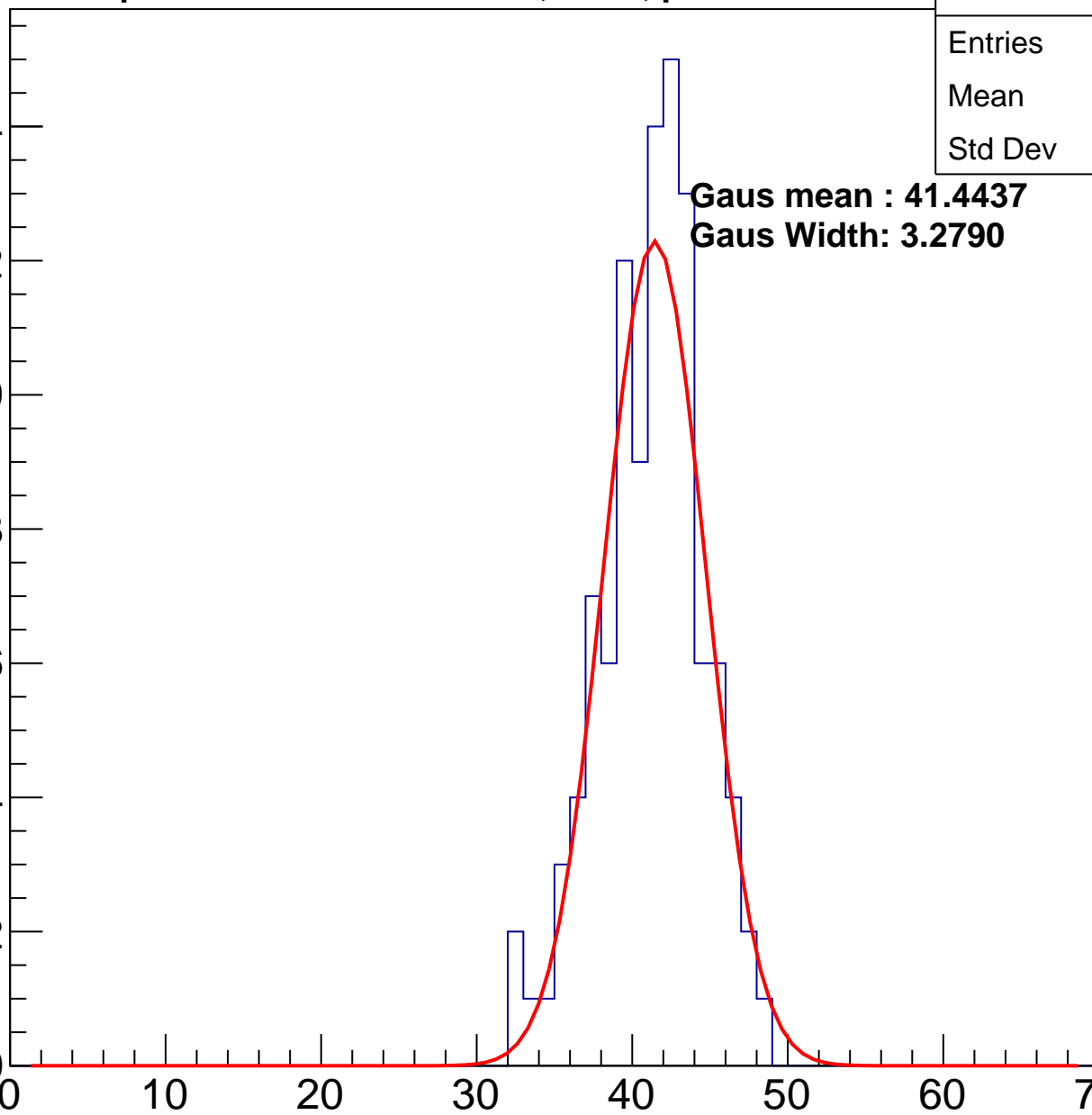
40.74

Std Dev

3.275

Gaus mean : 41.4437

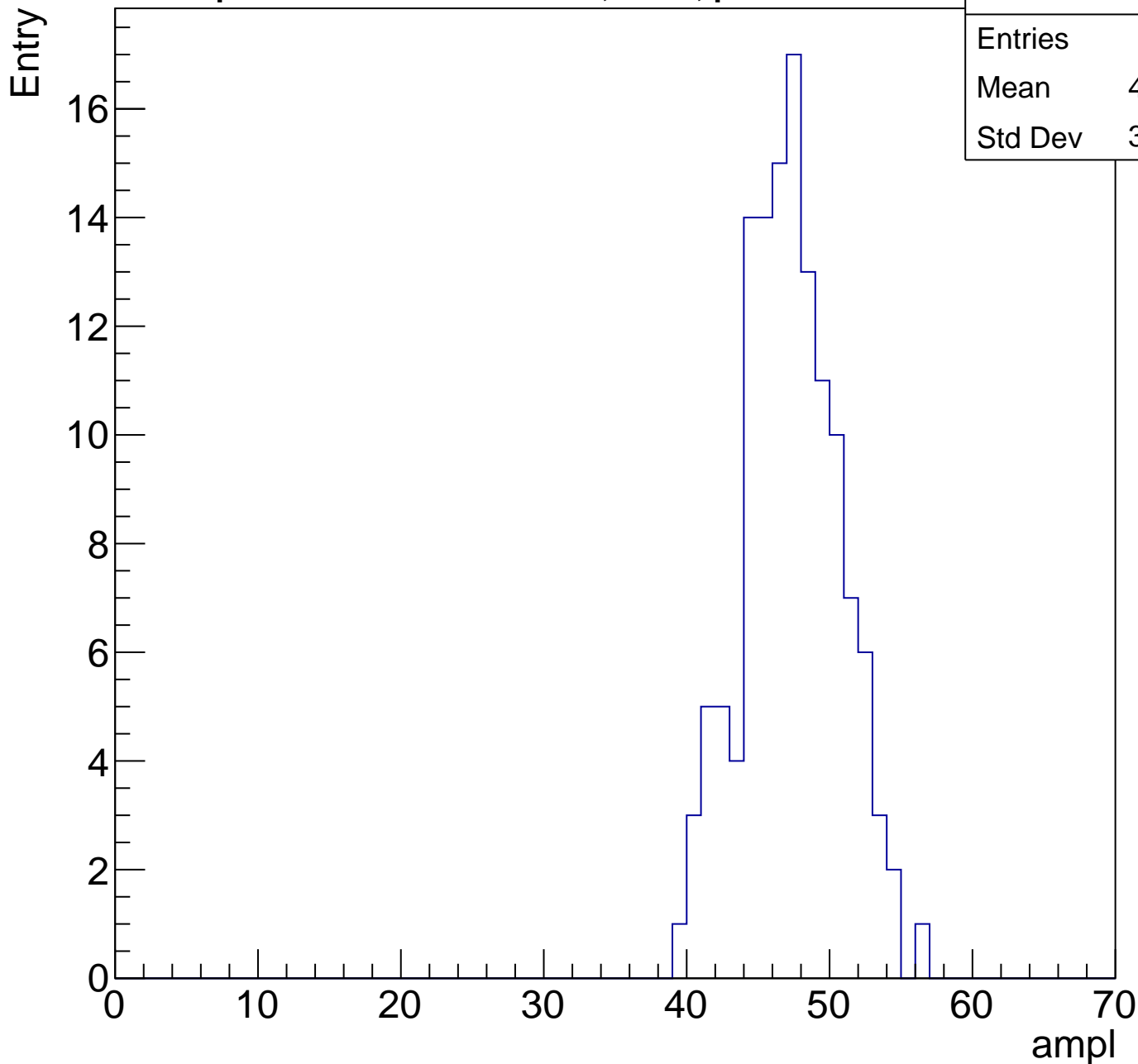
Gaus Width: 3.2790



B1L001S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#2, port C2

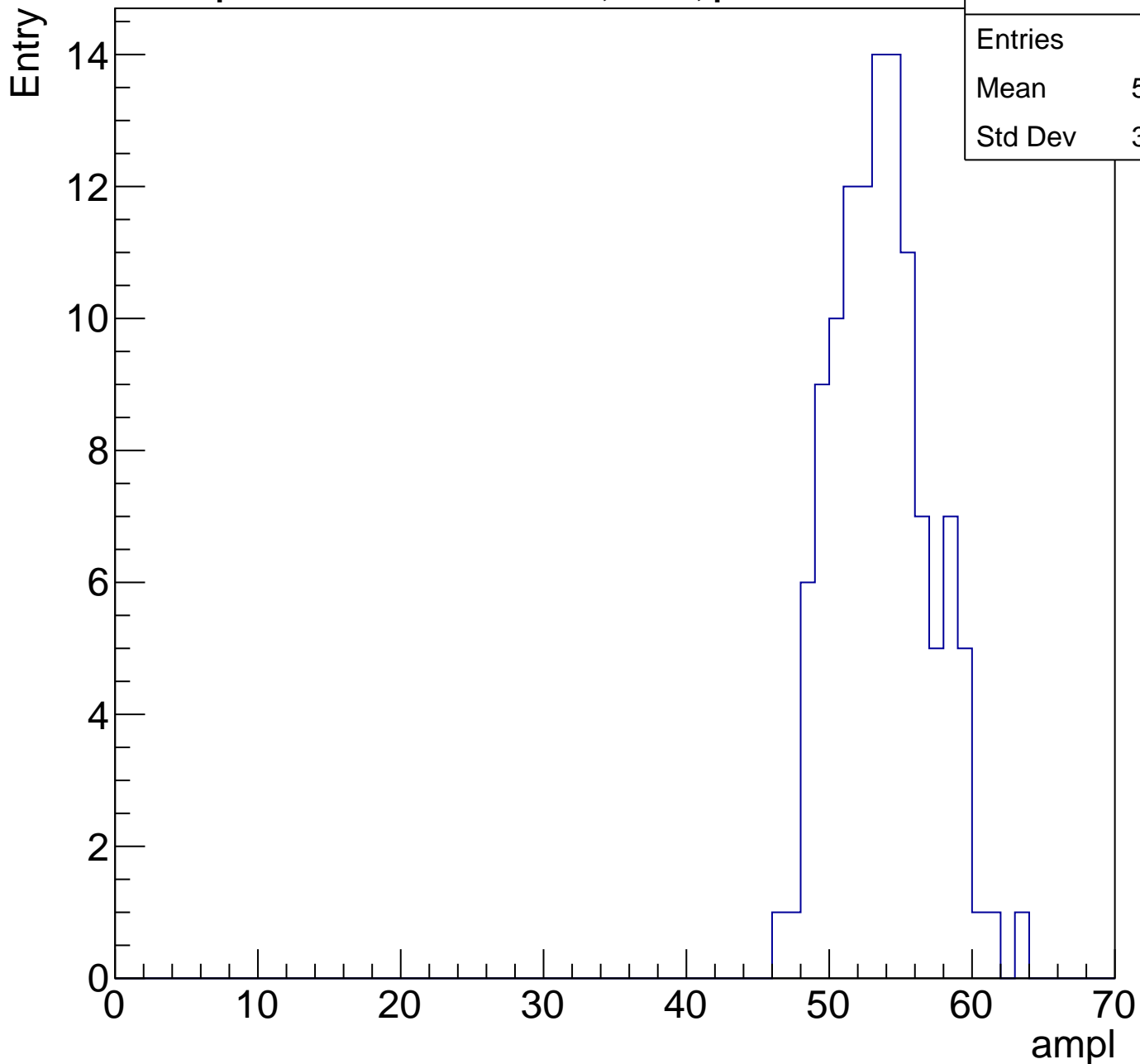
Entries	131
Mean	46.84
Std Dev	3.349



B1L001S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#2, port C2

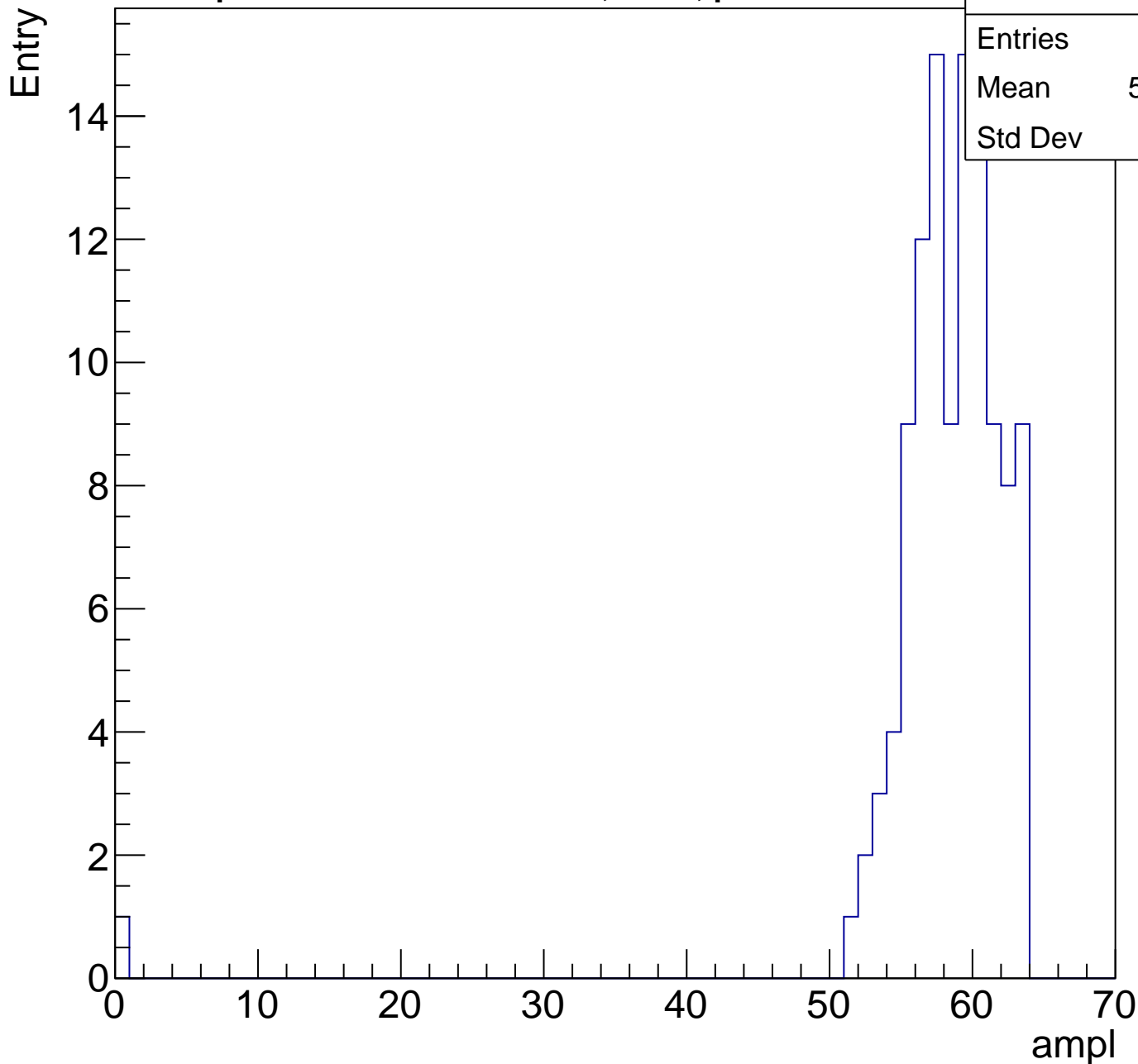
Entries	117
Mean	53.19
Std Dev	3.325



B1L001S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	57.76
Std Dev	6.2



B1L001S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

2

0

Entries

41

Mean

59.88

Std Dev

9.579

ampl

0

10

20

30

40

50

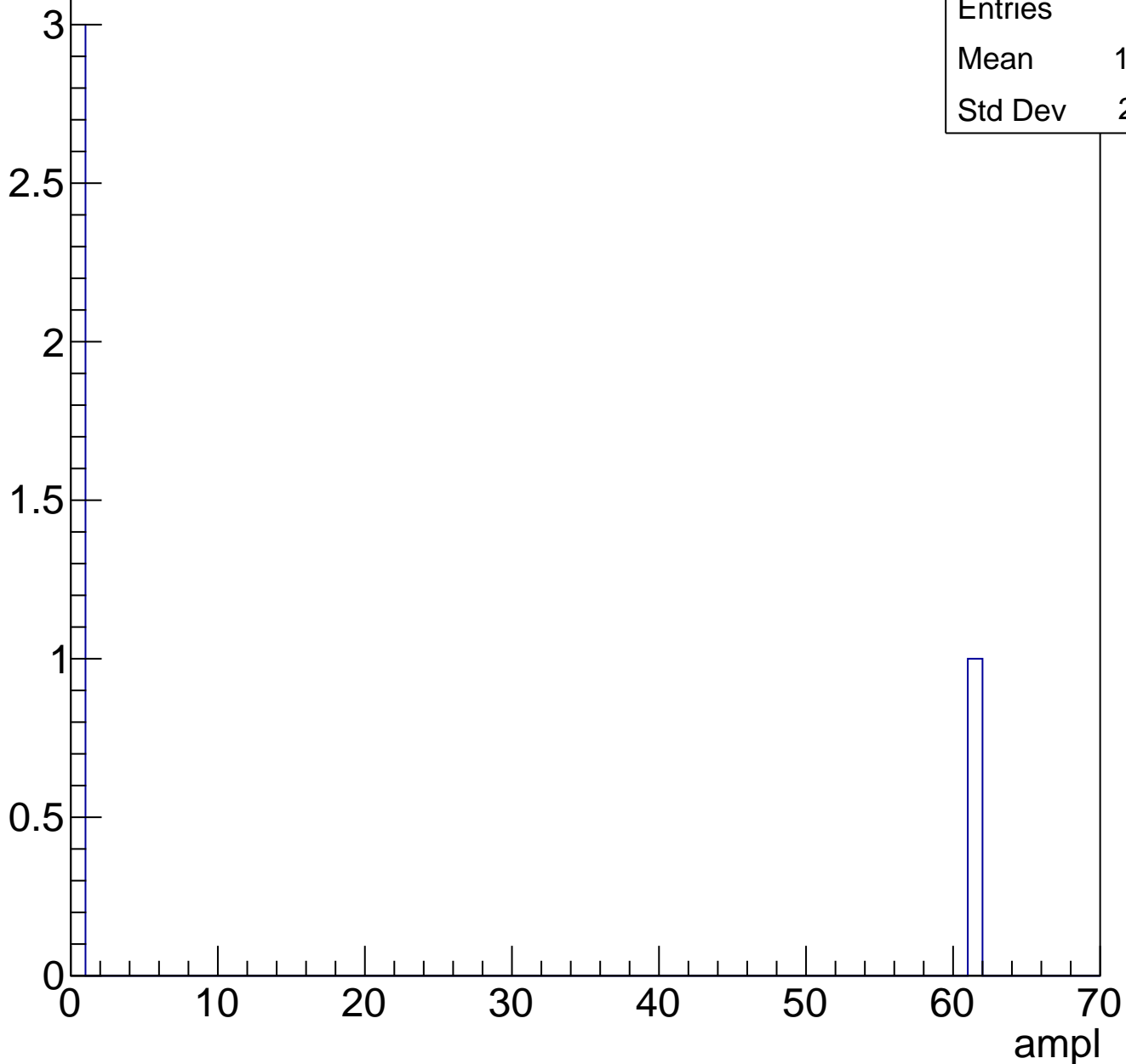
60

70

B1L001S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#2, port C2

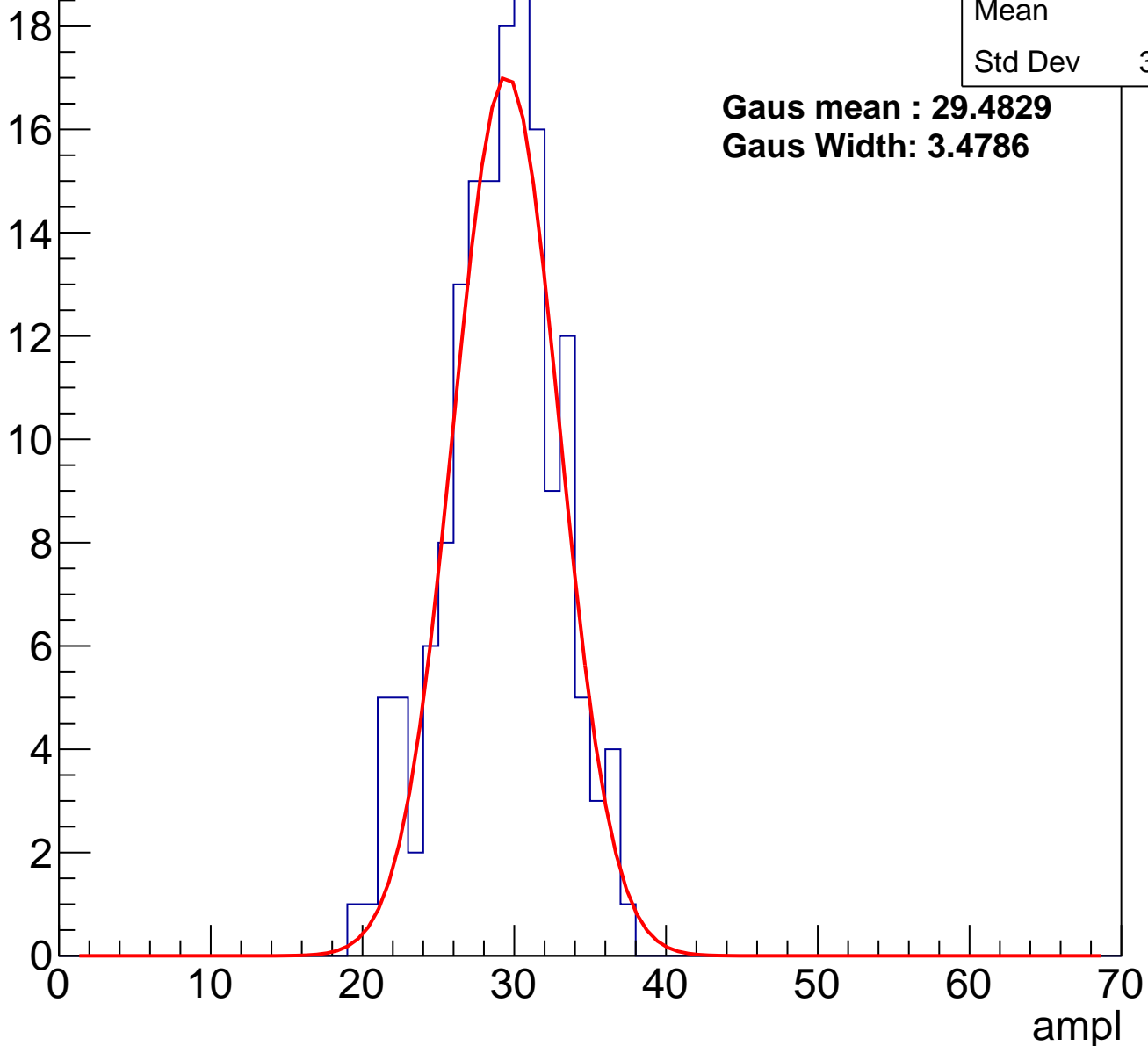
Entry



B1L001S, U14-ch115, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	158
Mean	28.7
Std Dev	3.666

B1L001S, U14-ch115, adc1

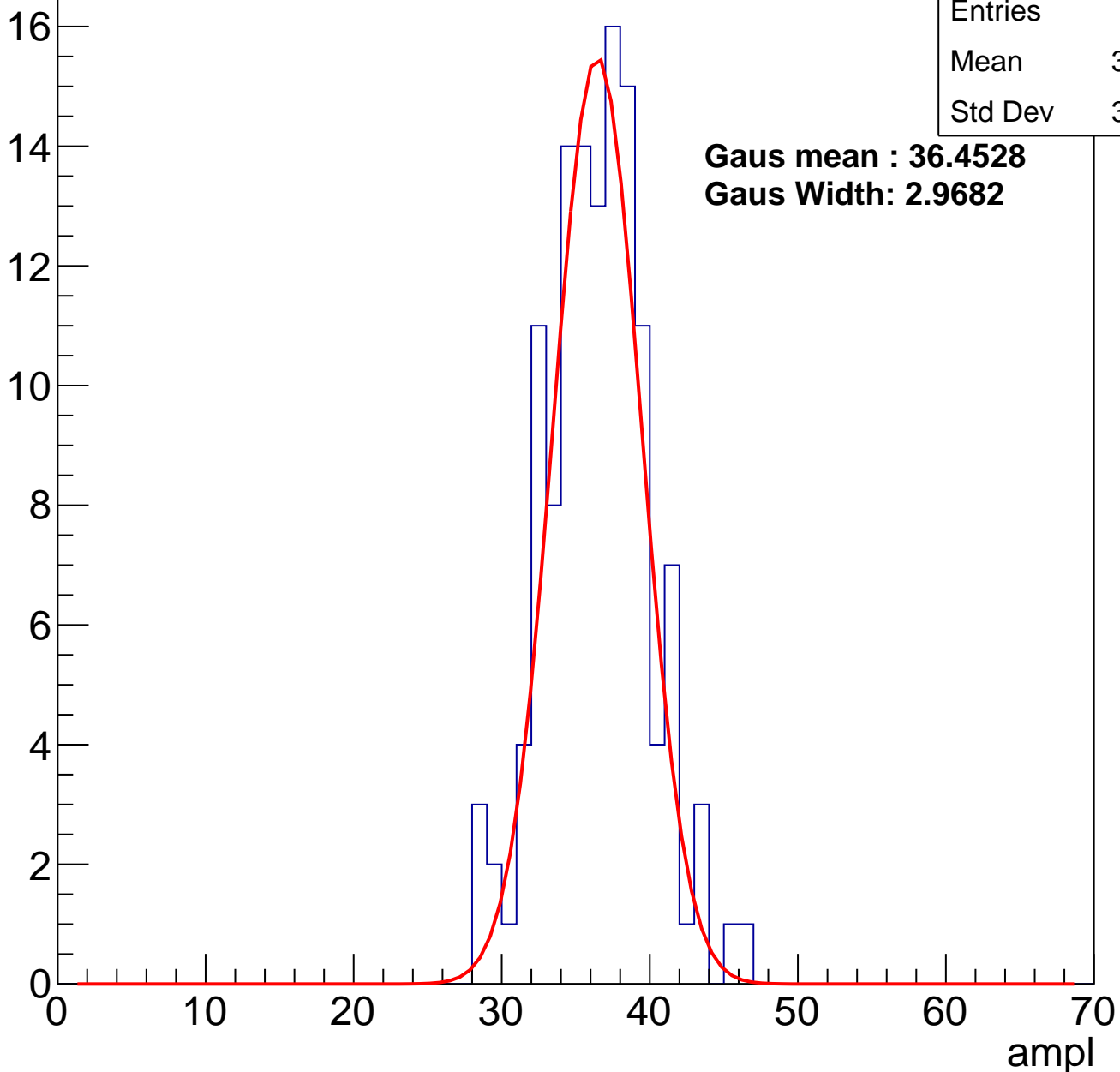
calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	36.02
Std Dev	3.435

Gaus mean : 36.4528

Gaus Width: 2.9682

Entry



B1L001S, U14-ch115, adc2

calib_packv5_042523_0143.root, FC#2, port C2

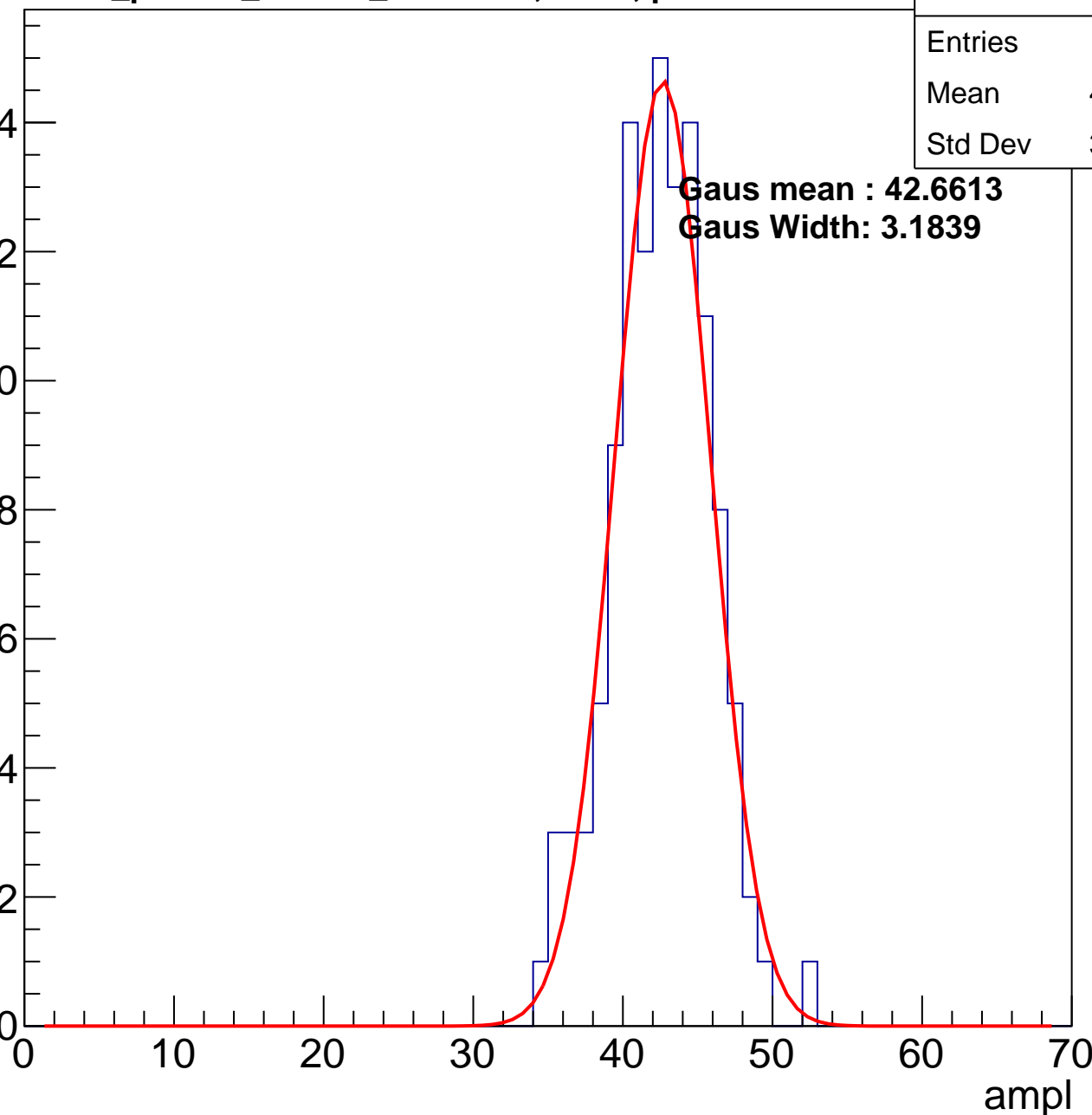
Entry

14
12
10
8
6
4
2
0

Entries	120
Mean	42.09
Std Dev	3.245

Gaus mean : 42.6613

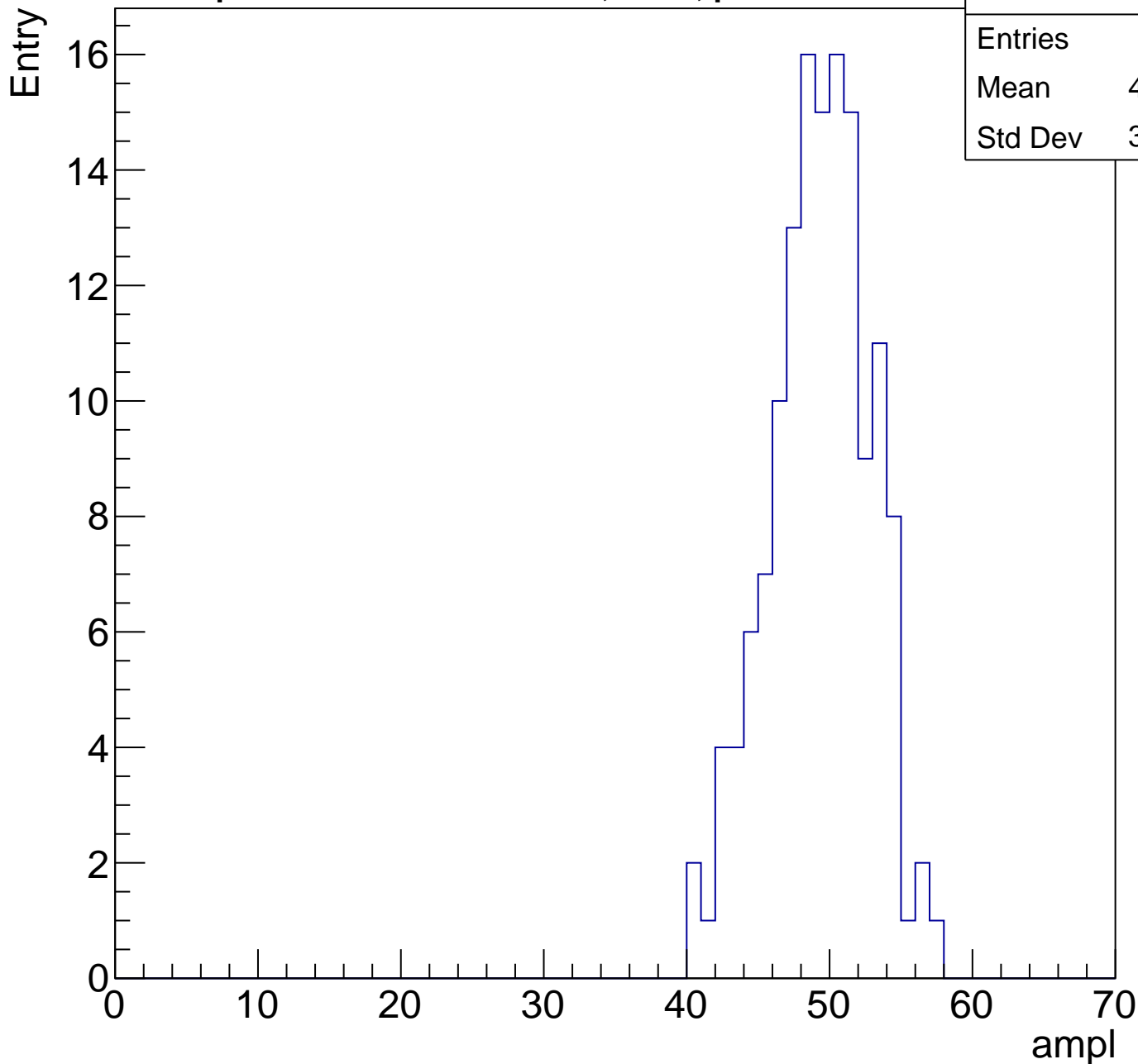
Gaus Width: 3.1839



B1L001S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#2, port C2

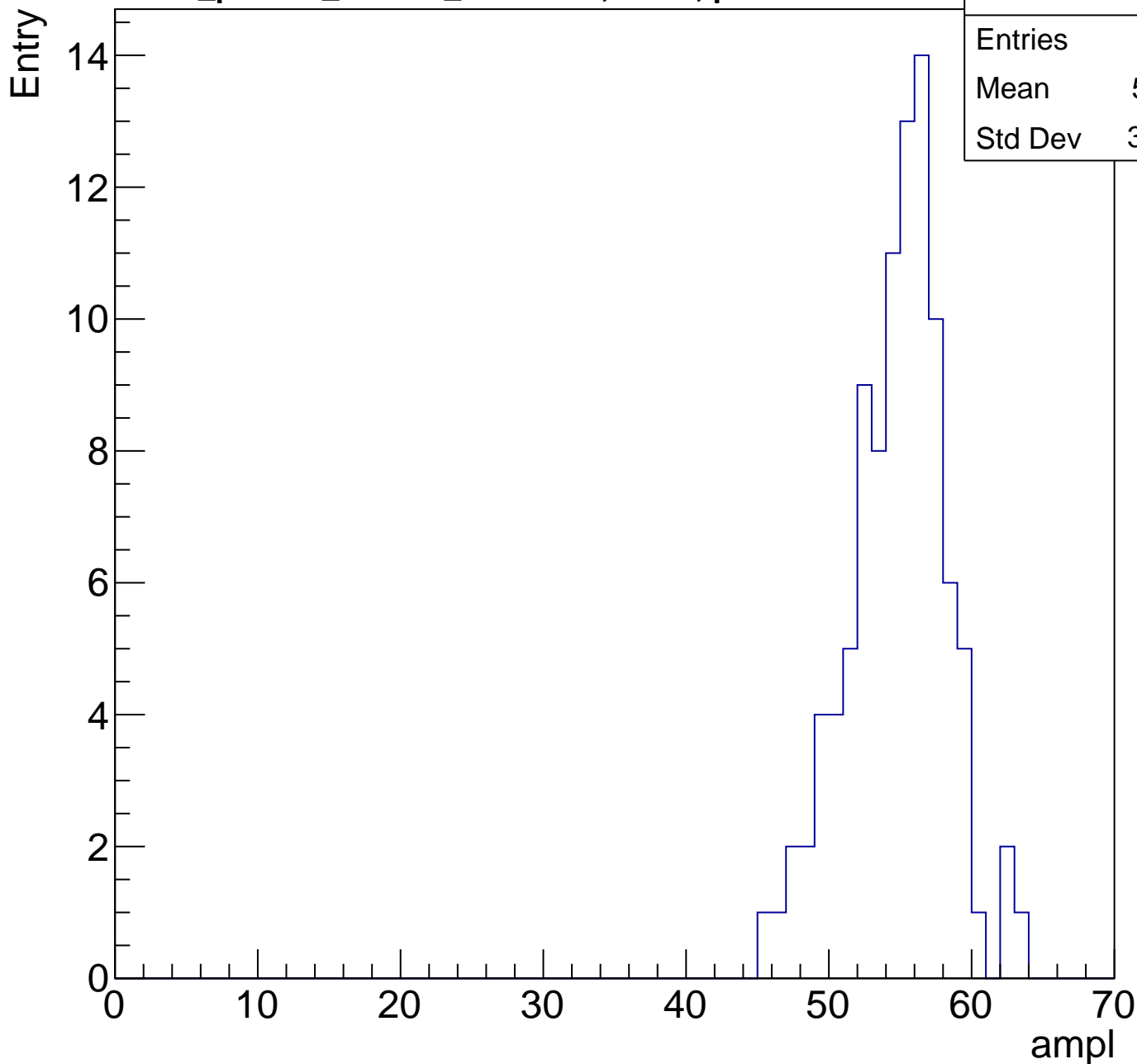
Entries	141
Mean	48.84
Std Dev	3.486



B1L001S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	99
Mean	54.31
Std Dev	3.457



B1L001S, U14-ch115, adc5

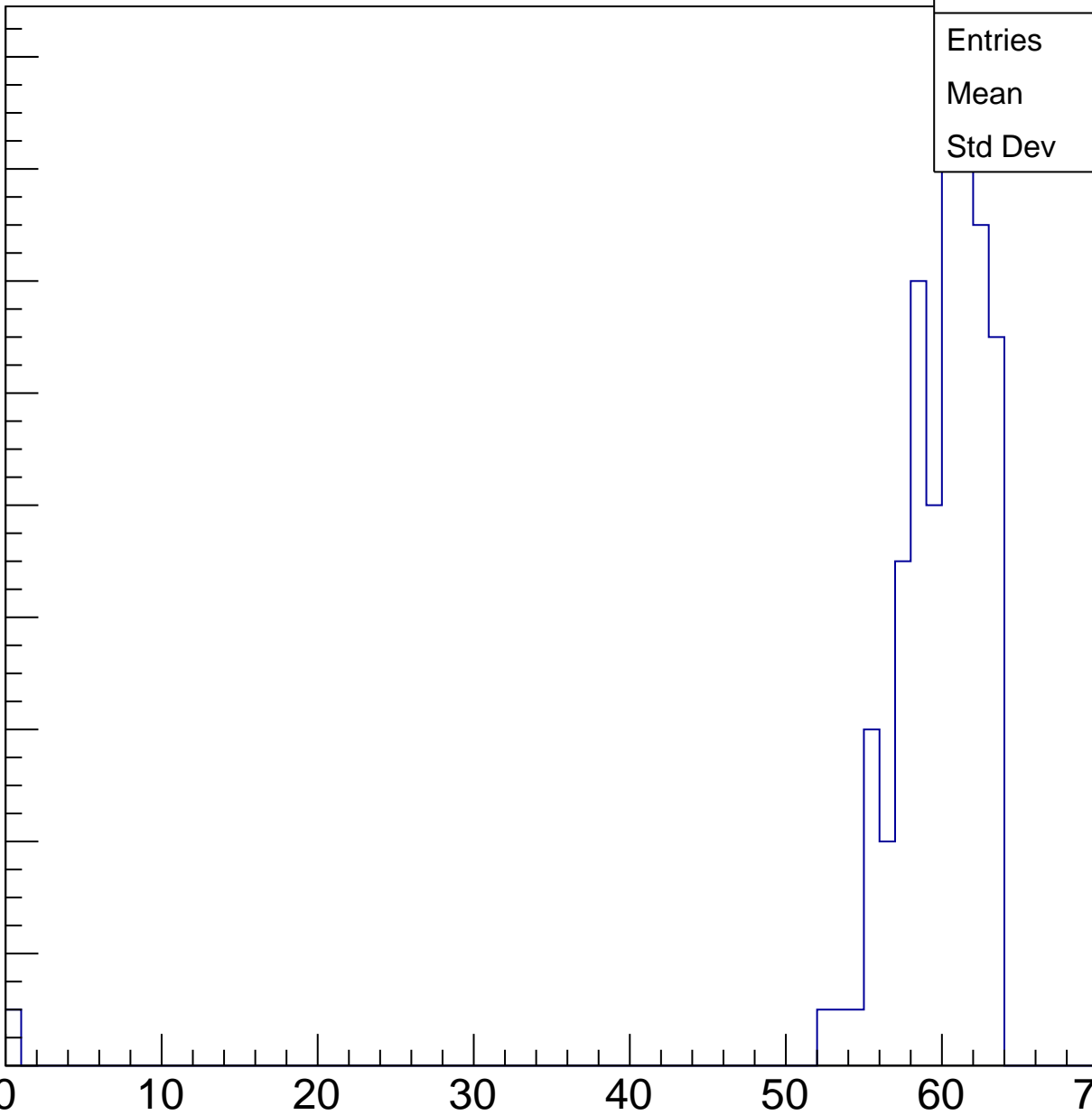
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	110
Mean	59.05
Std Dev	6.183

ampl

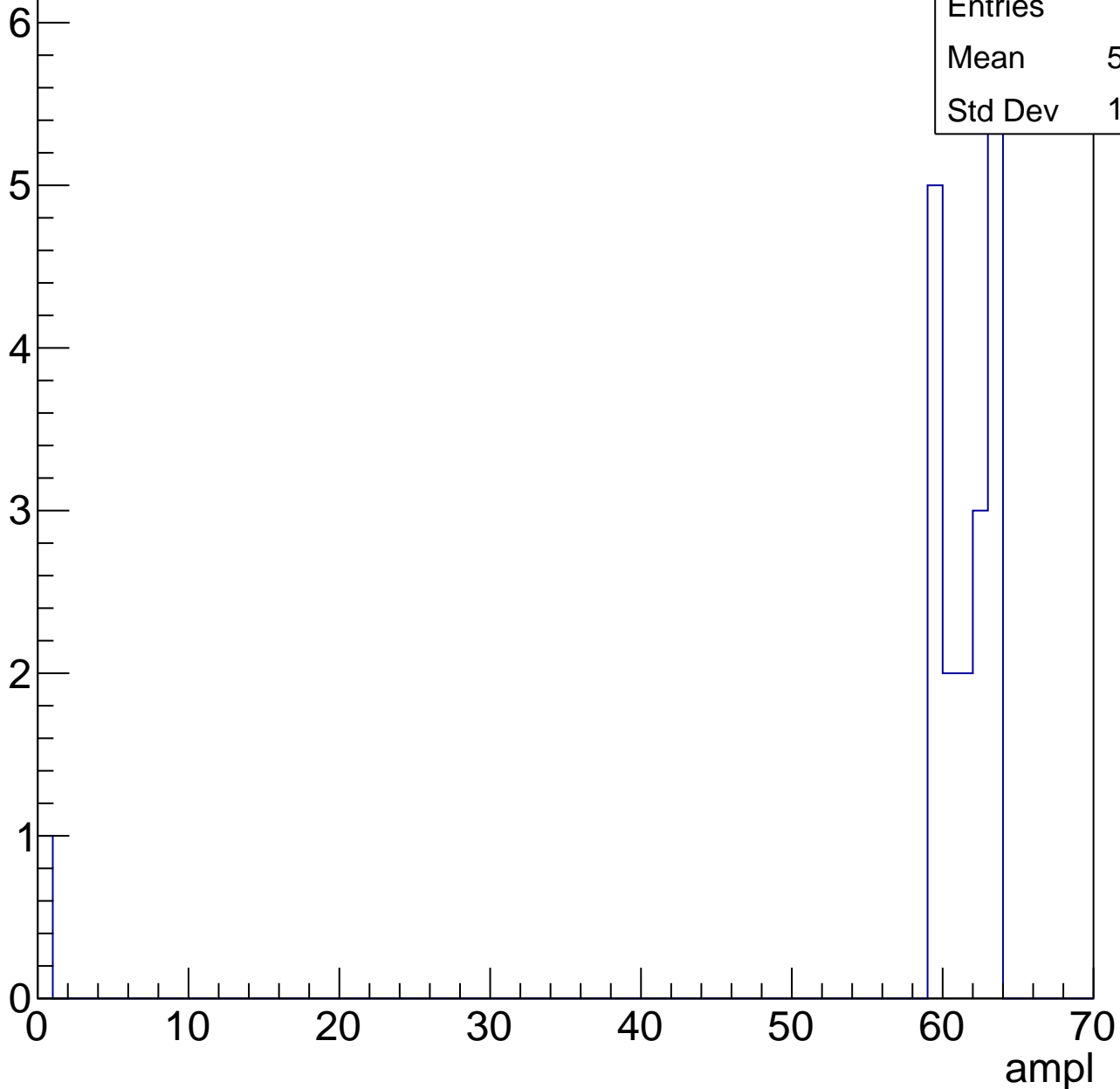


B1L001S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	57.95
Std Dev	13.75



B1L001S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch116, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	147
Mean	27.29
Std Dev	3.343

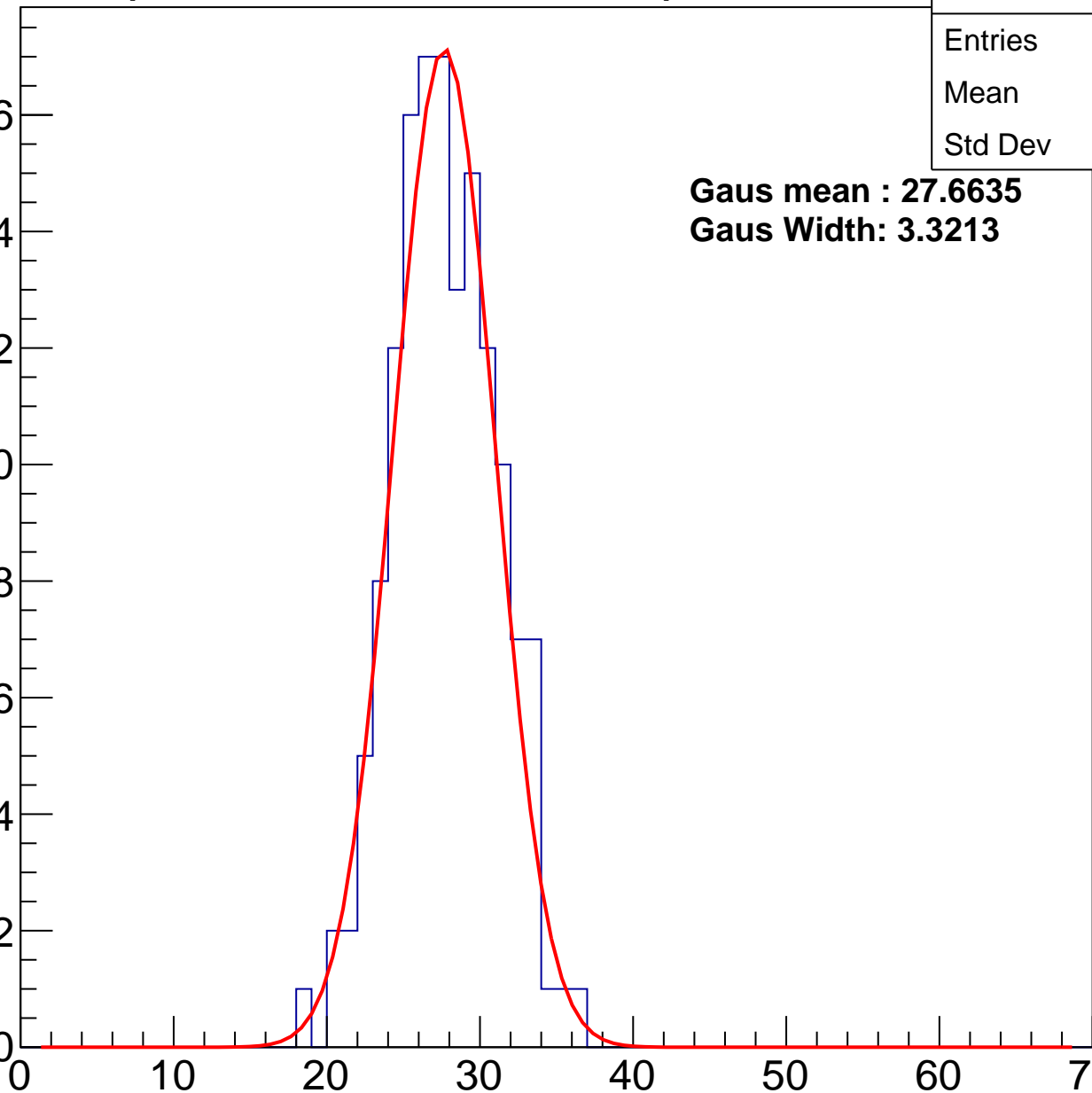
Gaus mean : 27.6635

Gaus Width: 3.3213

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch116, adc1

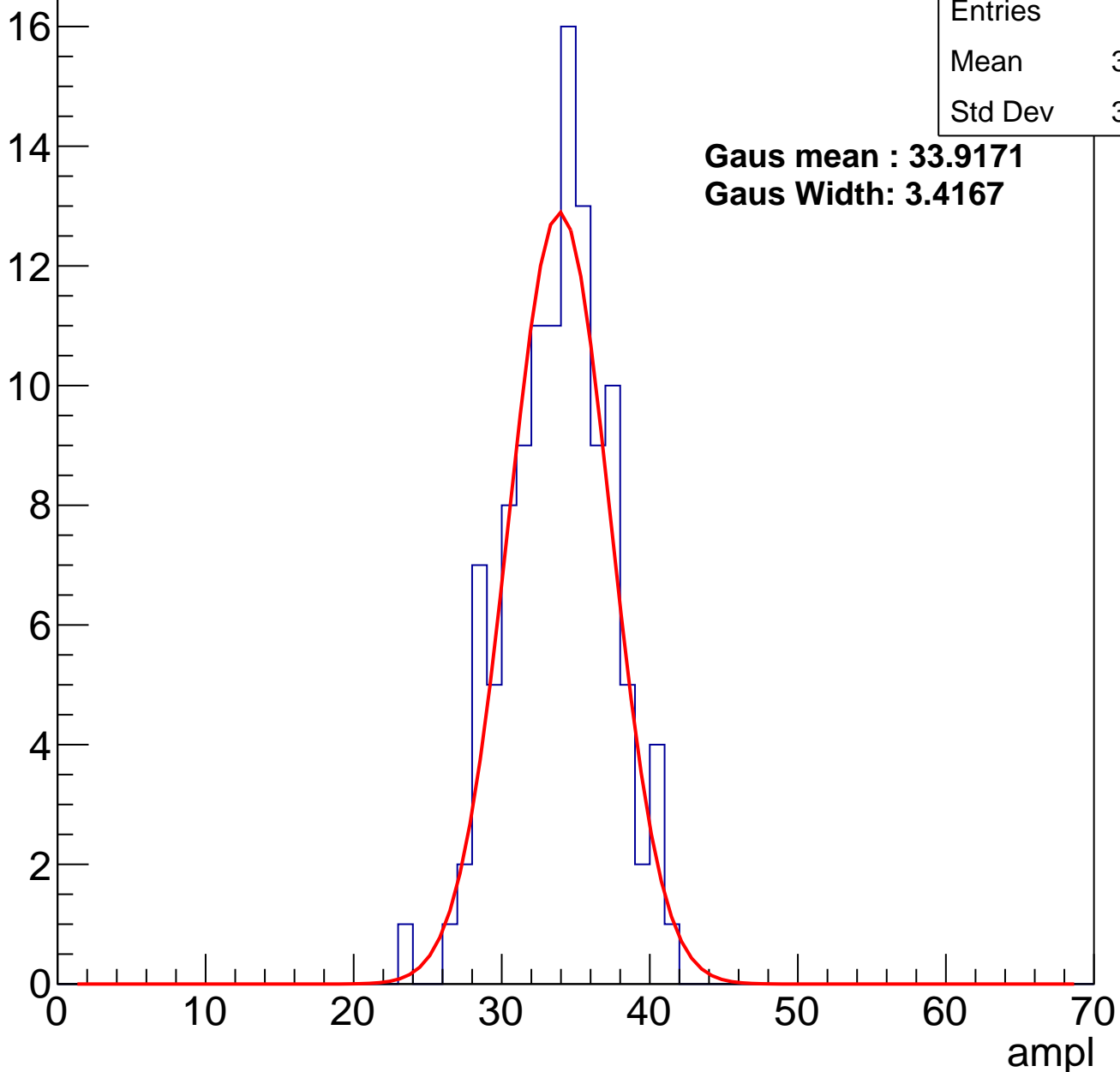
calib_packv5_042523_0143.root, FC#2, port C2

Entries	115
Mean	33.39
Std Dev	3.418

Gaus mean : 33.9171

Gaus Width: 3.4167

Entry



B1L001S, U14-ch116, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	136
Mean	39.9
Std Dev	3.438

Gaus mean : 40.2652

Gaus Width: 3.4560

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

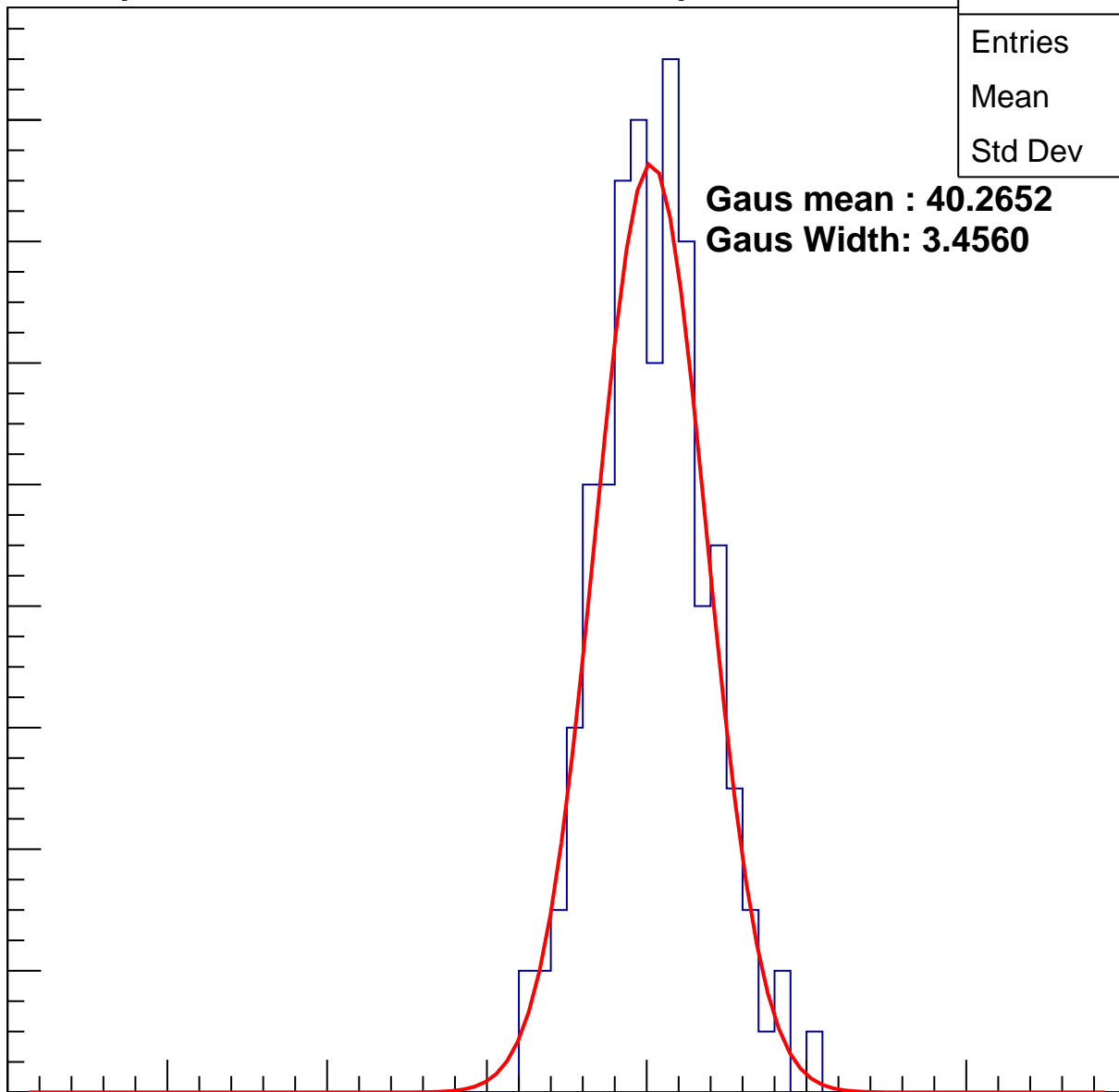
40

50

60

70

ampl

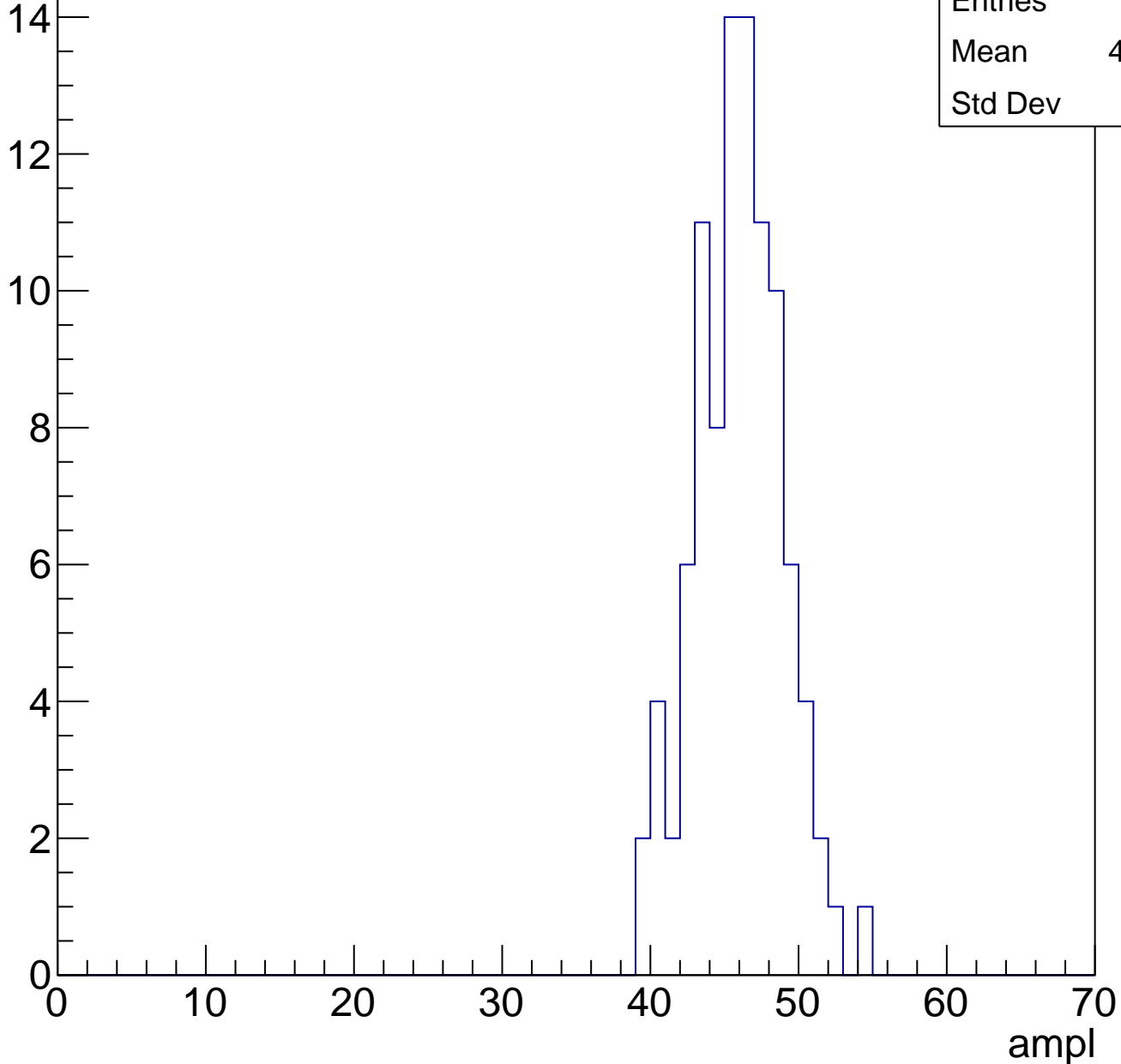


B1L001S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

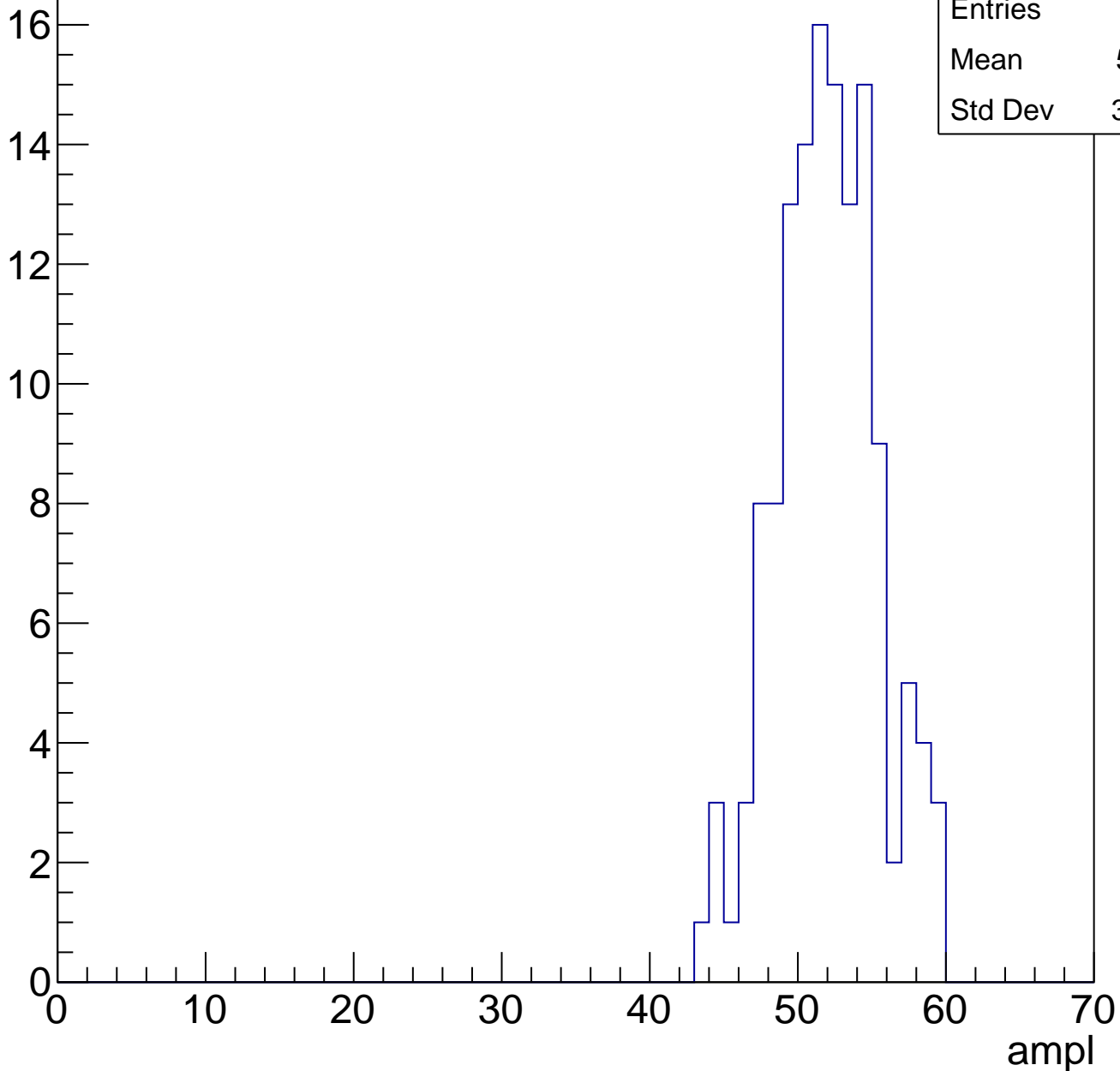
Entries	96
Mean	45.52
Std Dev	2.94



B1L001S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entry

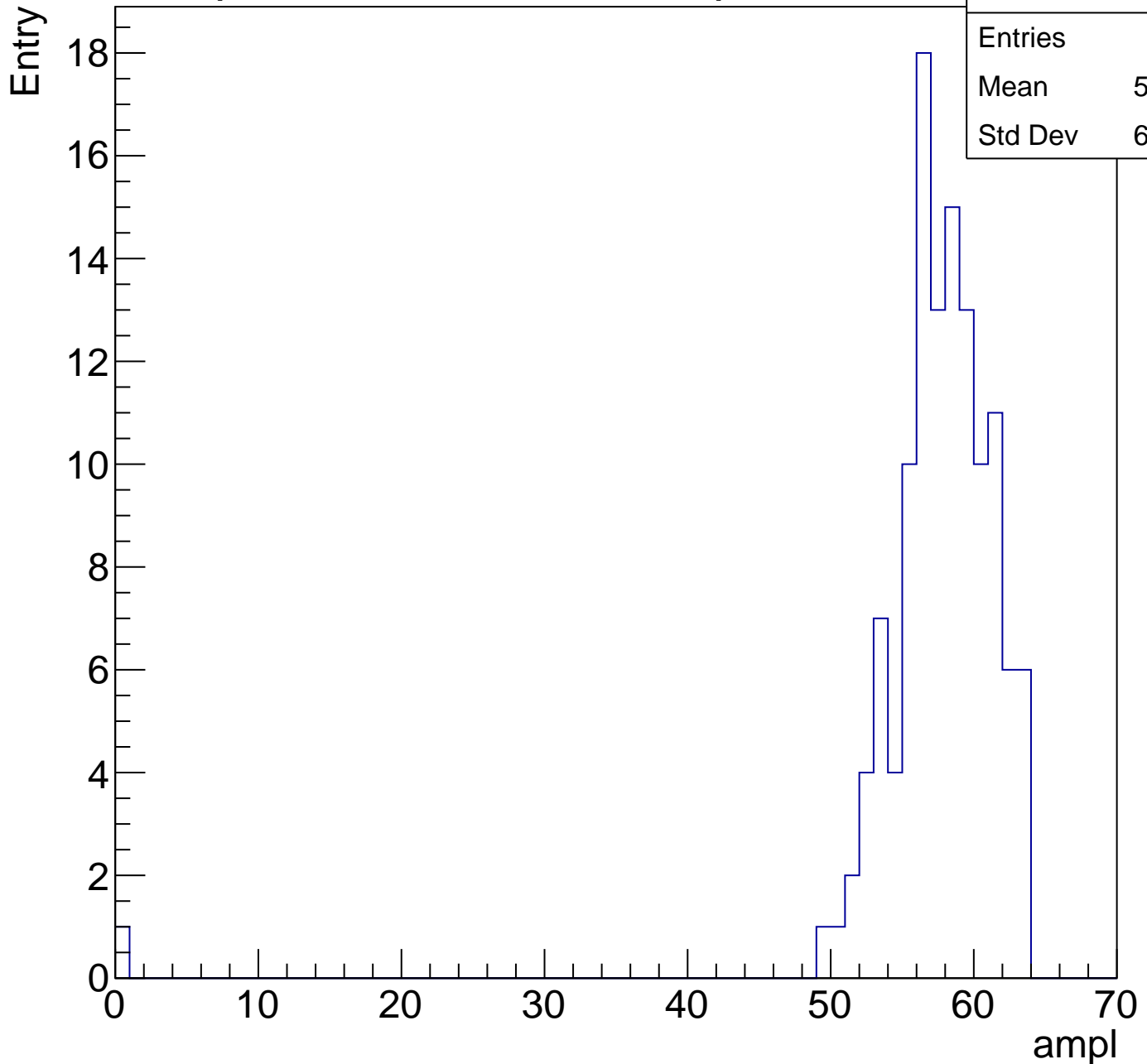


Entries	133
Mean	51.51
Std Dev	3.378

B1L001S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	122
Mean	56.99
Std Dev	6.032



B1L001S, U14-ch116, adc6

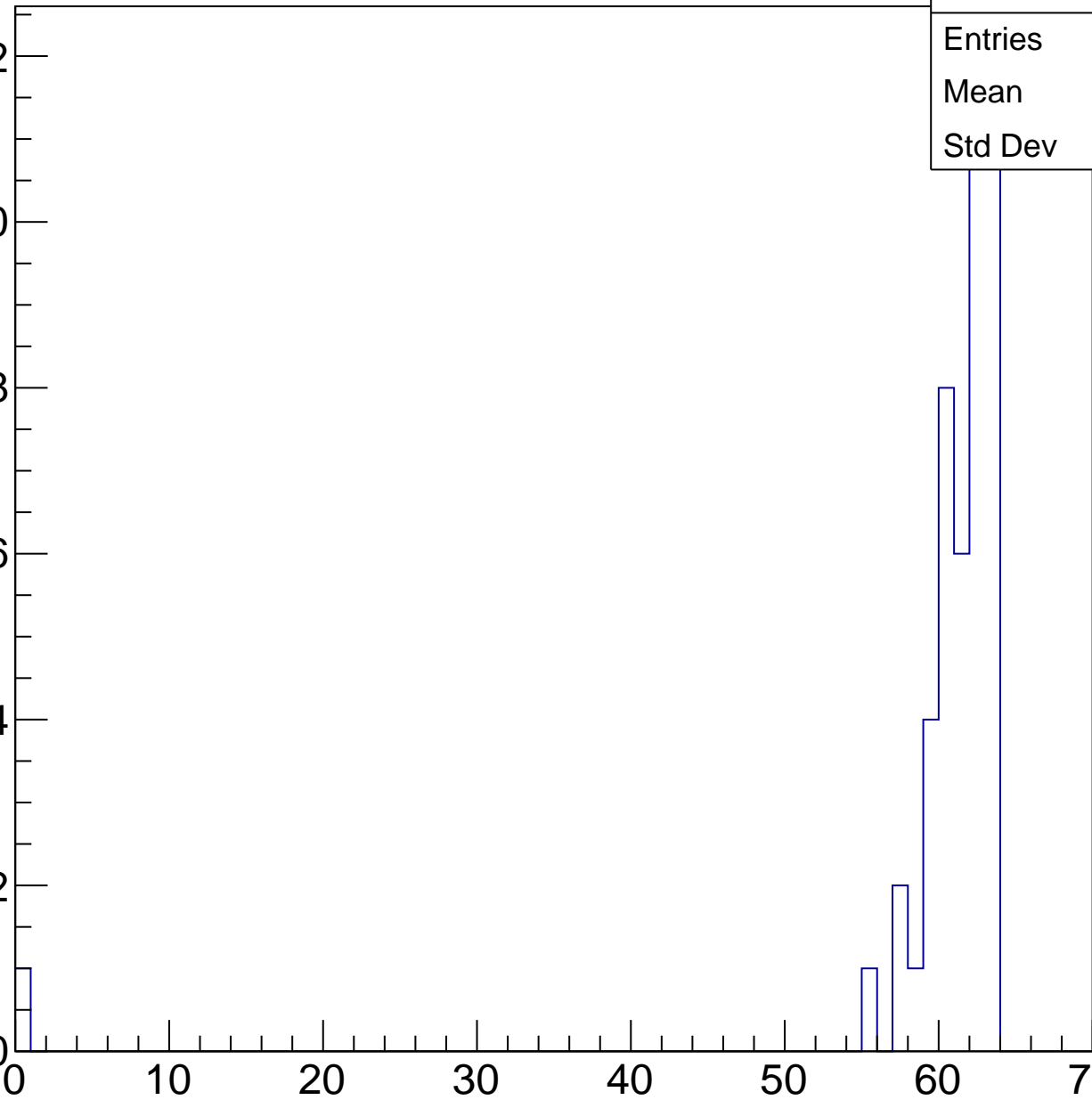
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	47
Mean	59.77
Std Dev	9.004

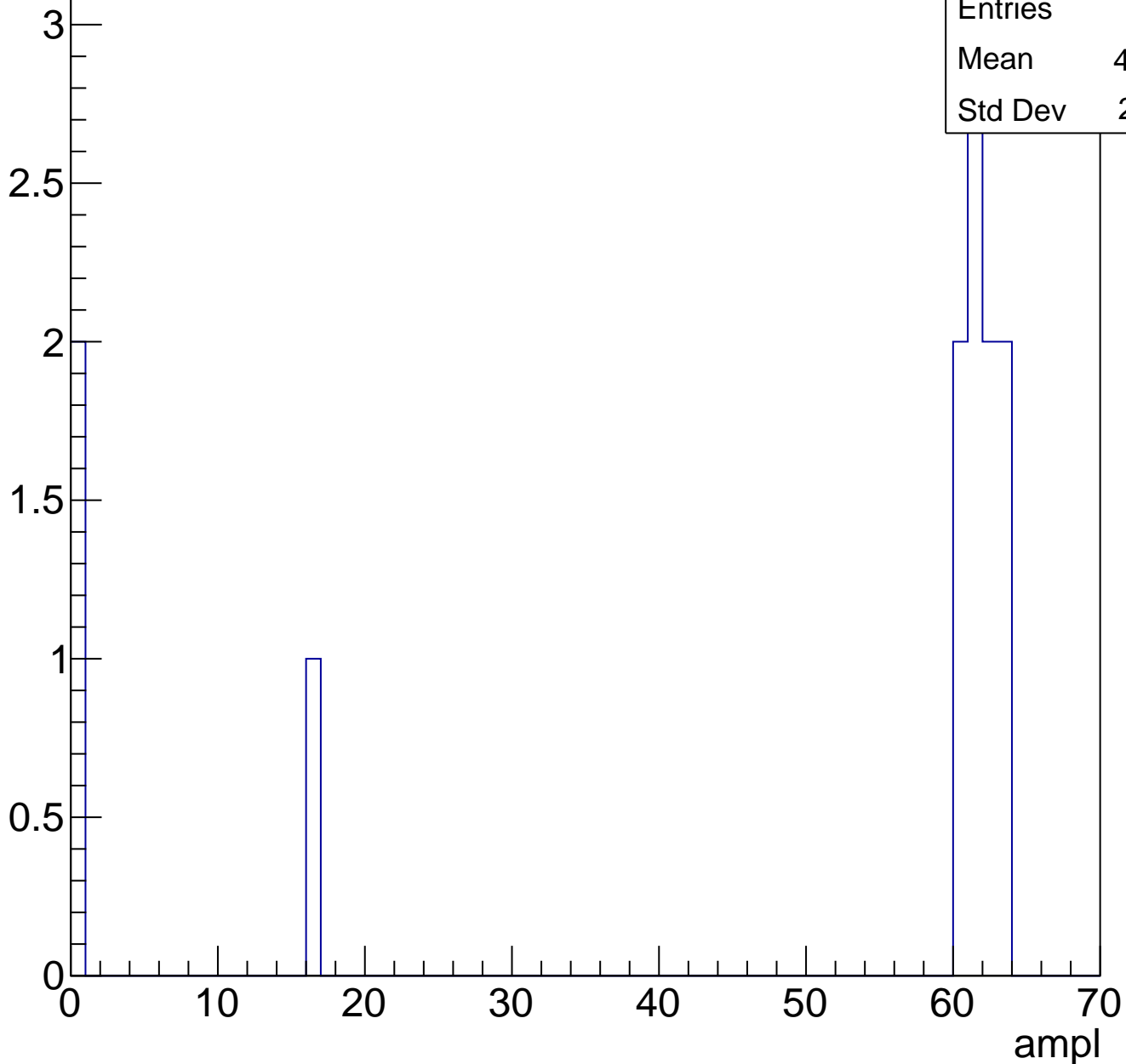
ampl



B1L001S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch117, adc0

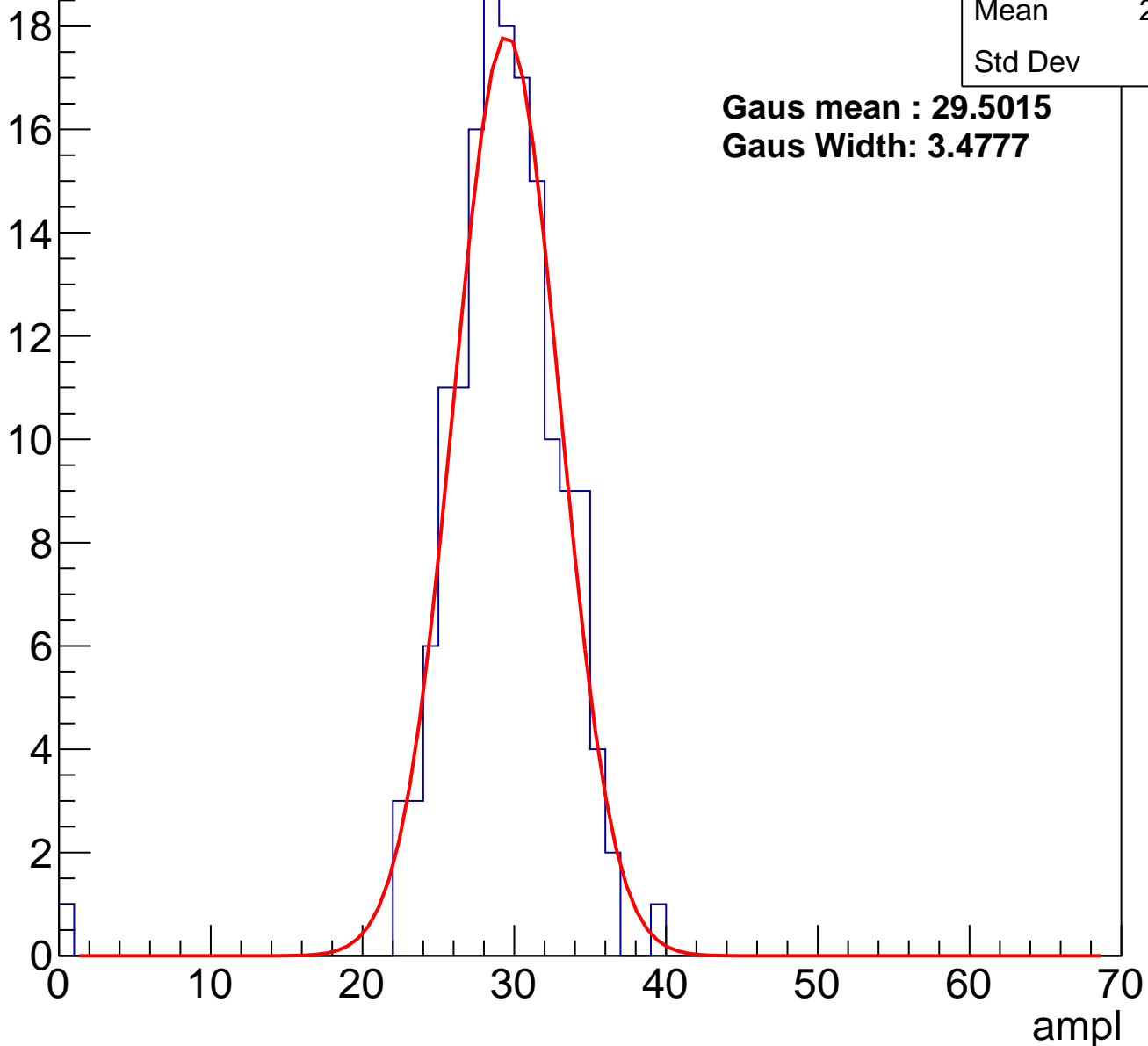
calib_packv5_042523_0143.root, FC#2, port C2

Entries	155
Mean	28.87
Std Dev	4

Gaus mean : 29.5015

Gaus Width: 3.4777

Entry



B1L001S, U14-ch117, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	133
Mean	36.05
Std Dev	3.53

Gaus mean : 36.5684

Gaus Width: 3.4355

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

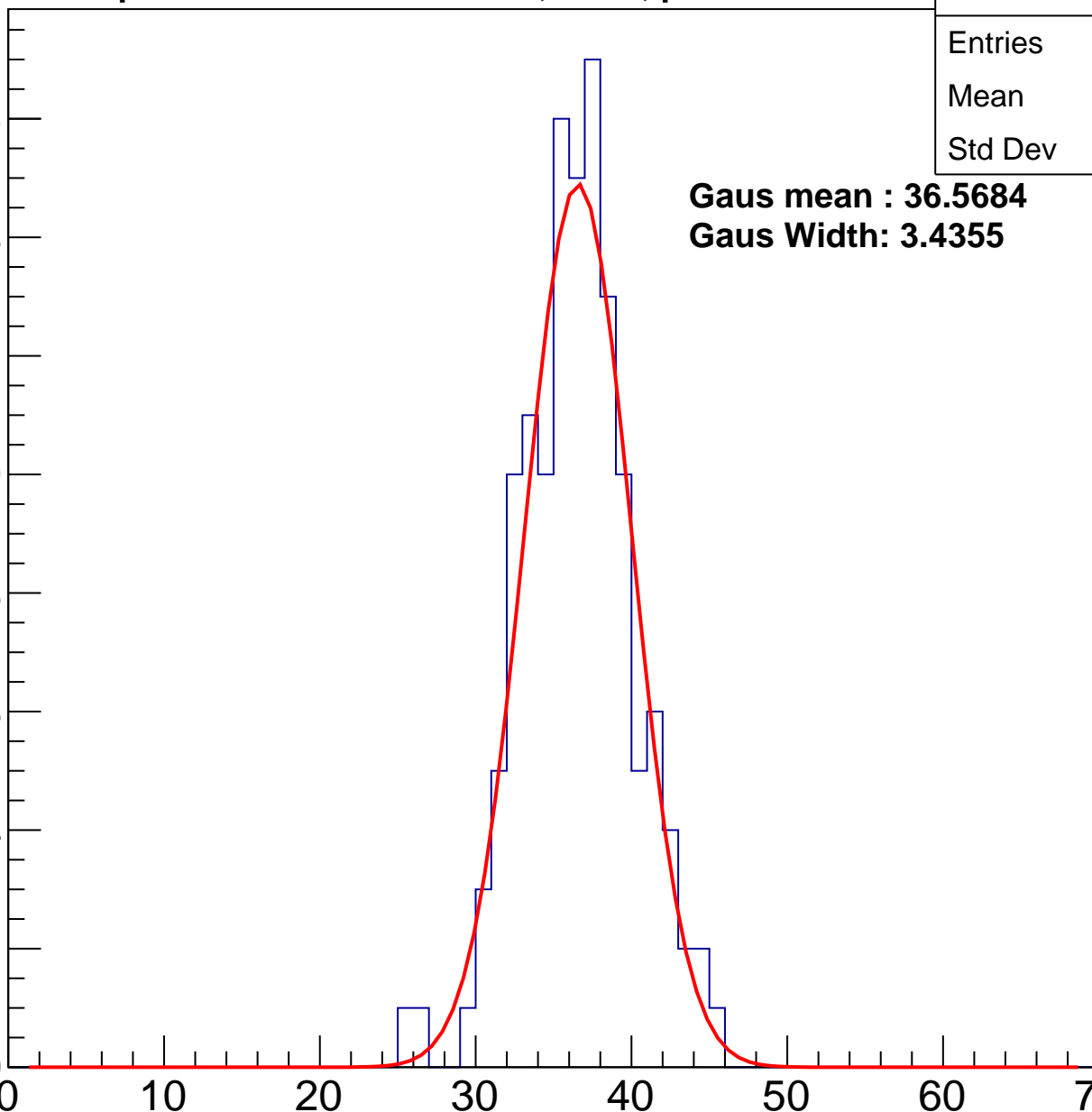
40

50

60

70

ampl



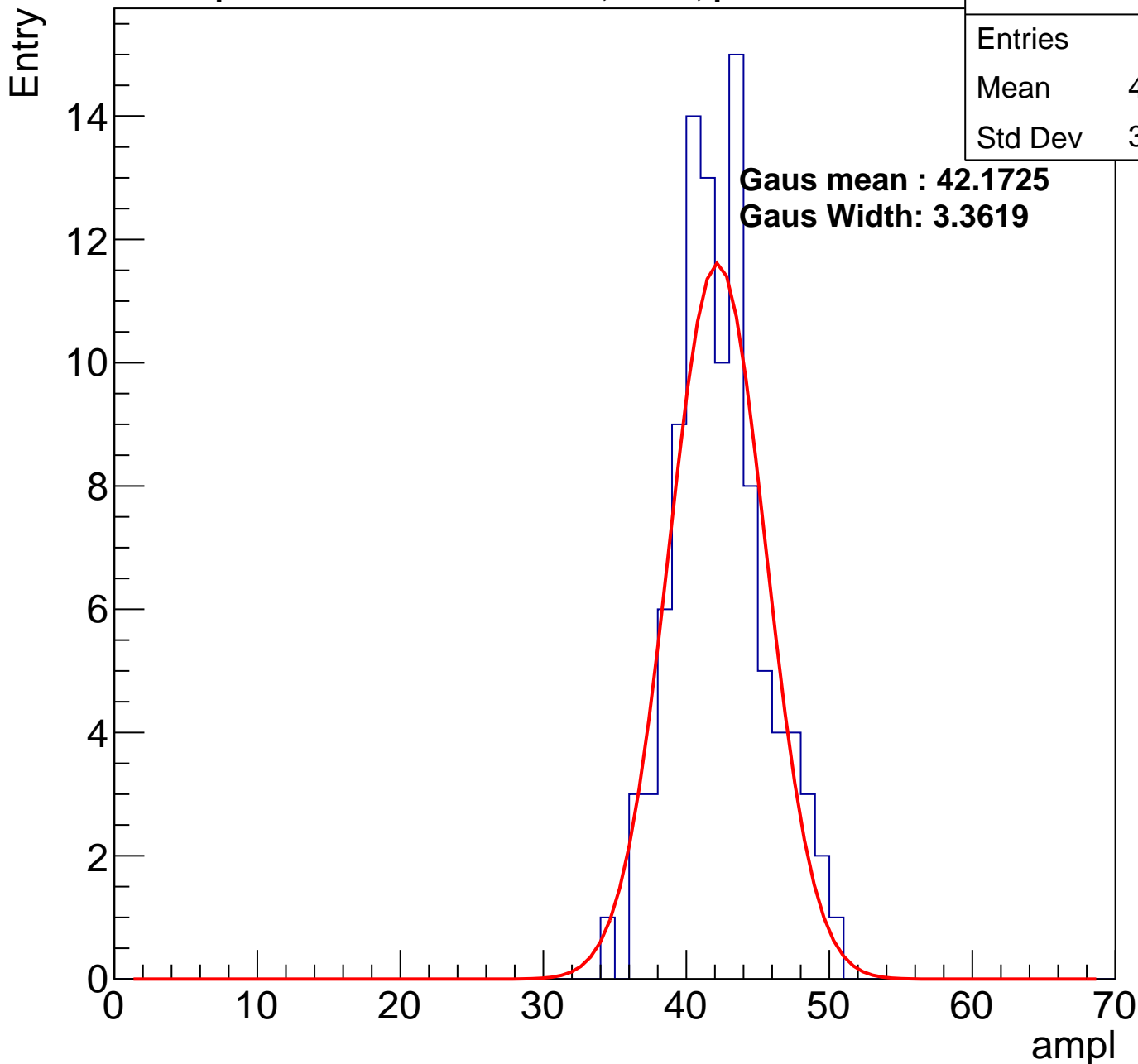
B1L001S, U14-ch117, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	101
Mean	41.89
Std Dev	3.174

Gaus mean : 42.1725

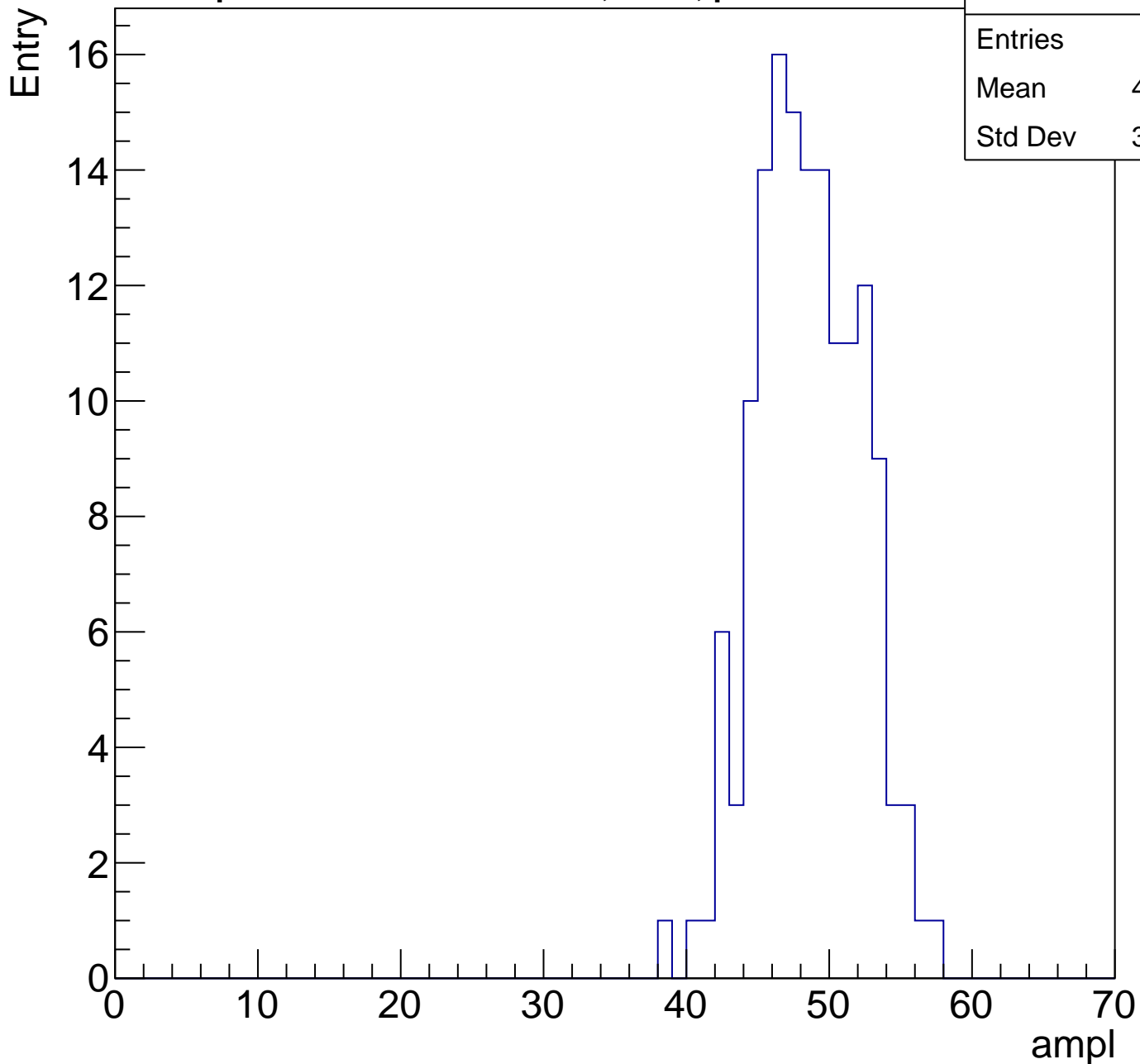
Gaus Width: 3.3619



B1L001S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#2, port C2

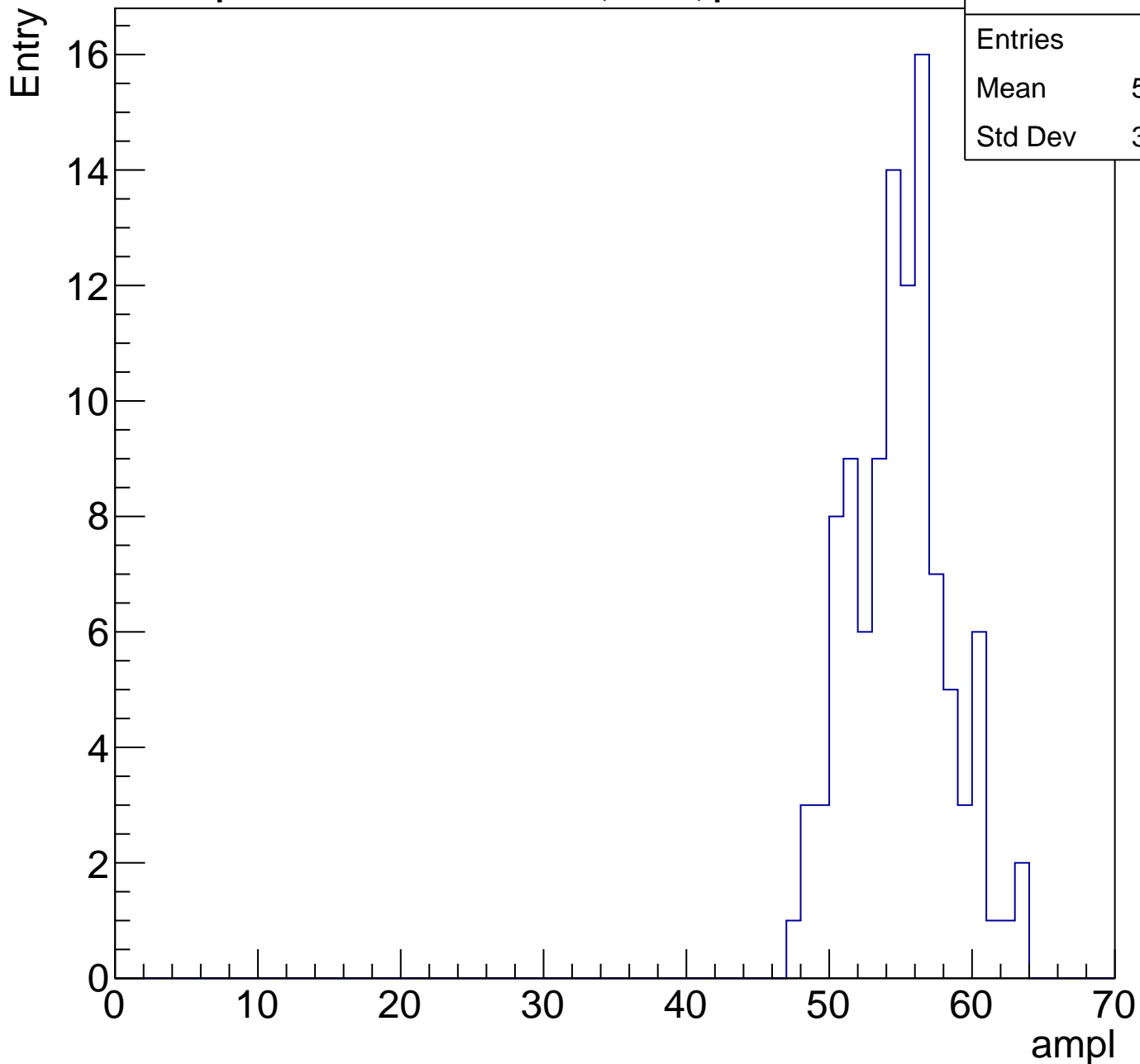
Entries	146
Mean	48.09
Std Dev	3.552



B1L001S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	106
Mean	54.46
Std Dev	3.415



B1L001S, U14-ch117, adc5

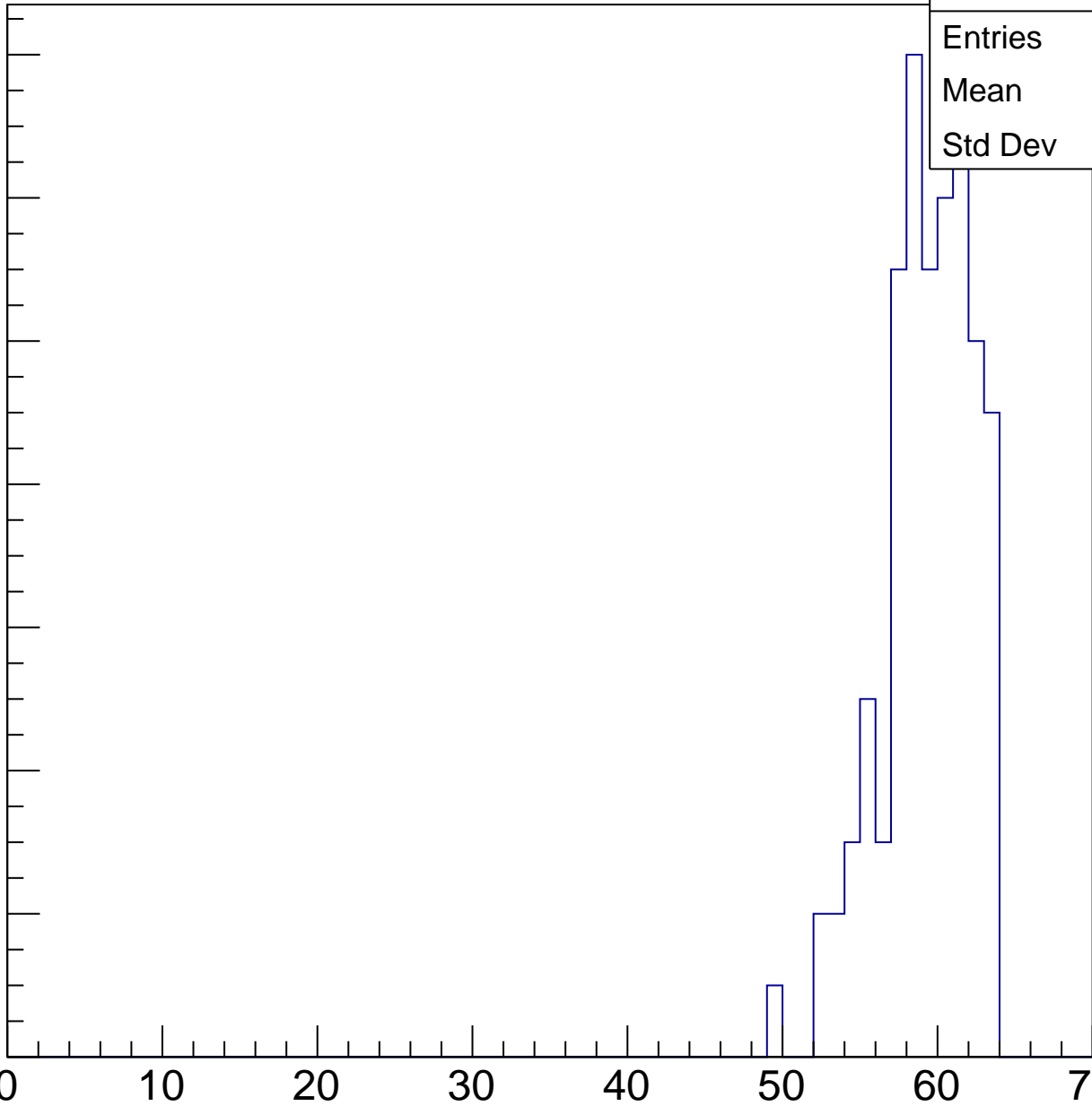
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	97
Mean	58.9
Std Dev	2.891

ampl

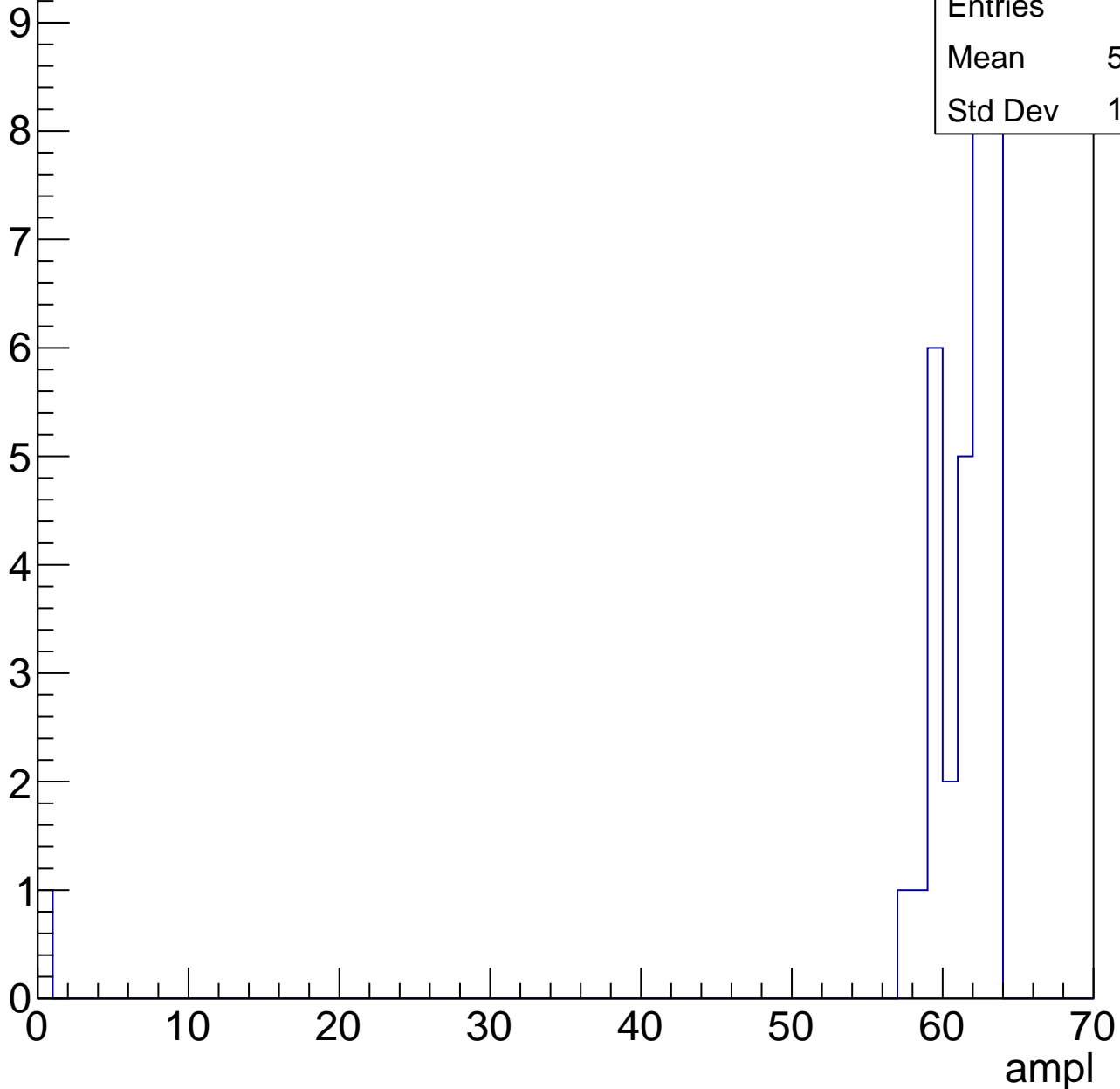


B1L001S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

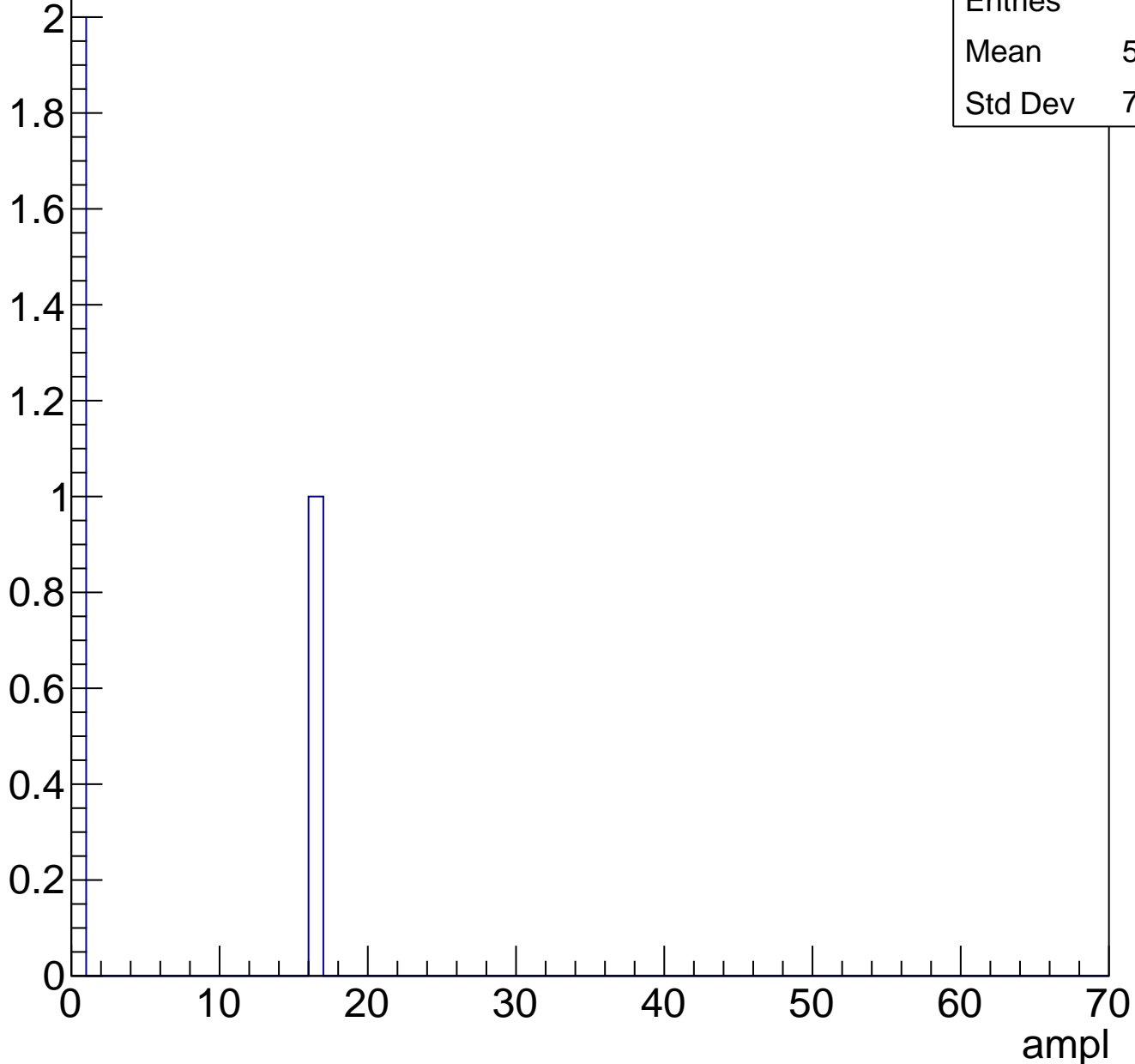
Entries	34
Mean	59.38
Std Dev	10.47



B1L001S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	3
Mean	5.333
Std Dev	7.542

B1L001S, U14-ch118, adc0

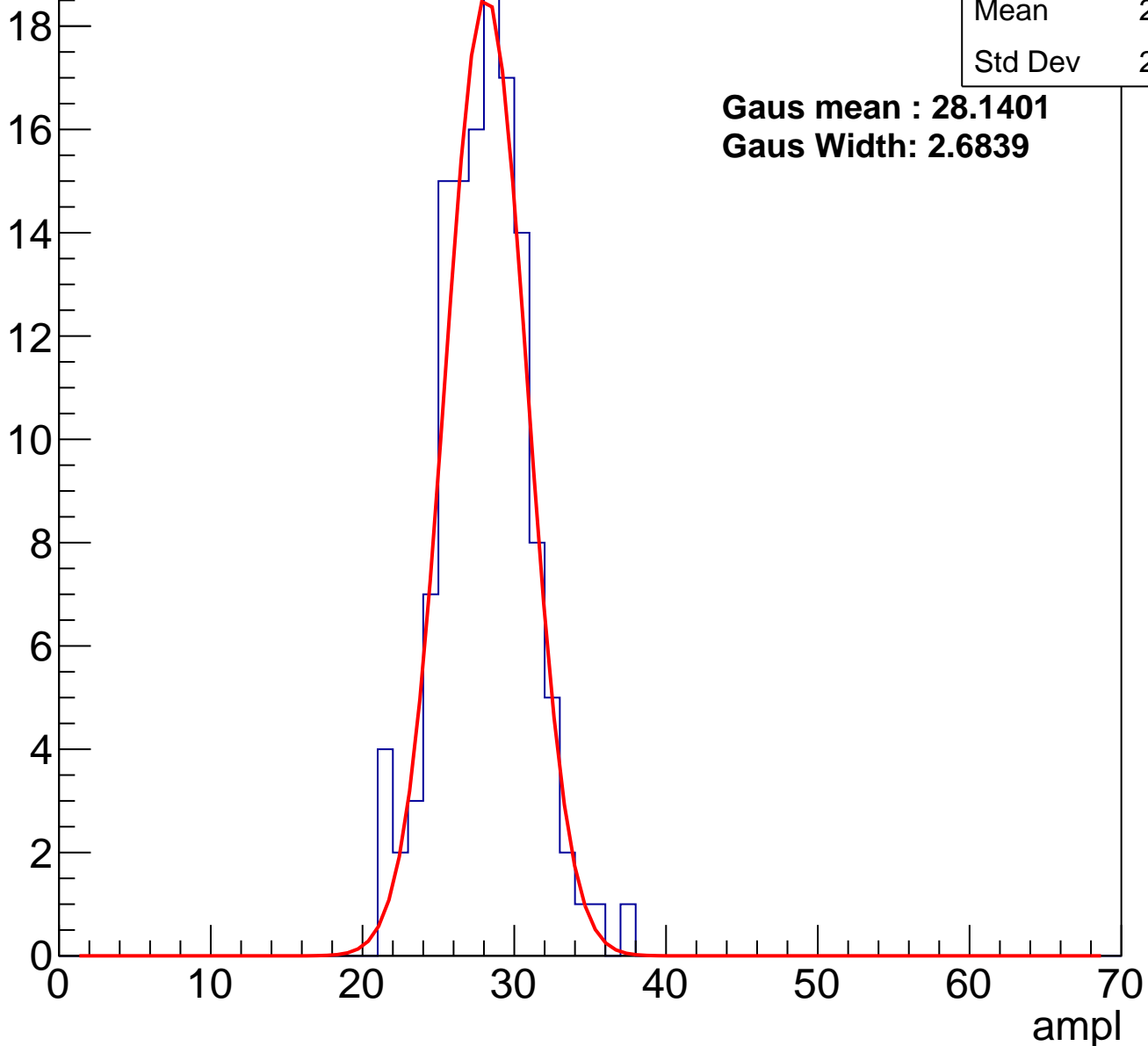
calib_packv5_042523_0143.root, FC#2, port C2

Entries	130
Mean	27.59
Std Dev	2.889

Gaus mean : 28.1401

Gaus Width: 2.6839

Entry



B1L001S, U14-ch118, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	127
Mean	33.95
Std Dev	3.019

Gaus mean : 34.3643

Gaus Width: 2.5332

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

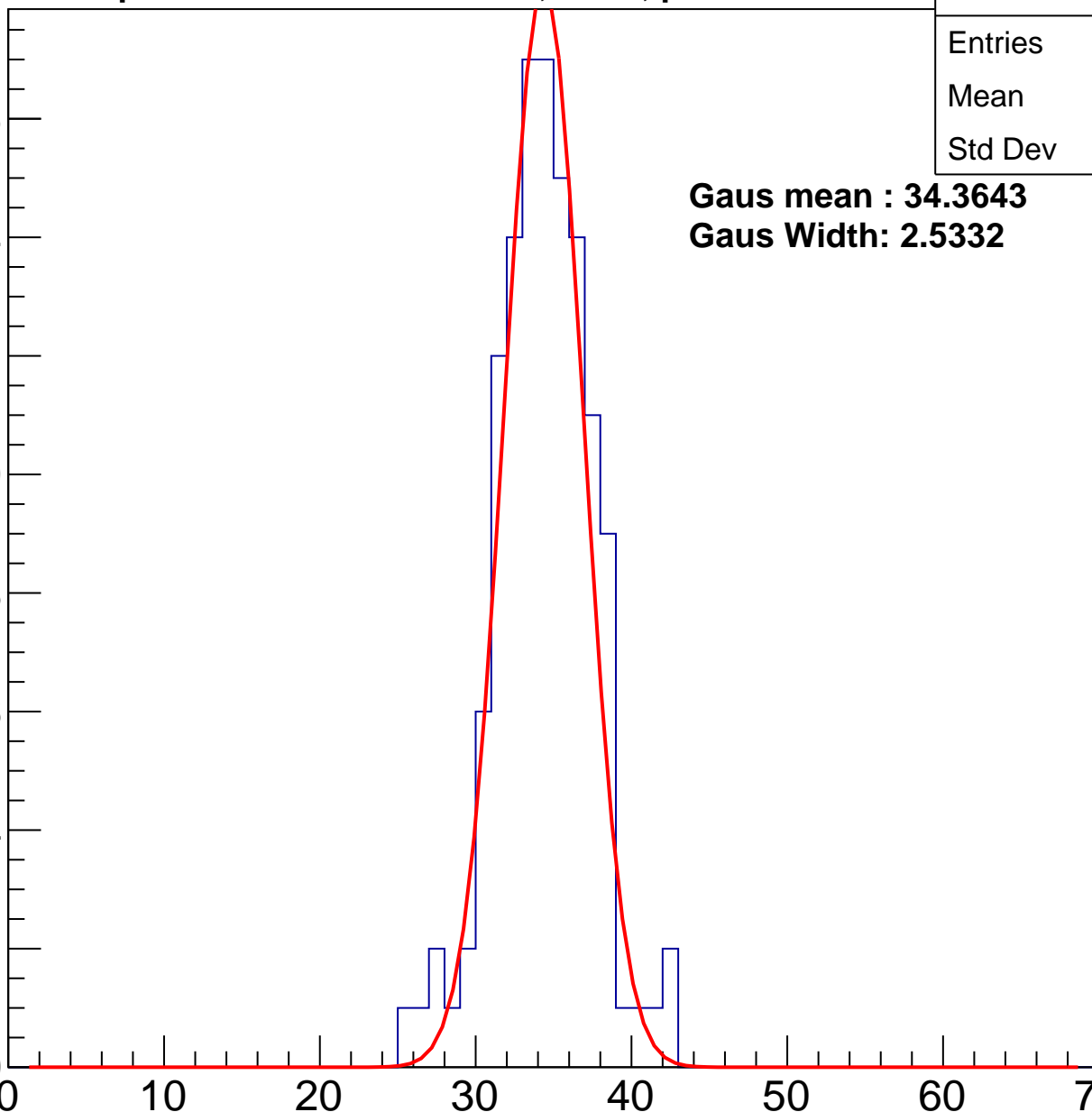
40

50

60

70

ampl



B1L001S, U14-ch118, adc2

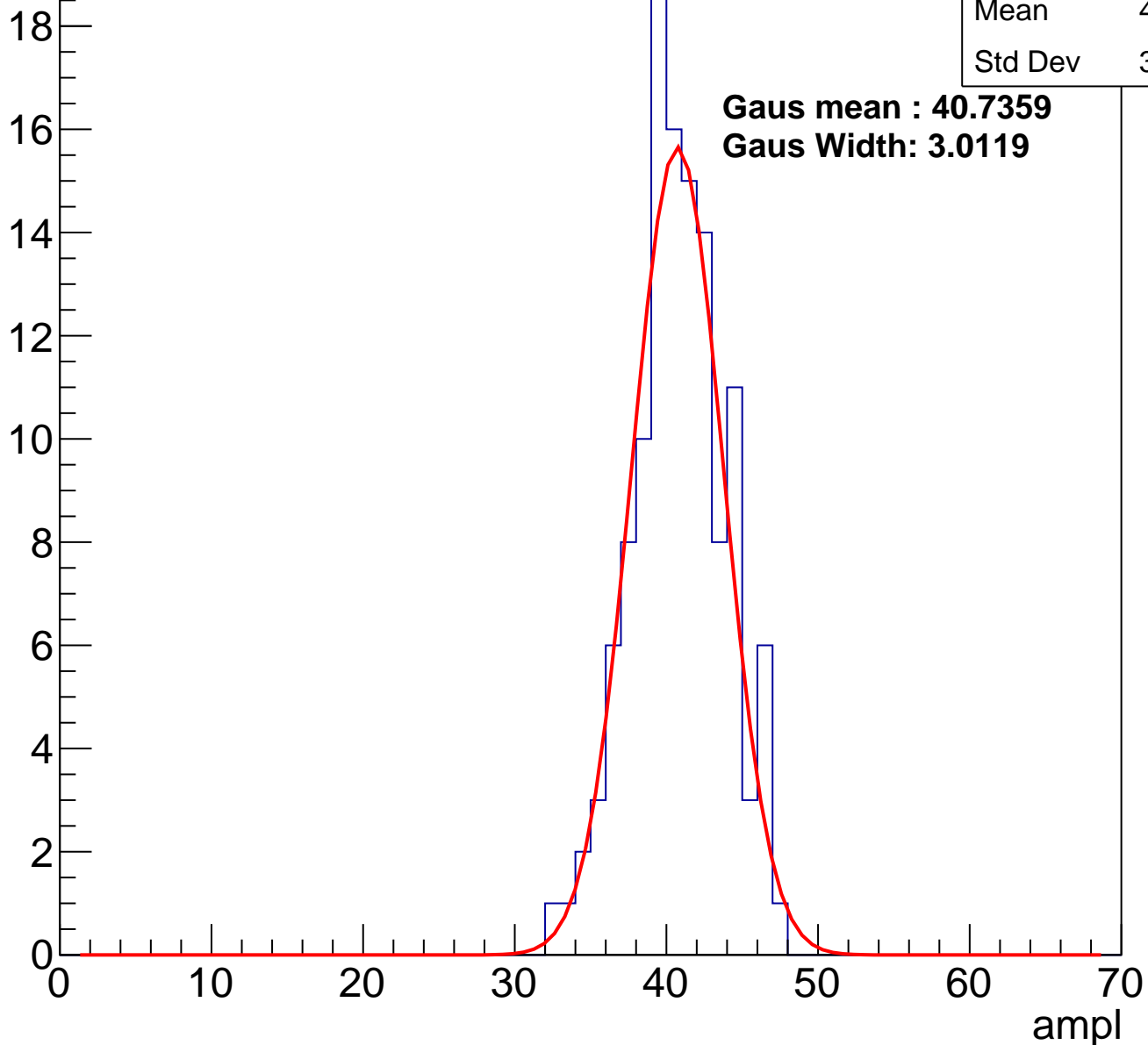
calib_packv5_042523_0143.root, FC#2, port C2

Entries	124
Mean	40.32
Std Dev	3.023

Gaus mean : 40.7359

Gaus Width: 3.0119

Entry

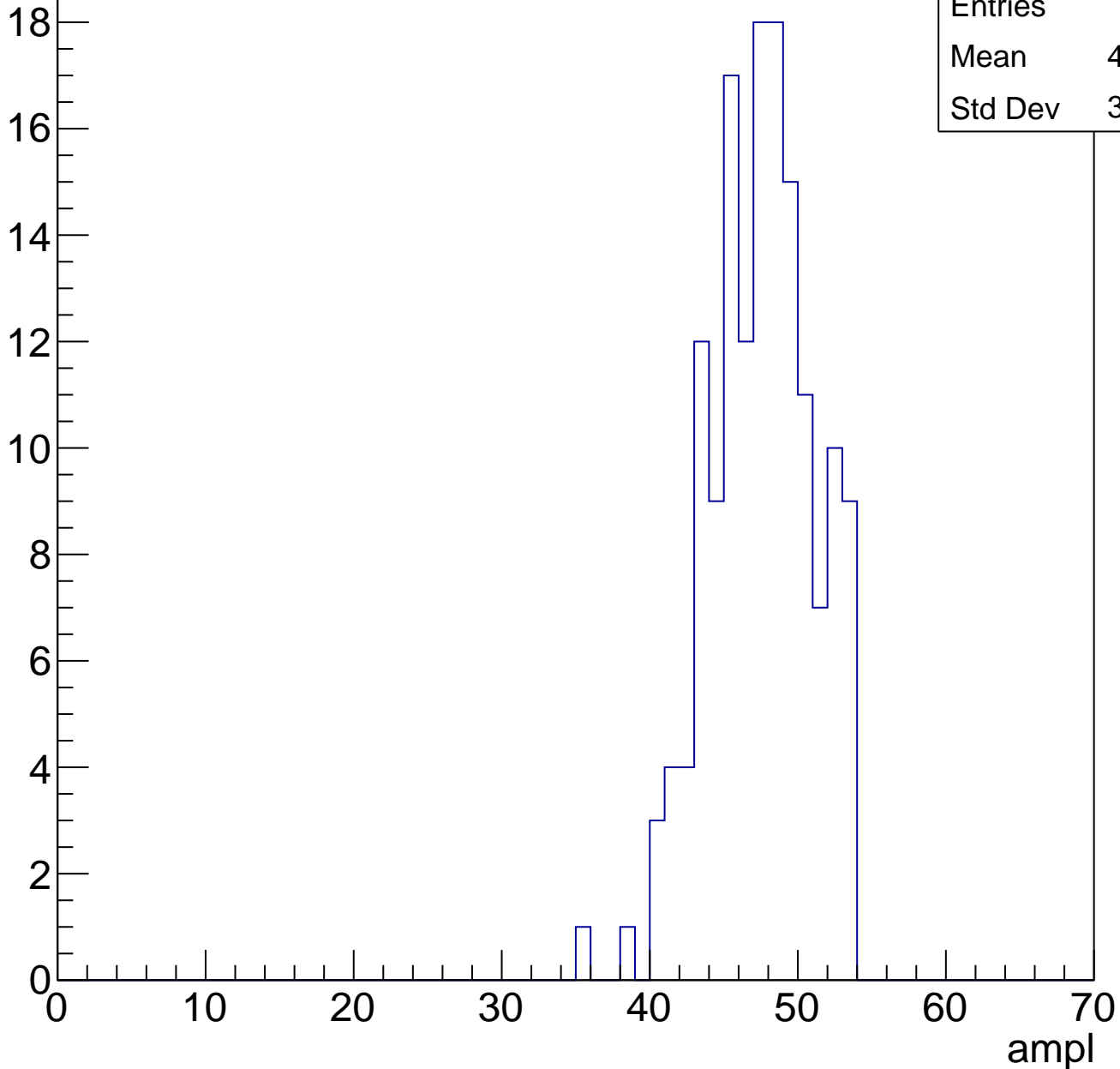


B1L001S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	151
Mean	47.04
Std Dev	3.483



B1L001S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#2, port C2

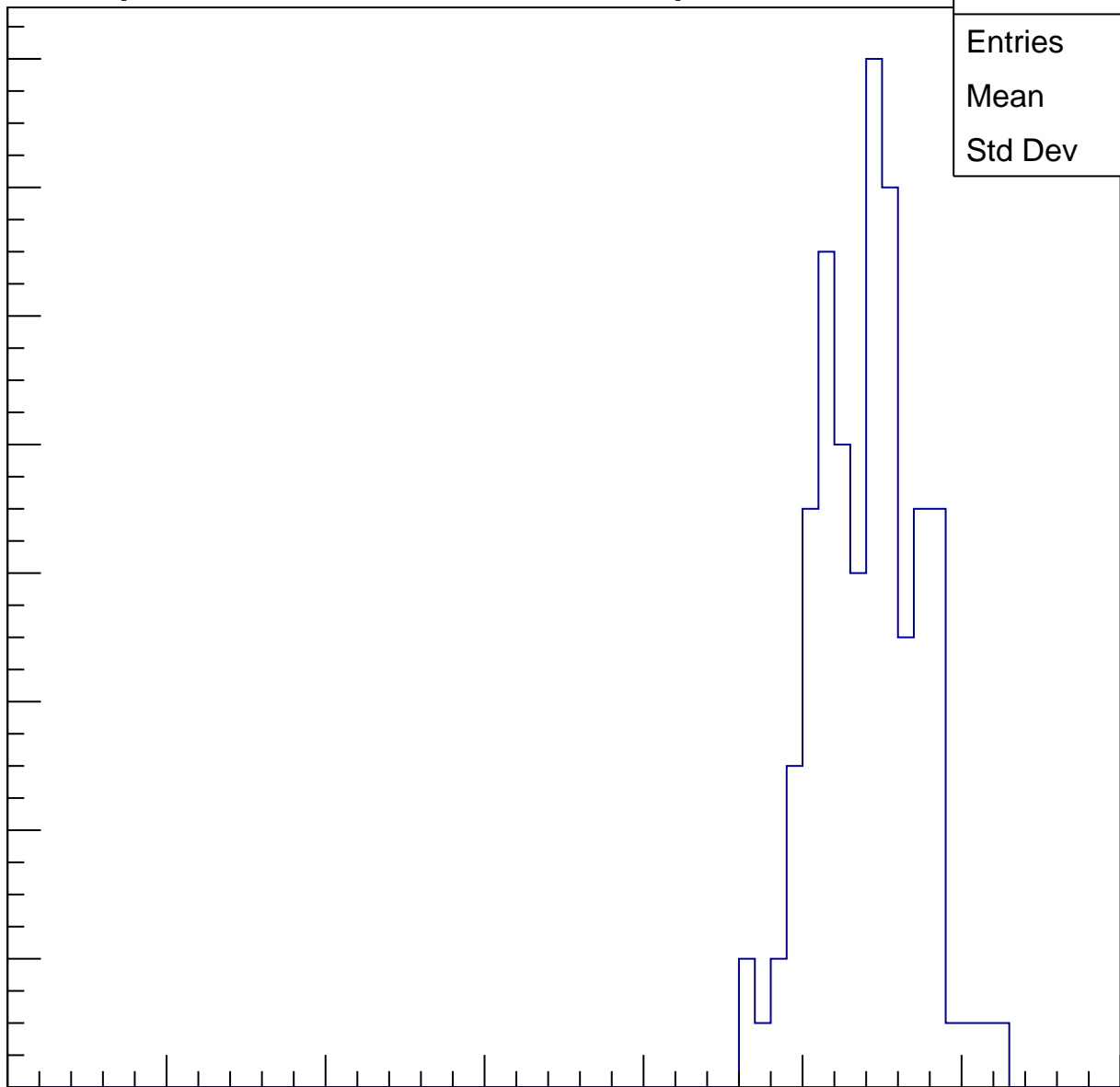
Entries	109
Mean	53.58
Std Dev	3.183

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

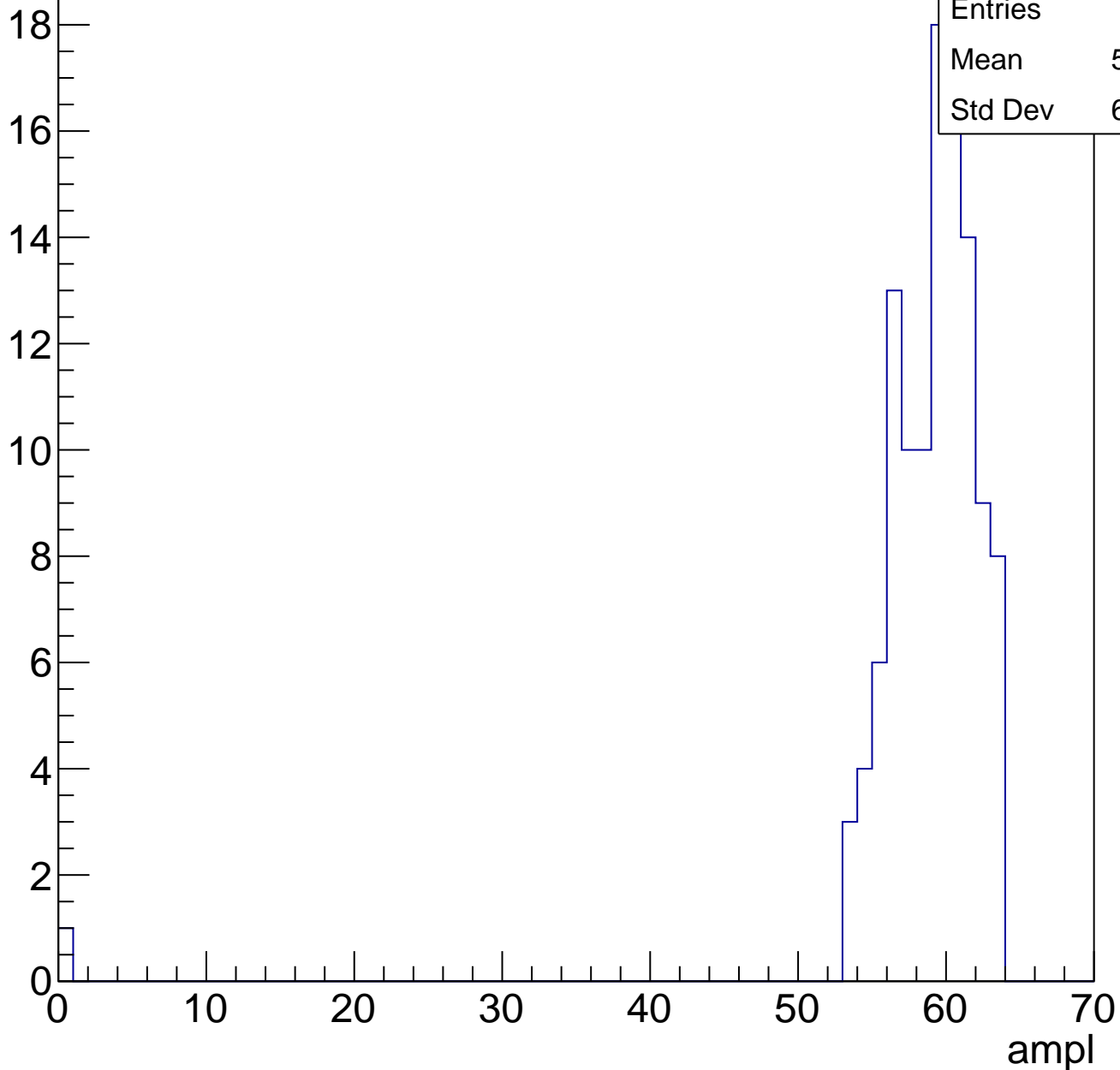


B1L001S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#2, port C2

Entries	113
Mean	58.24
Std Dev	6.074

Entry



B1L001S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	31
Mean	59.68
Std Dev	11

B1L001S, U14-ch118, adc7

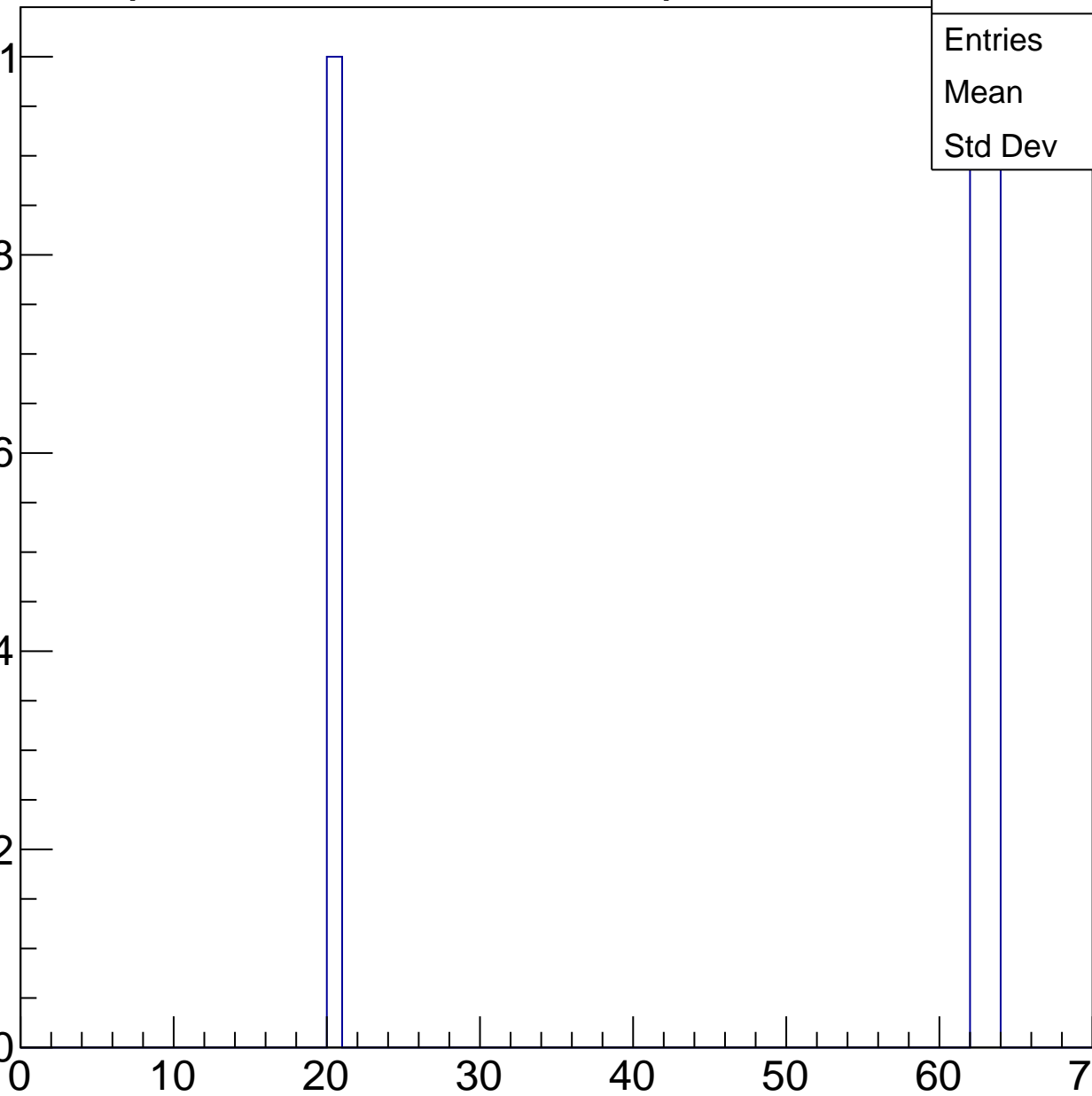
calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	48.33
Std Dev	20.04

ampl



B1L001S, U14-ch119, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	29.02
Std Dev	4.219

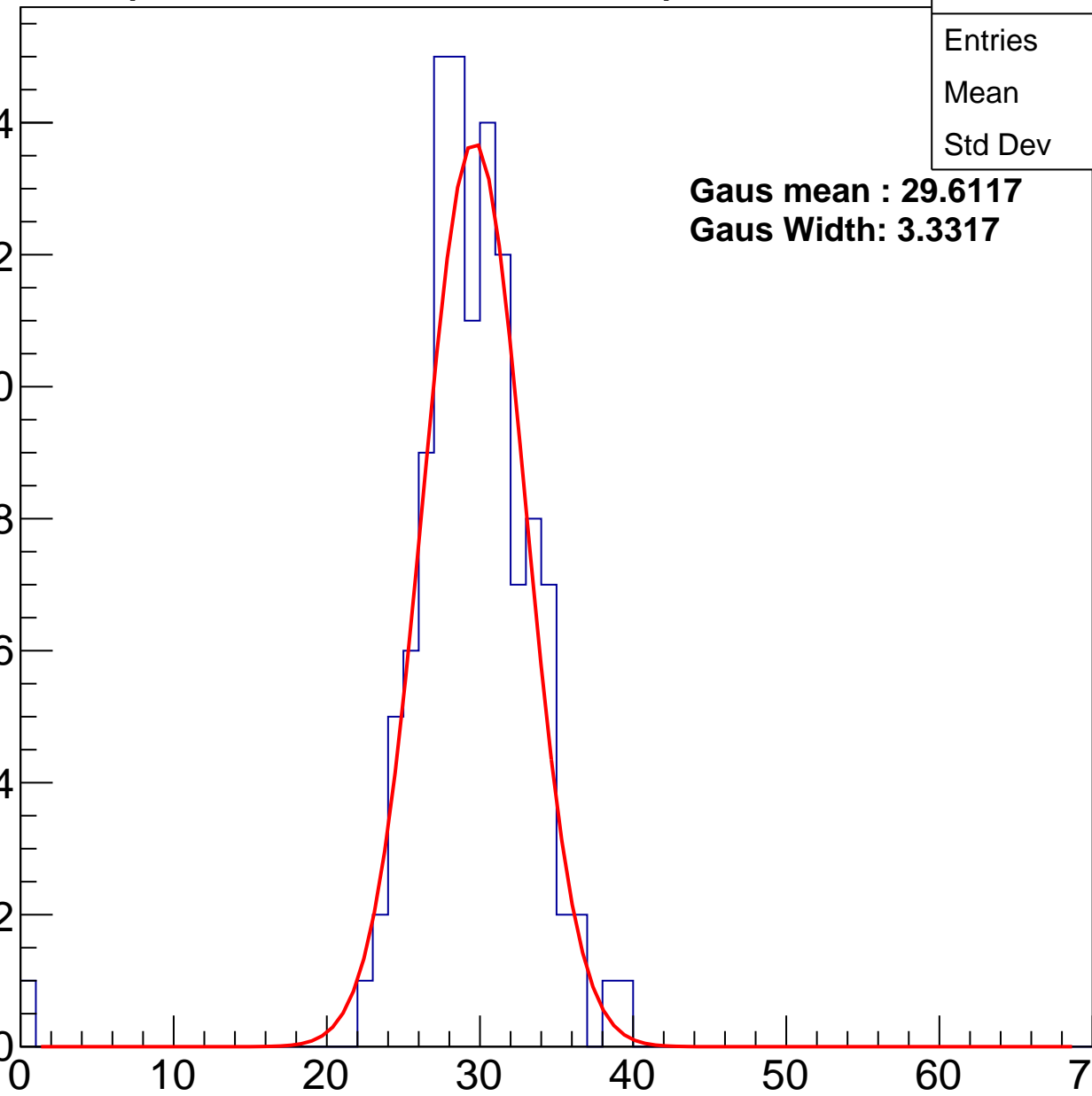
Gaus mean : 29.6117

Gaus Width: 3.3317

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch119, adc1

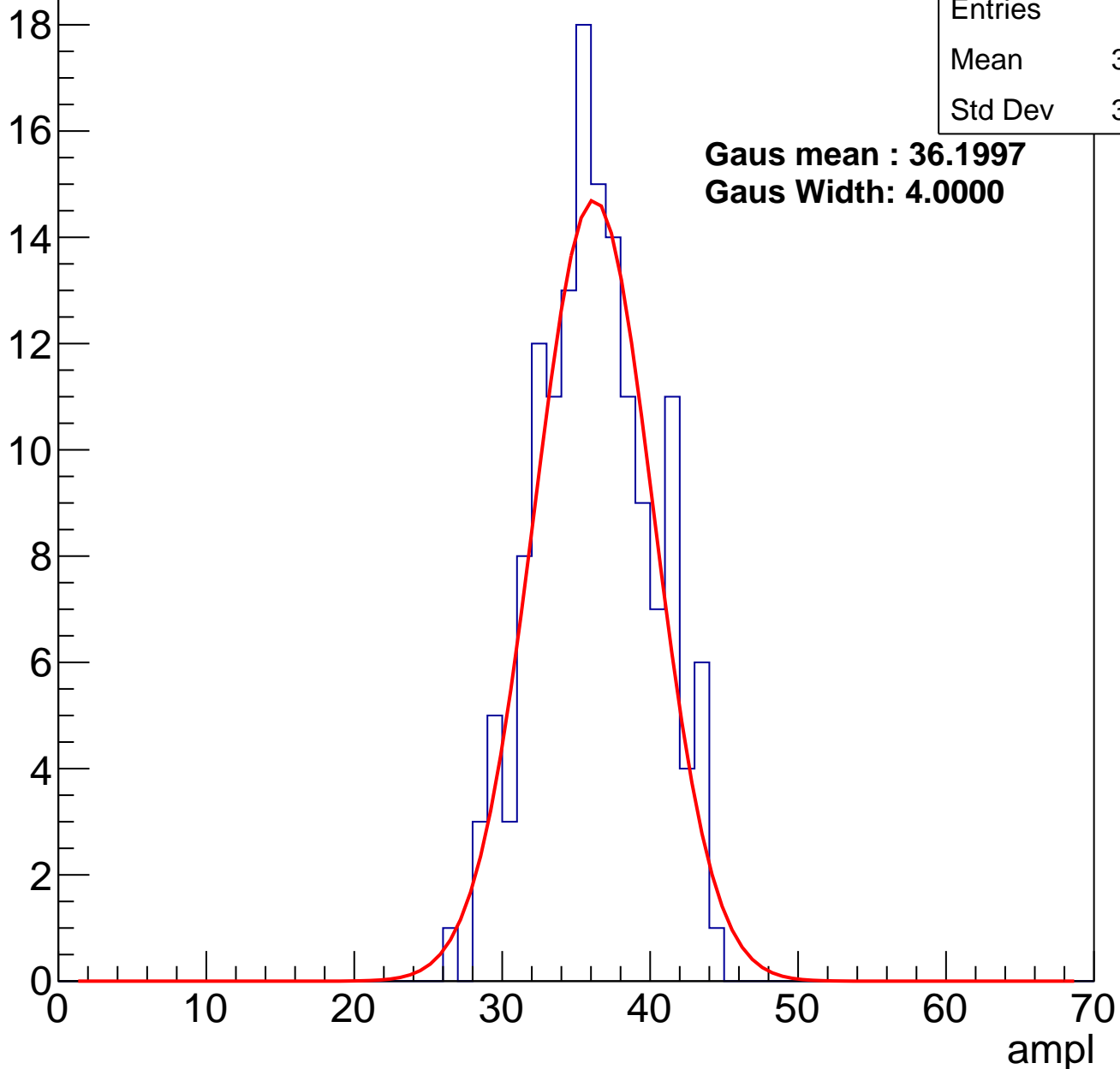
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	35.79
Std Dev	3.828

Gaus mean : 36.1997

Gaus Width: 4.0000

Entry



B1L001S, U14-ch119, adc2

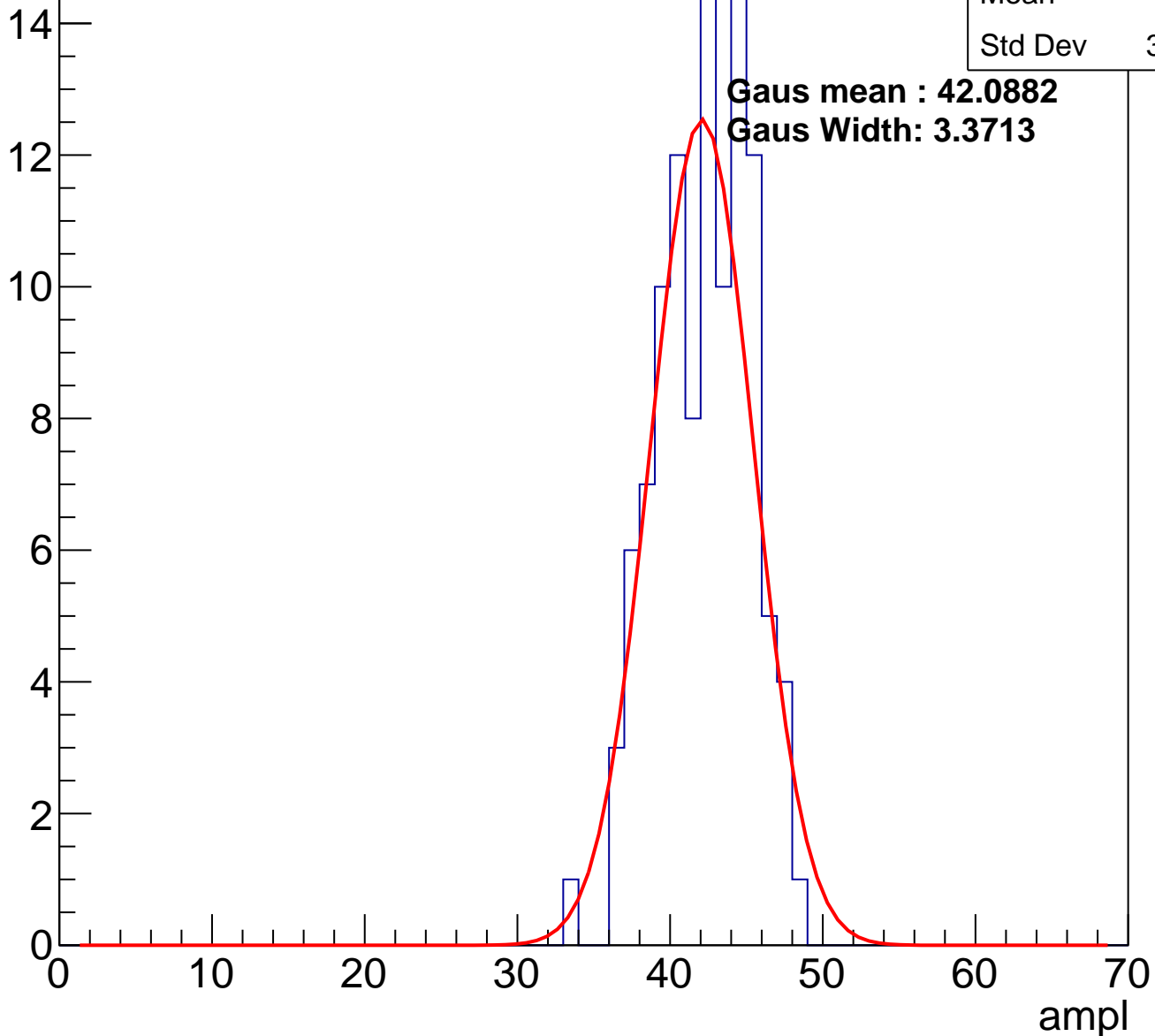
calib_packv5_042523_0143.root, FC#2, port C2

Entries	109
Mean	41.77
Std Dev	3.007

Gaus mean : 42.0882

Gaus Width: 3.3713

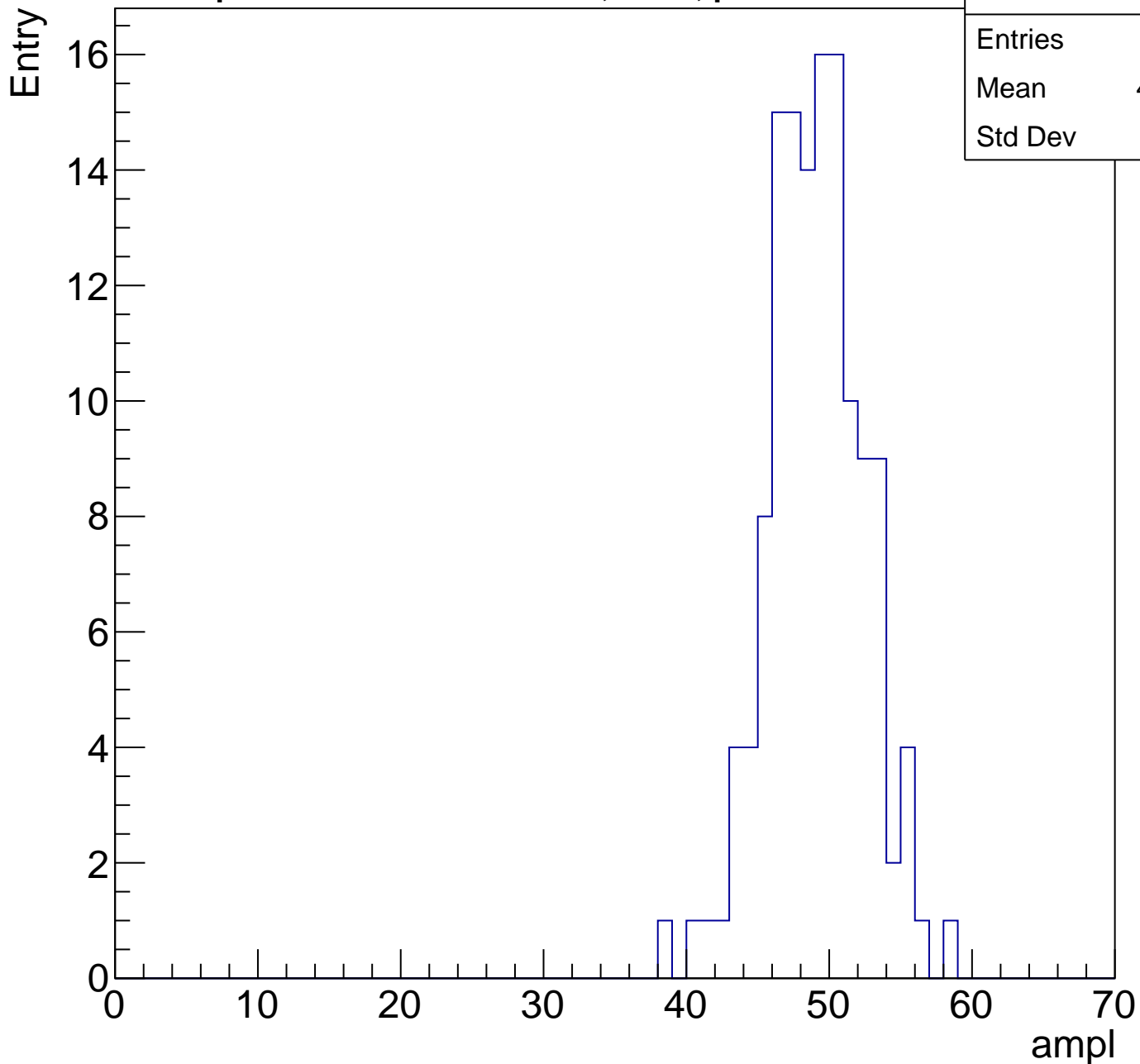
Entry



B1L001S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#2, port C2

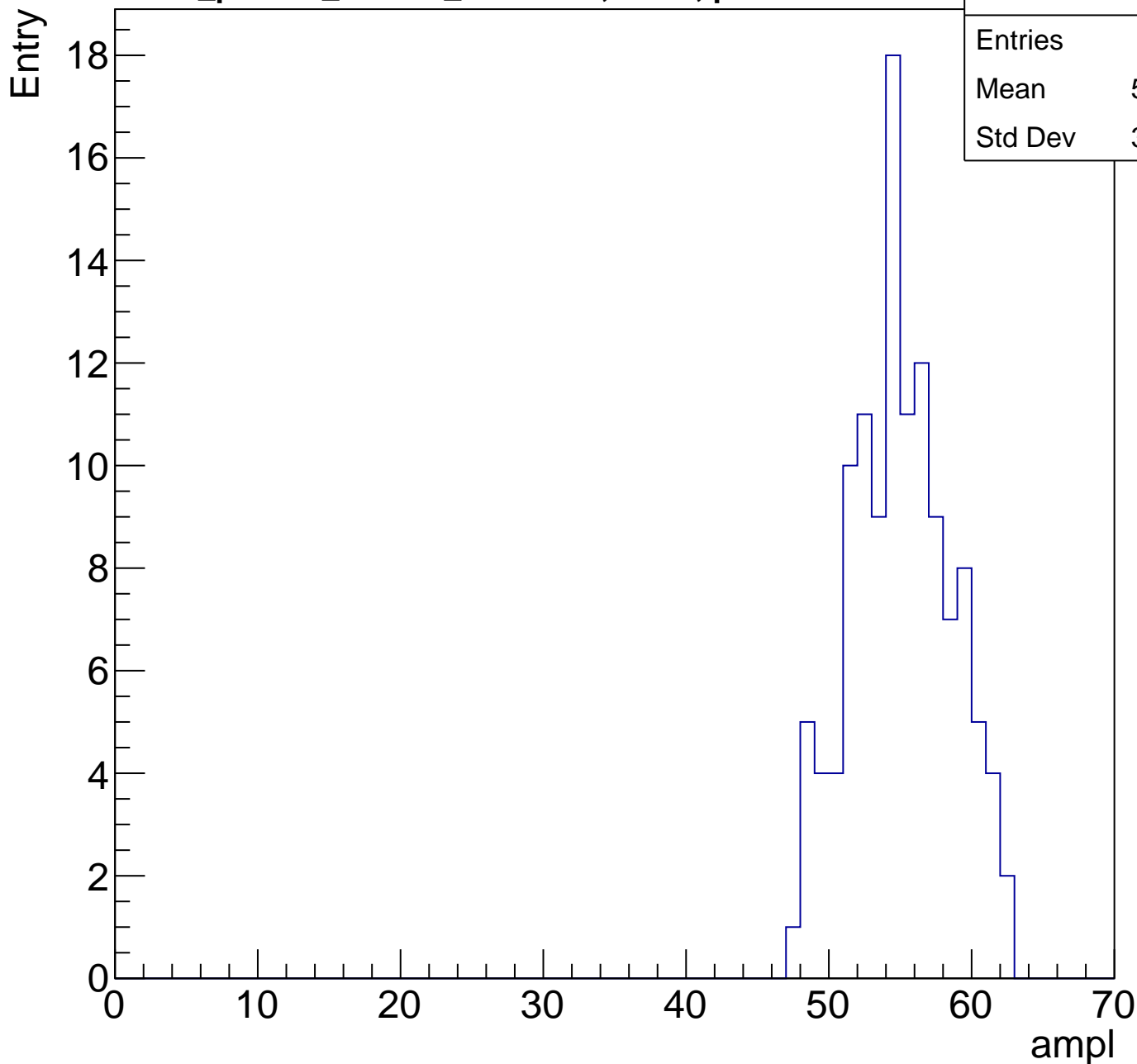
Entries	132
Mean	48.61
Std Dev	3.35



B1L001S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	120
Mean	54.58
Std Dev	3.478



B1L001S, U14-ch119, adc5

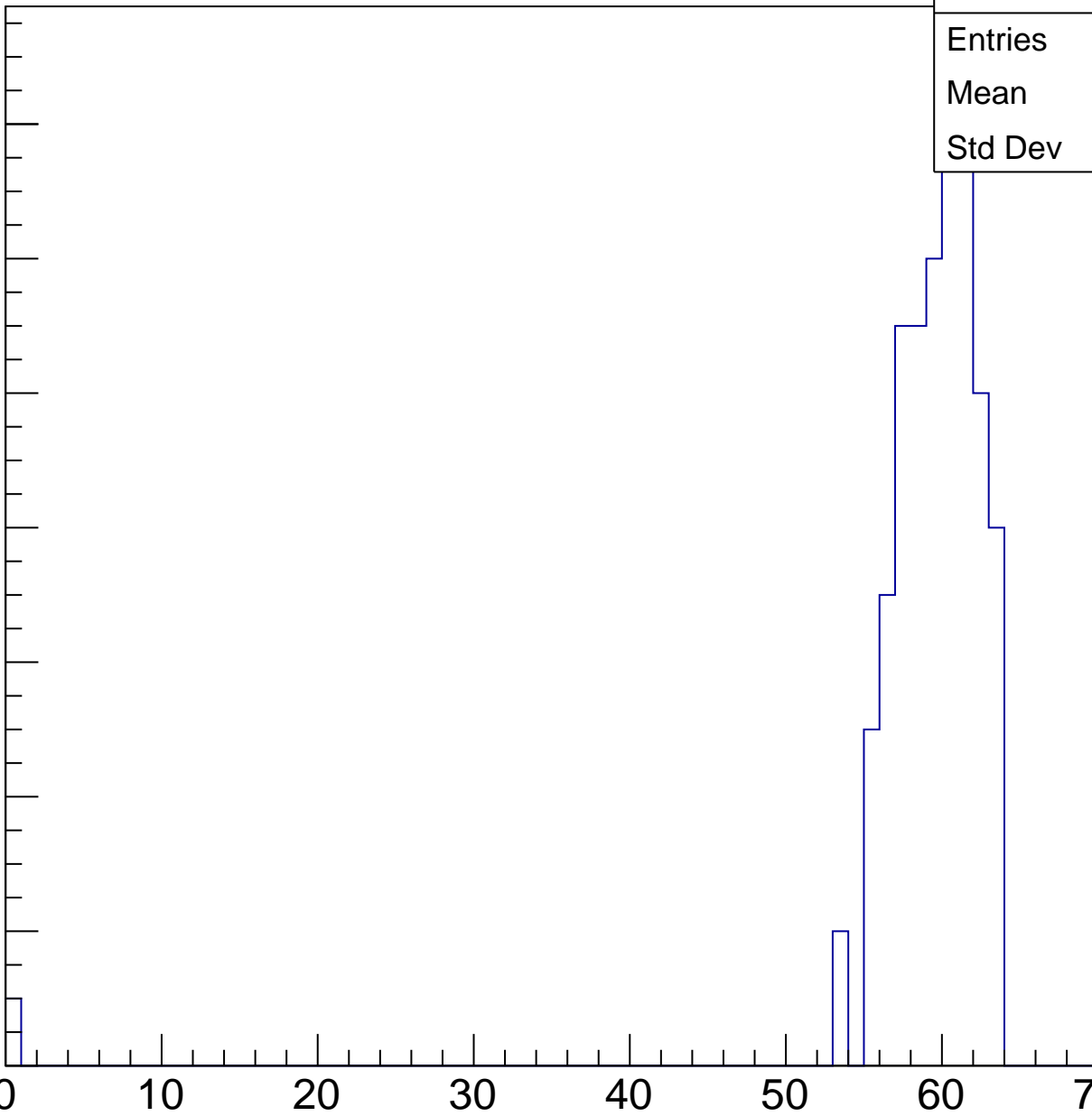
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	96
Mean	58.59
Std Dev	6.477

ampl



B1L001S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

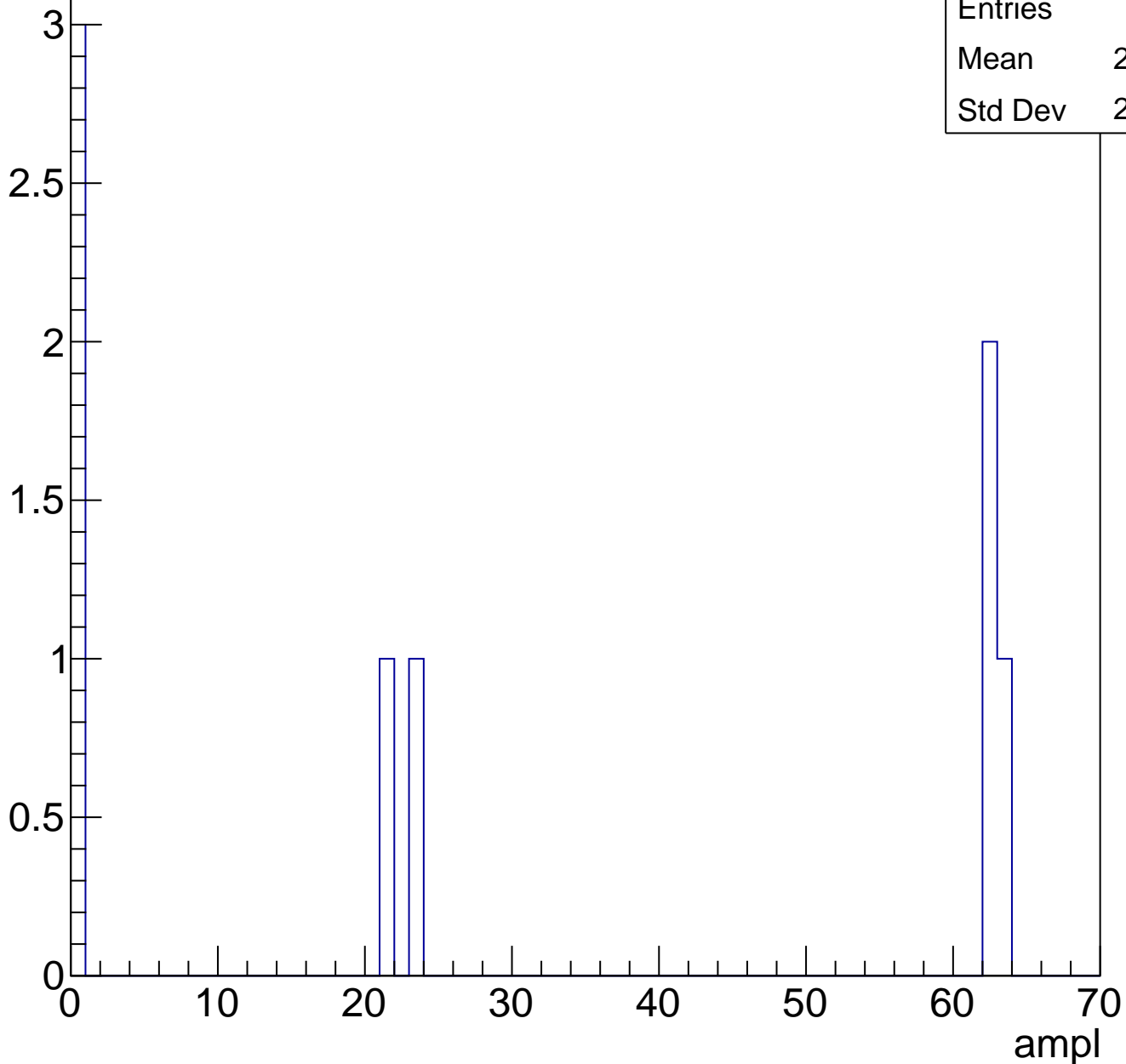
ampl

Entries	21
Mean	59.24
Std Dev	13.3

B1L001S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch120, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	29.16
Std Dev	4.04

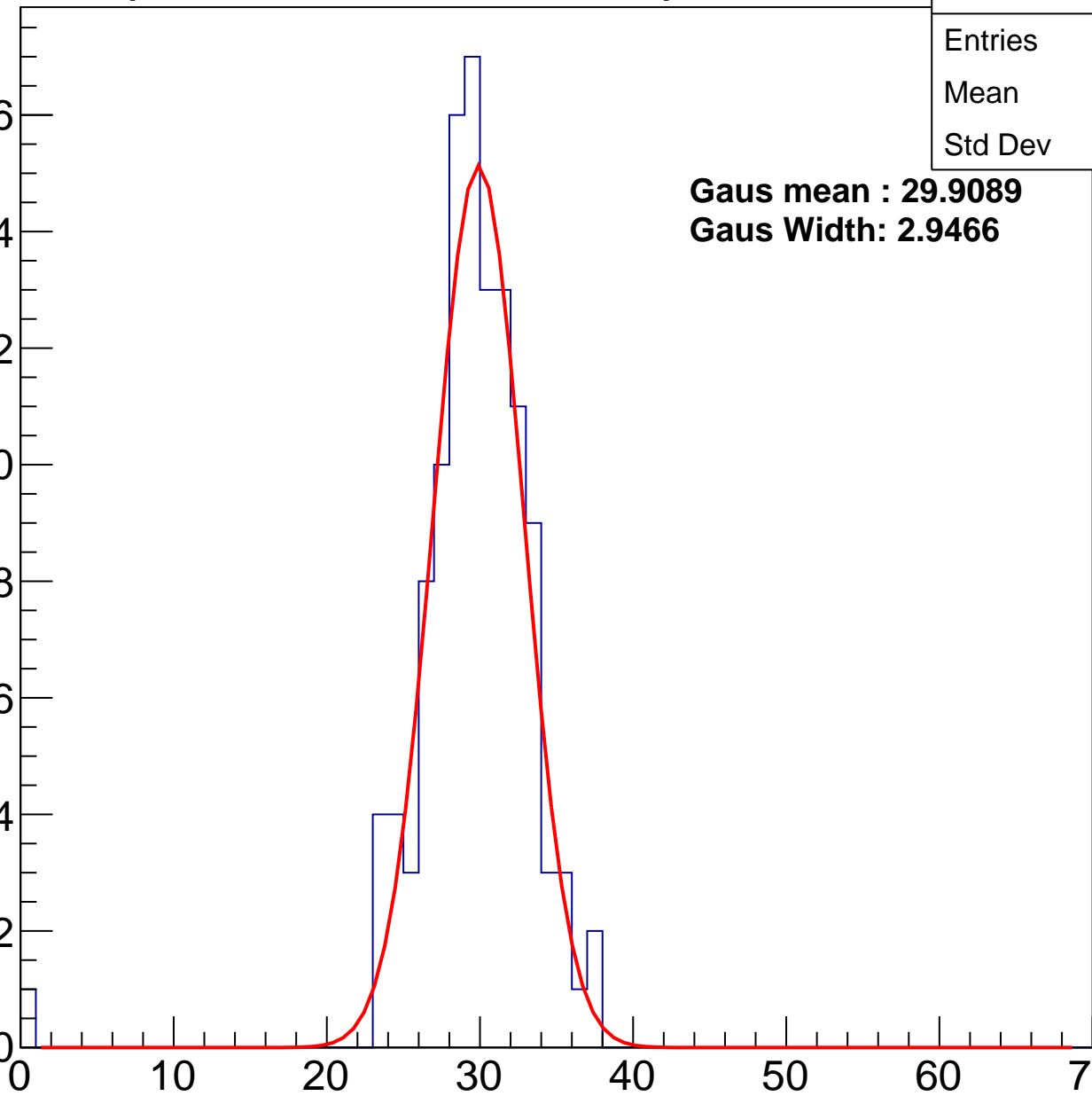
Gaus mean : 29.9089

Gaus Width: 2.9466

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch120, adc1

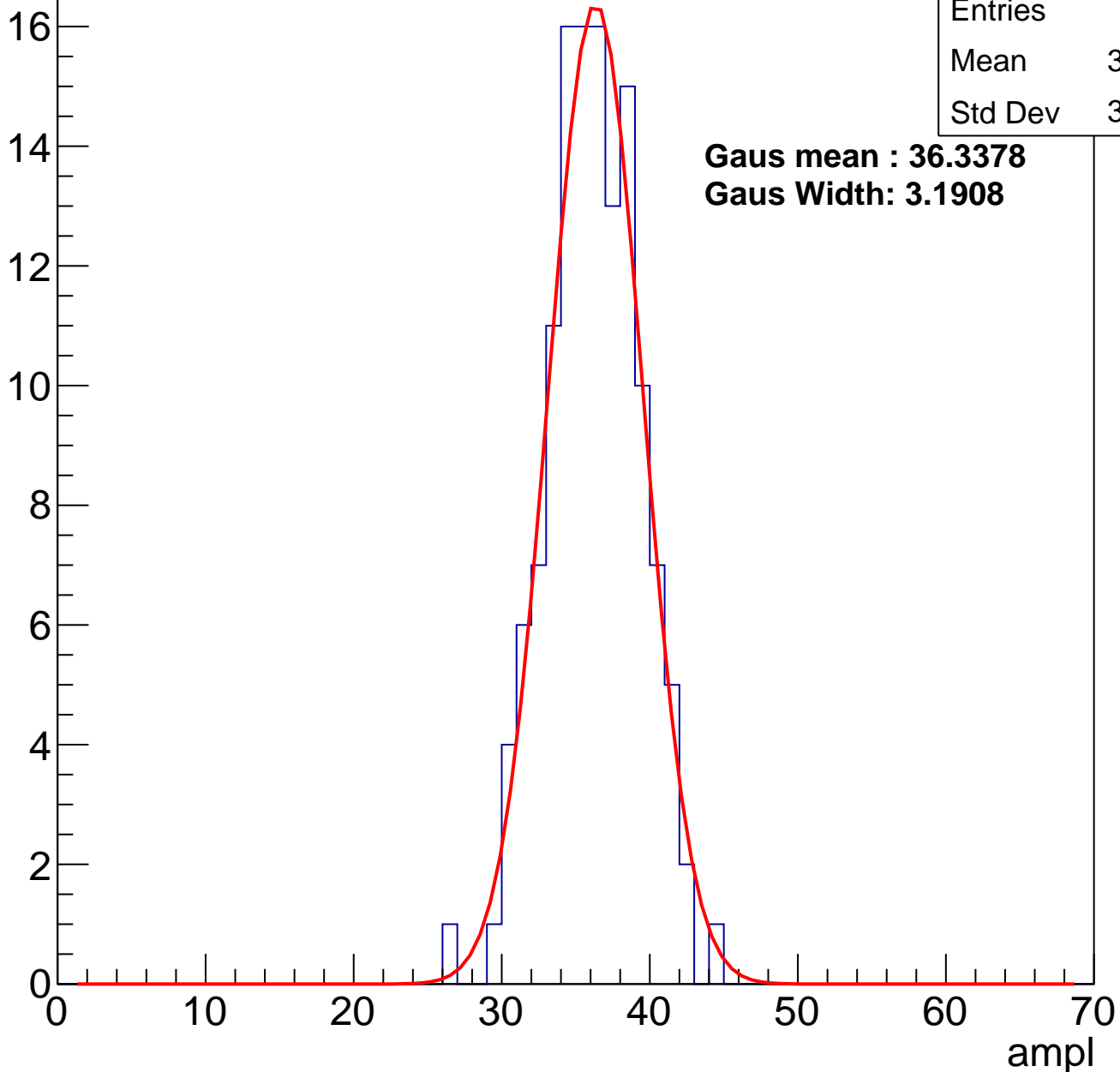
calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	35.74
Std Dev	3.103

Gaus mean : 36.3378

Gaus Width: 3.1908

Entry



B1L001S, U14-ch120, adc2

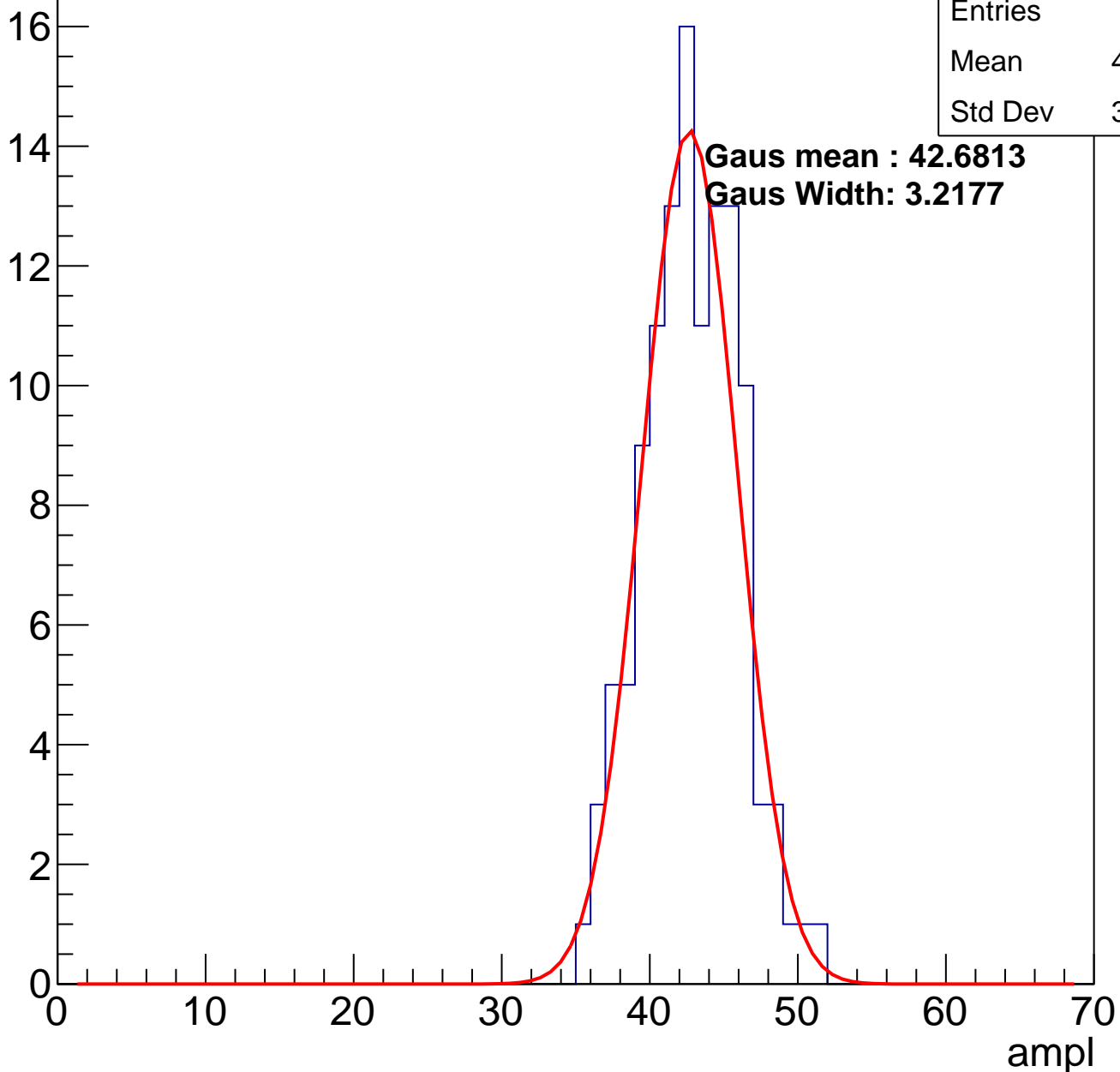
calib_packv5_042523_0143.root, FC#2, port C2

Entries	119
Mean	42.34
Std Dev	3.179

Gaus mean : 42.6813

Gaus Width: 3.2177

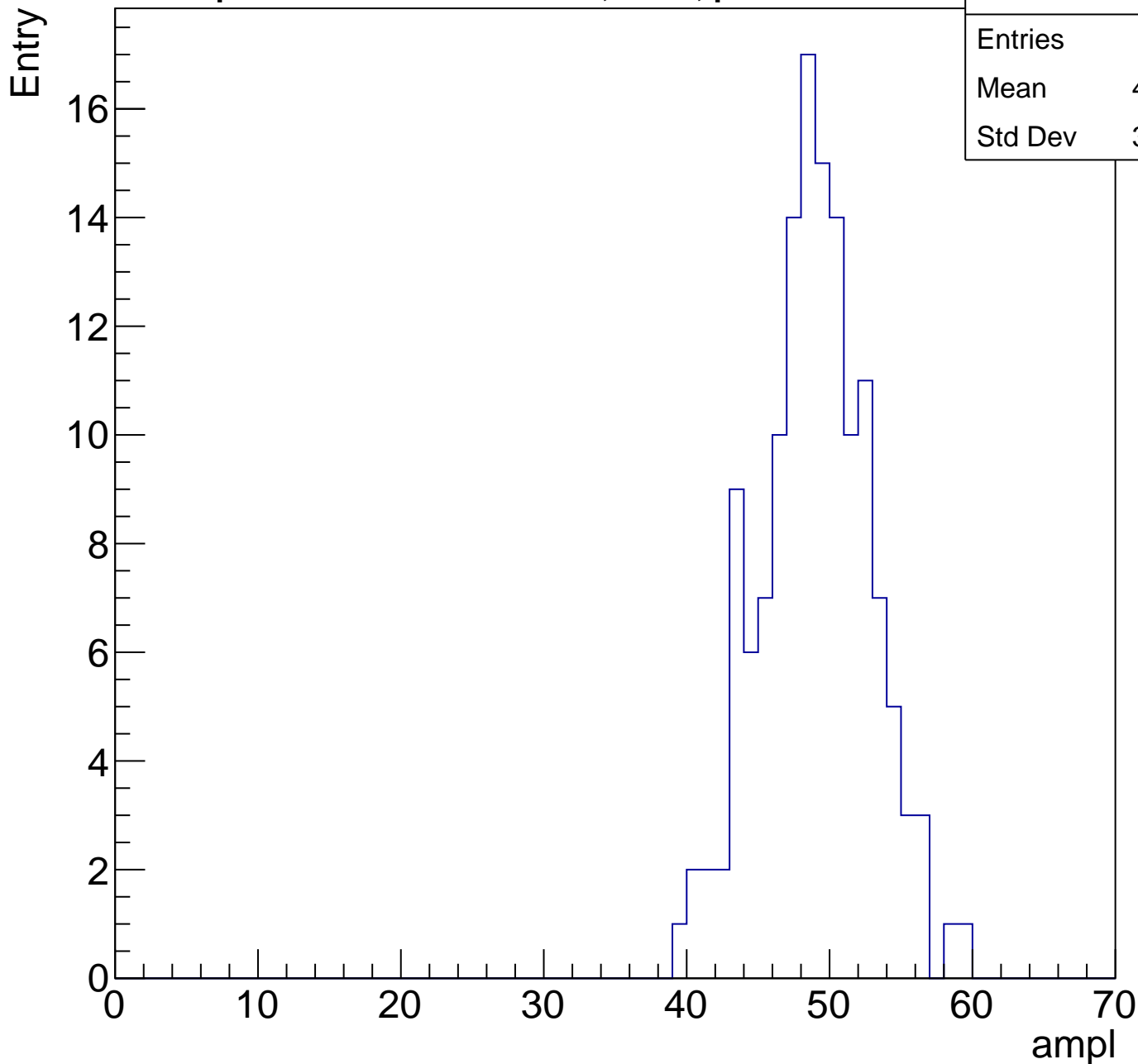
Entry



B1L001S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#2, port C2

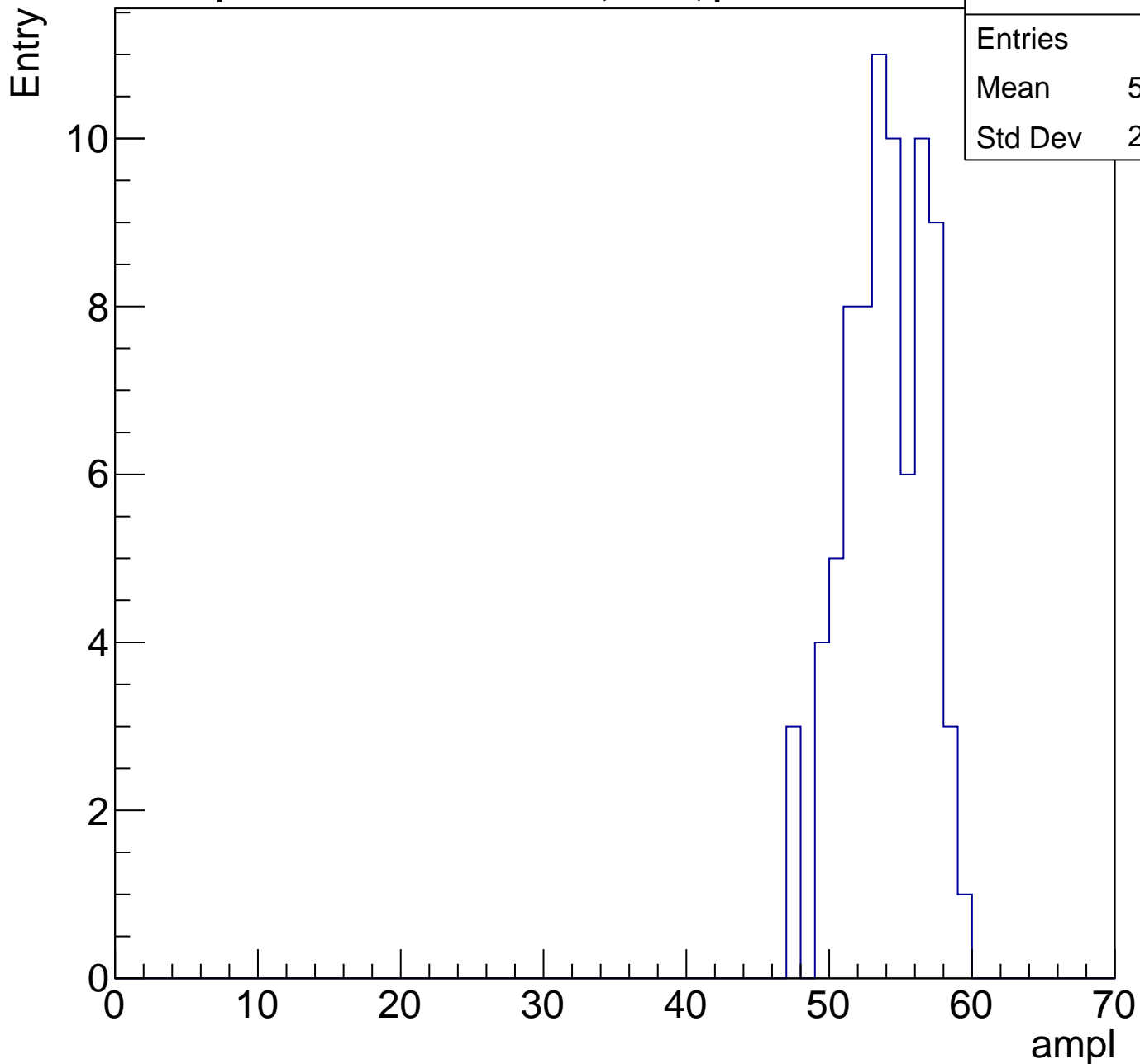
Entries	140
Mean	48.52
Std Dev	3.809



B1L001S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	78
Mean	53.46
Std Dev	2.818



B1L001S, U14-ch120, adc5

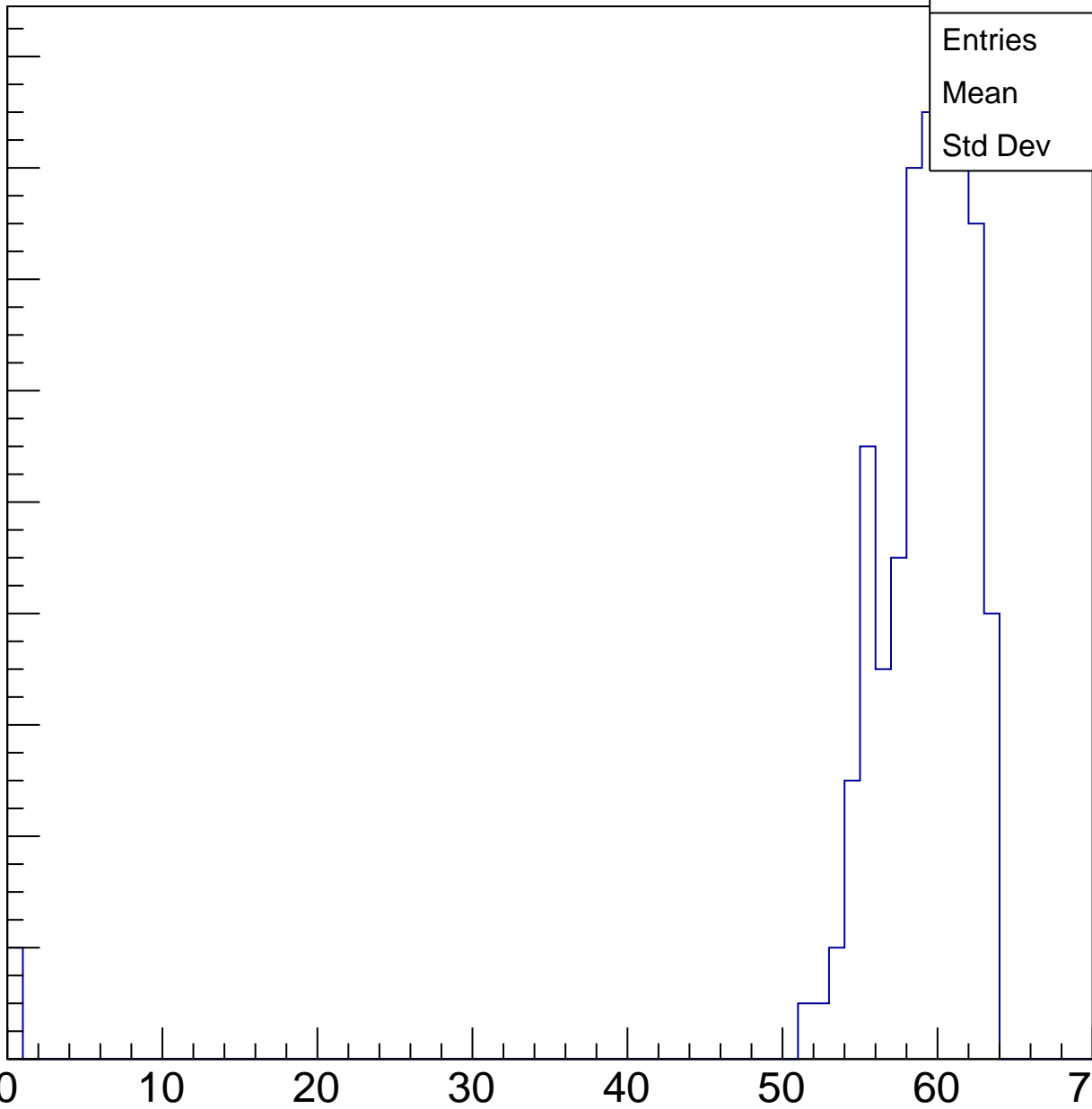
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	128
Mean	57.89
Std Dev	7.781

ampl

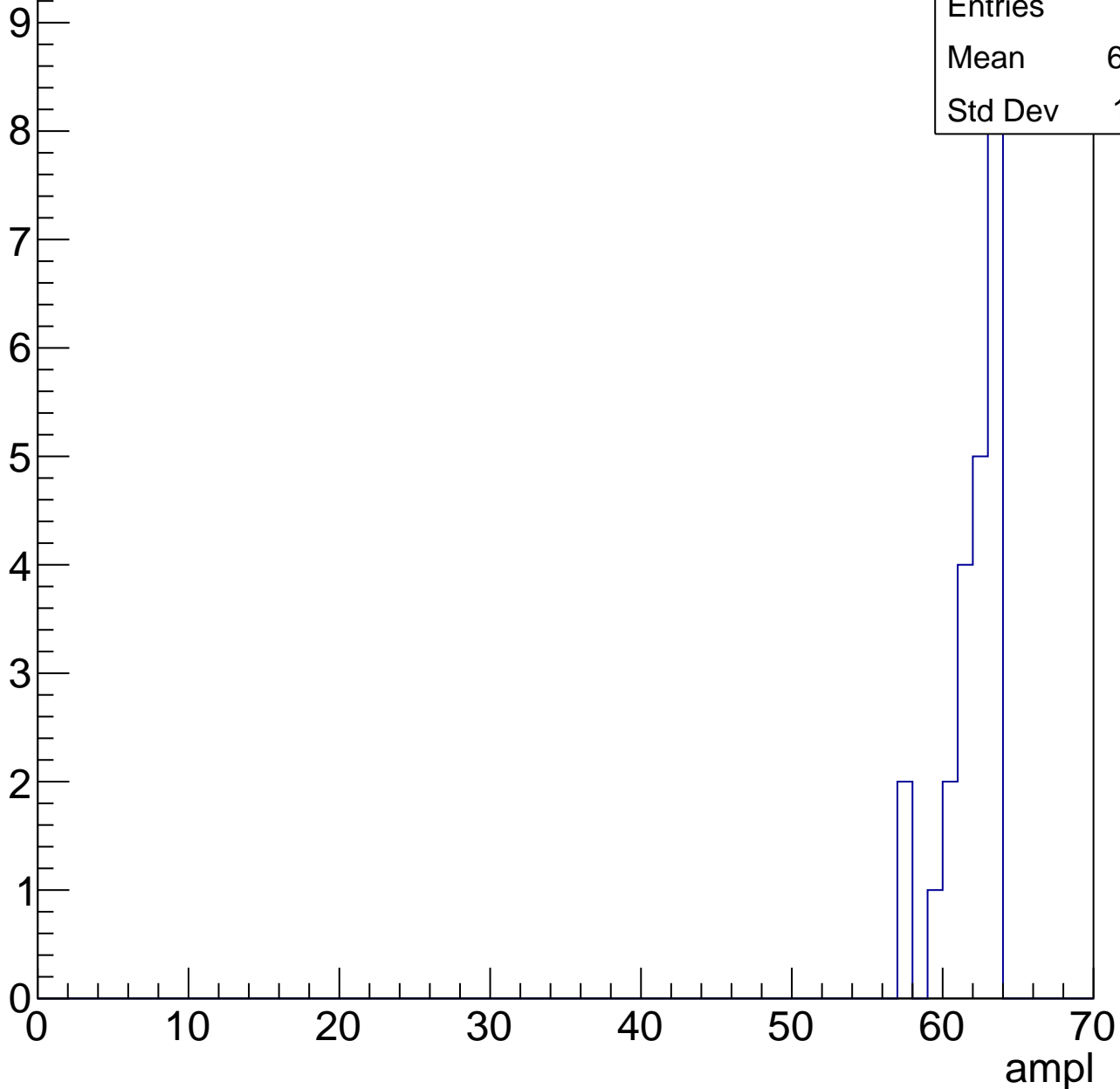


B1L001S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

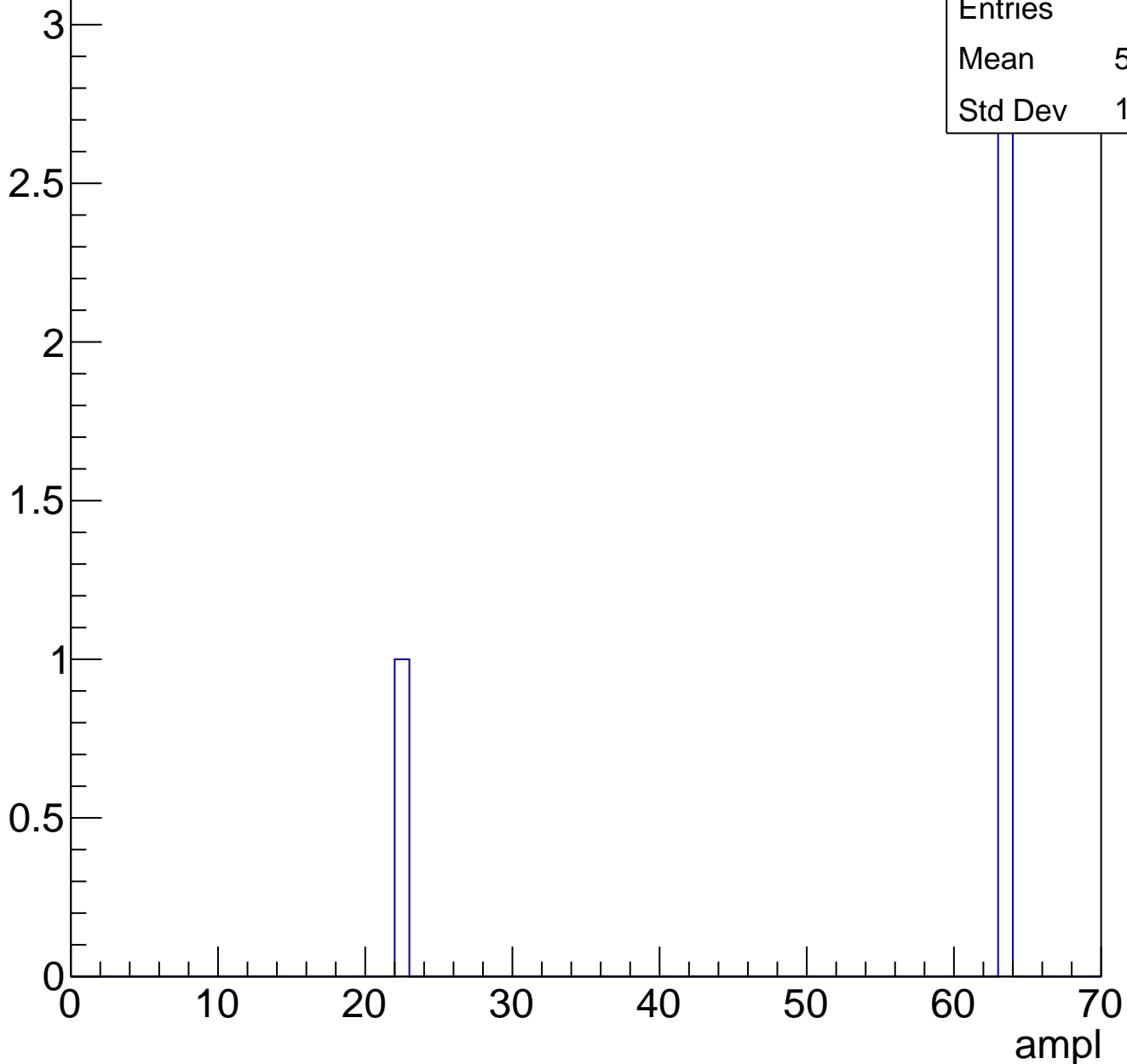
Entries	23
Mean	61.48
Std Dev	1.791



B1L001S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch121, adc0

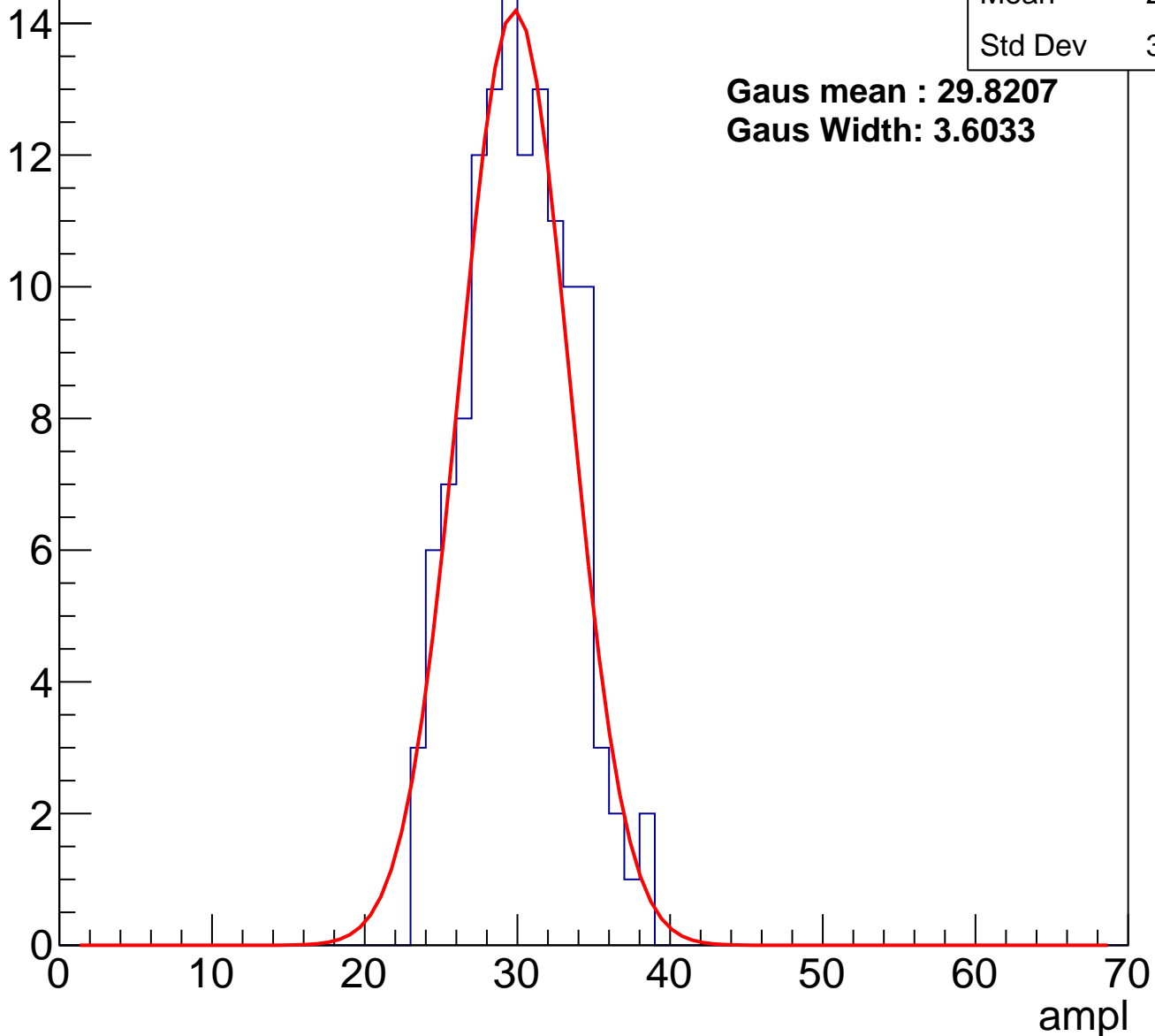
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	29.64
Std Dev	3.372

Gaus mean : 29.8207

Gaus Width: 3.6033

Entry



B1L001S, U14-ch121, adc1

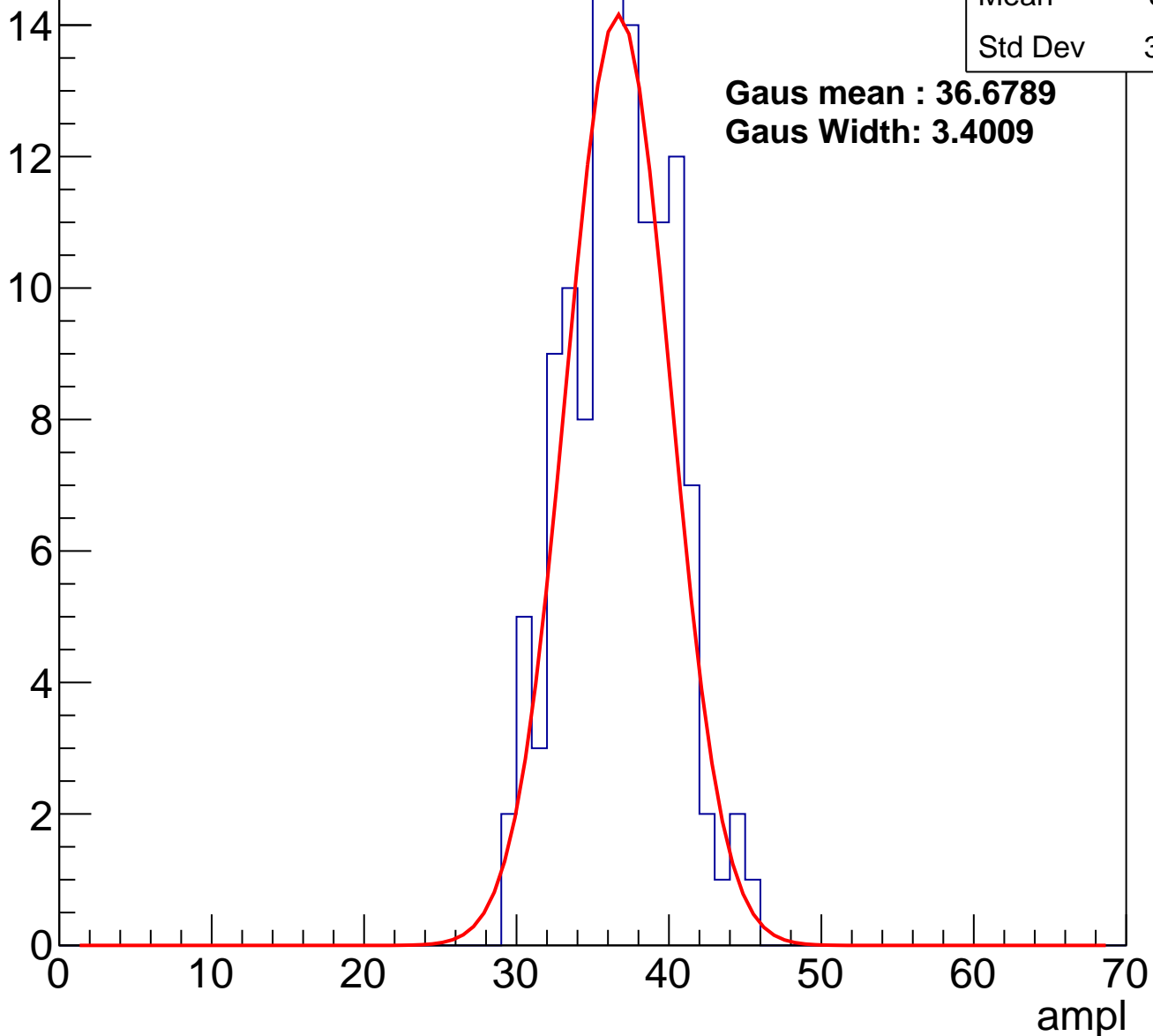
calib_packv5_042523_0143.root, FC#2, port C2

Entries	128
Mean	36.31
Std Dev	3.395

Gaus mean : 36.6789

Gaus Width: 3.4009

Entry



B1L001S, U14-ch121, adc2

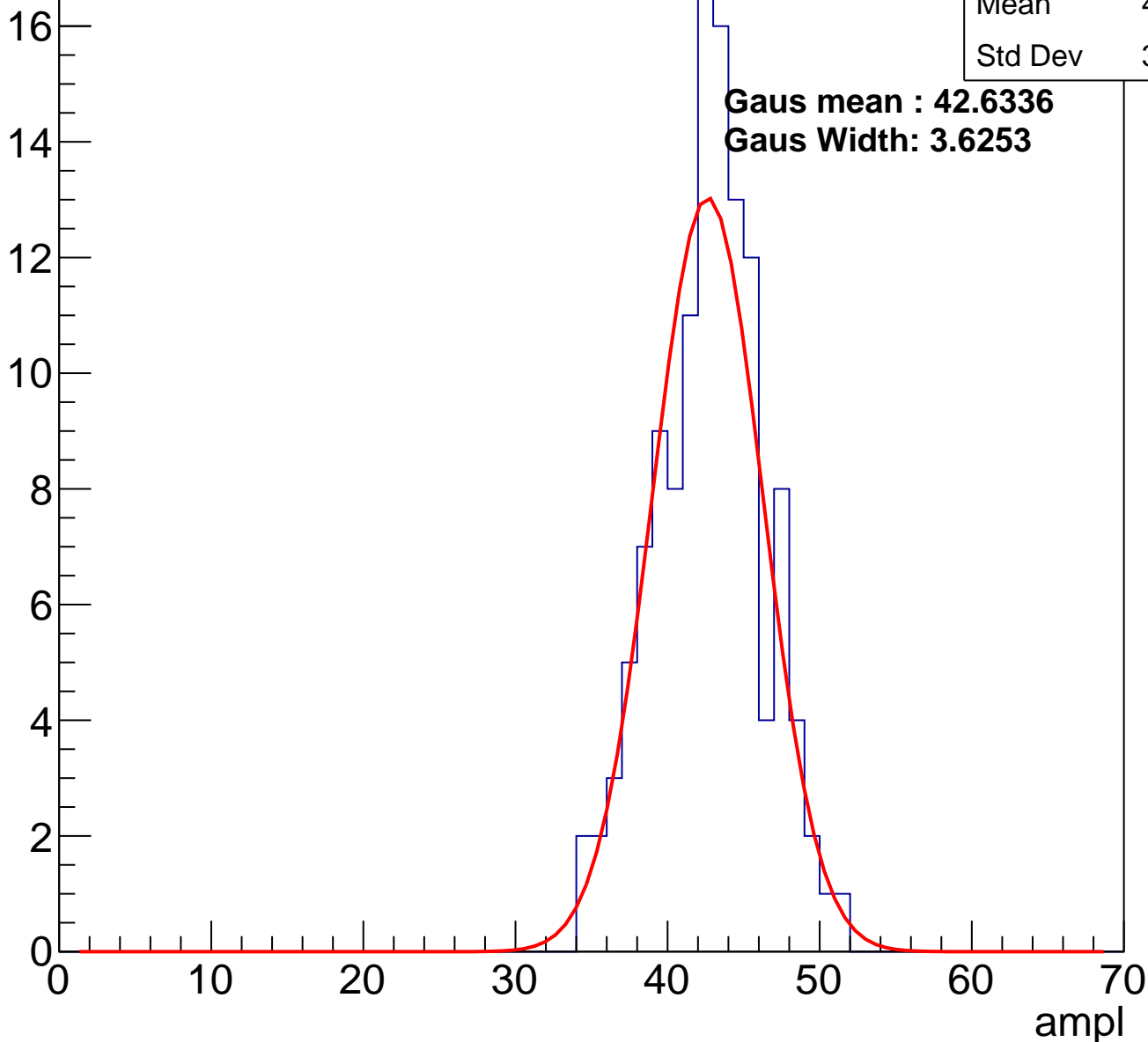
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	125
Mean	42.27
Std Dev	3.479

Gaus mean : 42.6336

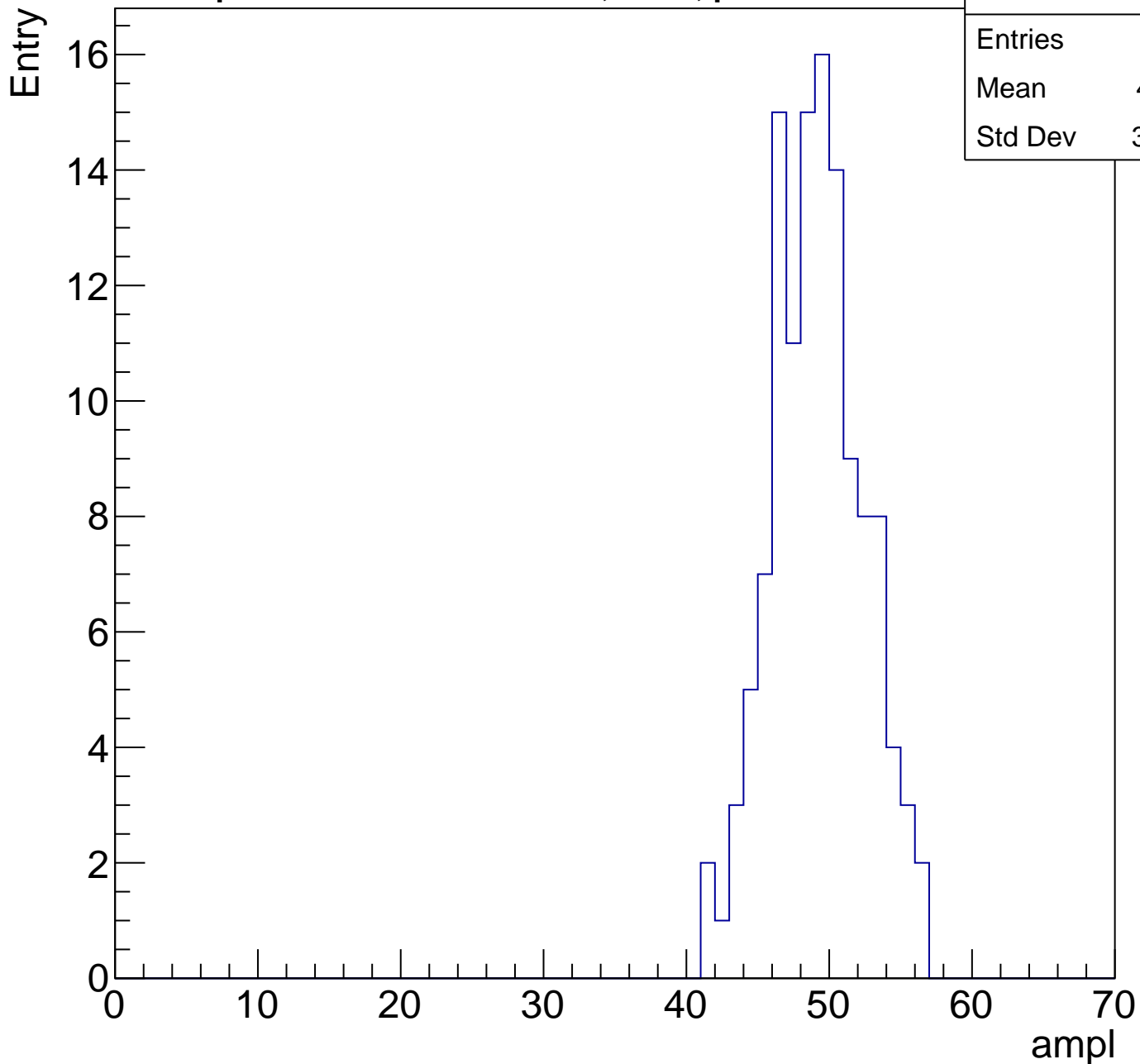
Gaus Width: 3.6253



B1L001S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	48.71
Std Dev	3.202



B1L001S, U14-ch121, adc4

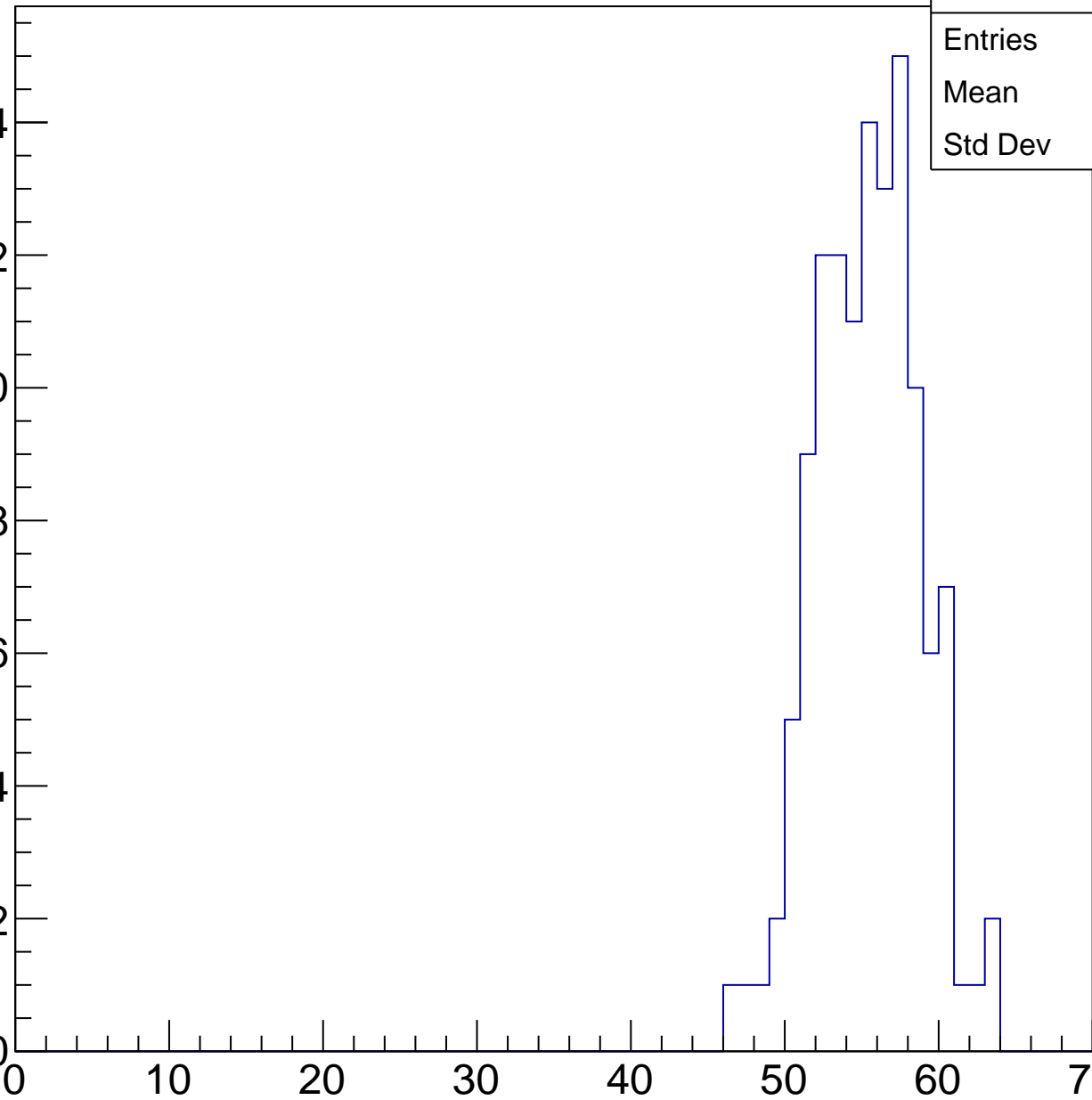
calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	54.94
Std Dev	3.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#2, port C2

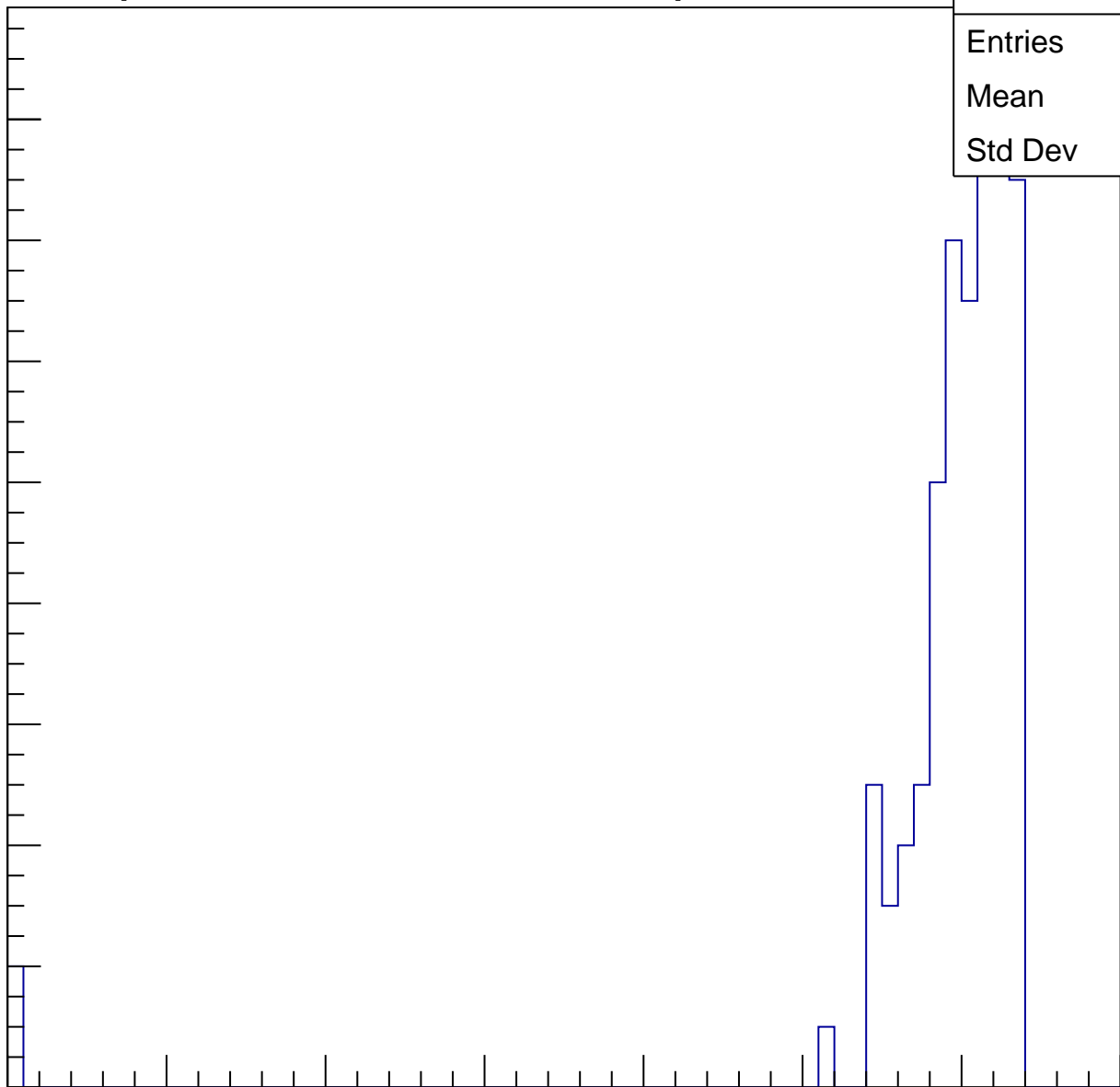
Entries	106
Mean	58.65
Std Dev	8.537

Entry

16
14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

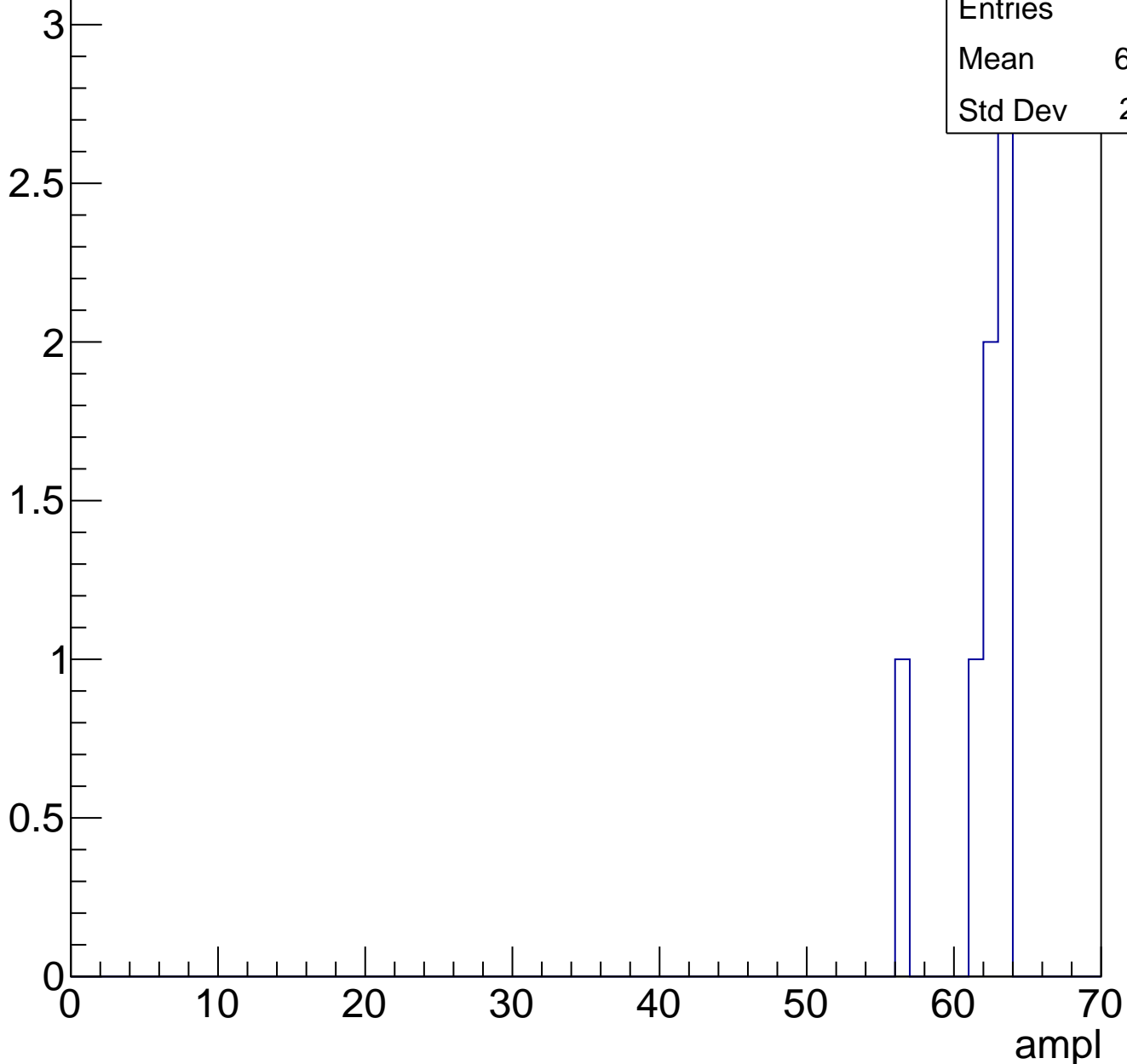
ampl



B1L001S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#2, port C2

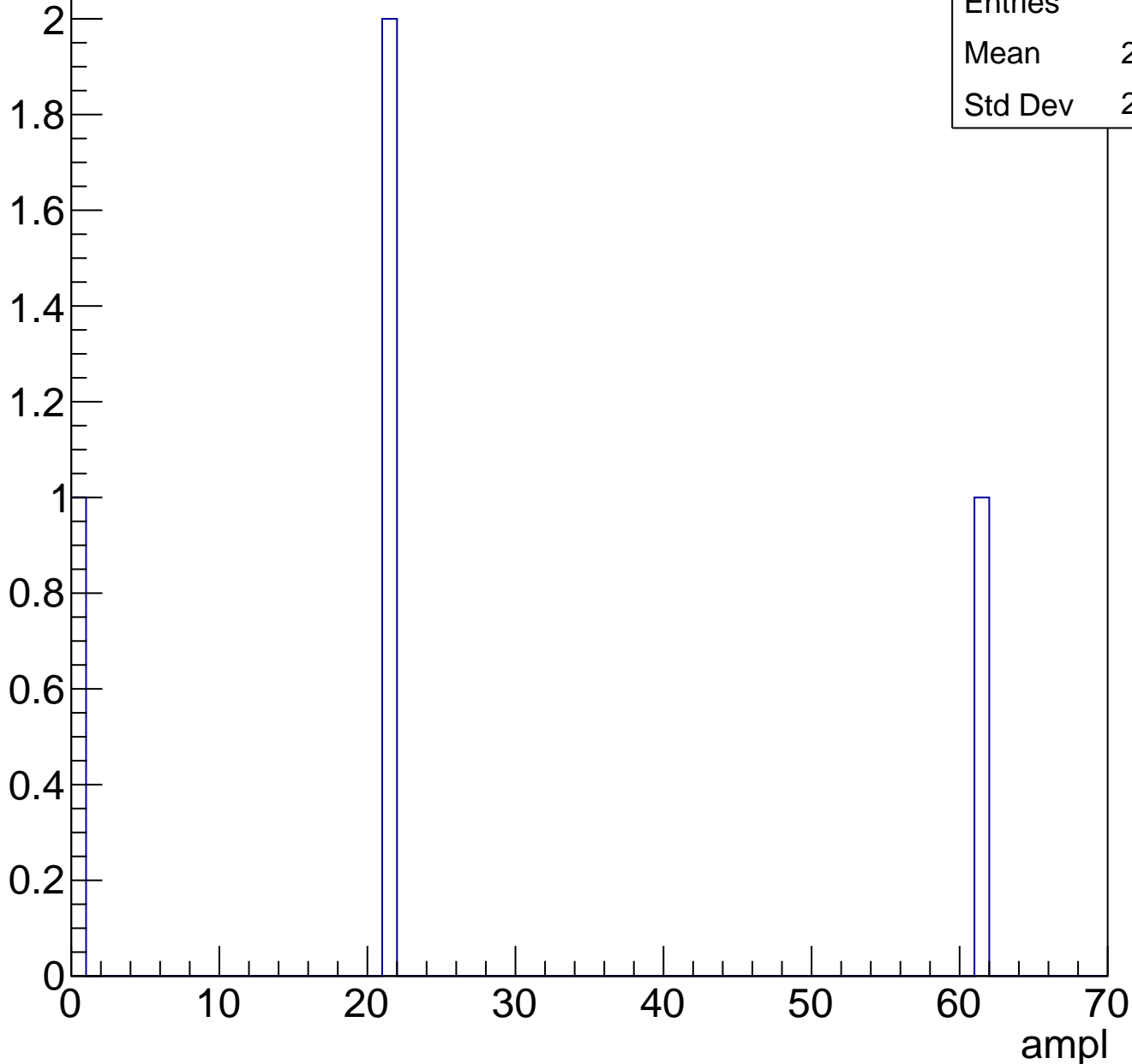
Entry



B1L001S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



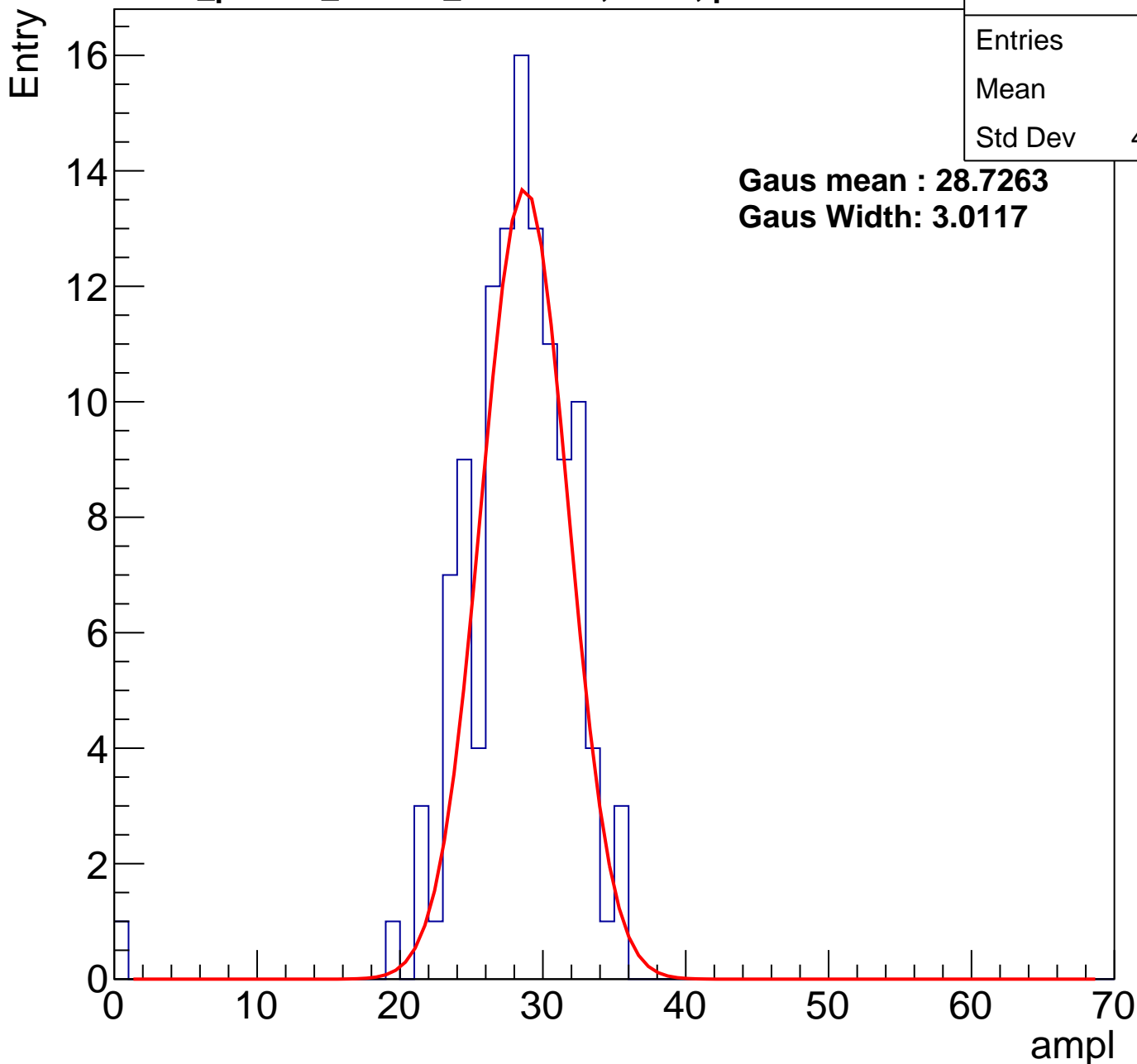
B1L001S, U14-ch122, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	118
Mean	27.7
Std Dev	4.152

Gaus mean : 28.7263

Gaus Width: 3.0117



B1L001S, U14-ch122, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	34.41
Std Dev	3.393

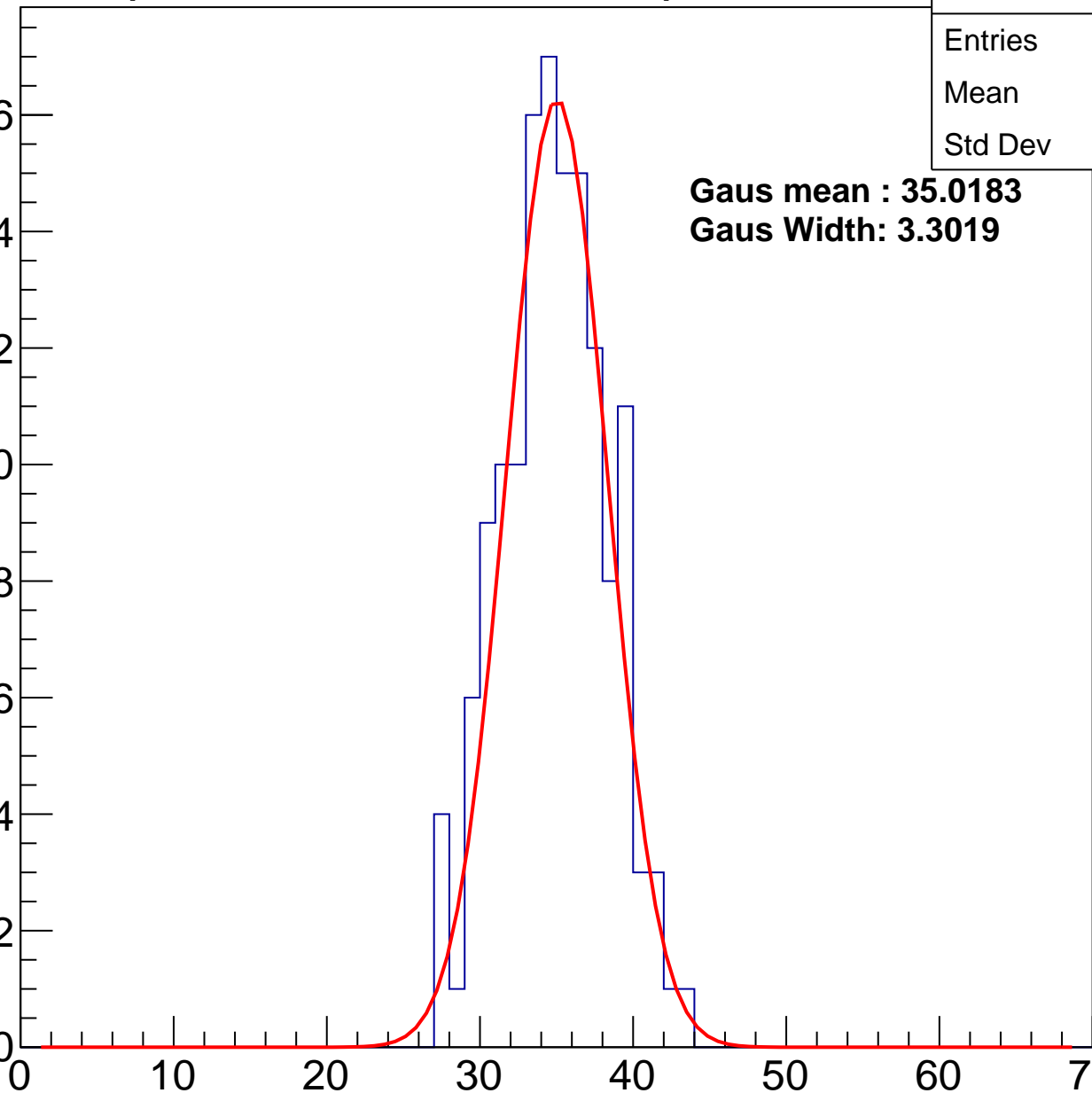
Gaus mean : 35.0183

Gaus Width: 3.3019

Entry

16
14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch122, adc2

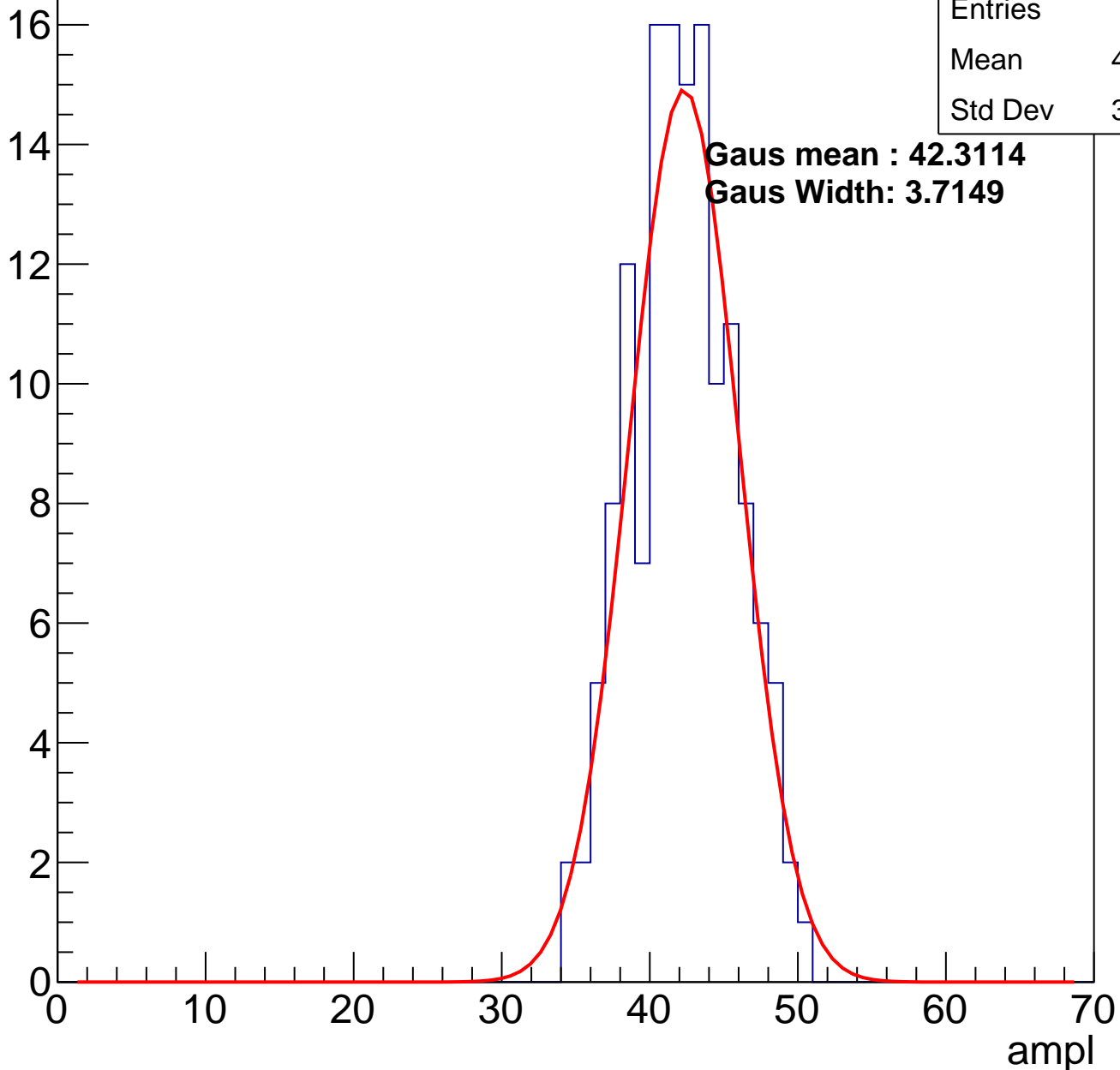
calib_packv5_042523_0143.root, FC#2, port C2

Entries	142
Mean	41.76
Std Dev	3.468

Gaus mean : 42.3114

Gaus Width: 3.7149

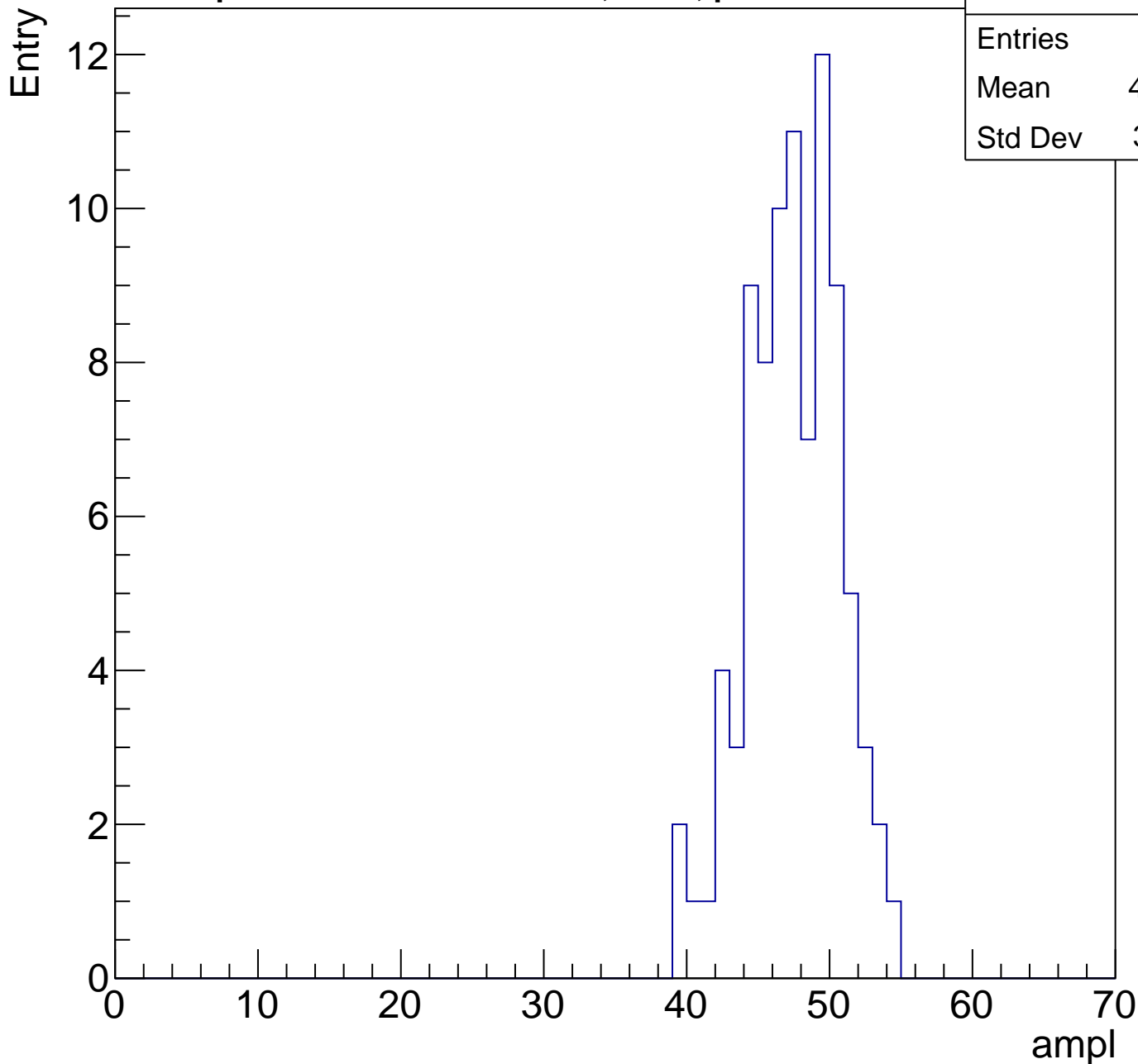
Entry



B1L001S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	88
Mean	46.98
Std Dev	3.191



B1L001S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#2, port C2

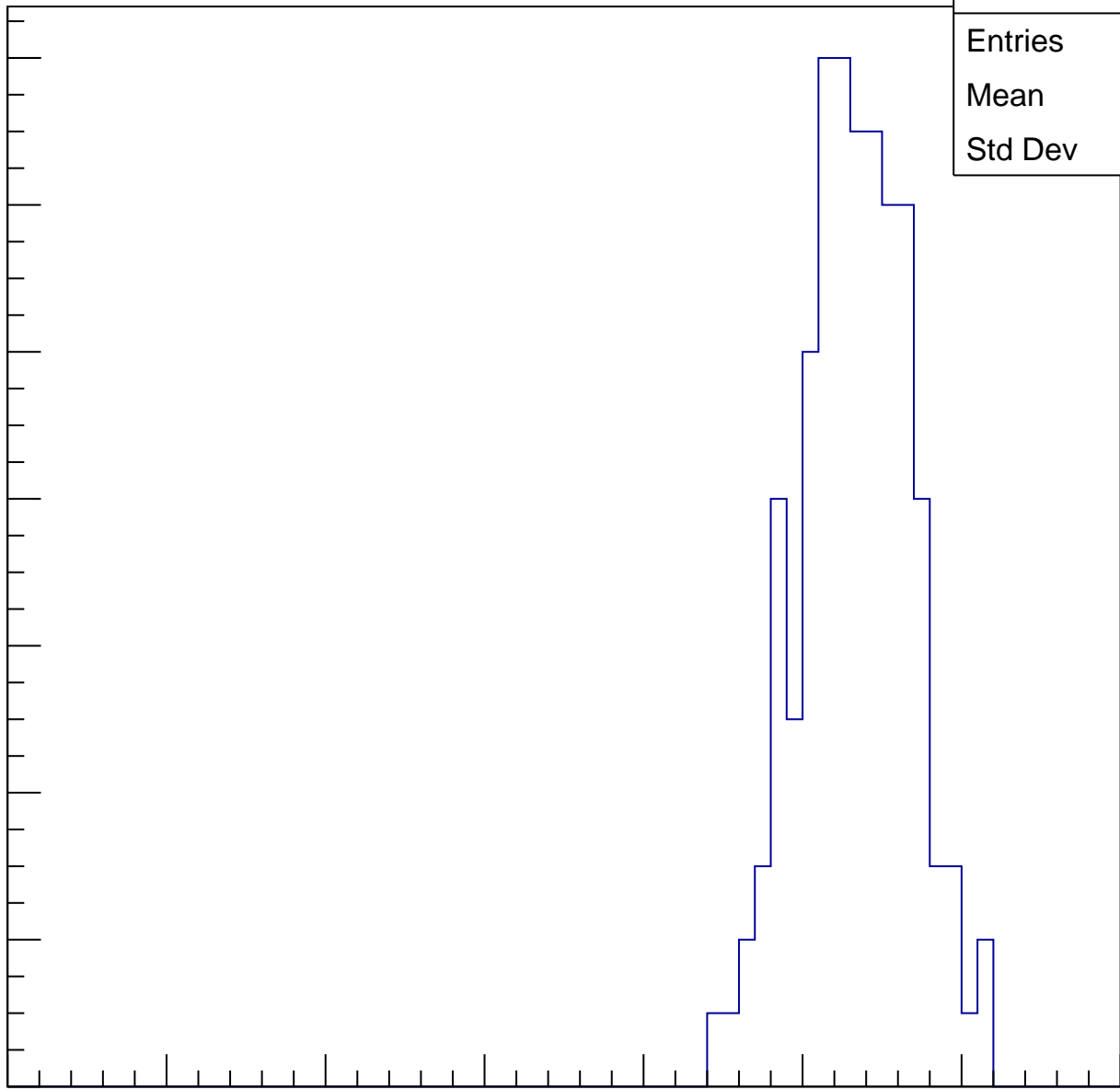
Entries	125
Mean	52.84
Std Dev	3.4

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch122, adc5

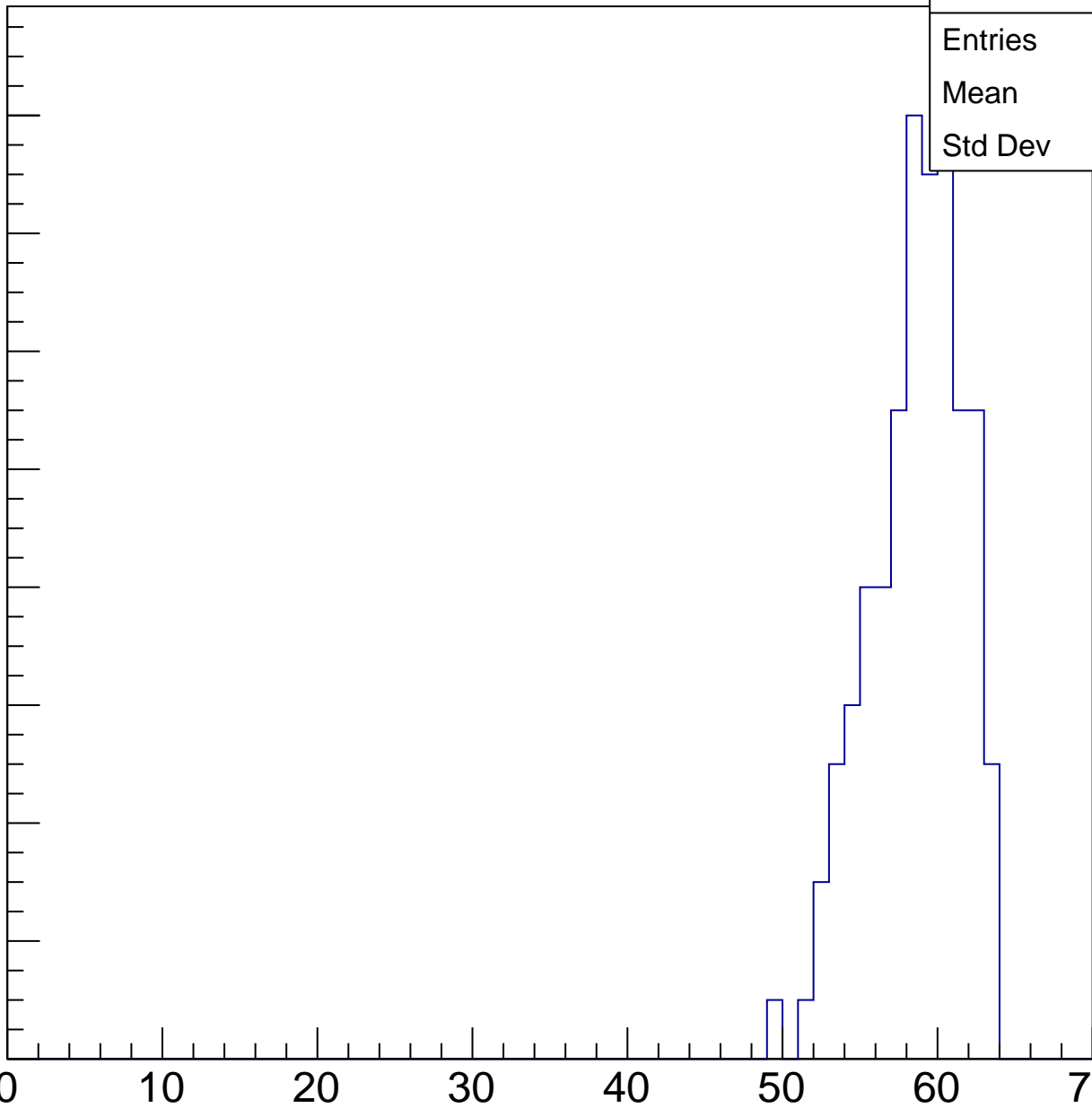
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	118
Mean	58.14
Std Dev	2.989

ampl



B1L001S, U14-ch122, adc6

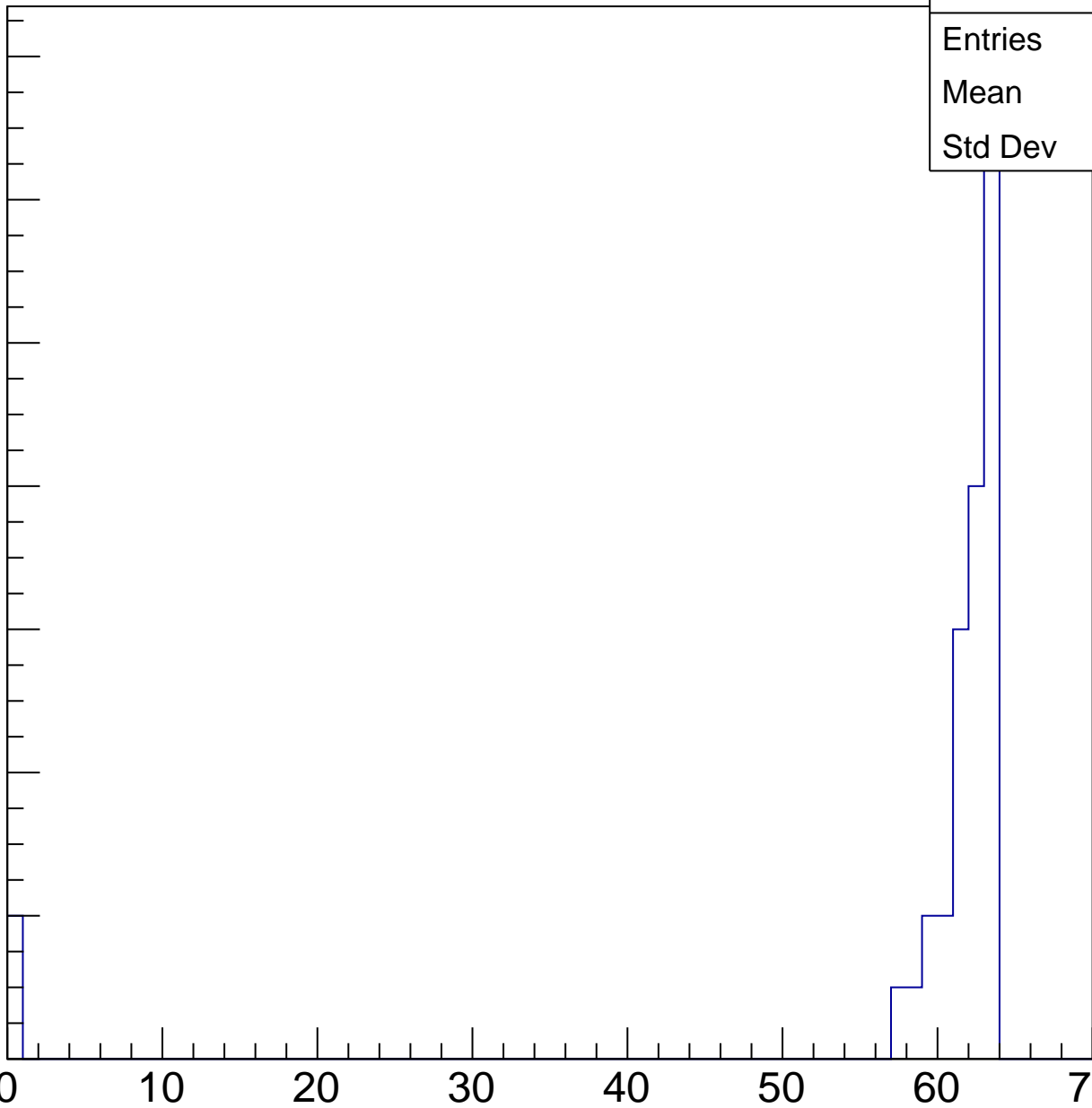
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	36
Mean	58.25
Std Dev	14.21

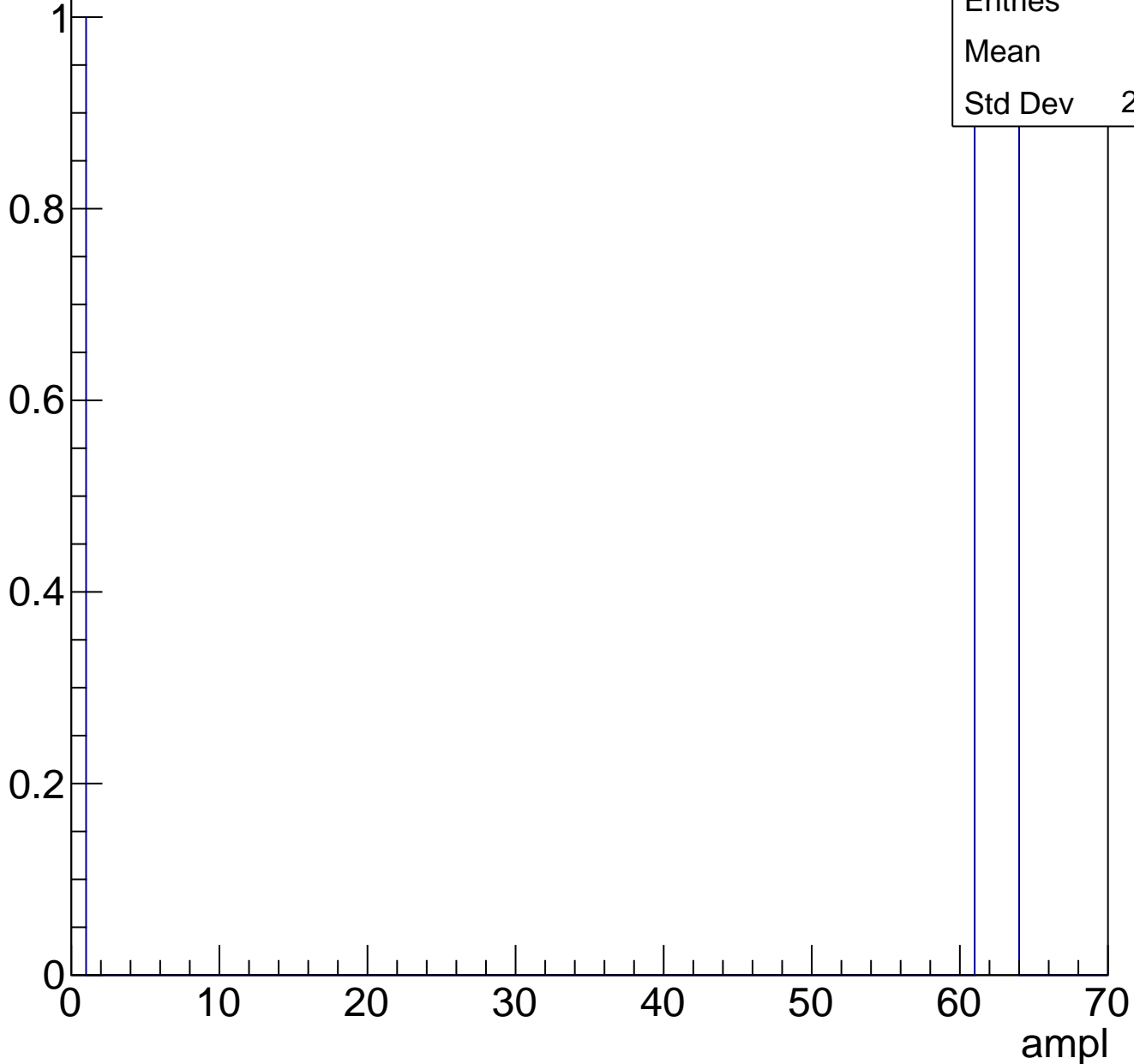
ampl



B1L001S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch123, adc0

calib_packv5_042523_0143.root, FC#2, port C2

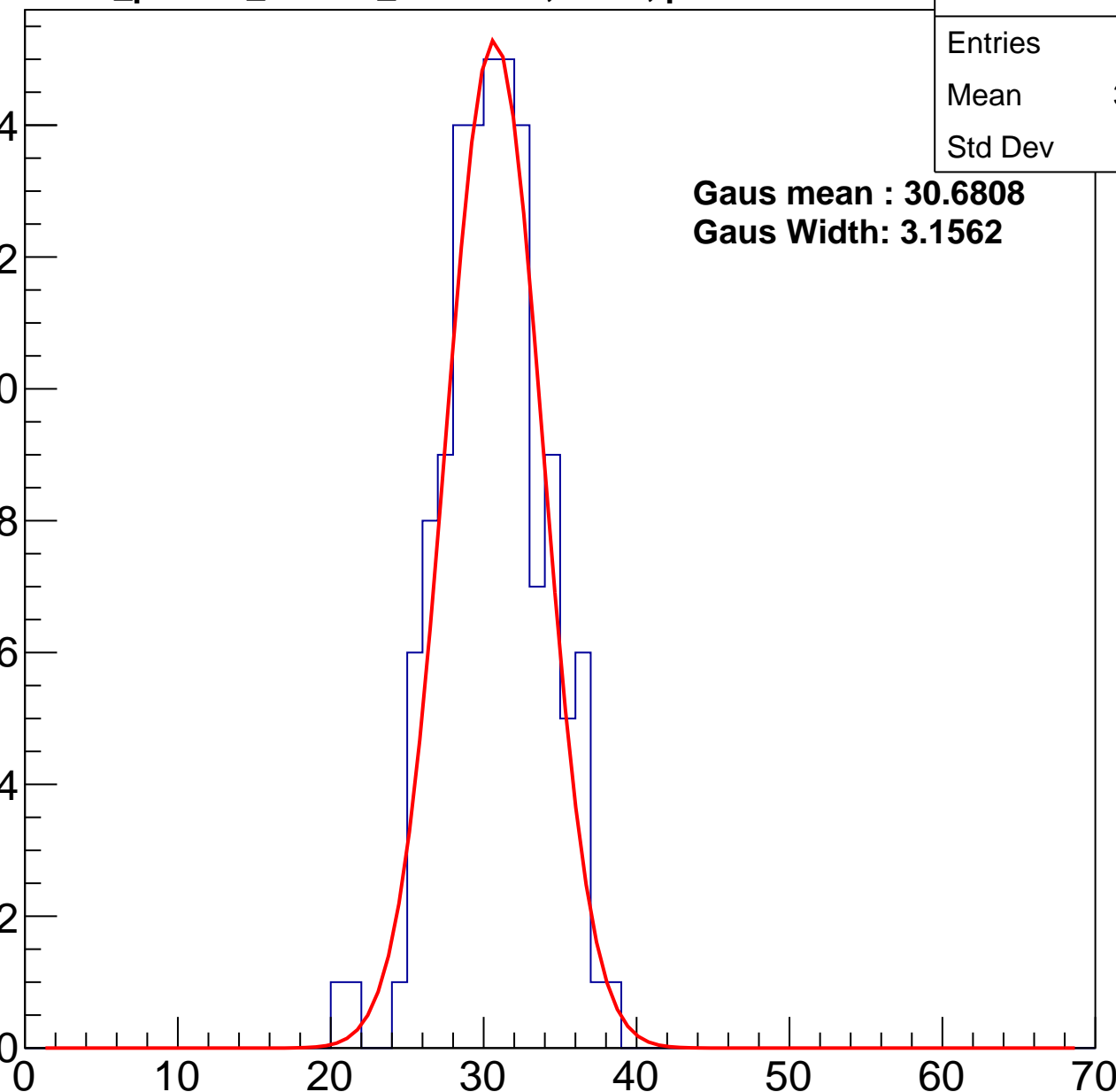
Entry

14
12
10
8
6
4
2
0

Entries	127
Mean	30.16
Std Dev	3.29

Gaus mean : 30.6808

Gaus Width: 3.1562



ampl

B1L001S, U14-ch123, adc1

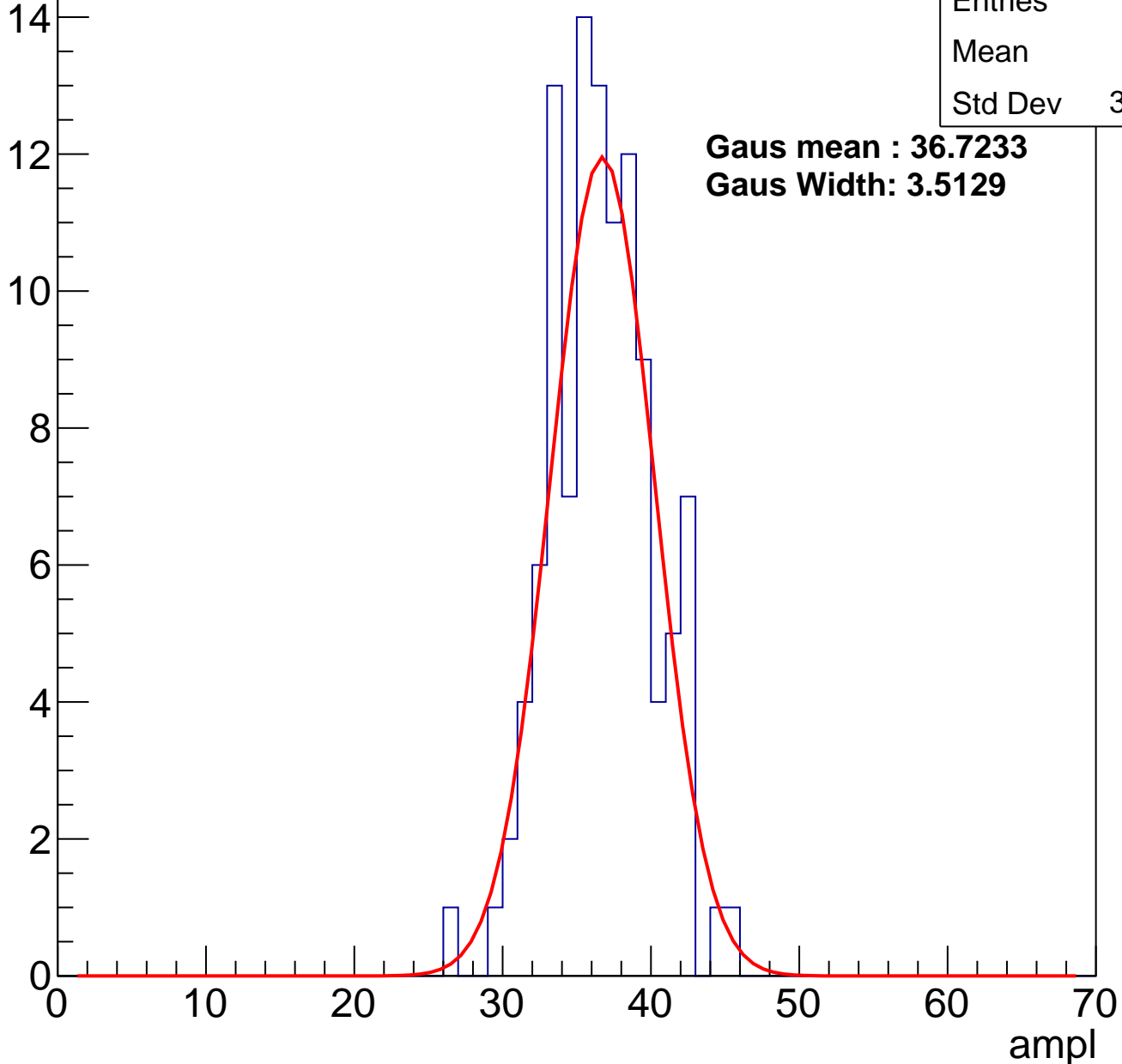
calib_packv5_042523_0143.root, FC#2, port C2

Entries	111
Mean	36.2
Std Dev	3.419

Gaus mean : 36.7233

Gaus Width: 3.5129

Entry



B1L001S, U14-ch123, adc2

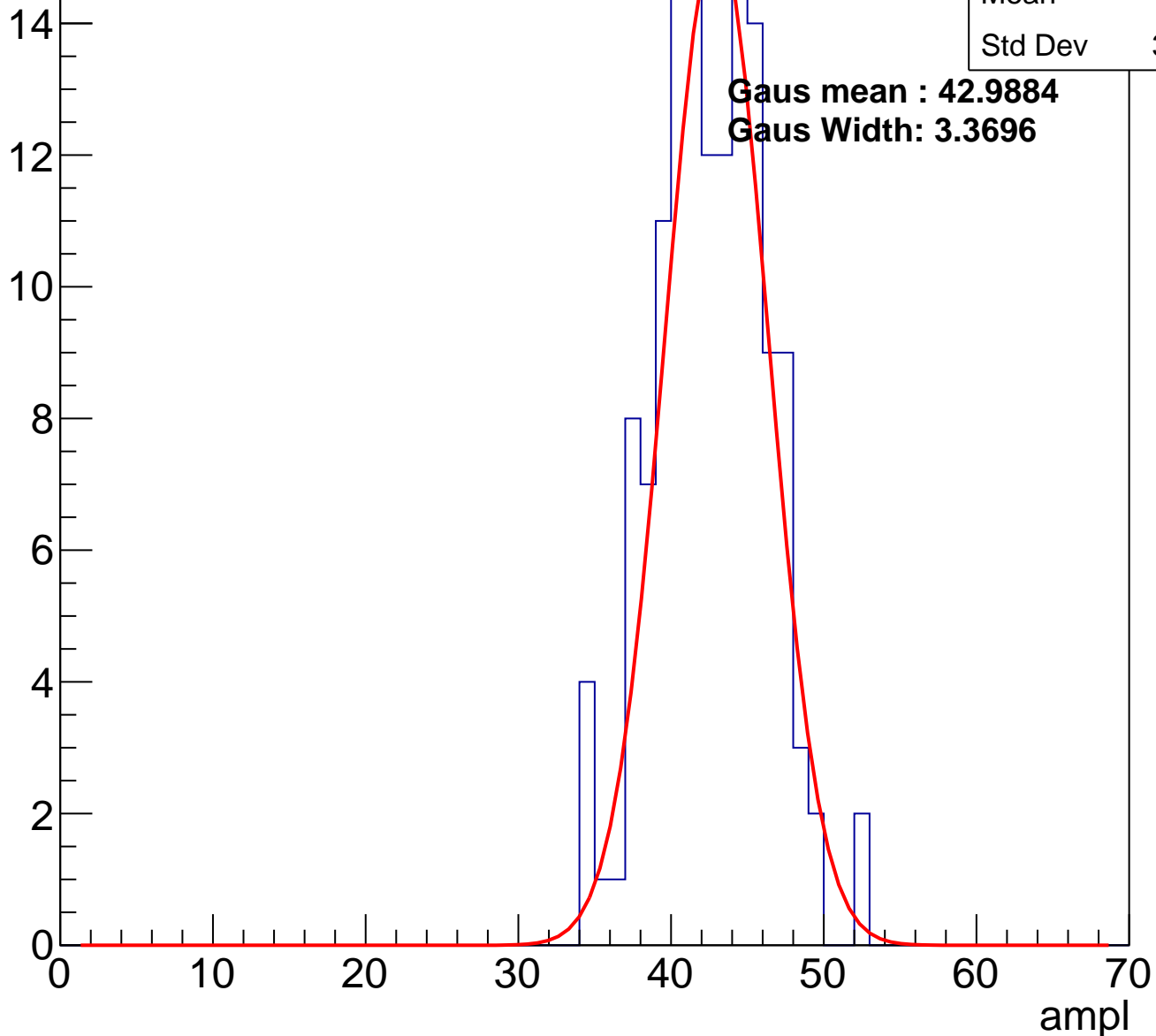
calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	42.19
Std Dev	3.581

Gaus mean : 42.9884

Gaus Width: 3.3696

Entry

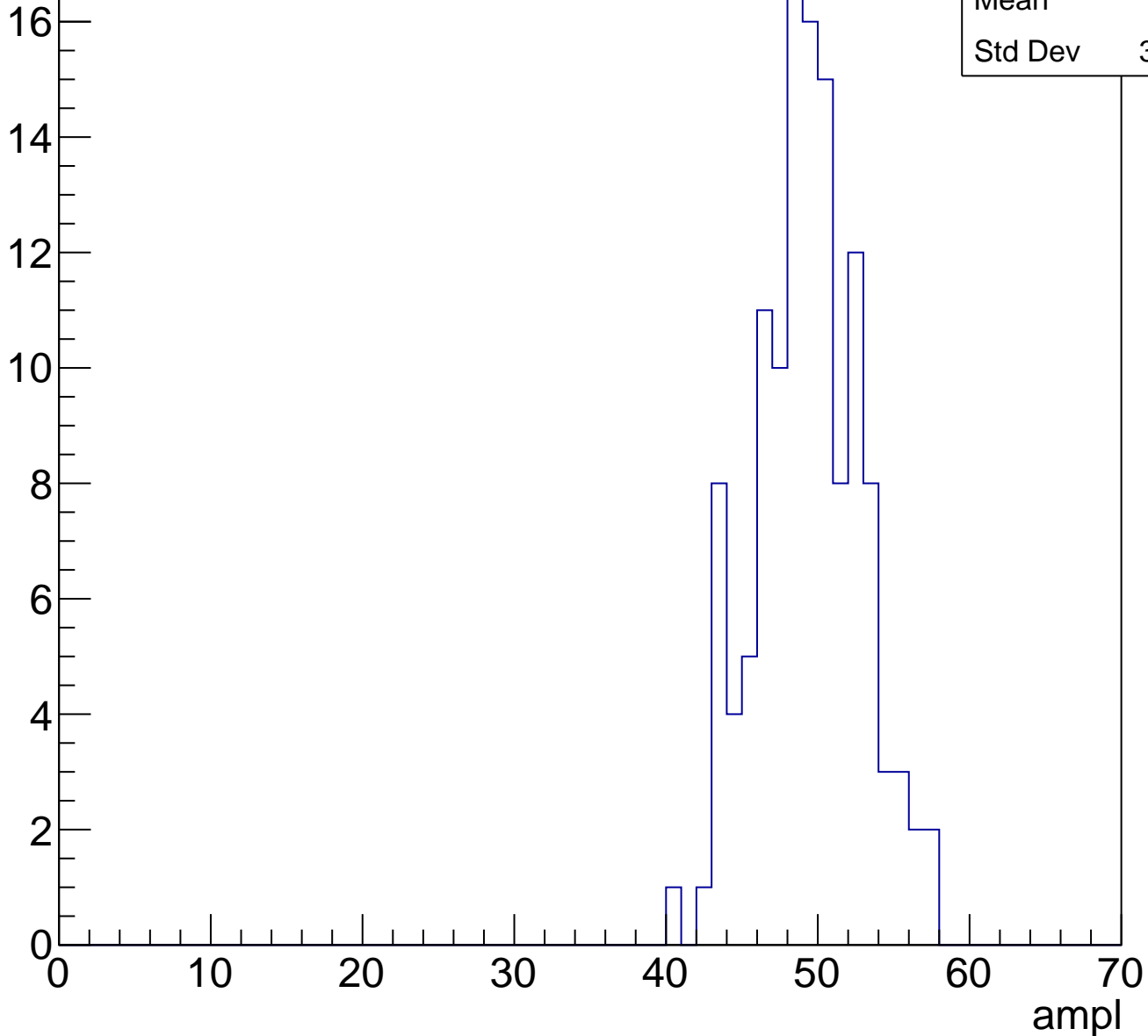


B1L001S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	126
Mean	48.9
Std Dev	3.386



B1L001S, U14-ch123, adc4

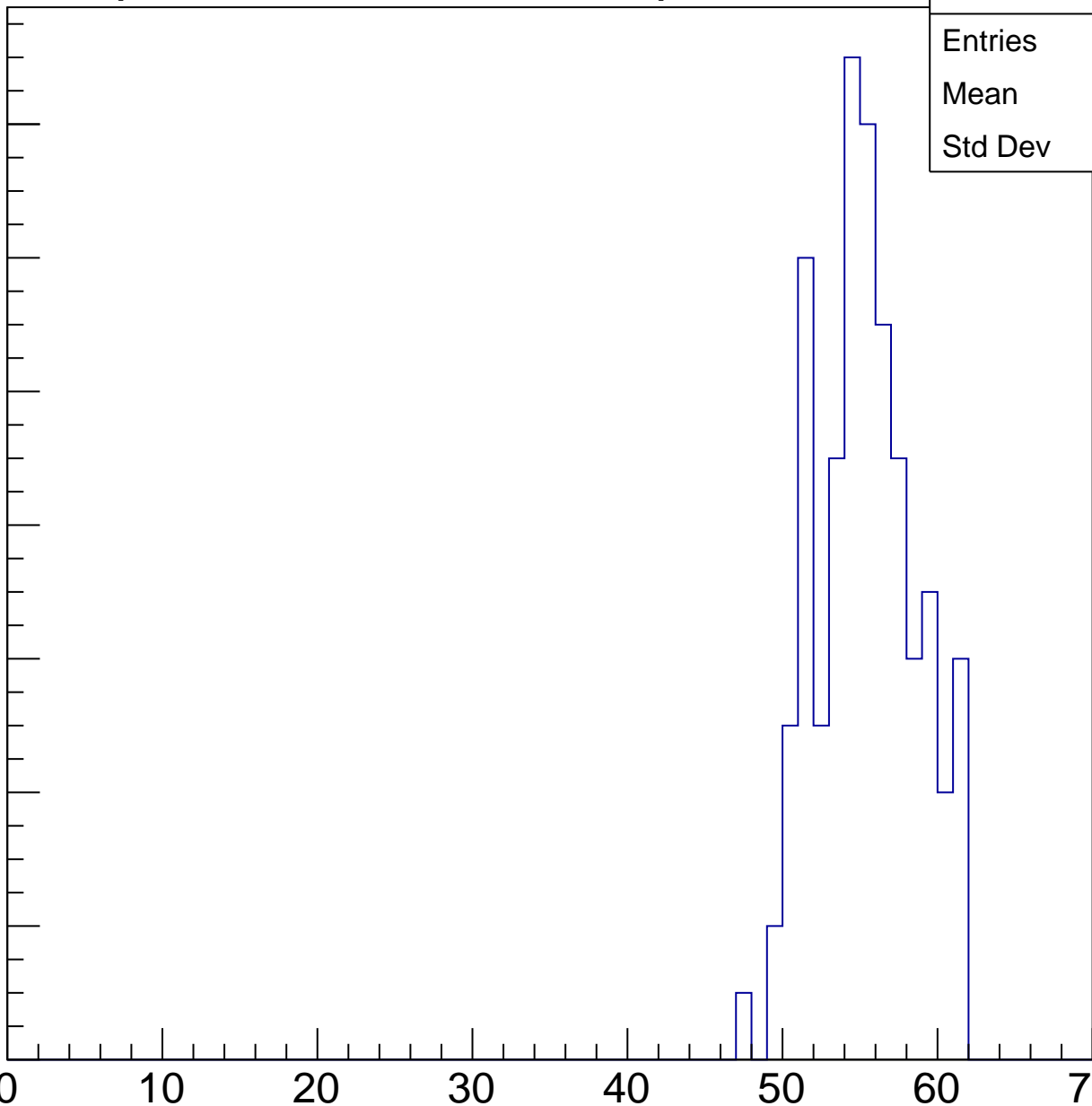
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	106
Mean	54.91
Std Dev	3.188

ampl



B1L001S, U14-ch123, adc5

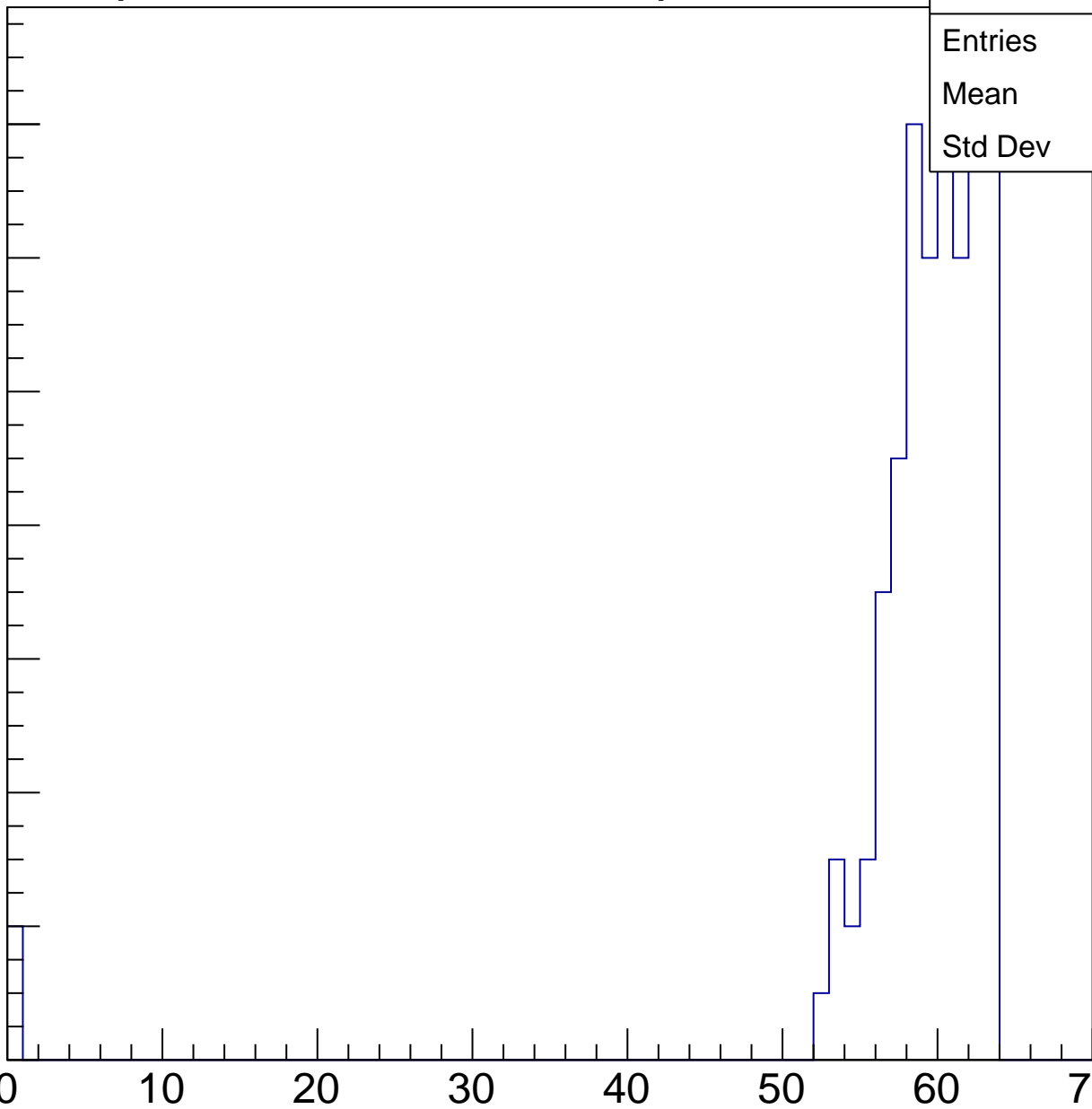
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	110
Mean	58.33
Std Dev	8.376

ampl

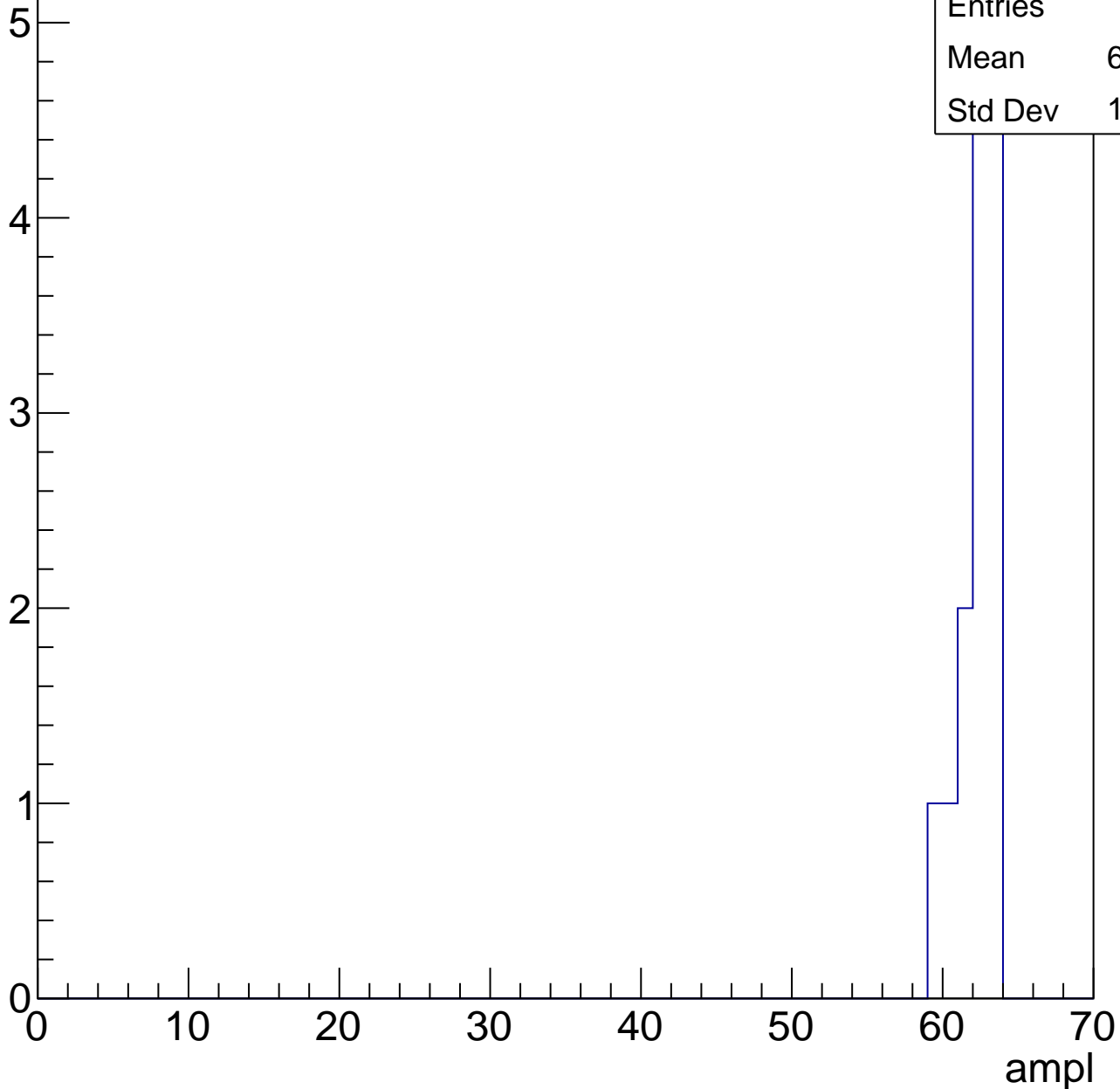


B1L001S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	14
Mean	61.86
Std Dev	1.187



B1L001S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L001S, U14-ch124, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	28.3
Std Dev	4.047

Gaus mean : 28.8398

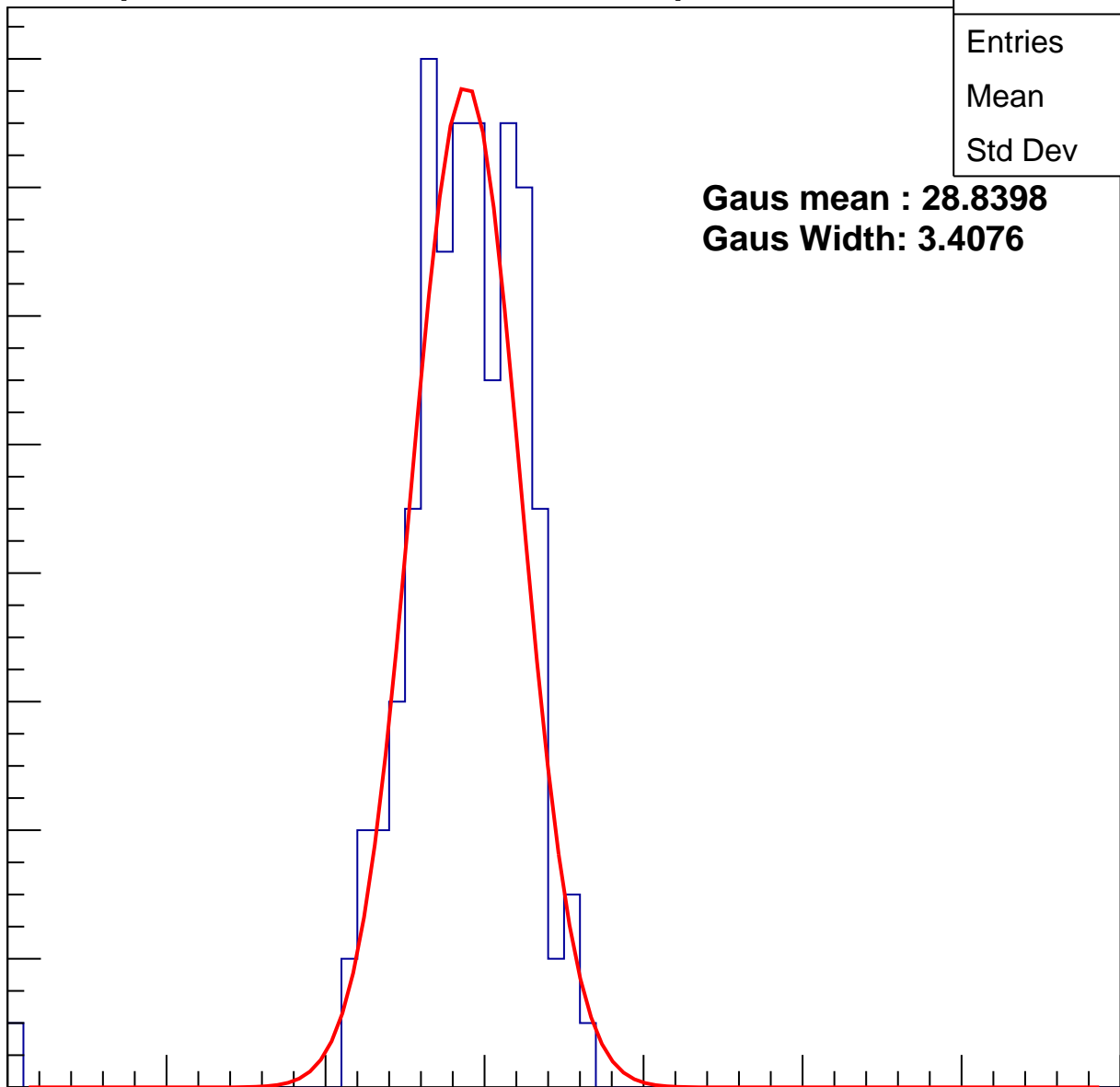
Gaus Width: 3.4076

Entry

16
14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



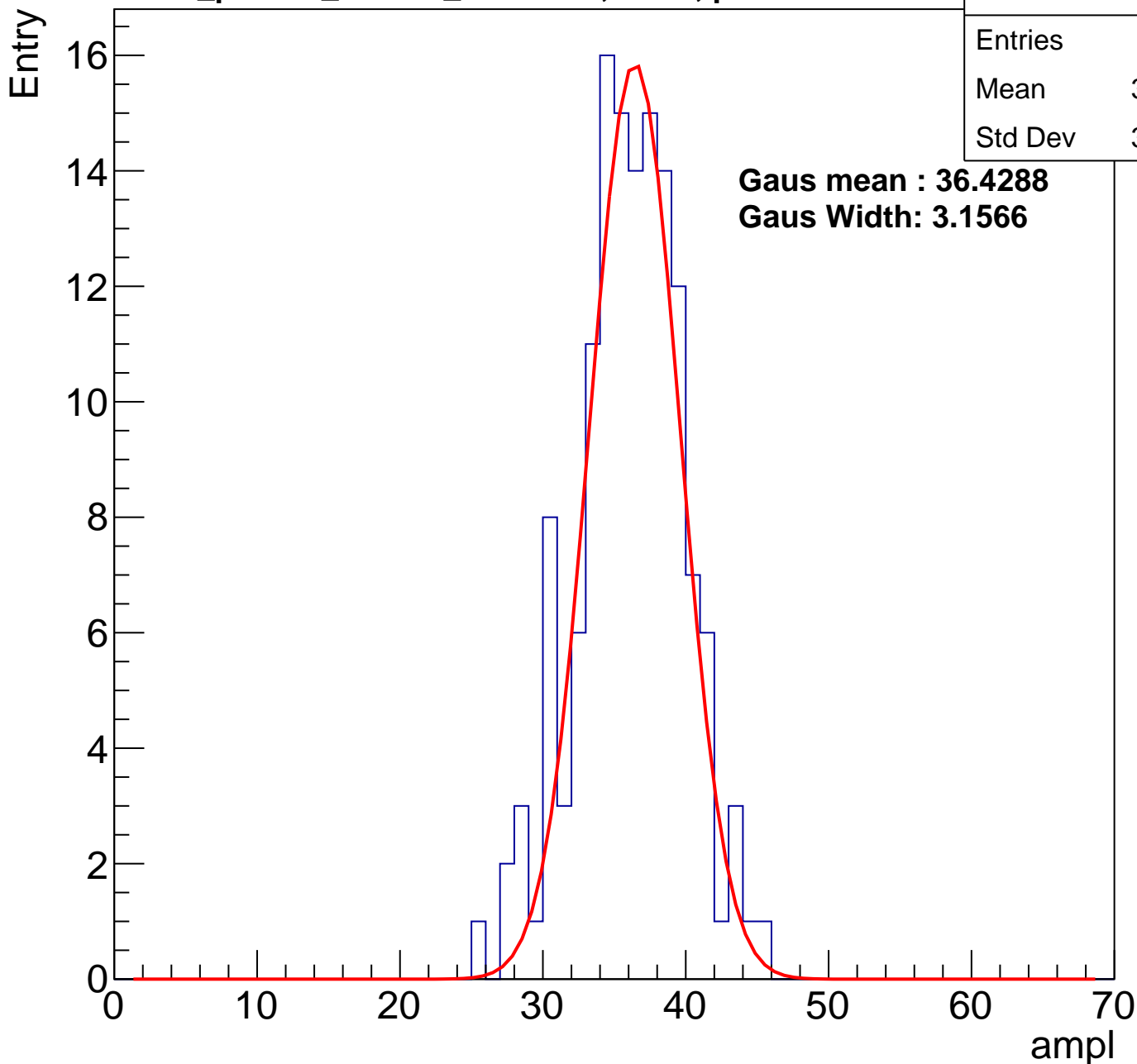
B1L001S, U14-ch124, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	140
Mean	35.67
Std Dev	3.698

Gaus mean : 36.4288

Gaus Width: 3.1566



B1L001S, U14-ch124, adc2

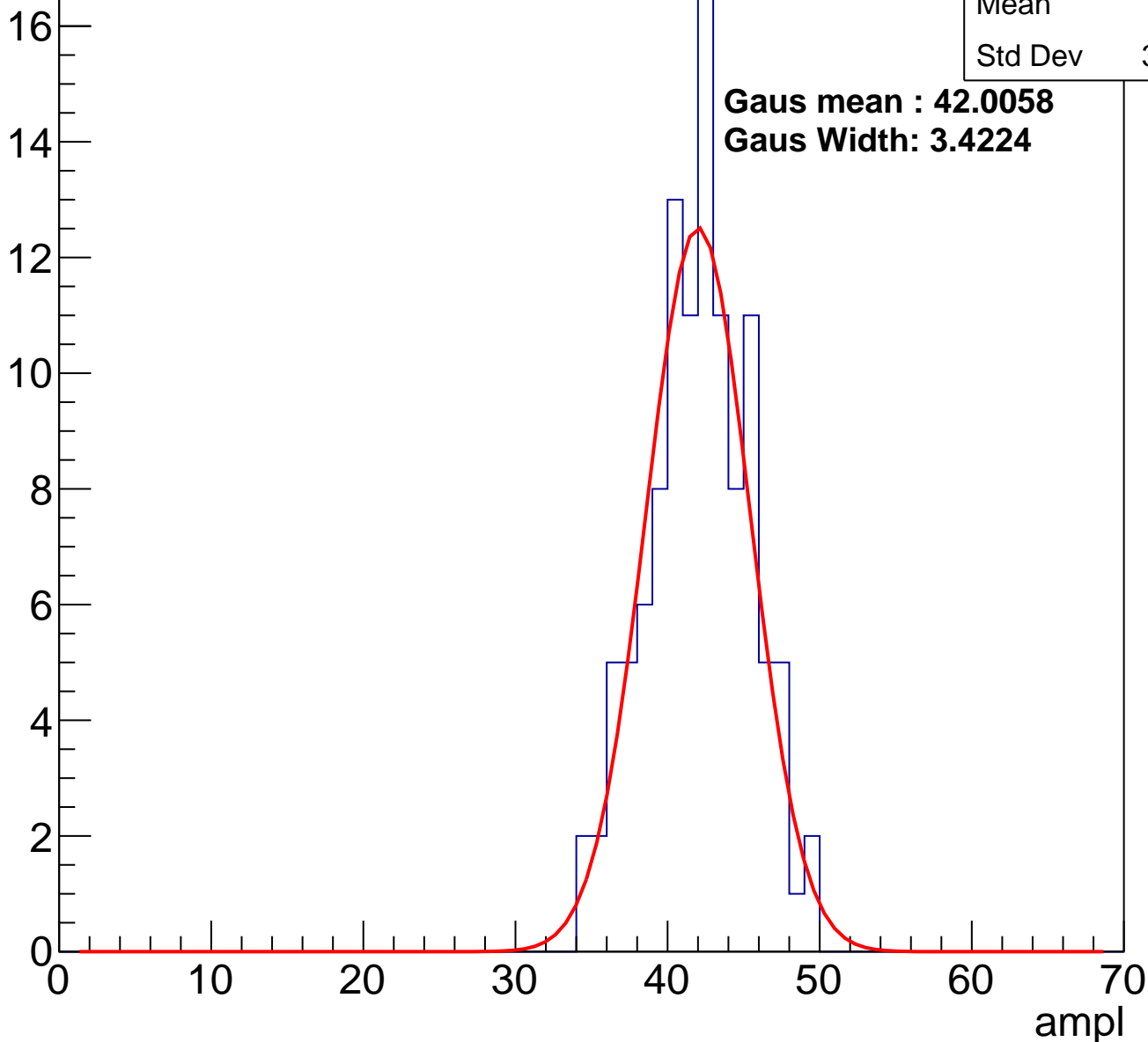
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	112
Mean	41.6
Std Dev	3.307

Gaus mean : 42.0058

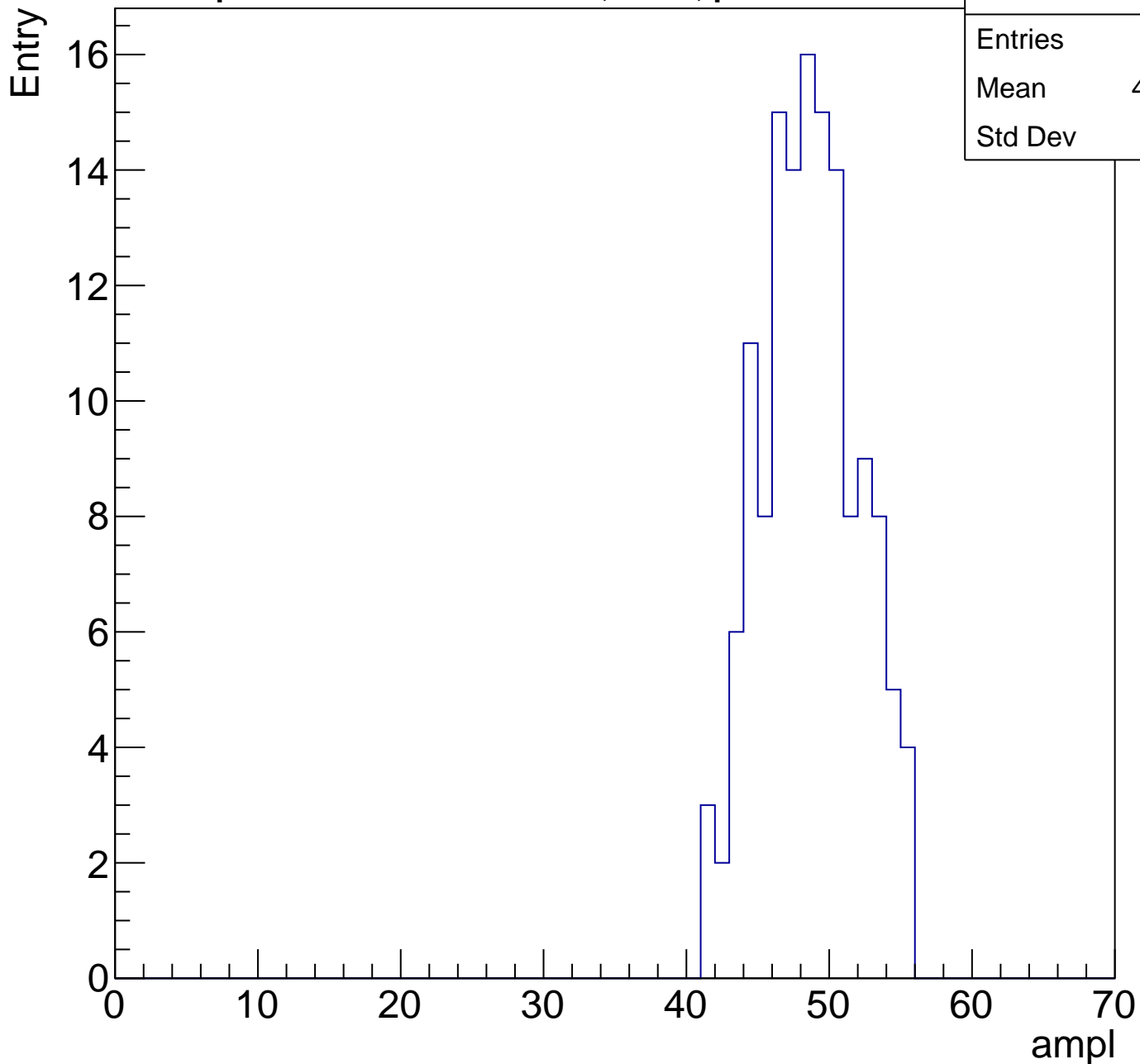
Gaus Width: 3.4224



B1L001S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#2, port C2

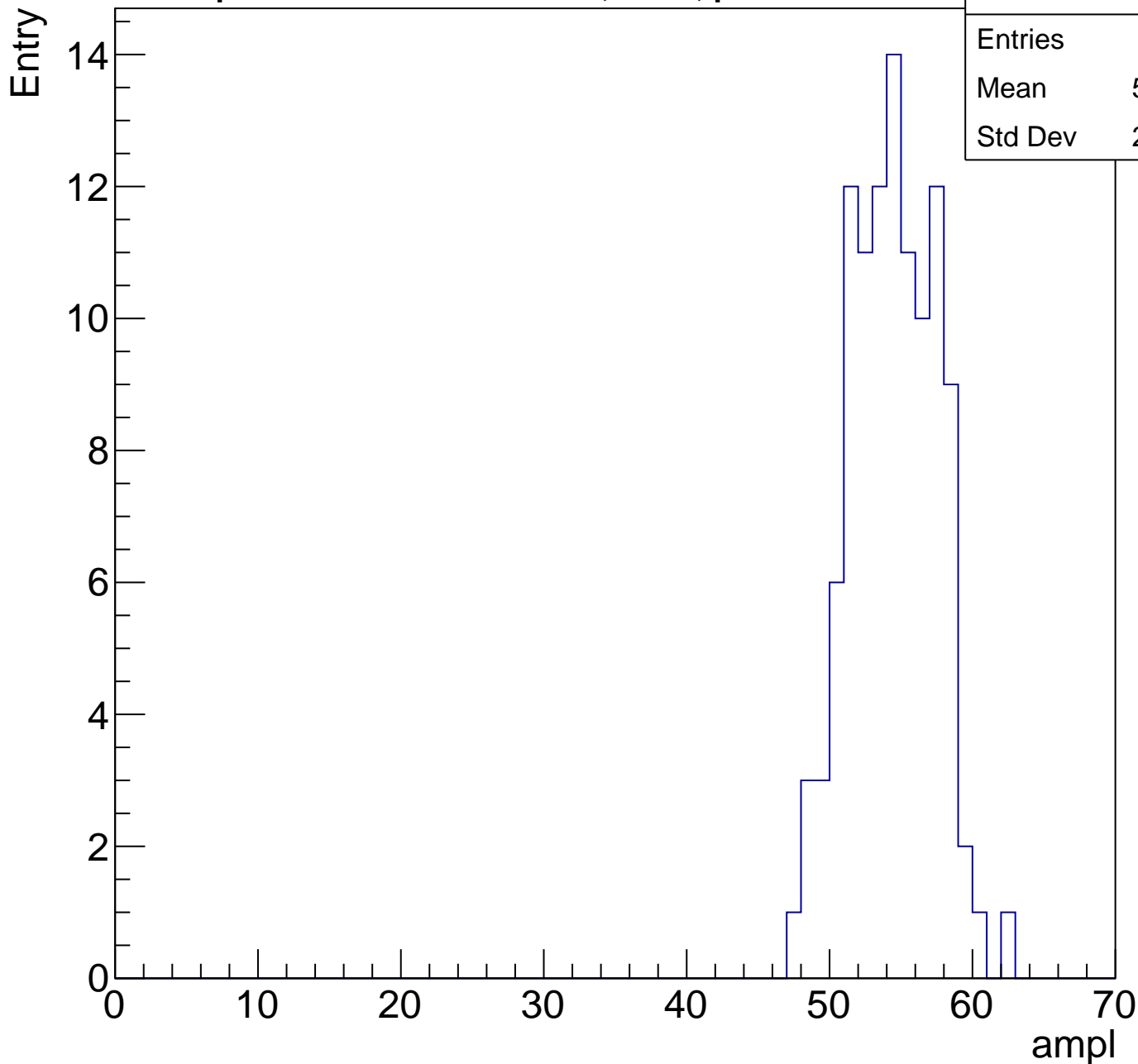
Entries	138
Mean	48.19
Std Dev	3.35



B1L001S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	108
Mean	53.94
Std Dev	2.957



B1L001S, U14-ch124, adc5

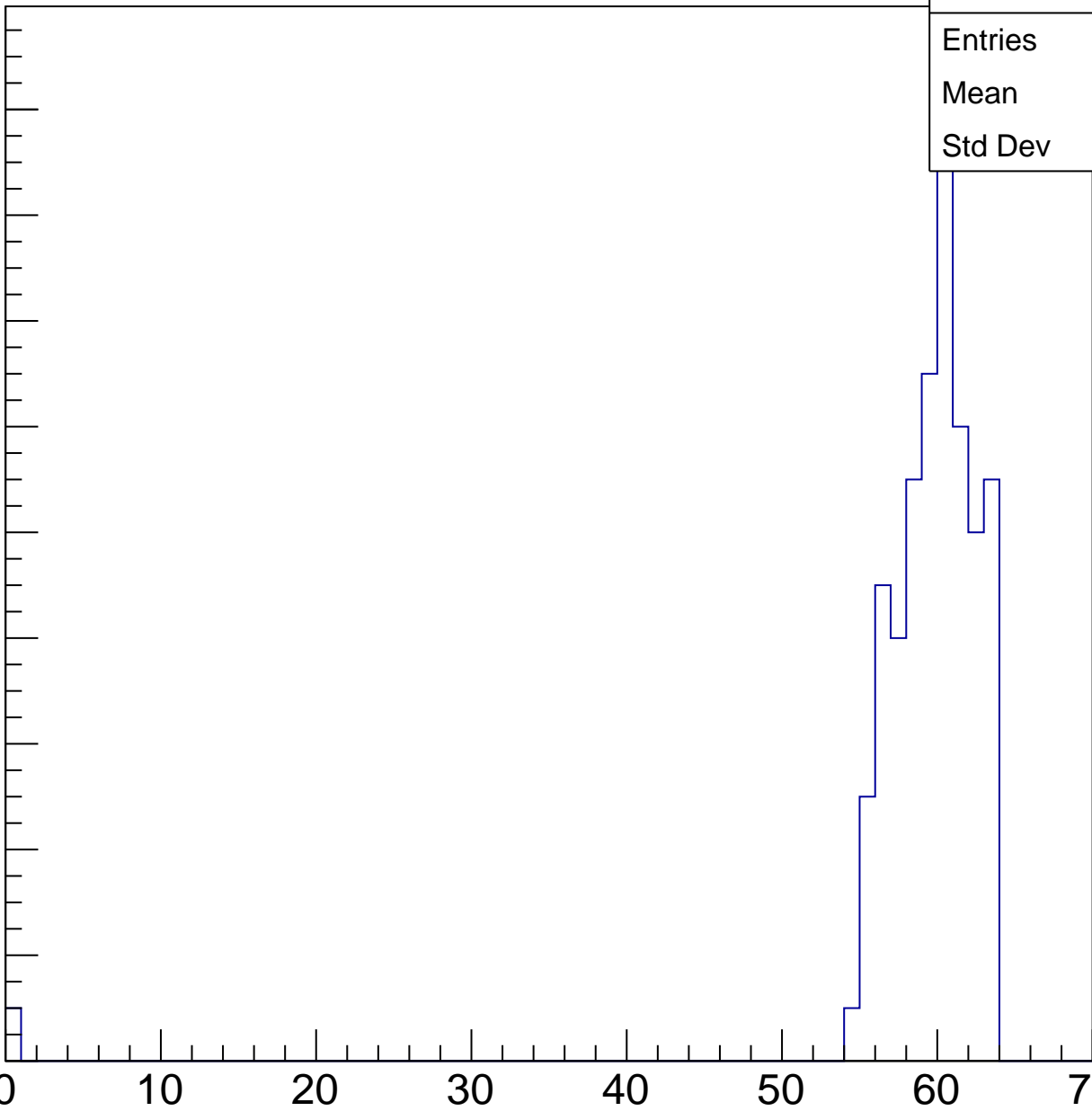
calib_packv5_042523_0143.root, FC#2, port C2

Entry

18
16
14
12
10
8
6
4
2
0

Entries	100
Mean	58.79
Std Dev	6.355

ampl

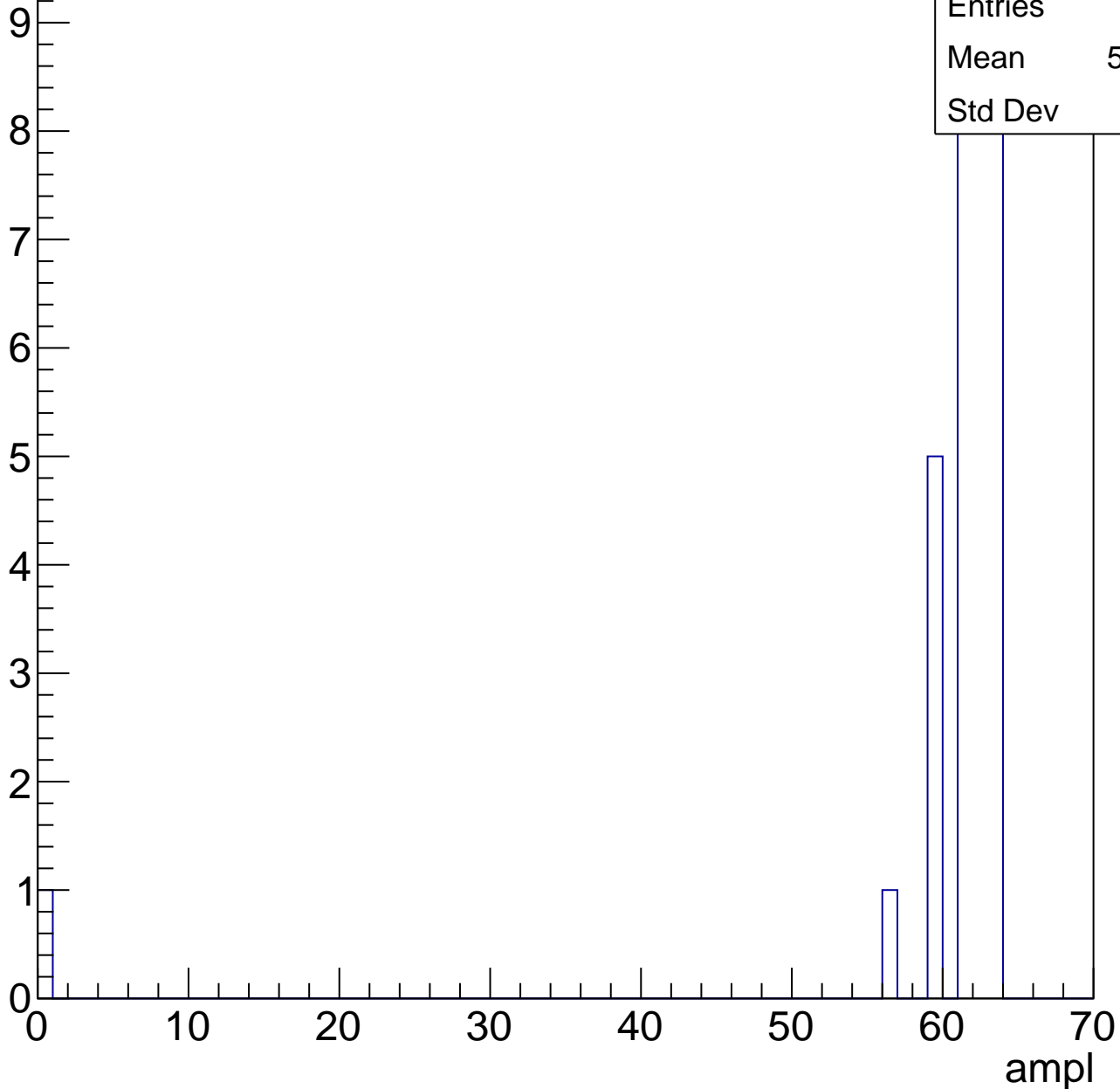


B1L001S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	32
Mean	59.44
Std Dev	10.8



B1L001S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



B1L001S, U14-ch125, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	131
Mean	29.98
Std Dev	4.077

Gaus mean : 31.0873

Gaus Width: 2.9112

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

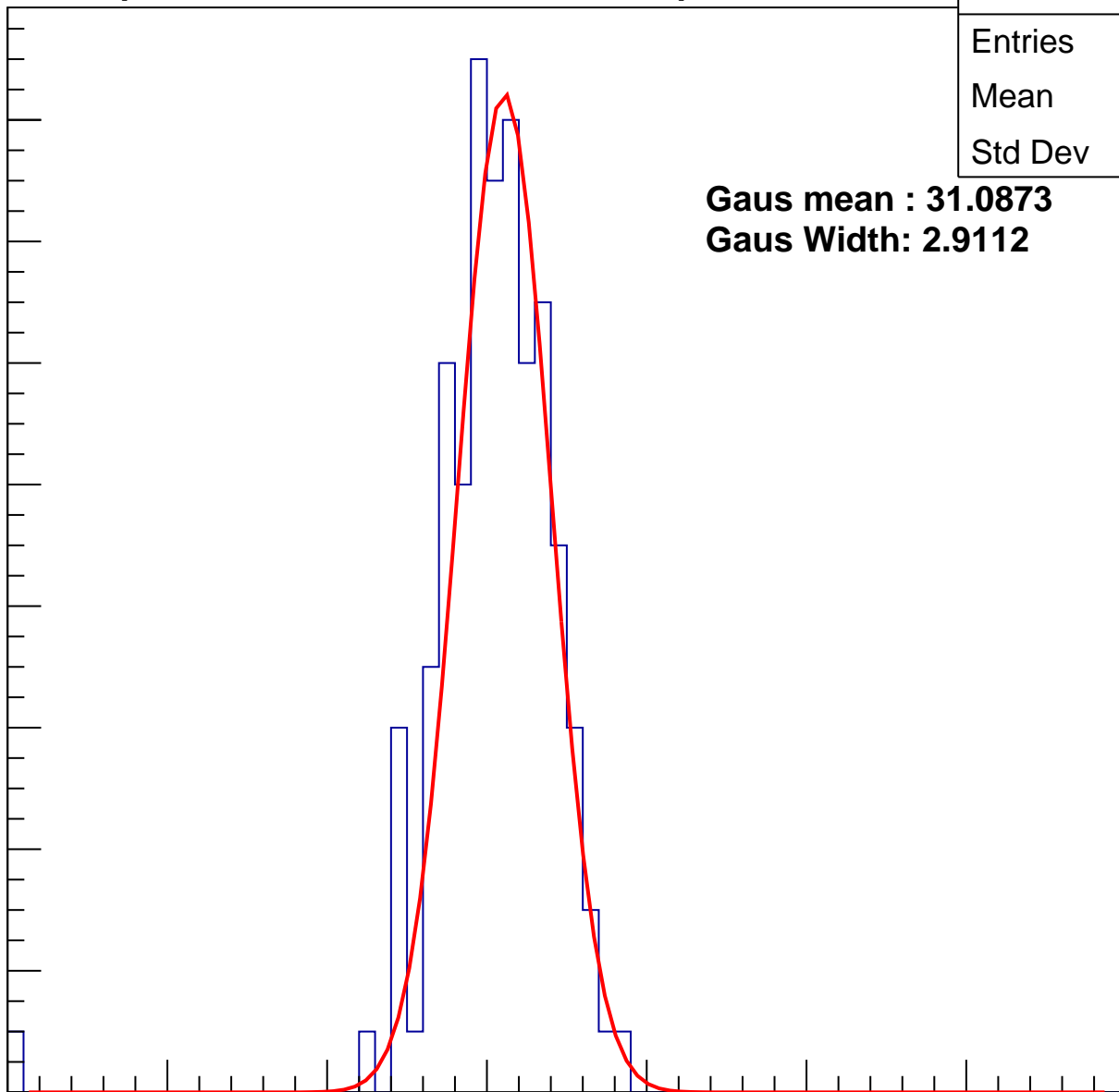
40

50

60

70

ampl



B1L001S, U14-ch125, adc1

calib_packv5_042523_0143.root, FC#2, port C2

Entries	116
Mean	36.15
Std Dev	3.102

Gaus mean : 36.6910

Gaus Width: 2.8052

Entry

16
14
12
10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L001S, U14-ch125, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	126
Mean	42.81
Std Dev	3.282

Gaus mean : 43.7697

Gaus Width: 3.1791

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

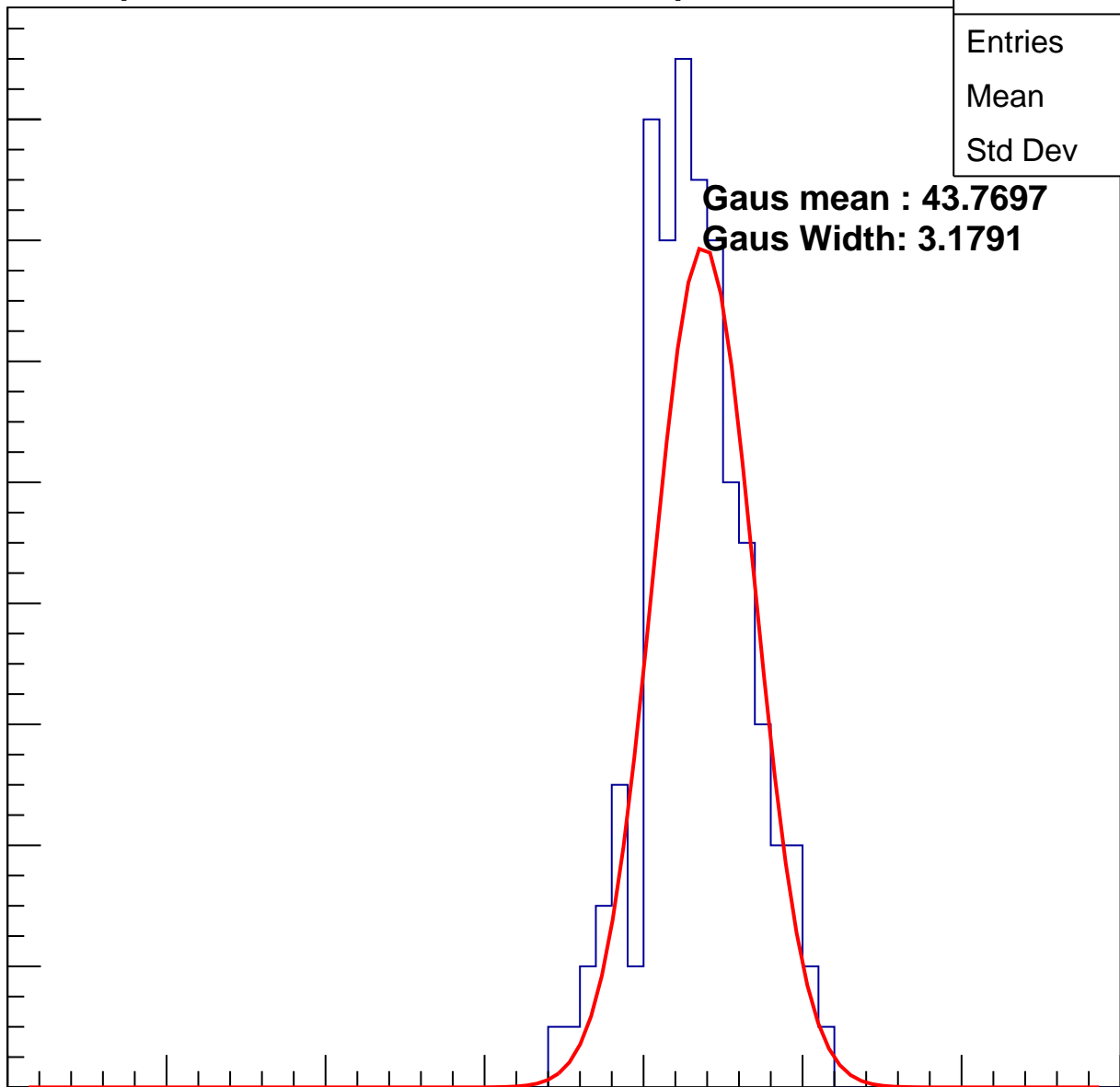
40

50

60

70

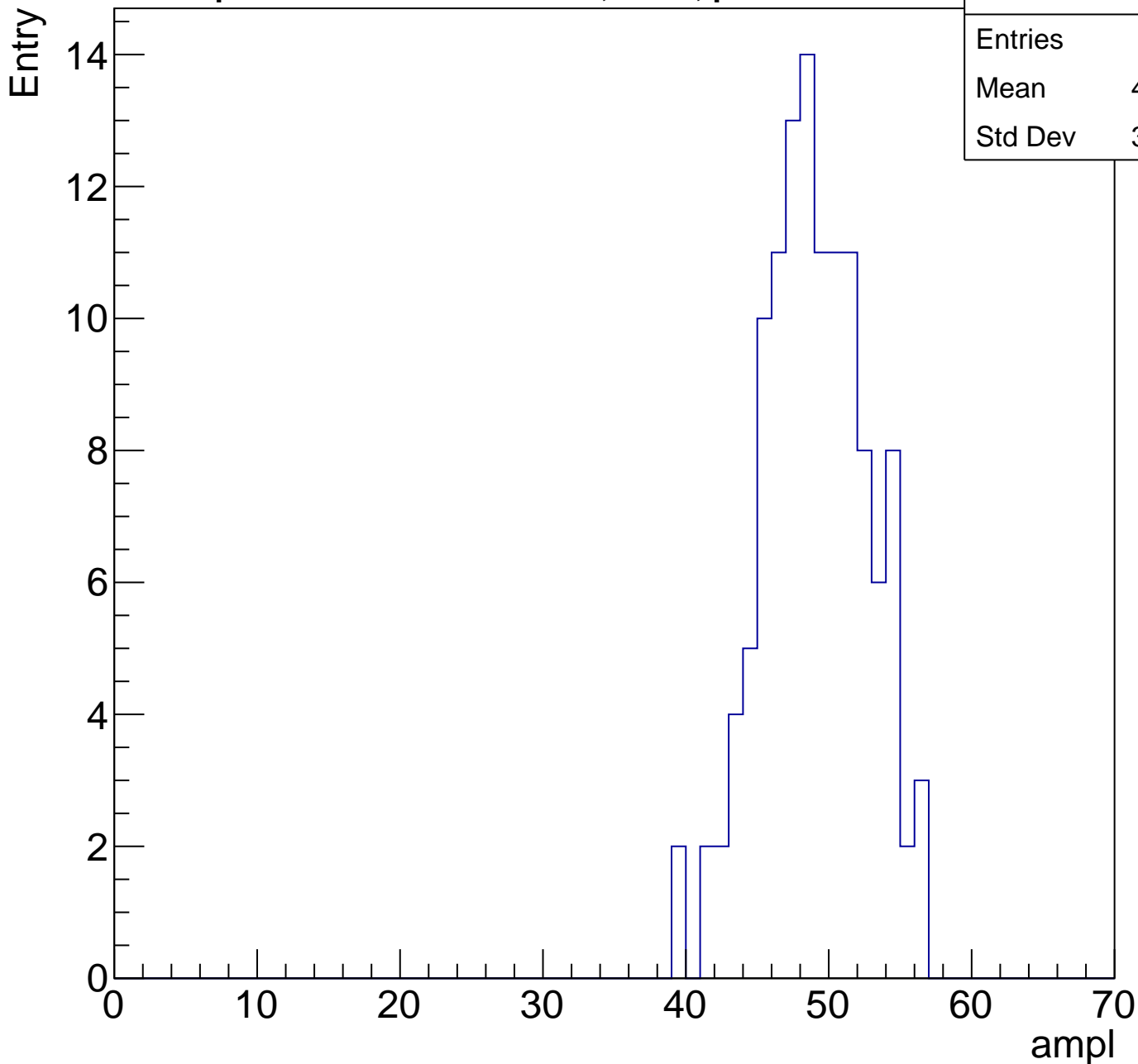
ampl



B1L001S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	123
Mean	48.53
Std Dev	3.646



B1L001S, U14-ch125, adc4

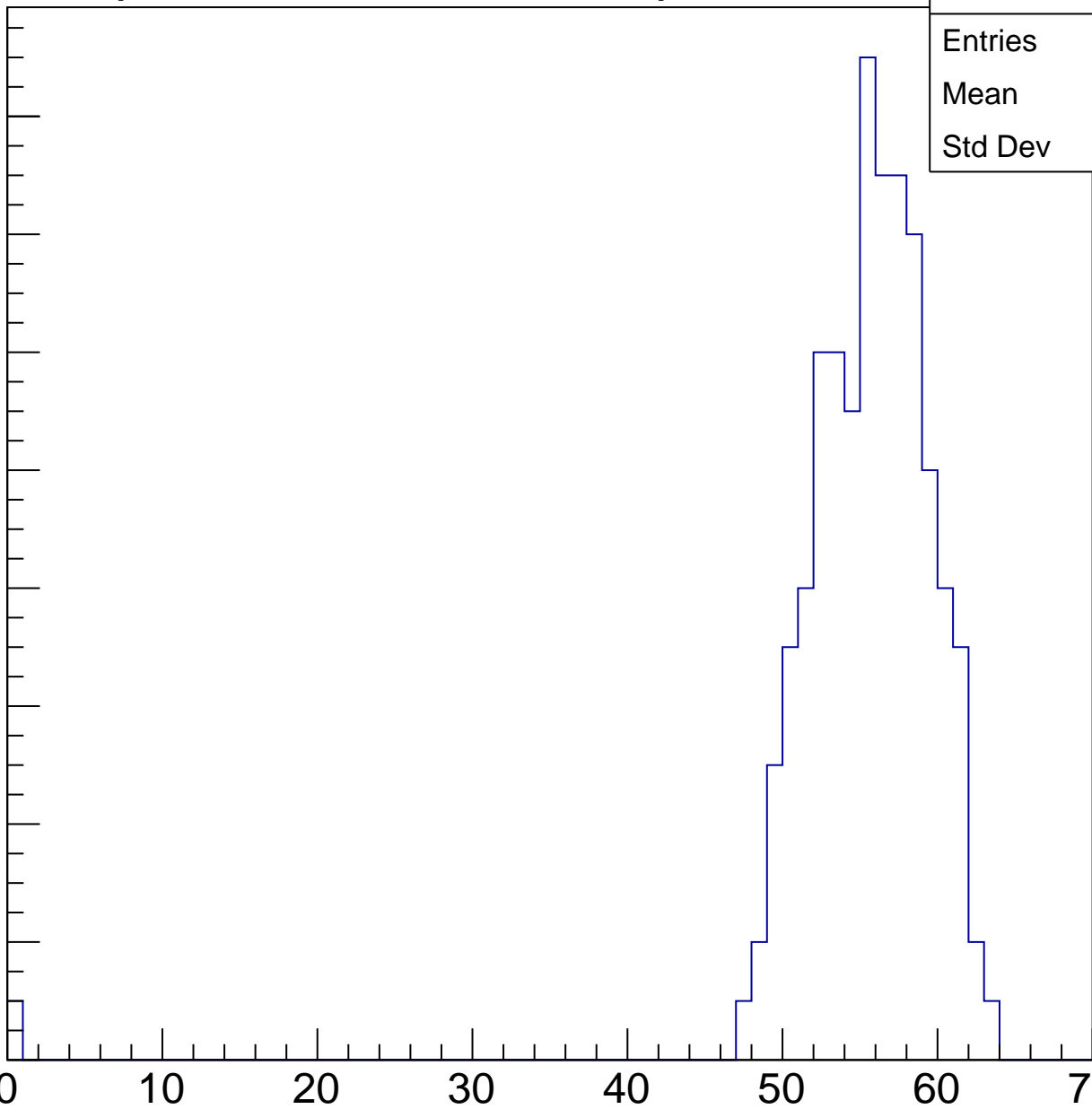
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	148
Mean	54.91
Std Dev	5.701

ampl



B1L001S, U14-ch125, adc5

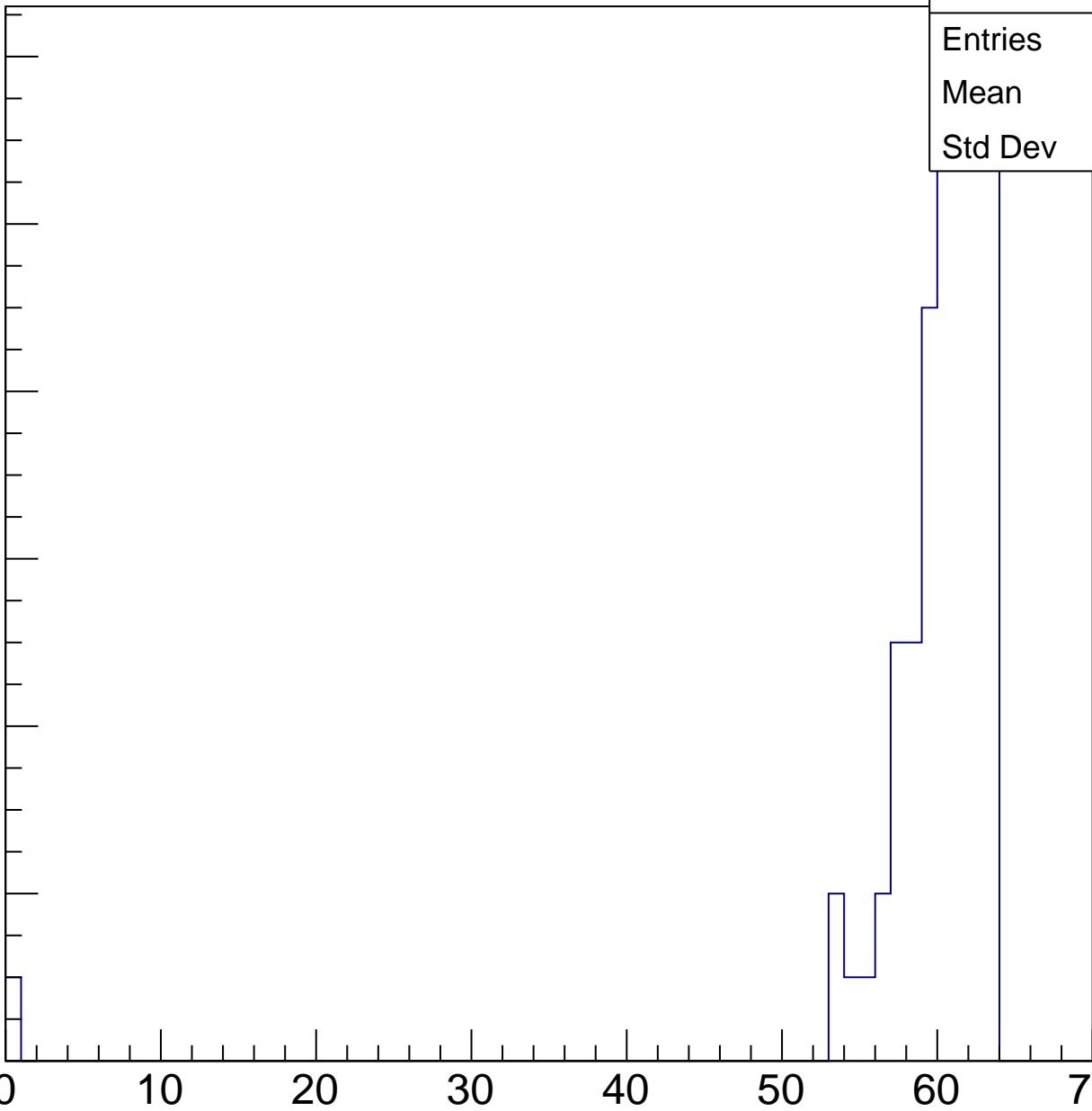
calib_packv5_042523_0143.root, FC#2, port C2

Entry

12
10
8
6
4
2
0

Entries	71
Mean	59.15
Std Dev	7.477

ampl

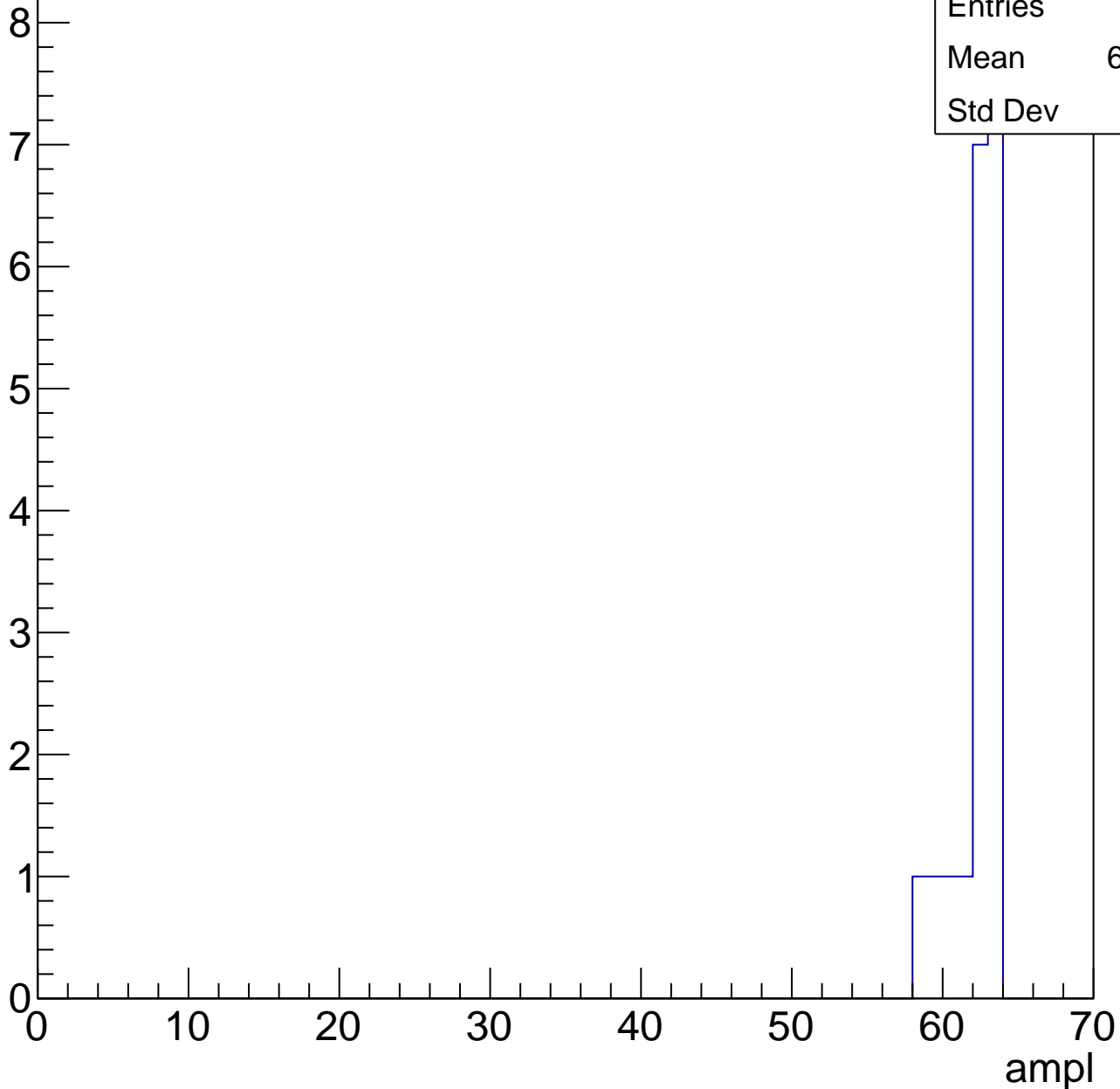


B1L001S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	19
Mean	61.89
Std Dev	1.41



B1L001S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U14-ch126, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	129
Mean	28.54
Std Dev	3.301

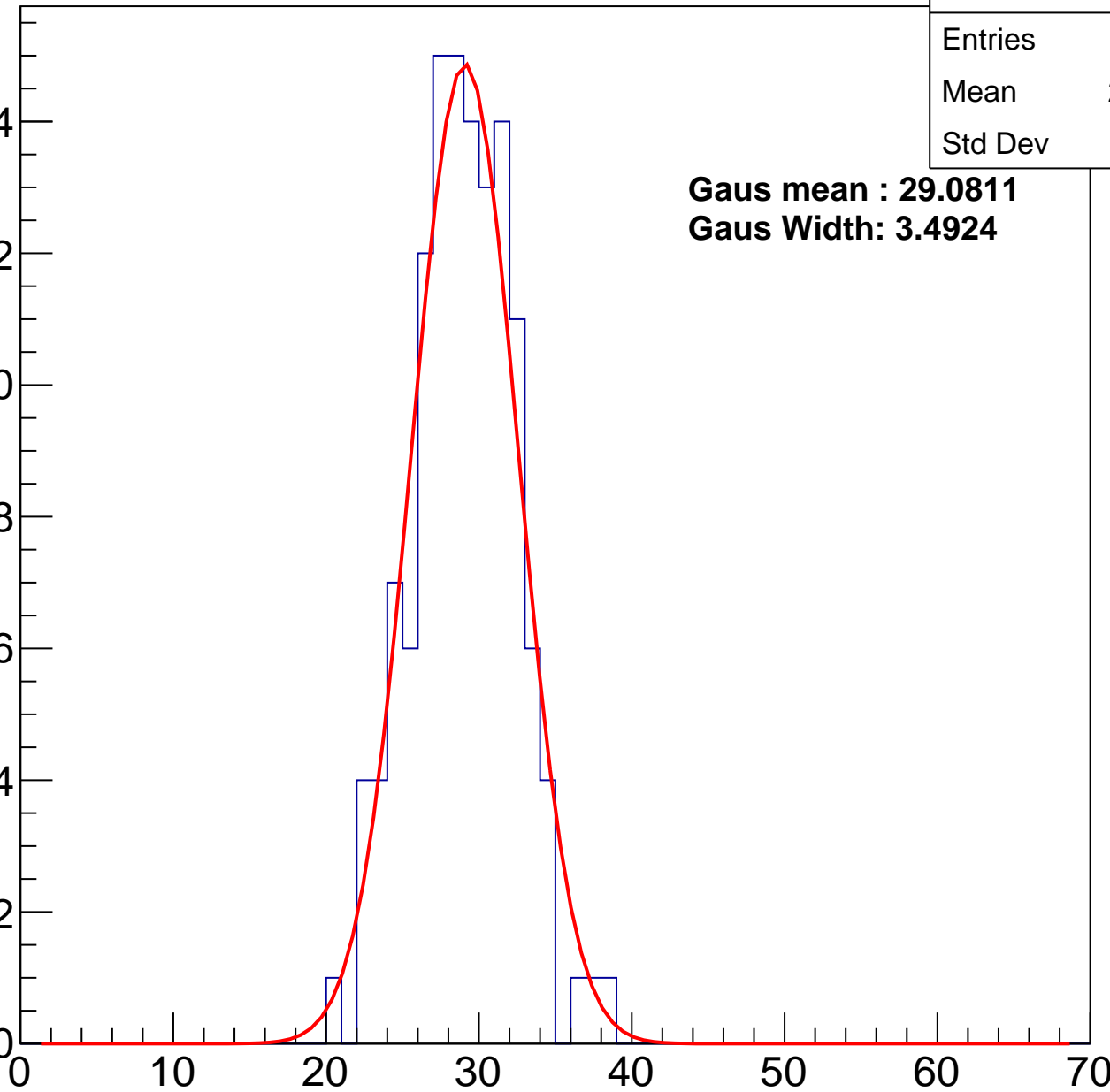
Gaus mean : 29.0811

Gaus Width: 3.4924

Entry

14
12
10
8
6
4
2
0

ampl



B1L001S, U14-ch126, adc1

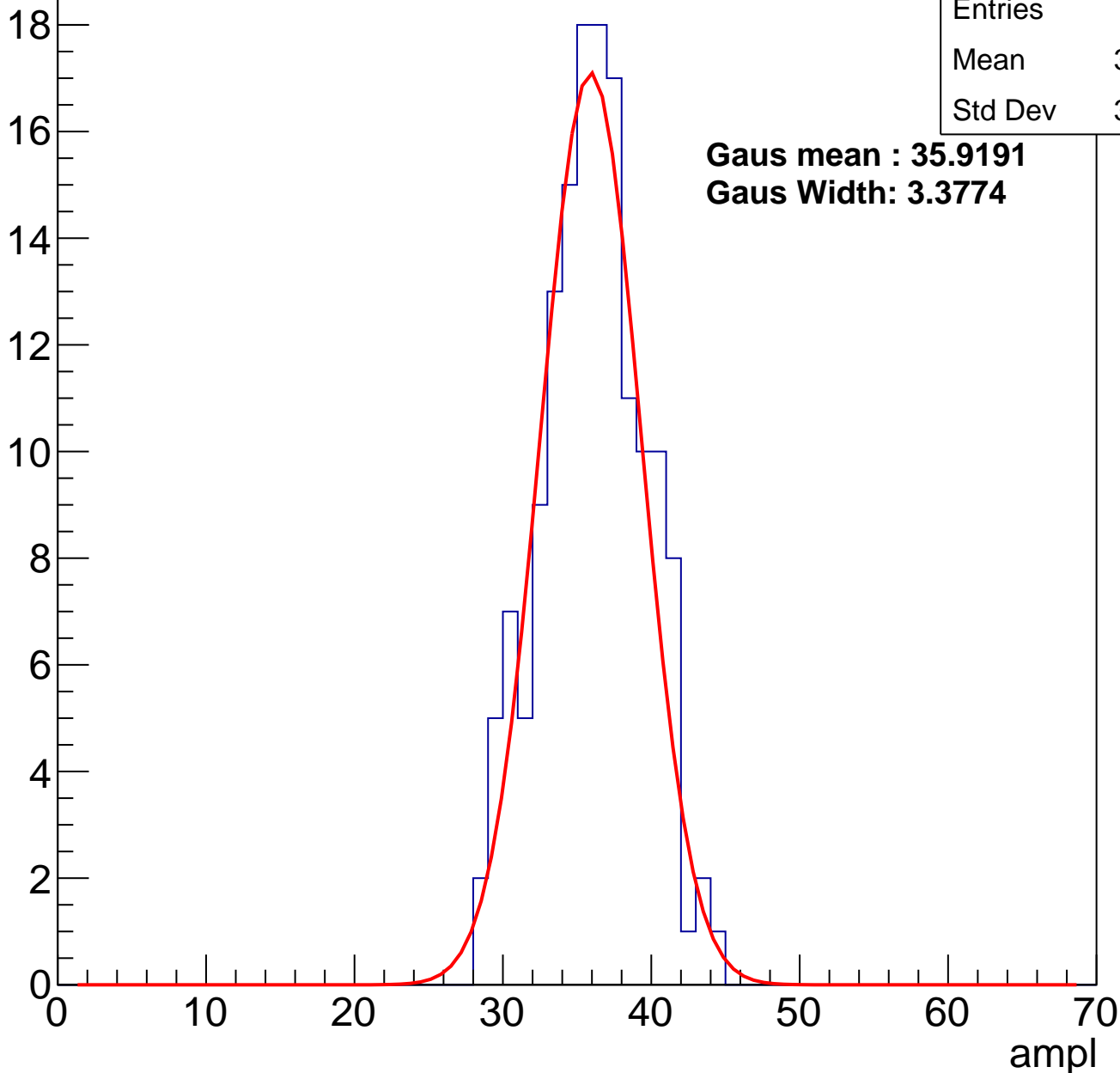
calib_packv5_042523_0143.root, FC#2, port C2

Entries	152
Mean	35.58
Std Dev	3.413

Gaus mean : 35.9191

Gaus Width: 3.3774

Entry



B1L001S, U14-ch126, adc2

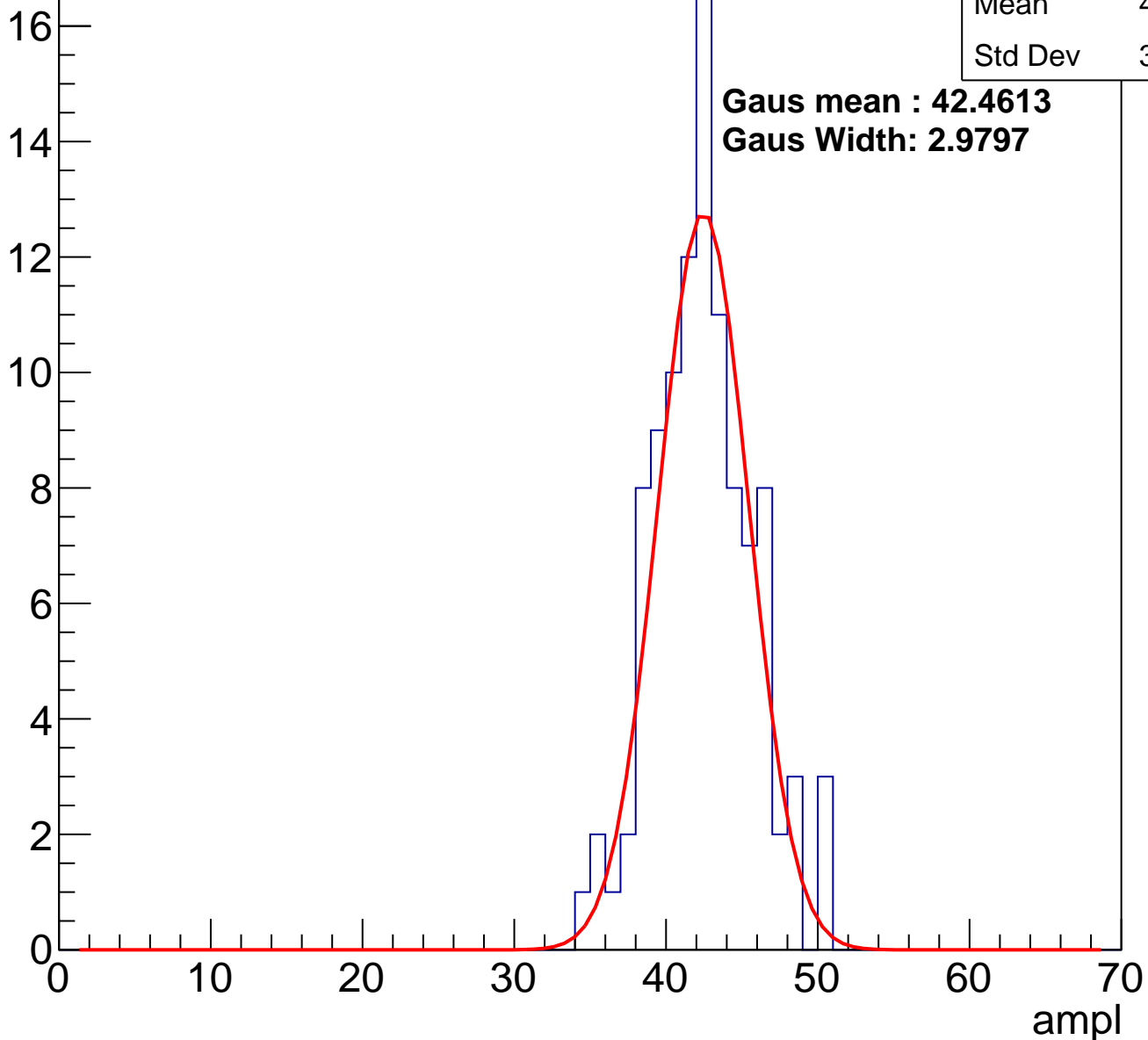
calib_packv5_042523_0143.root, FC#2, port C2

Entry

Entries	104
Mean	42.03
Std Dev	3.242

Gaus mean : 42.4613

Gaus Width: 2.9797

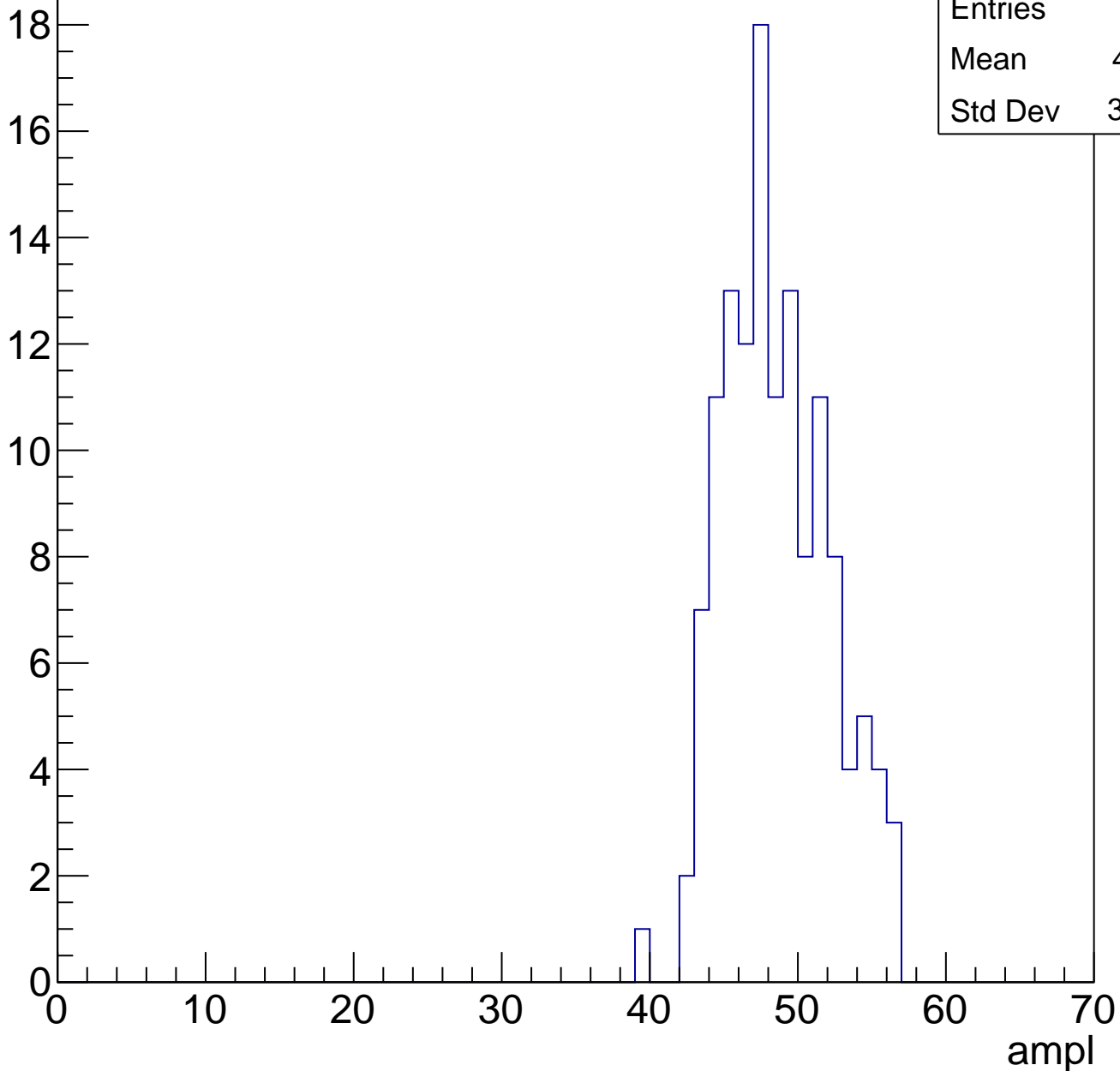


B1L001S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entry

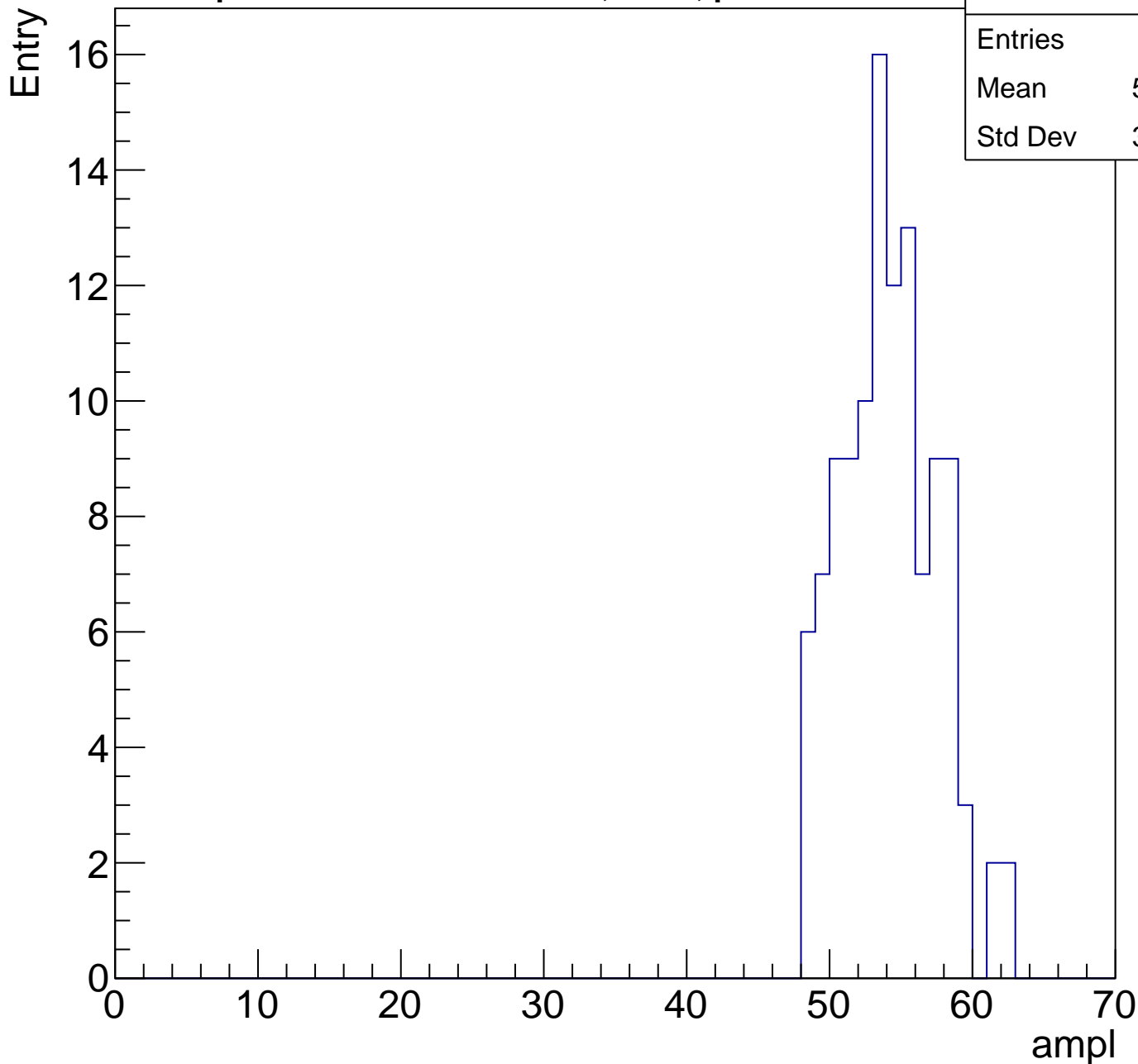
Entries	131
Mean	48.11
Std Dev	3.522



B1L001S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	53.69
Std Dev	3.282



B1L001S, U14-ch126, adc5

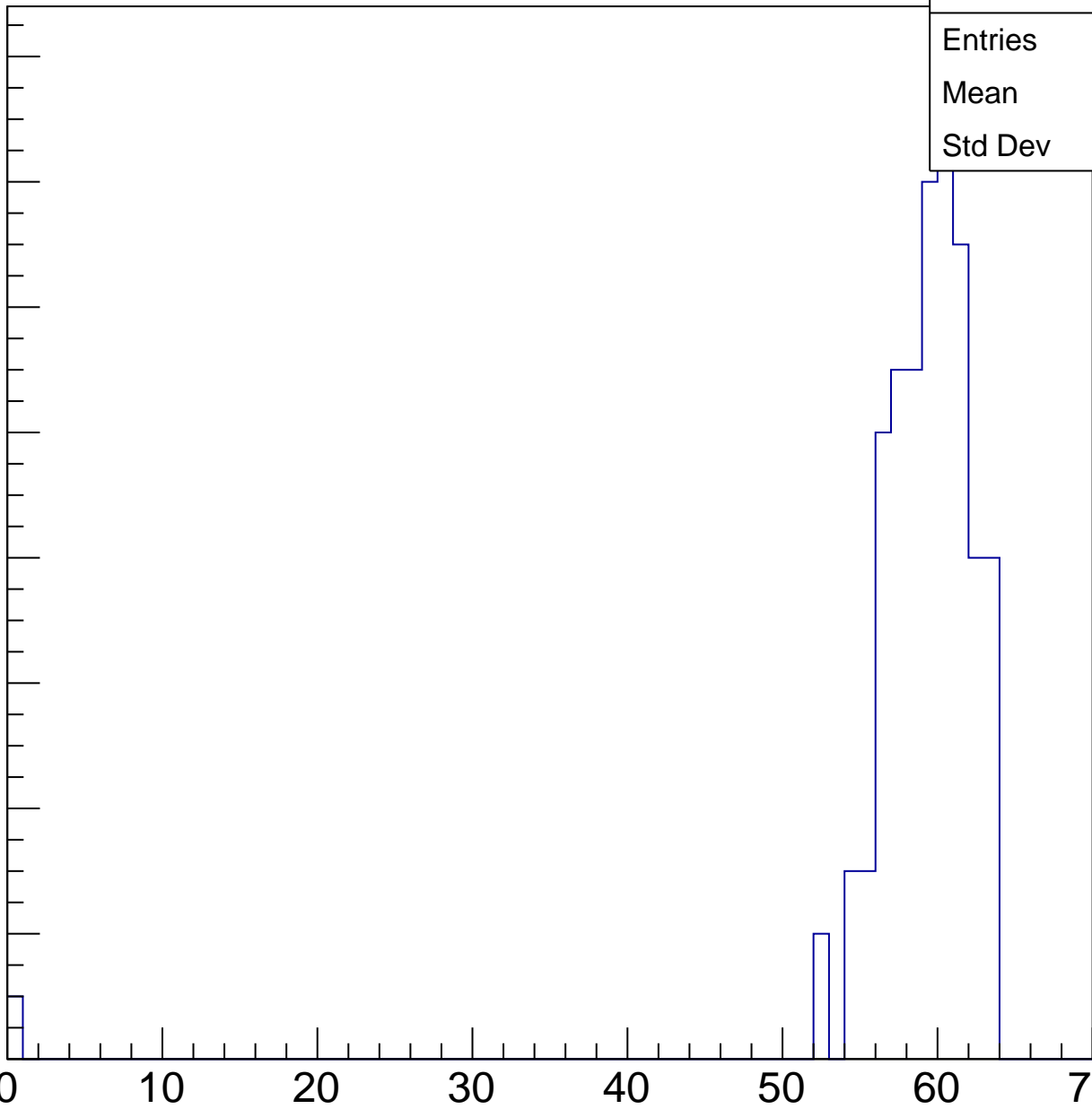
calib_packv5_042523_0143.root, FC#2, port C2

Entry

16
14
12
10
8
6
4
2
0

Entries	100
Mean	58.35
Std Dev	6.382

ampl



B1L001S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entries	34
Mean	59.85
Std Dev	10.5

Entry

10

8

6

4

2

0

0

10

20

30

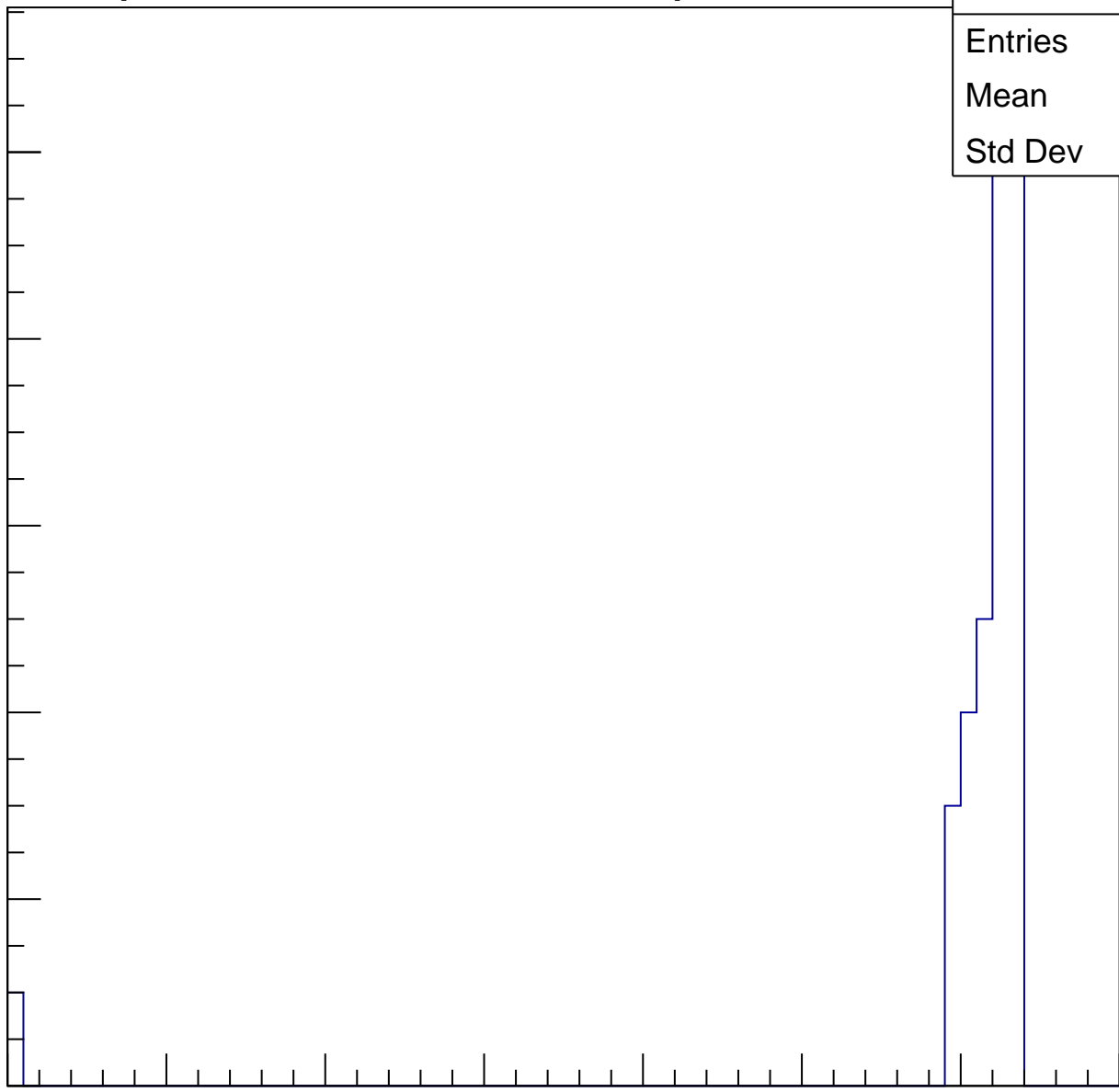
40

50

60

70

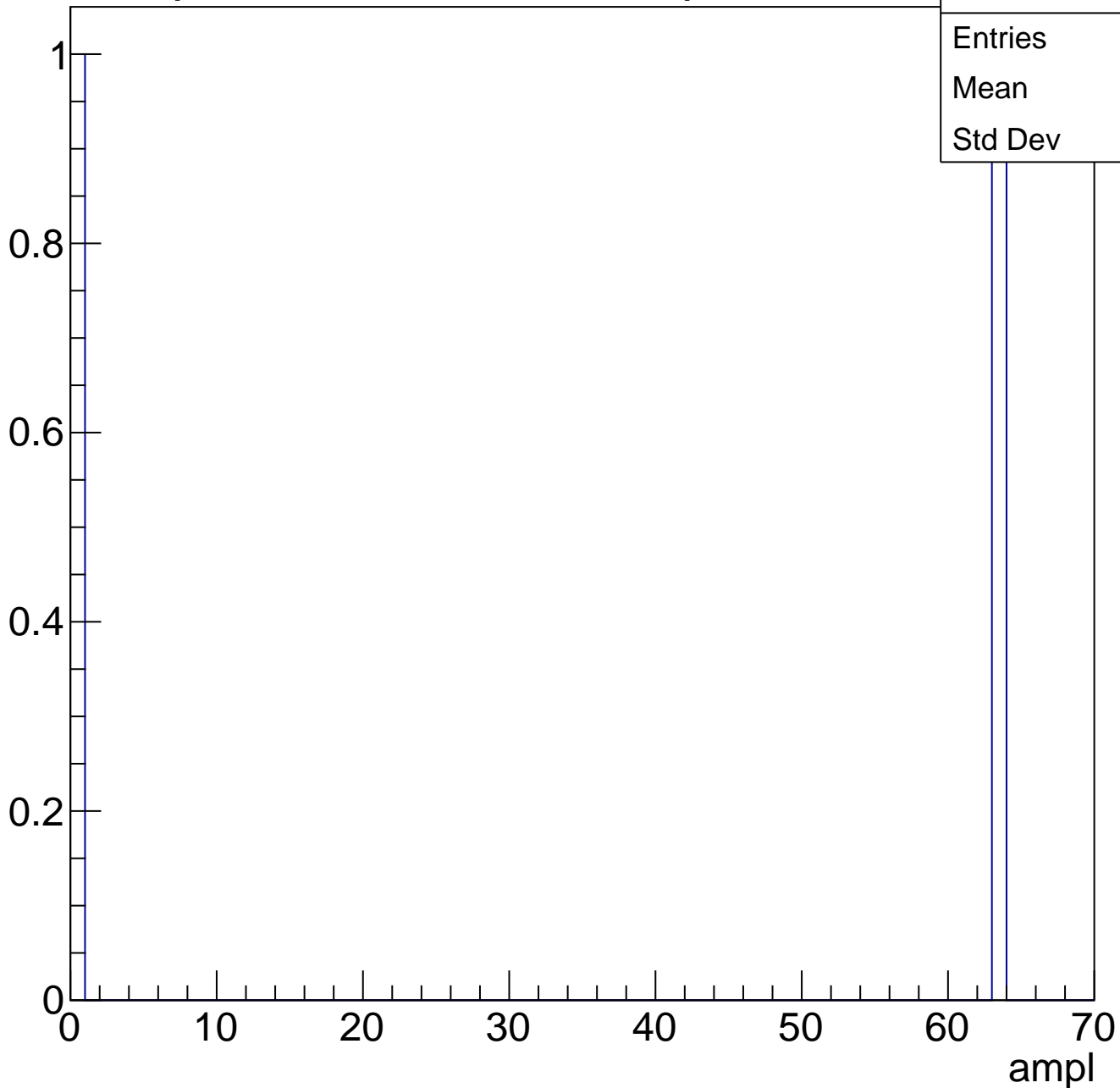
ampl



B1L001S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



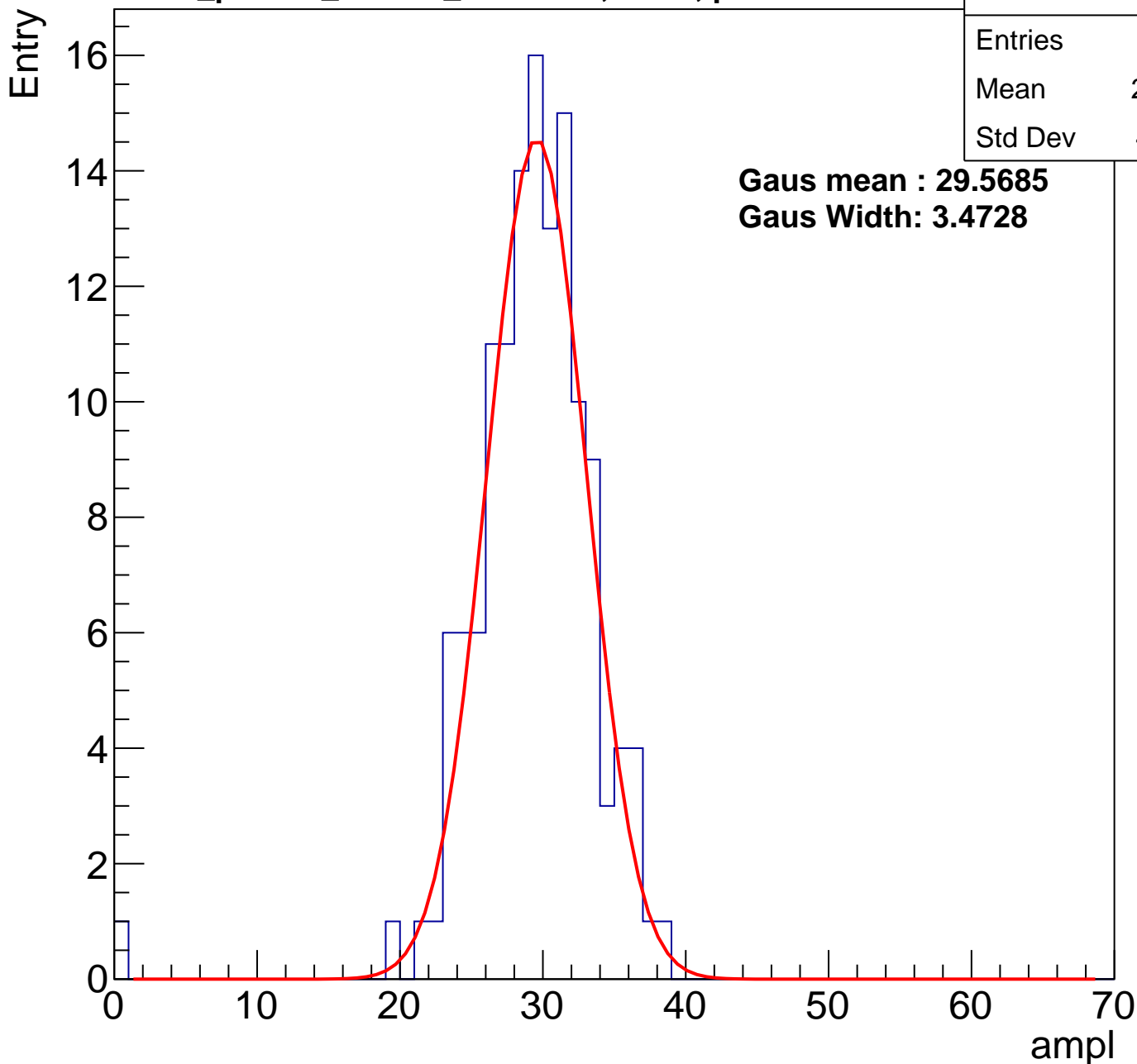
B1L001S, U14-ch127, adc0

calib_packv5_042523_0143.root, FC#2, port C2

Entries	134
Mean	28.85
Std Dev	4.351

Gaus mean : 29.5685

Gaus Width: 3.4728



B1L001S, U14-ch127, adc1

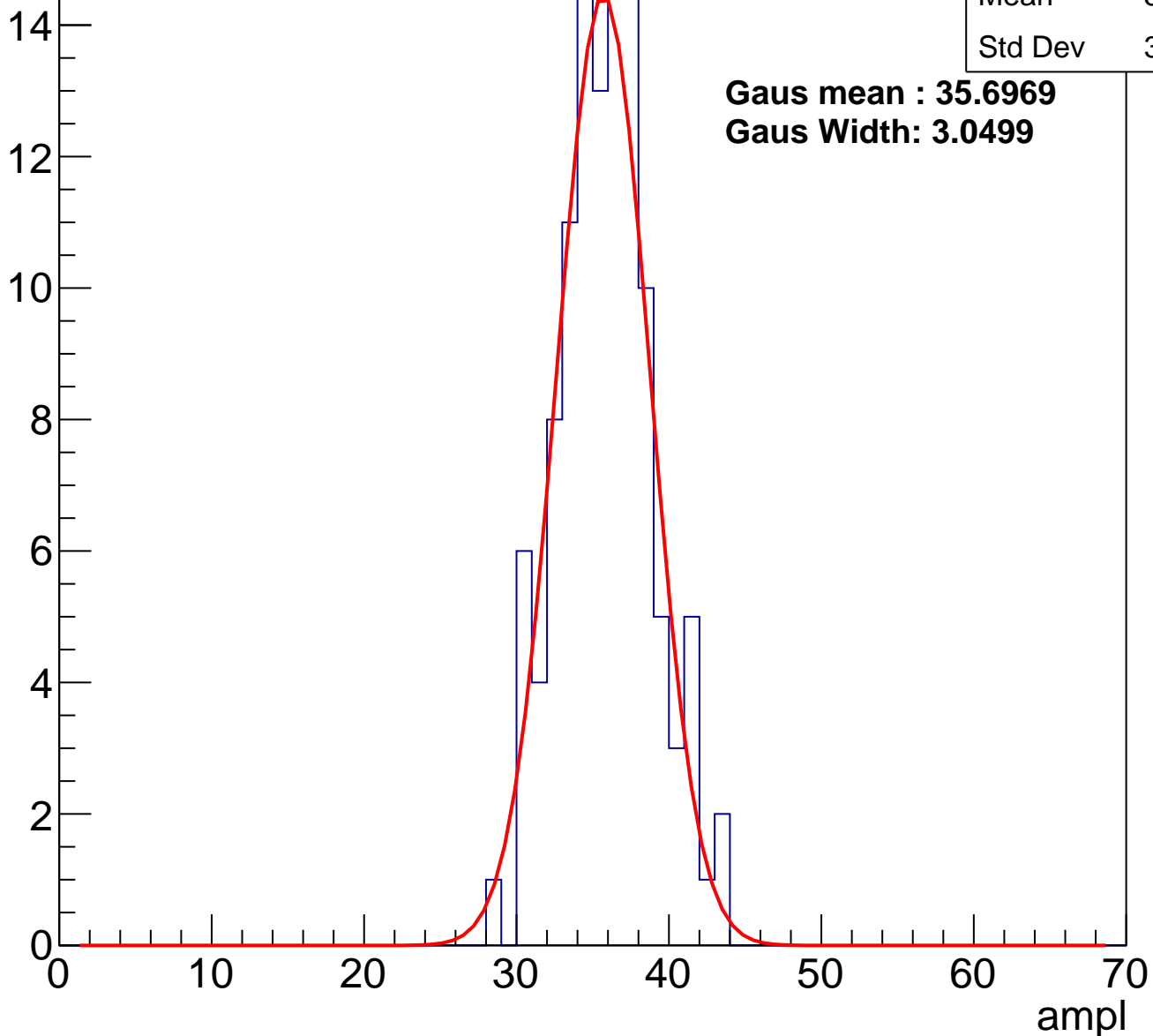
calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	35.43
Std Dev	3.035

Gaus mean : 35.6969

Gaus Width: 3.0499

Entry



B1L001S, U14-ch127, adc2

calib_packv5_042523_0143.root, FC#2, port C2

Entries	144
Mean	42.12
Std Dev	3.691

Gaus mean : 42.7019

Gaus Width: 3.3865

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

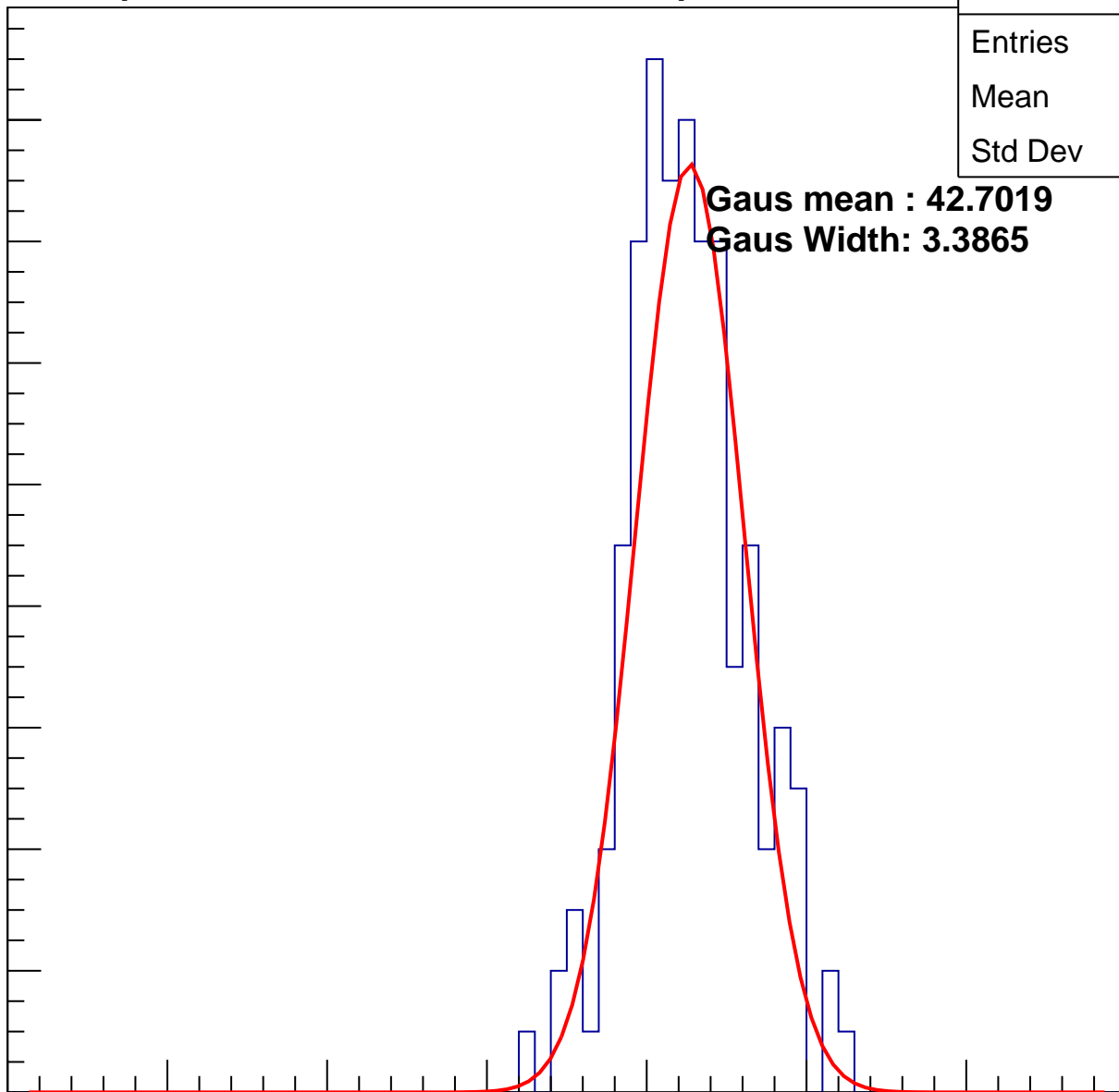
40

50

60

70

ampl

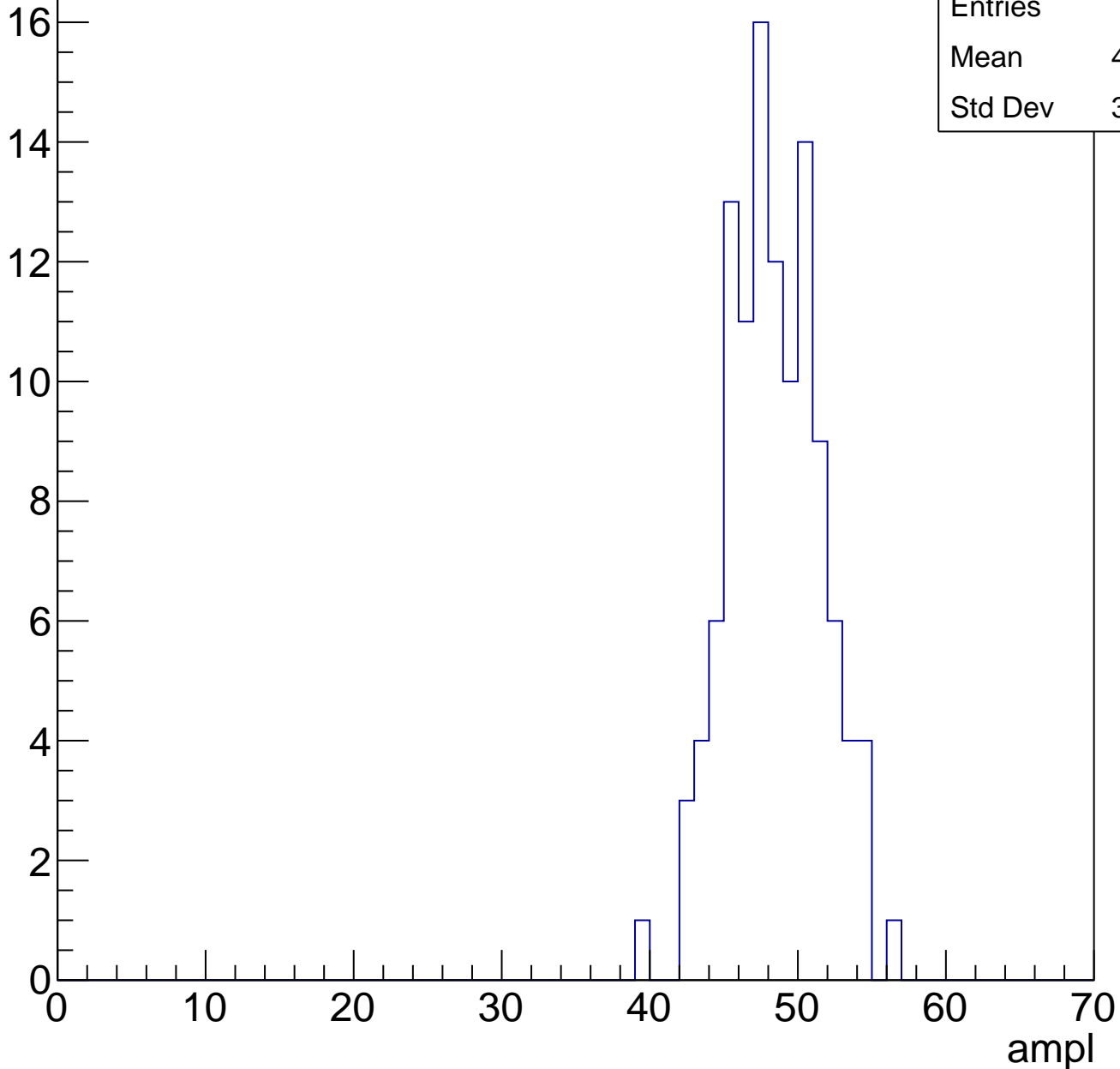


B1L001S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#2, port C2

Entries	114
Mean	47.94
Std Dev	3.124

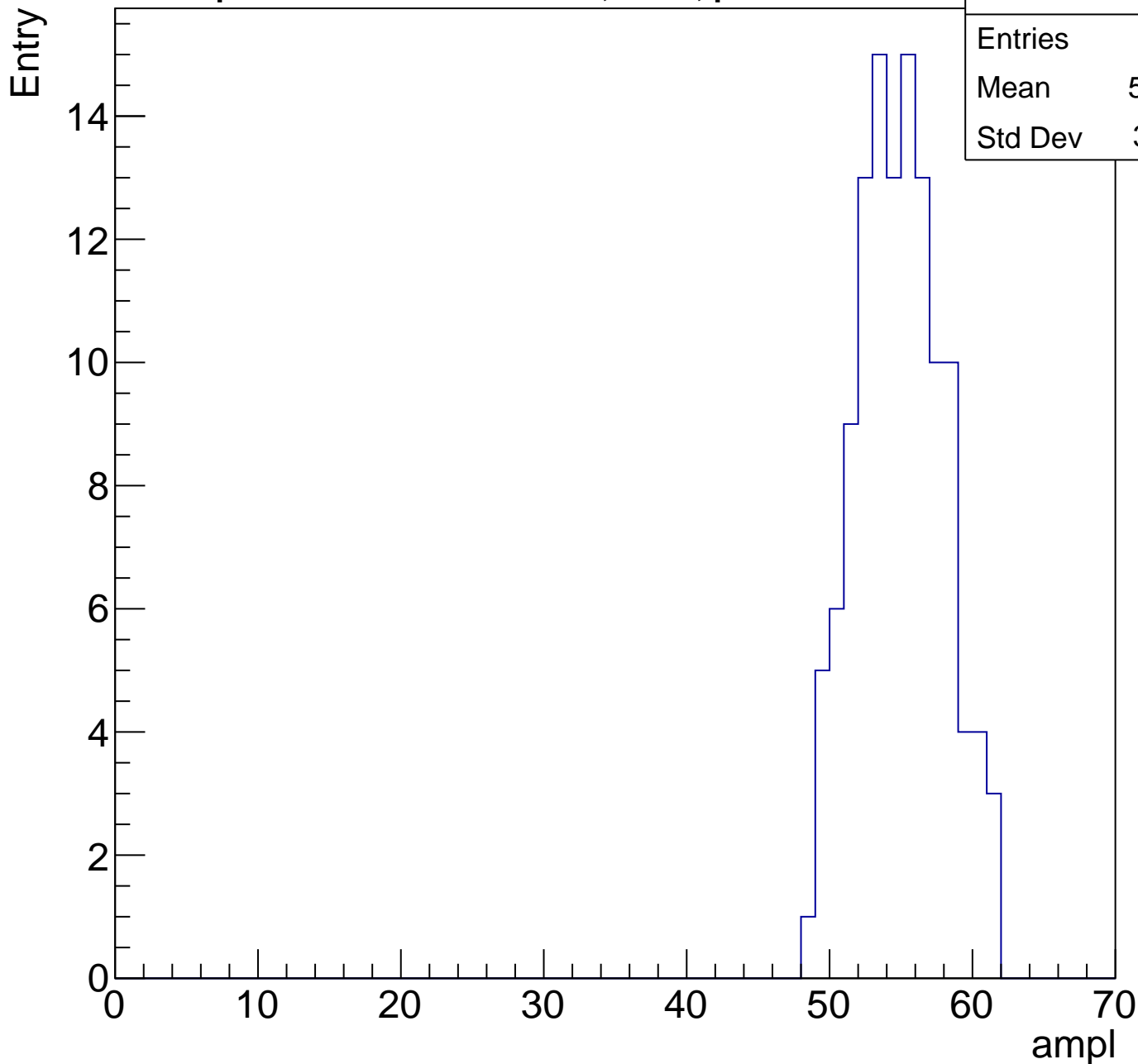
Entry



B1L001S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#2, port C2

Entries	121
Mean	54.44
Std Dev	3.001



B1L001S, U14-ch127, adc5

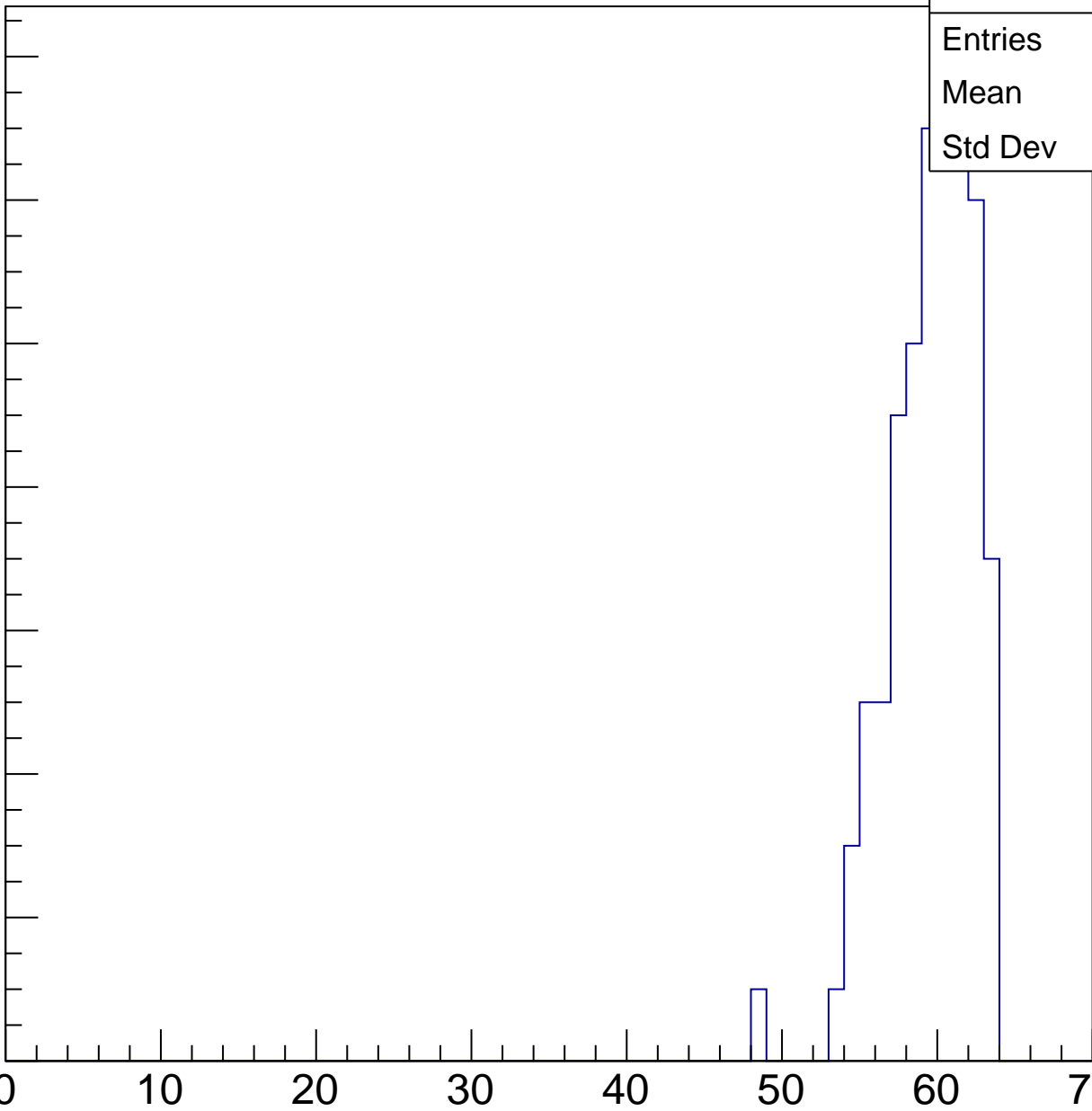
calib_packv5_042523_0143.root, FC#2, port C2

Entry

14
12
10
8
6
4
2
0

Entries	93
Mean	59.1
Std Dev	2.72

ampl



B1L001S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#2, port C2

Entry

12

10

8

6

4

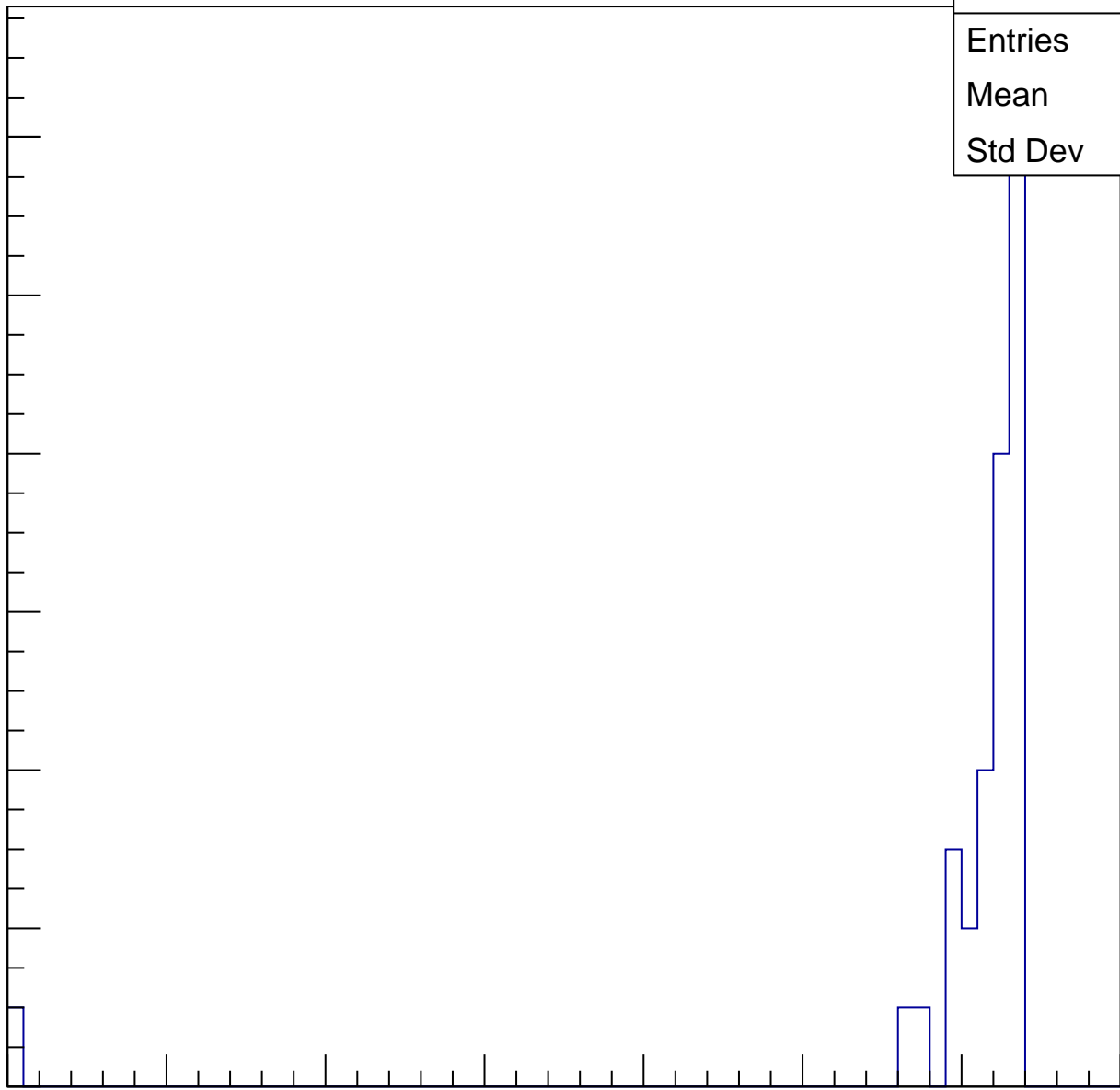
2

0

Entries	33
Mean	59.67
Std Dev	10.7

ampl

0 10 20 30 40 50 60 70



B1L001S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L001S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#2, port C2

Entry



Entries	1
Mean	0
Std Dev	0