

B1L000S, U24-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.9052

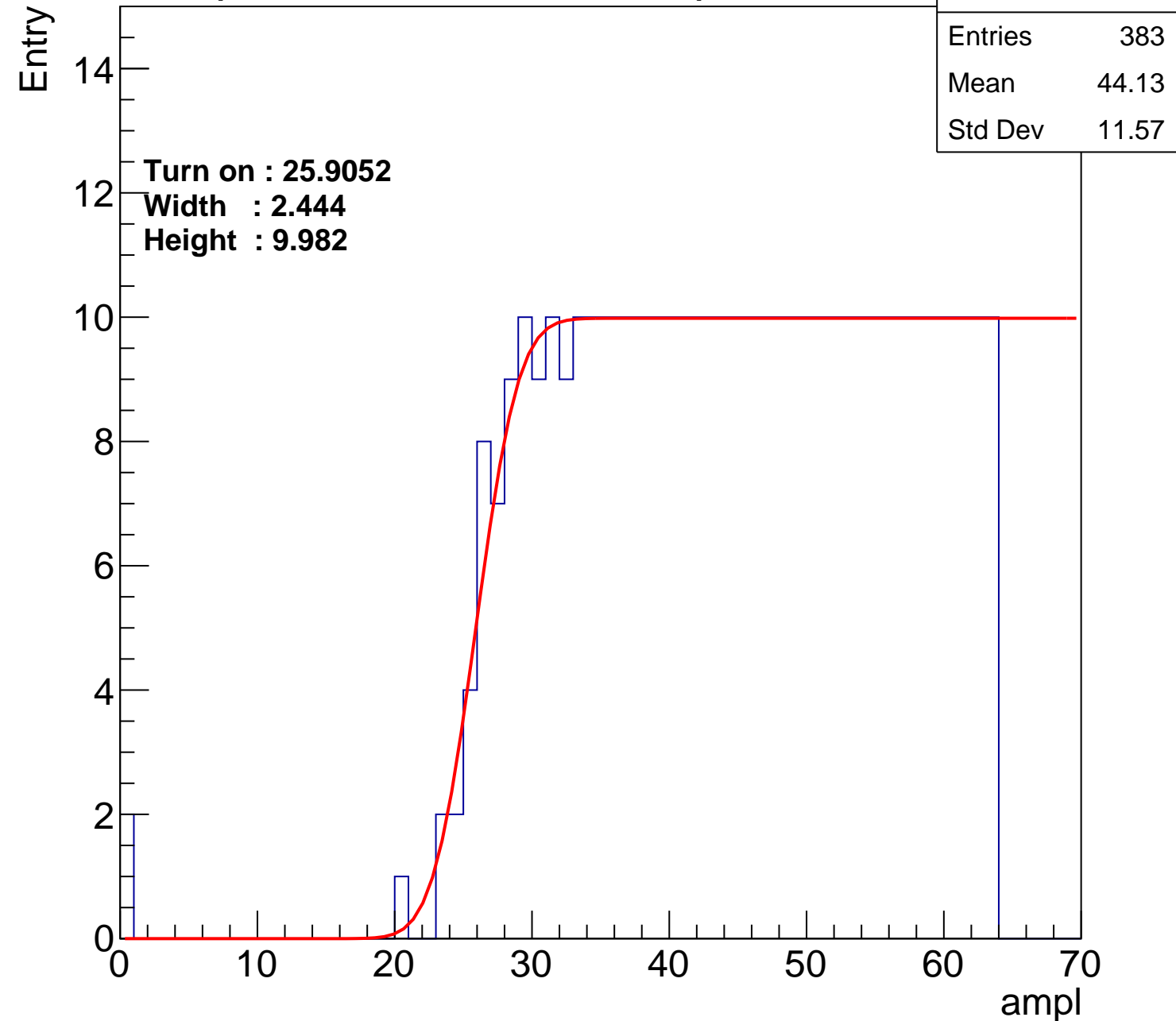
Width : 2.444

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.75

Turn on : 25.5743

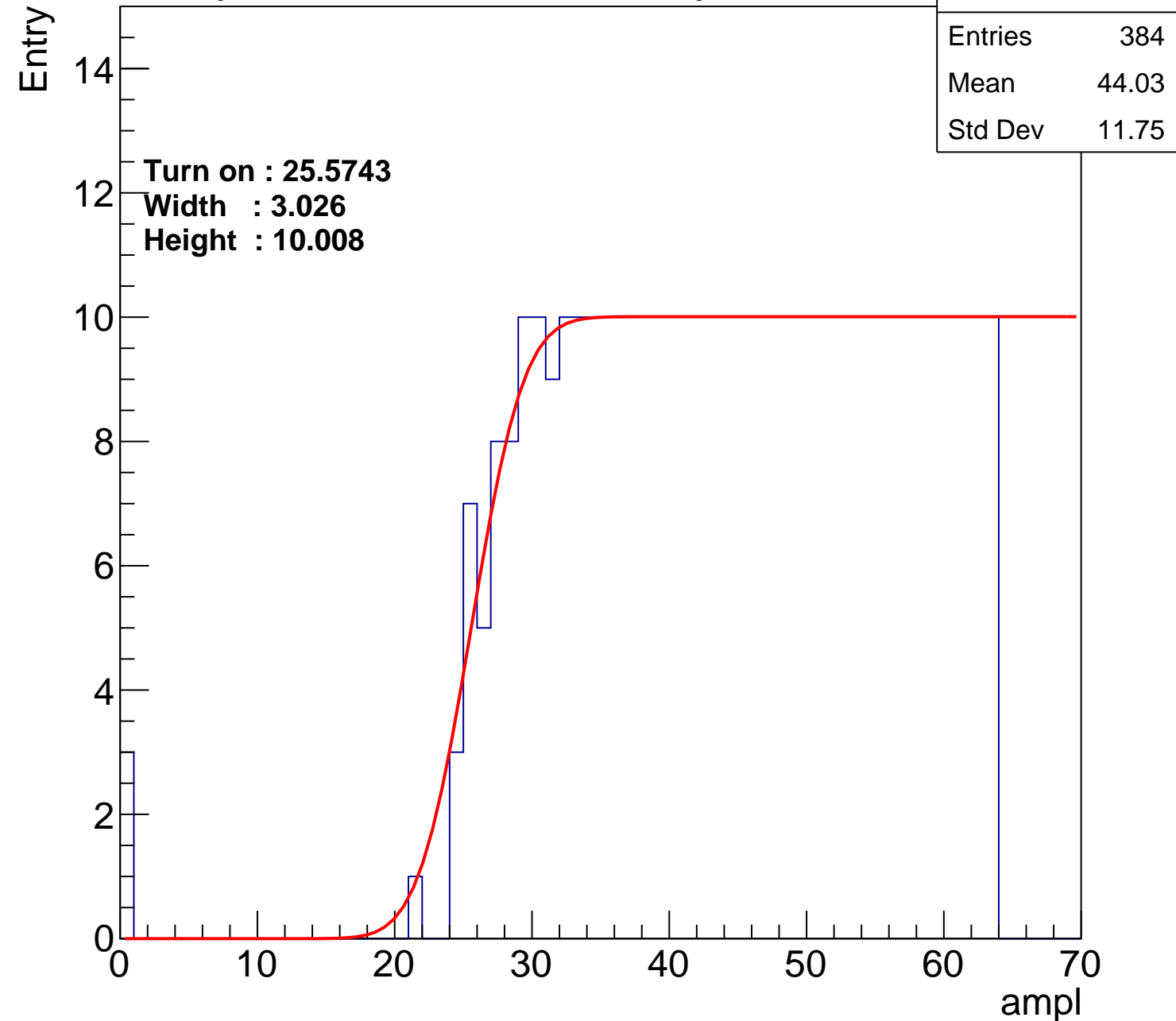
Width : 3.026

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.4396

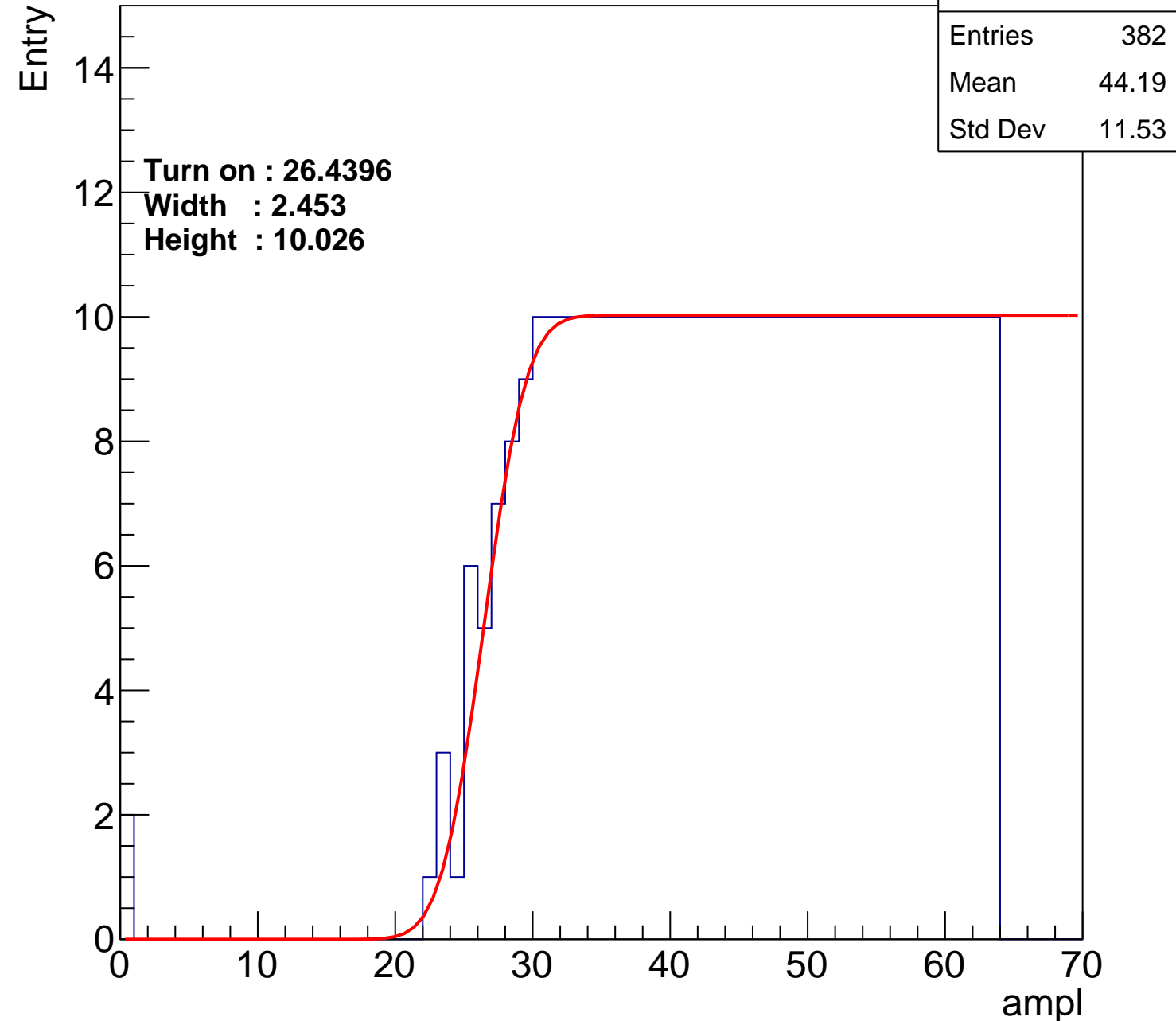
Width : 2.453

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.9322

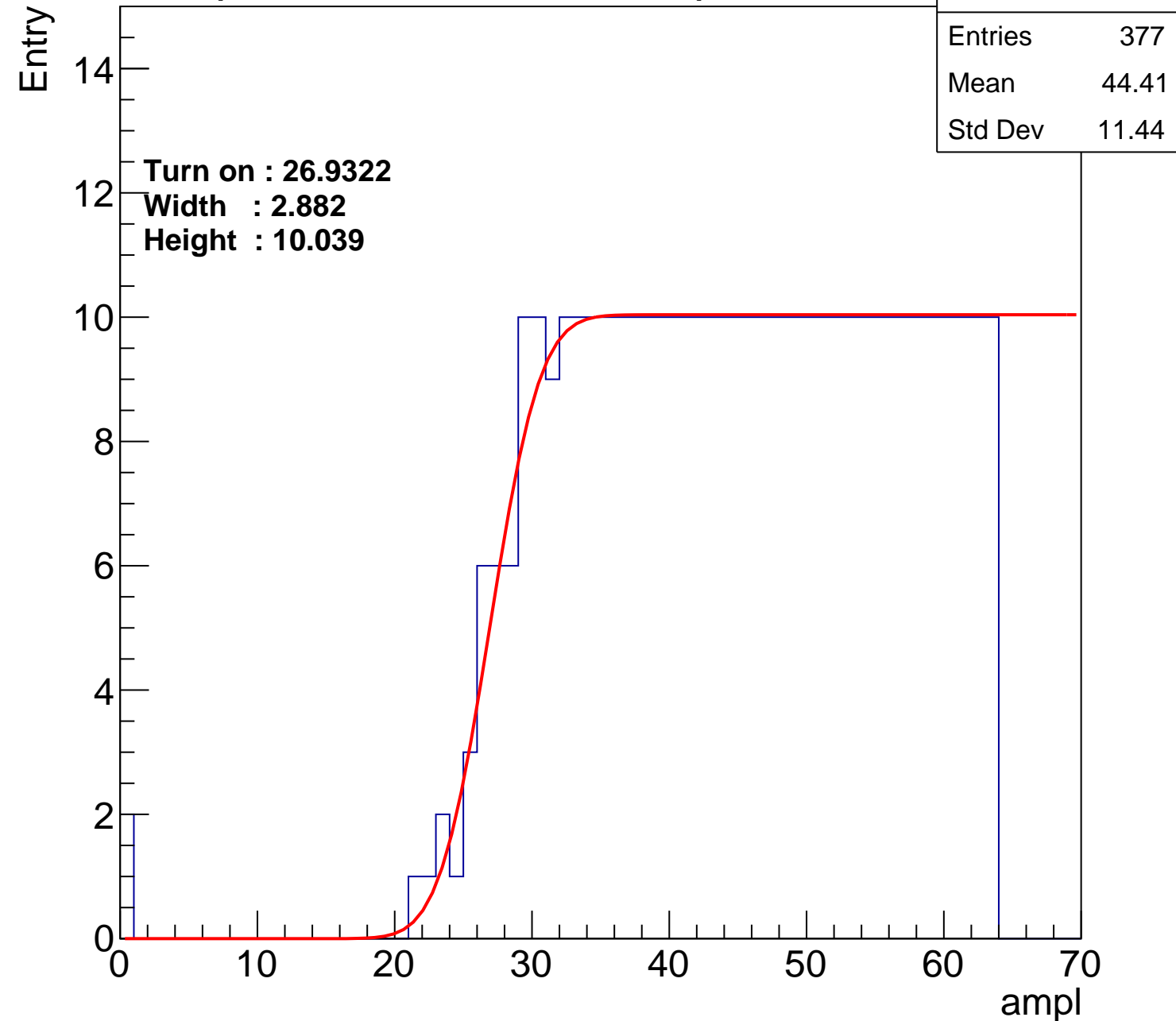
Width : 2.882

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.98
Std Dev	11.9

Turn on : 26.1646

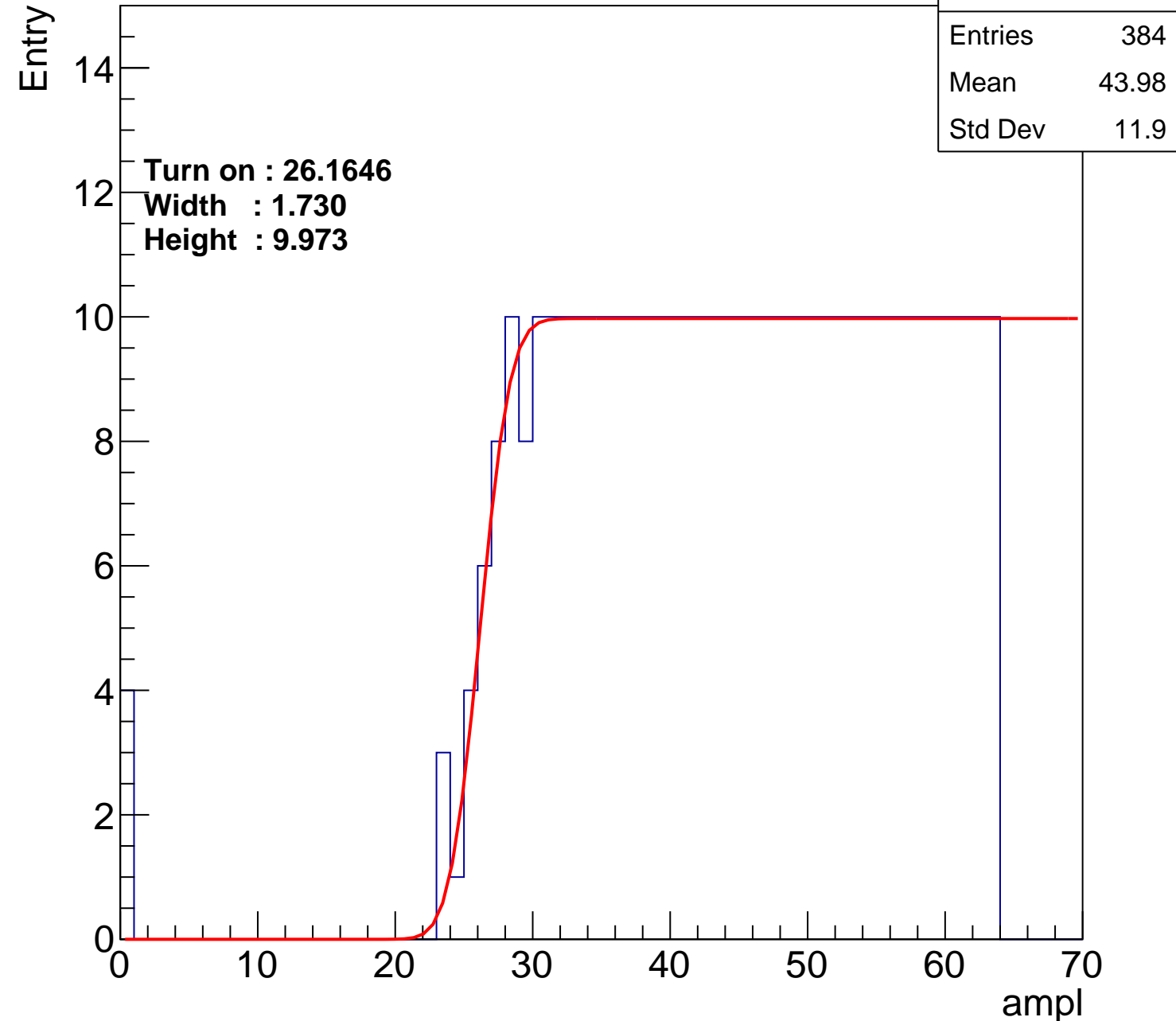
Width : 1.730

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.03
Std Dev	11.08

Turn on : 27.9083

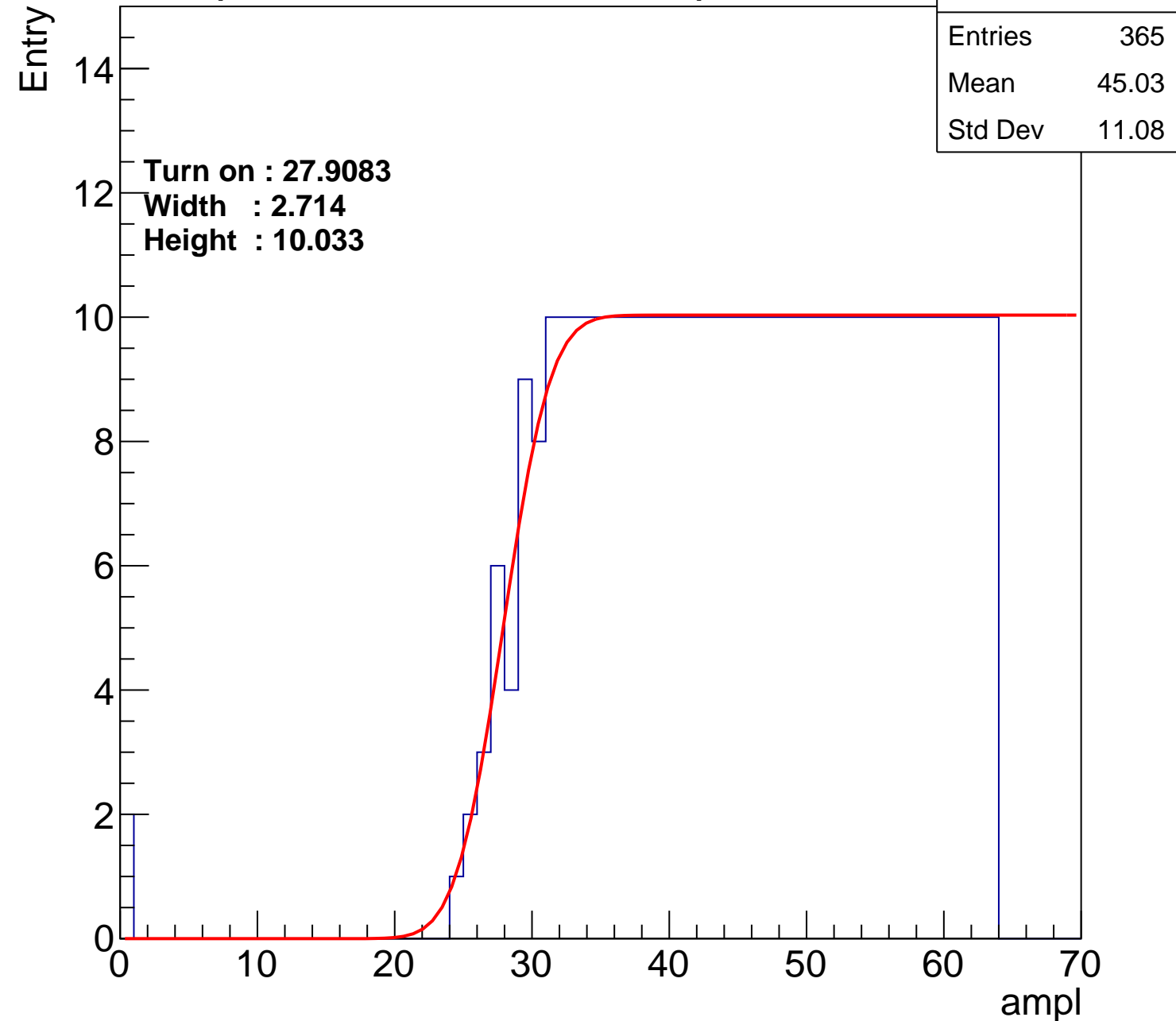
Width : 2.714

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.24
Std Dev	11.49

Turn on : 26.2126

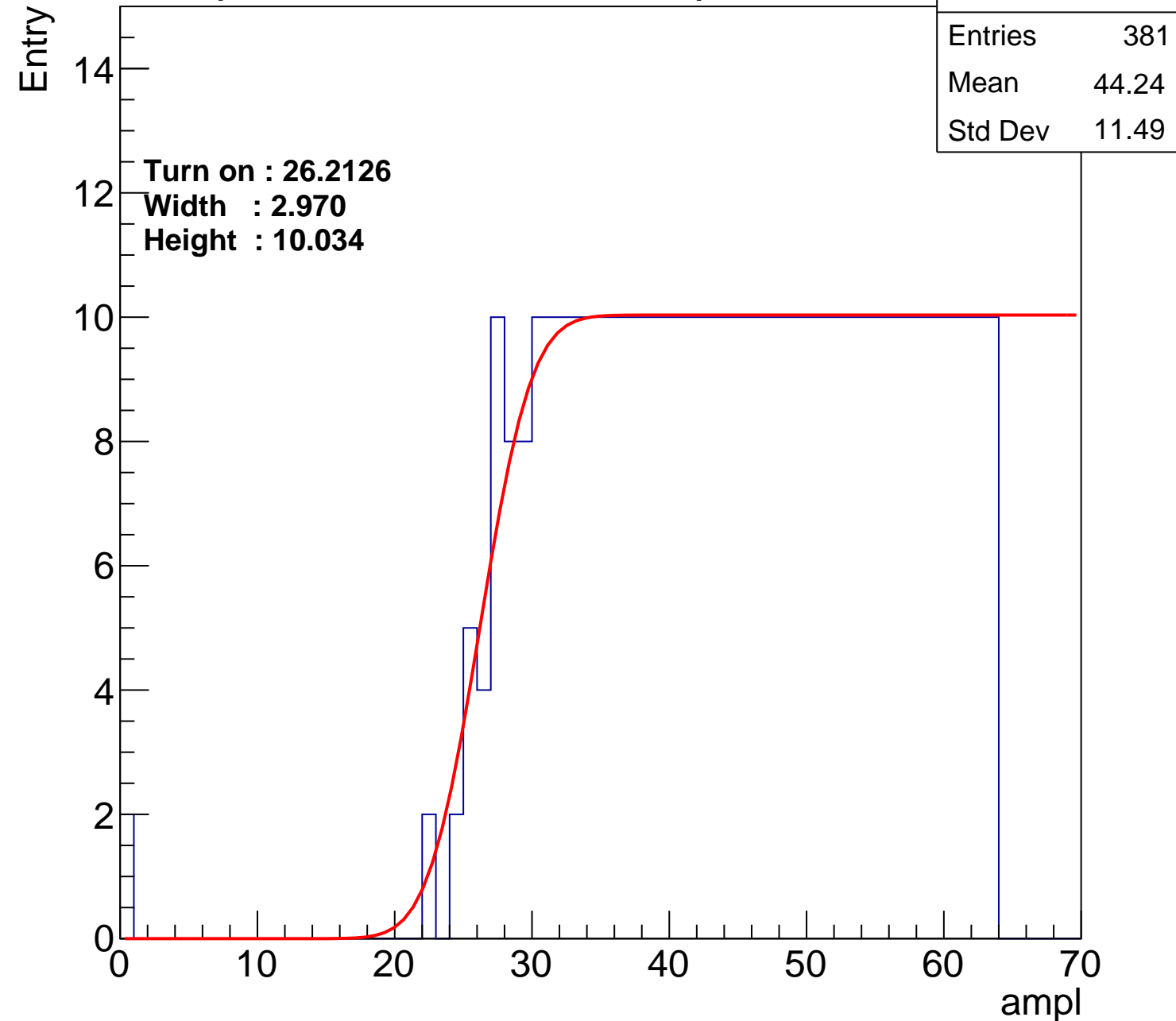
Width : 2.970

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch7

calib_packv5_042523_0143.root, FC#3, port B3

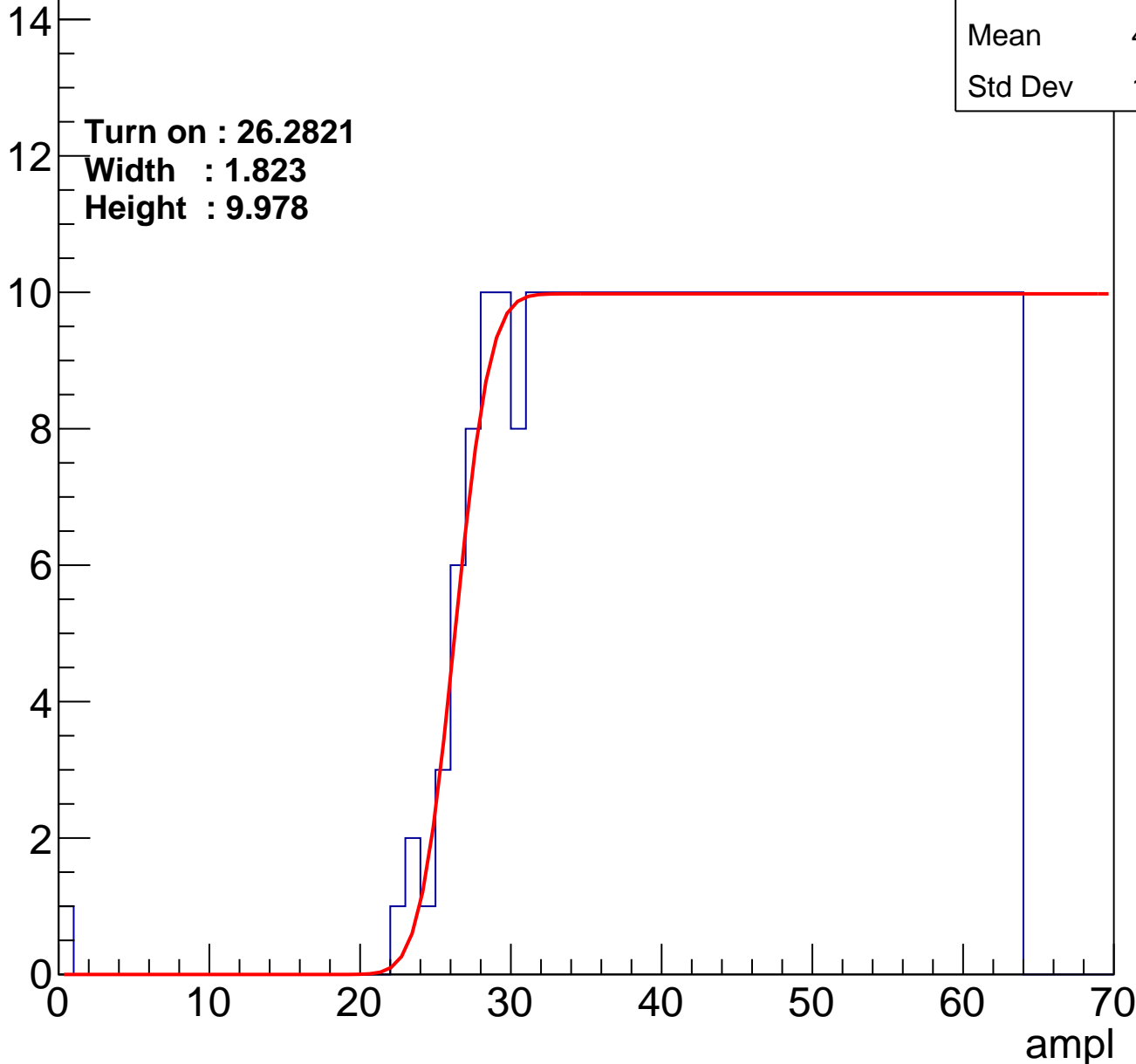
Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.2821

Width : 1.823

Height : 9.978

Entry



B1L000S, U24-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.17
Std Dev	11.37

Turn on : 25.7023

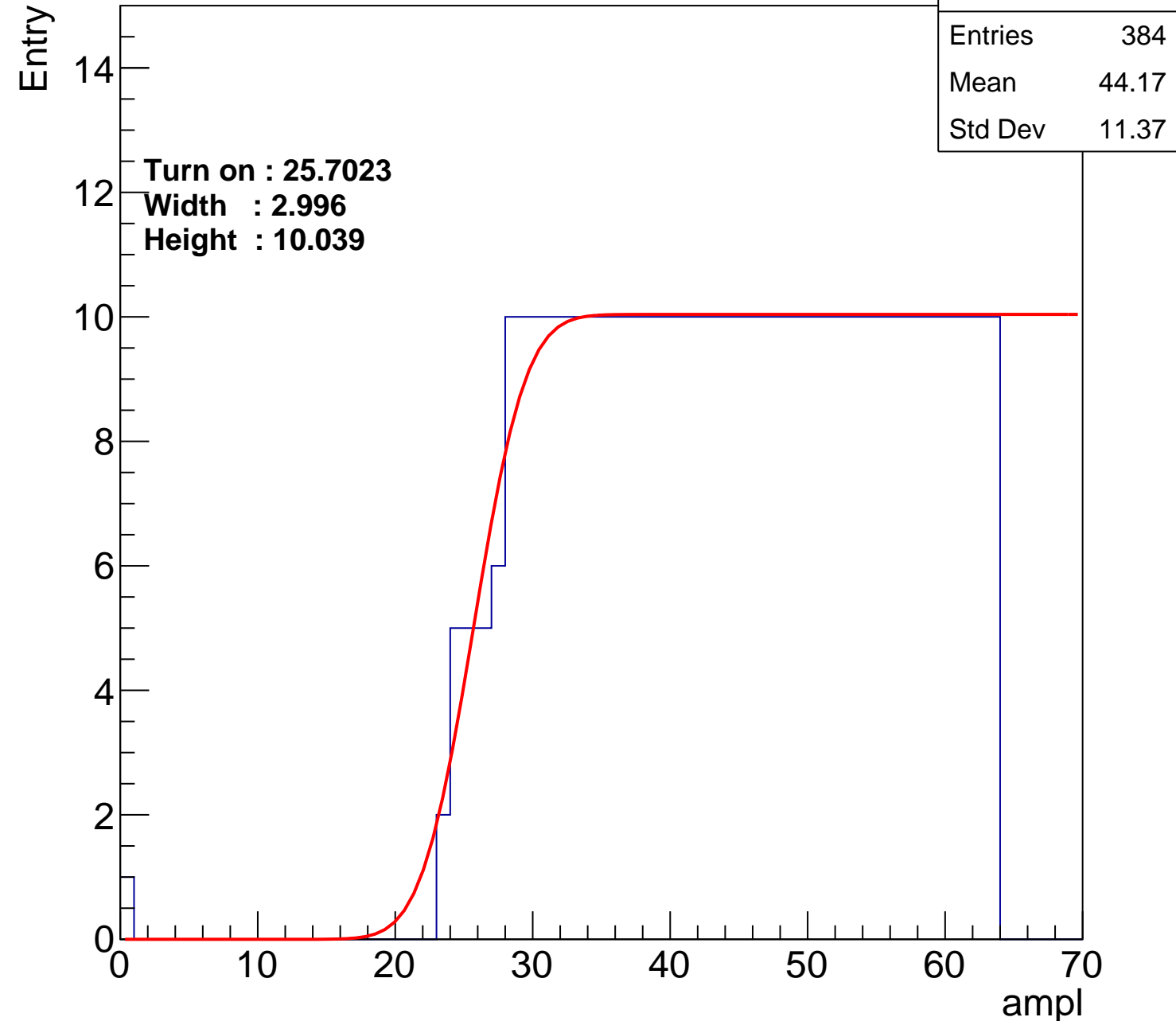
Width : 2.996

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.3348

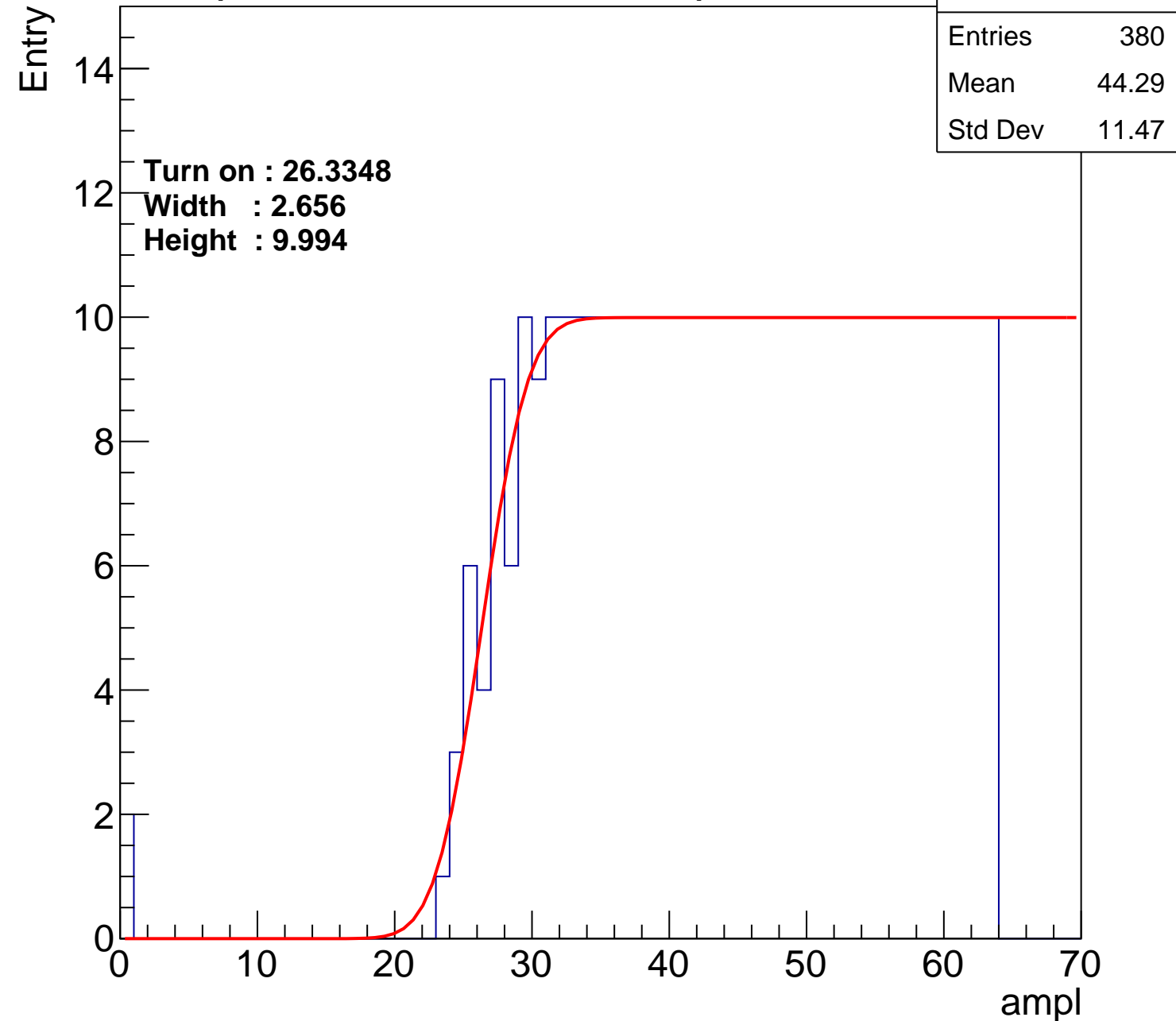
Width : 2.656

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch10

calib_packv5_042523_0143.root, FC#3, port B3

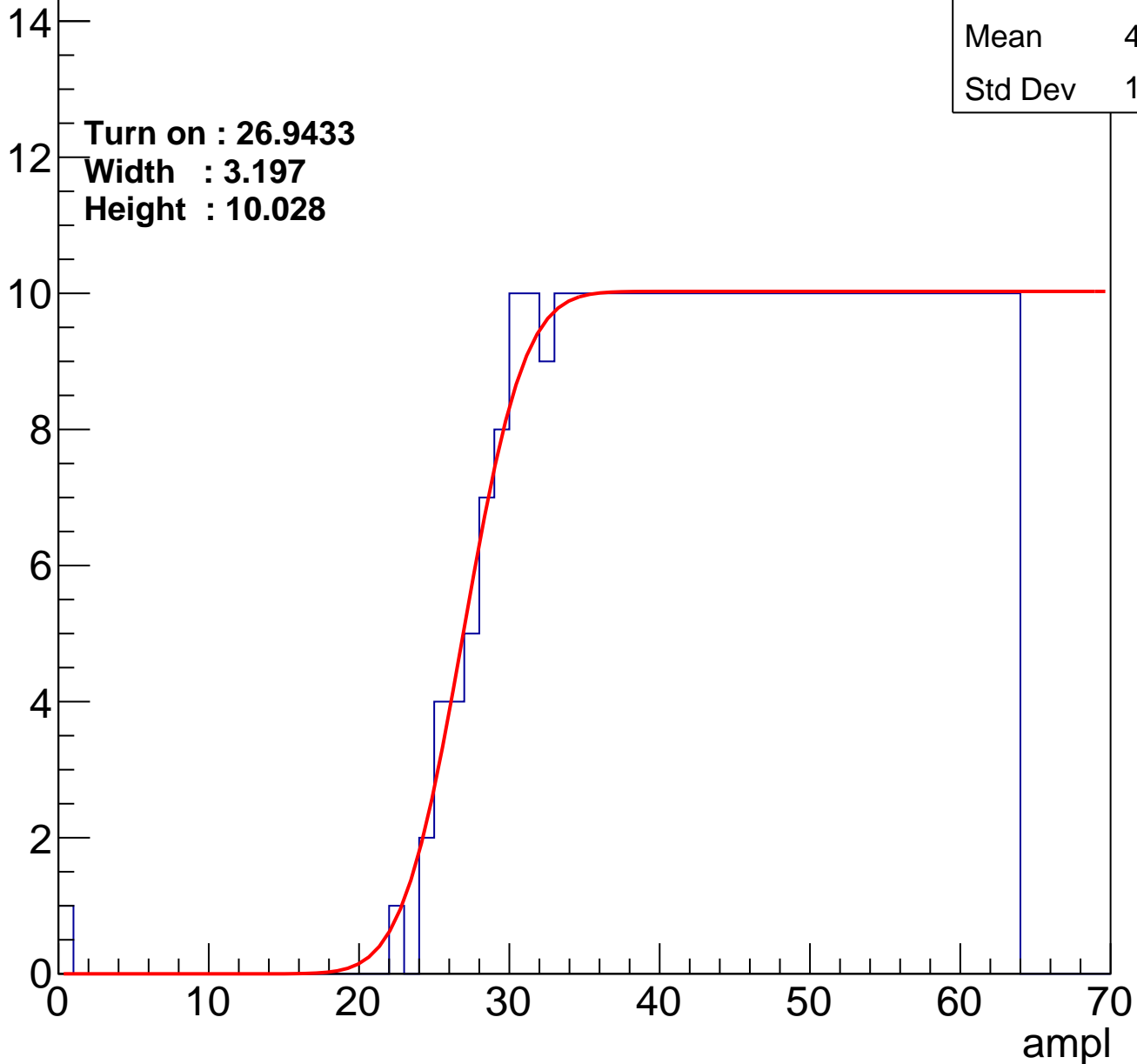
Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 26.9433

Width : 3.197

Height : 10.028

Entry



B1L000S, U24-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.69
Std Dev	11.26

Turn on : 27.2415

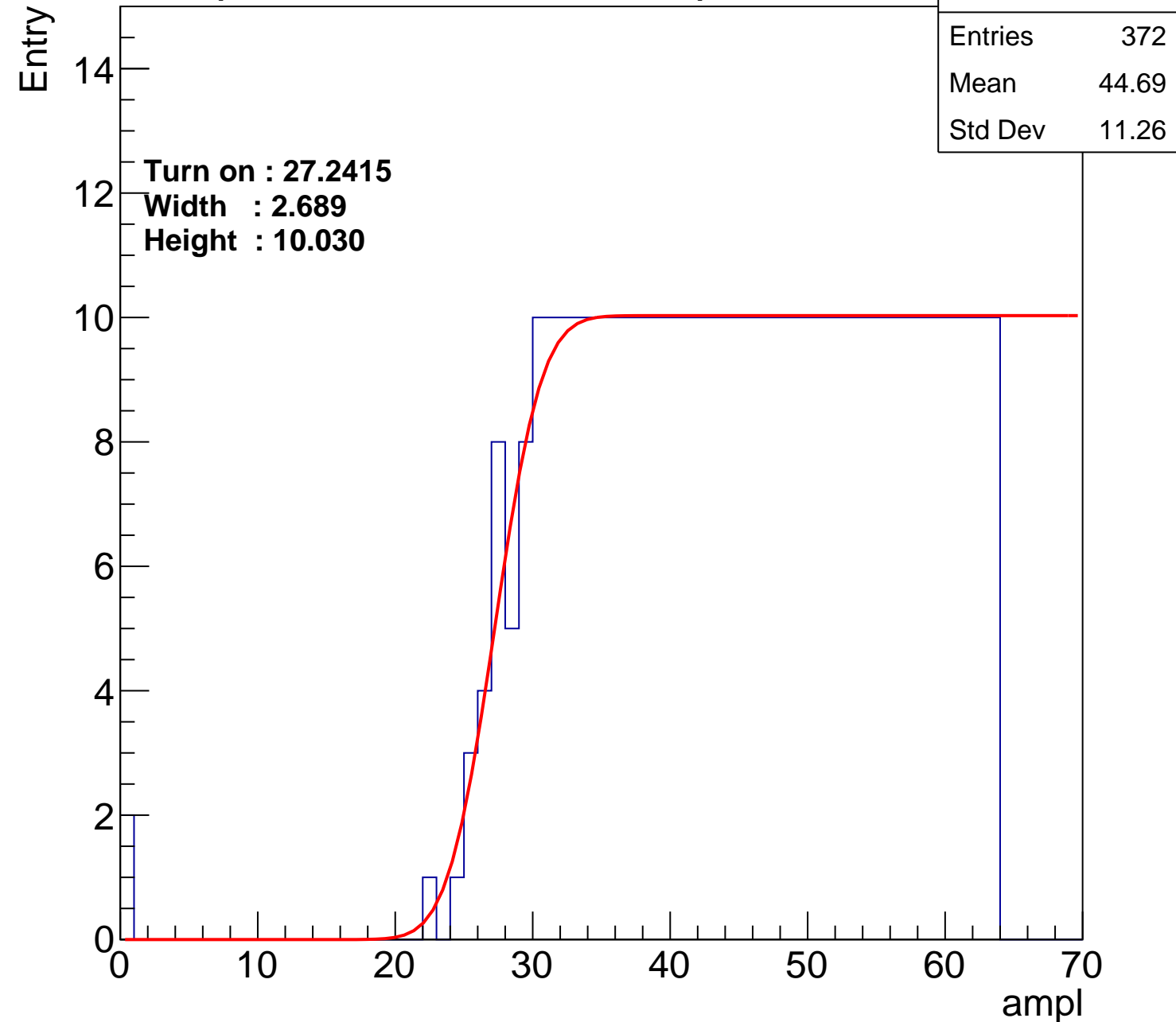
Width : 2.689

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.08
Std Dev	11.54

Turn on : 25.5974

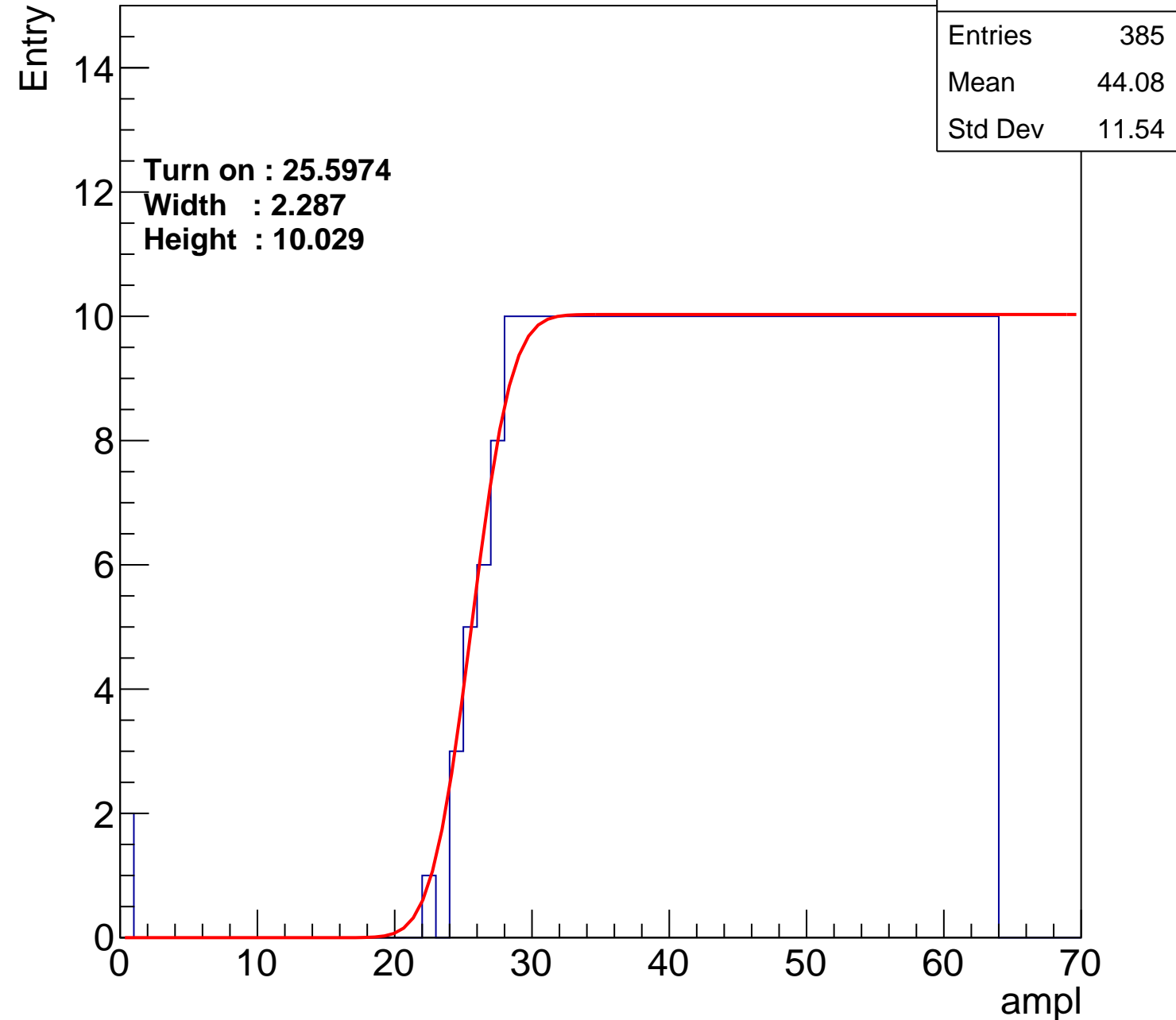
Width : 2.287

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.89
Std Dev	11.51

Turn on : 25.0985

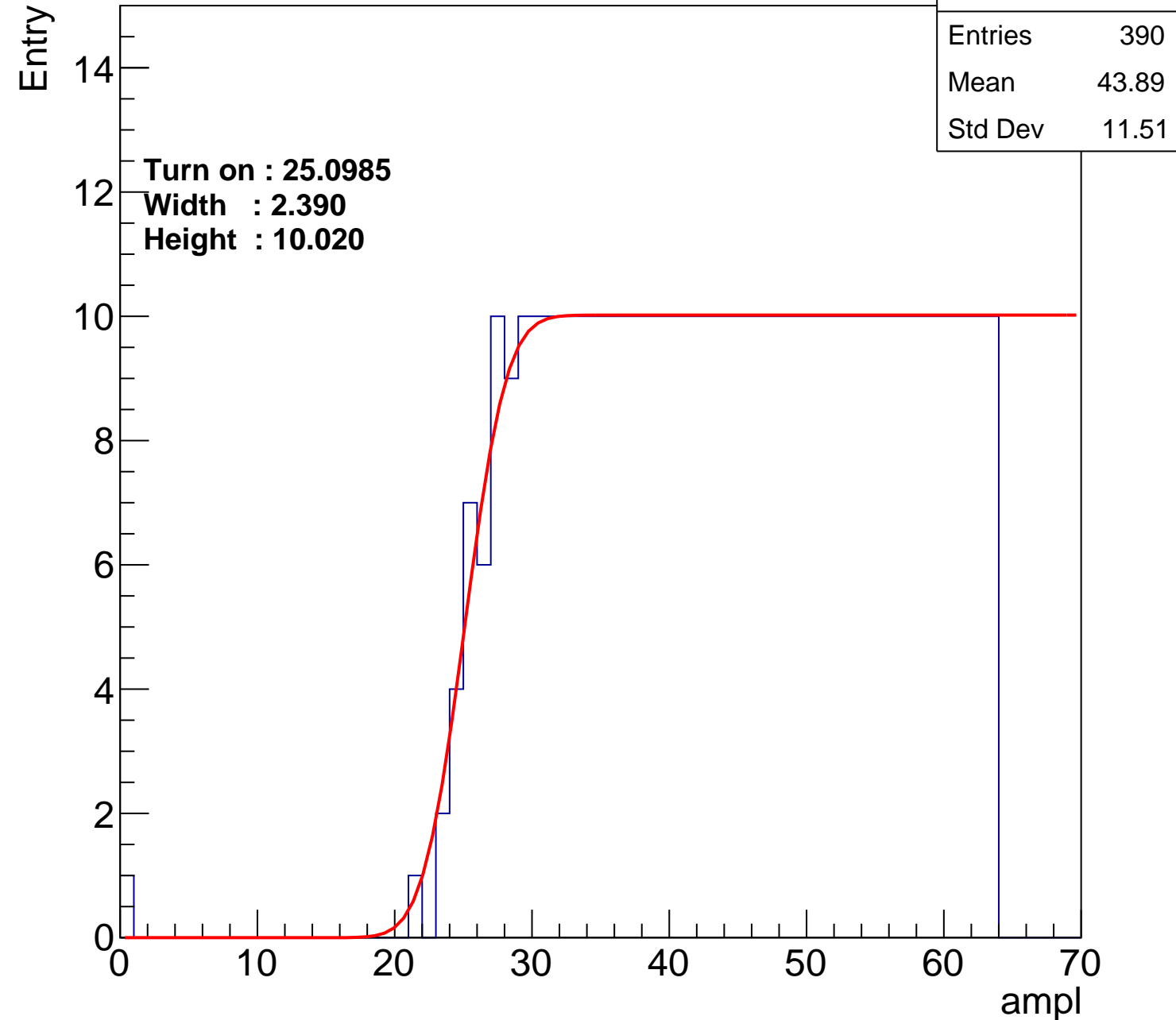
Width : 2.390

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	45.02
Std Dev	10.89

Turn on : 27.7279

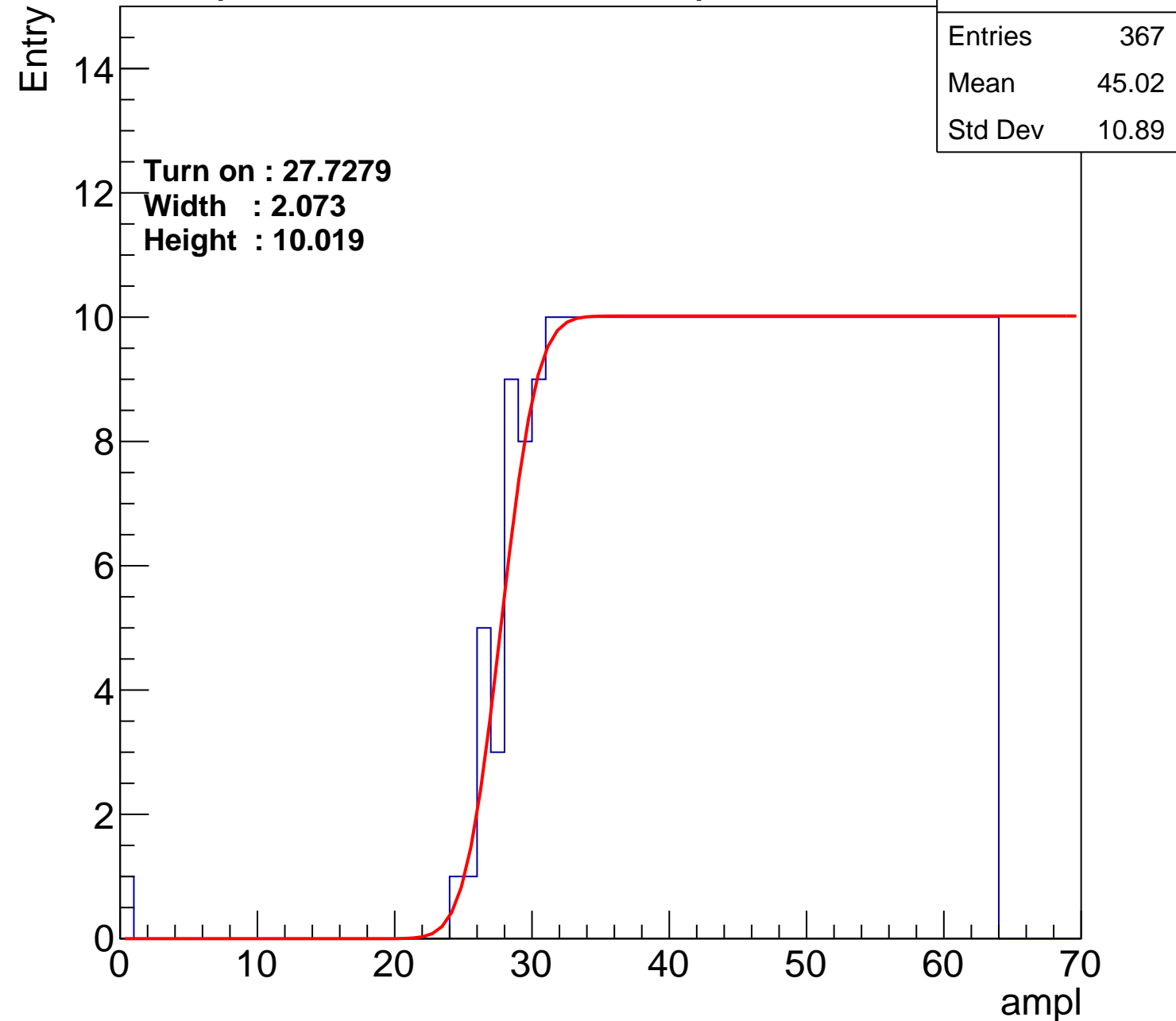
Width : 2.073

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	359
Mean	45.26
Std Dev	11.03

Turn on : 28.5409

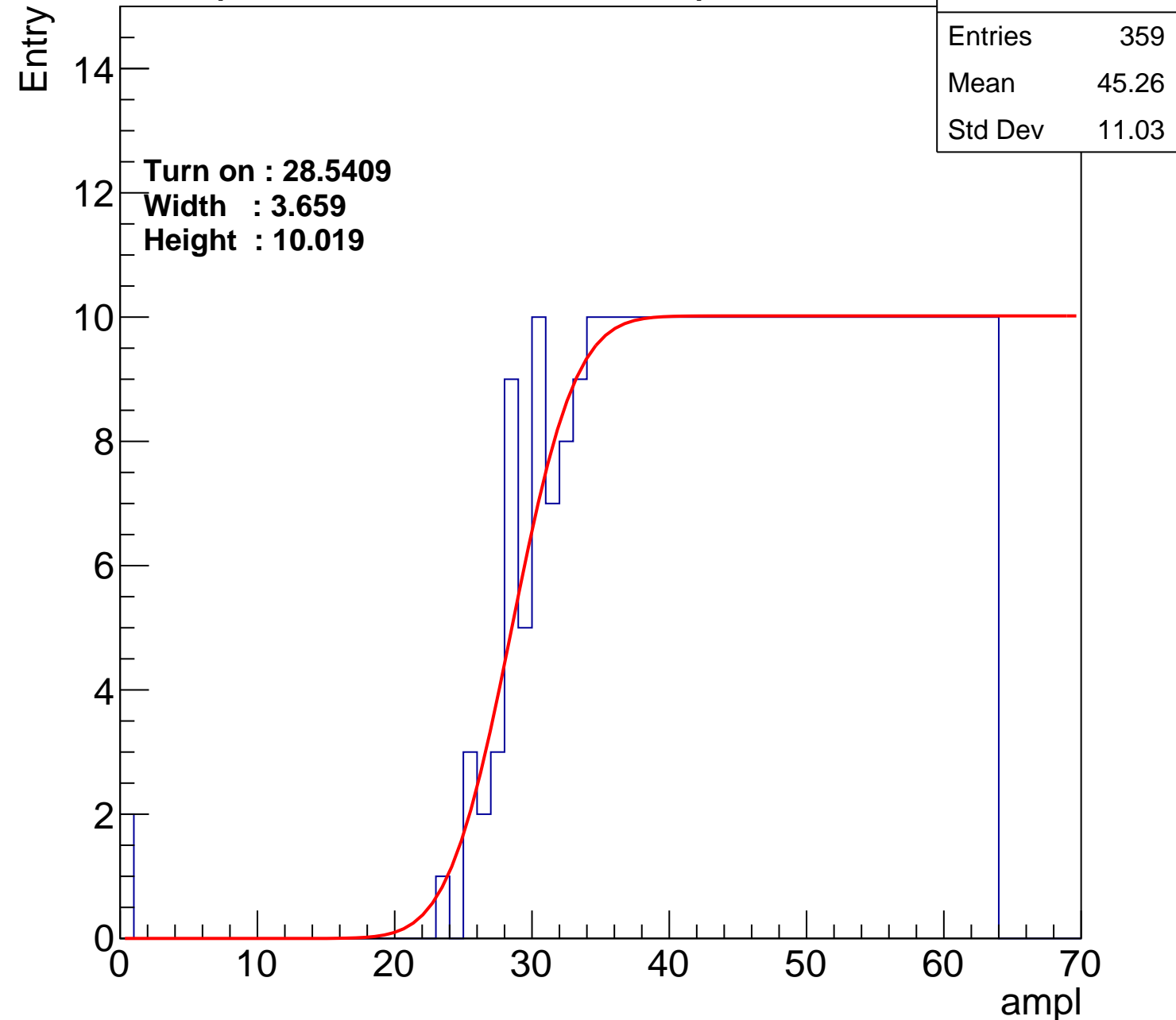
Width : 3.659

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch16

calib_packv5_042523_0143.root, FC#3, port B3

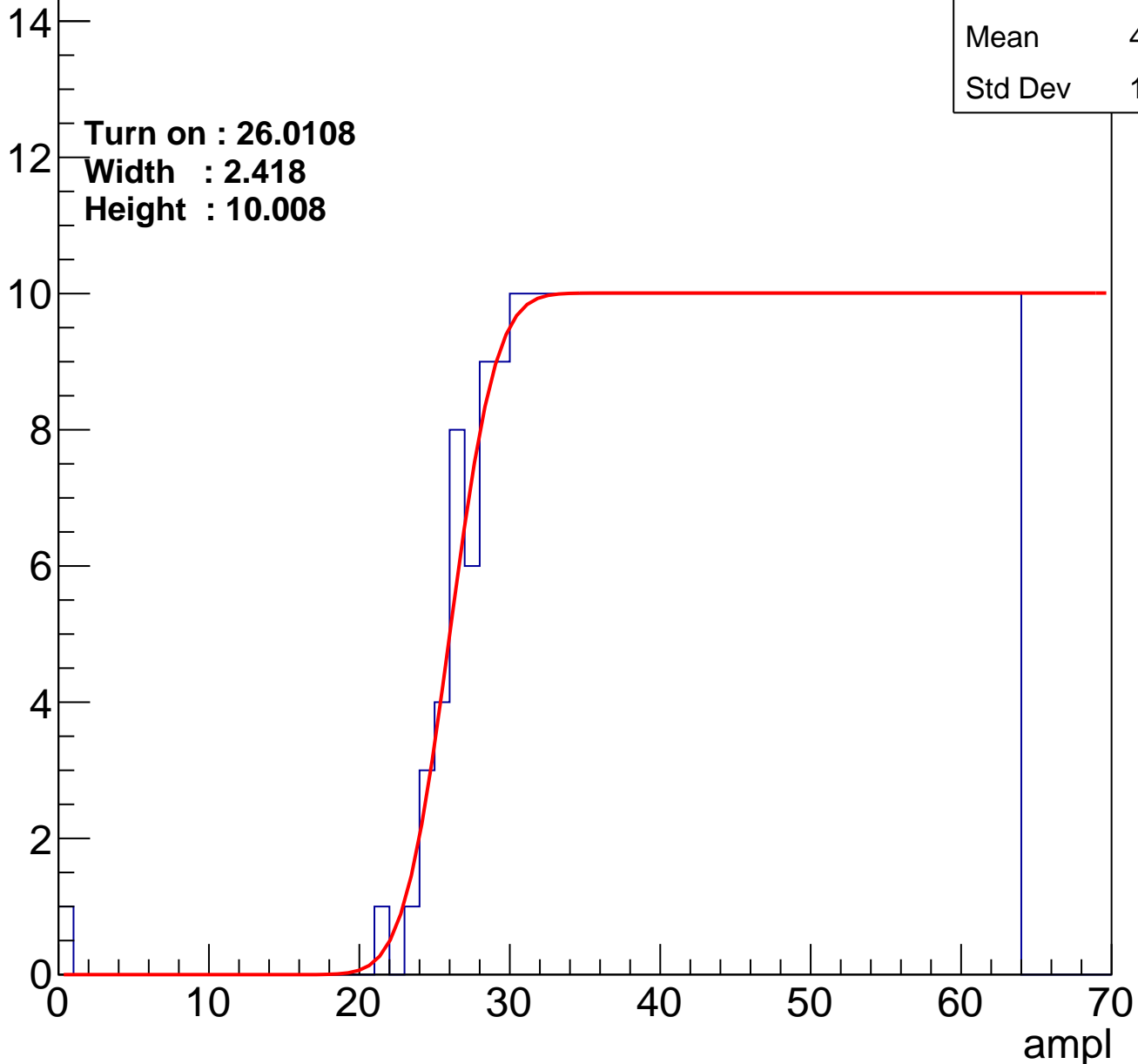
Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 26.0108

Width : 2.418

Height : 10.008

Entry



B1L000S, U24-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.87
Std Dev	11.12

Turn on : 27.1946

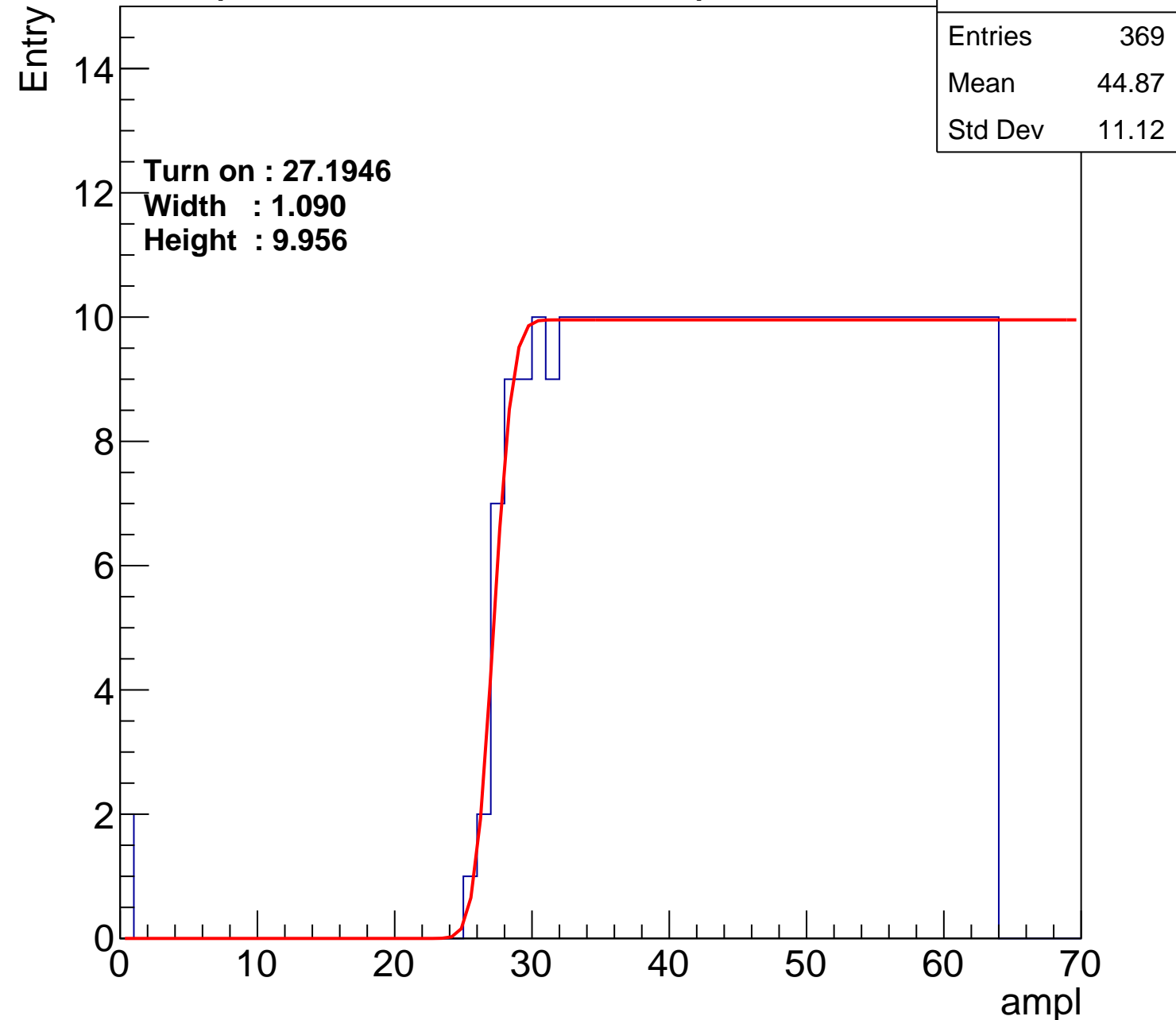
Width : 1.090

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.88
Std Dev	11.58

Turn on : 24.9636

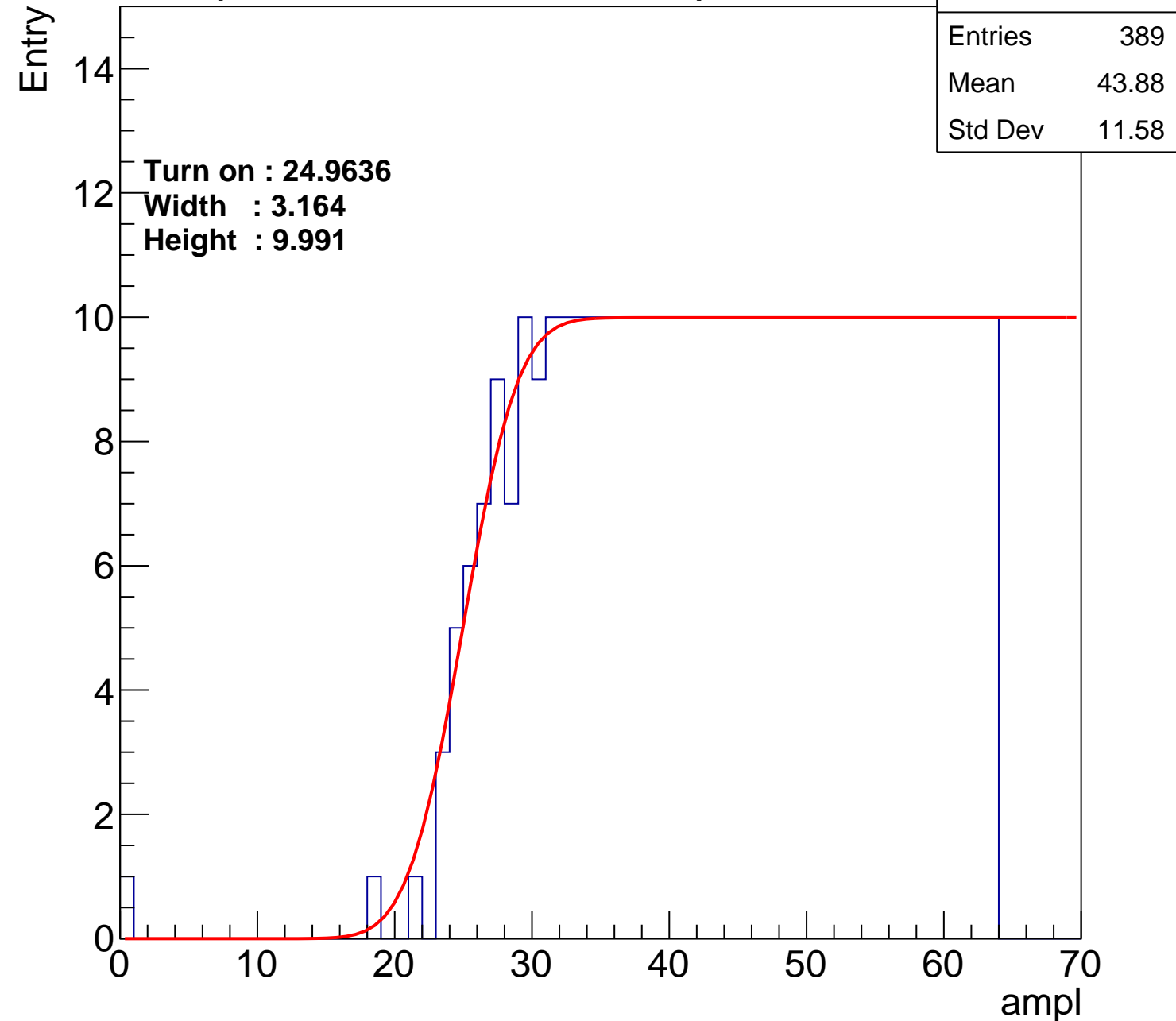
Width : 3.164

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 27.3129

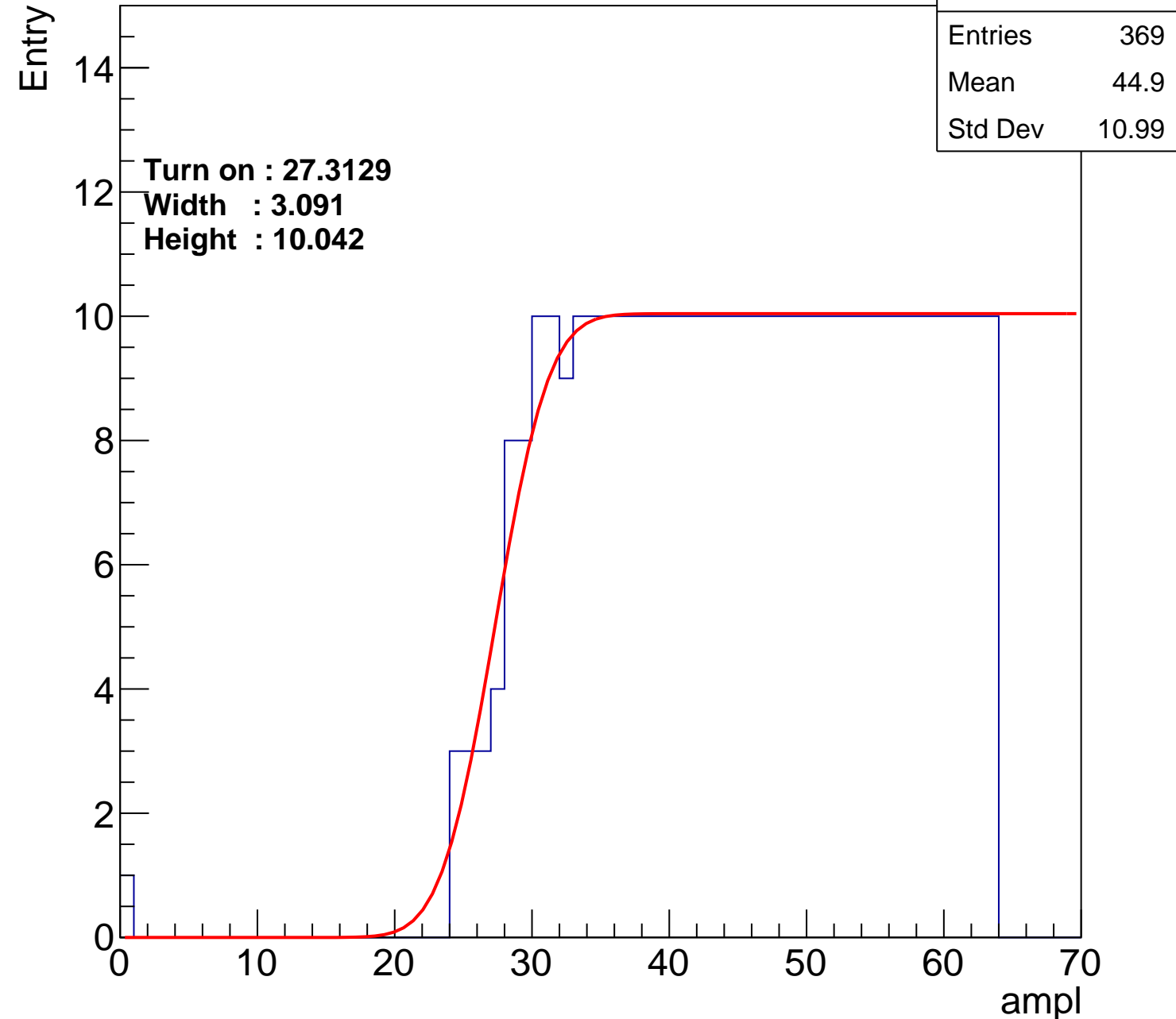
Width : 3.091

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.74
Std Dev	11.23

Turn on : 27.1738

Width : 2.890

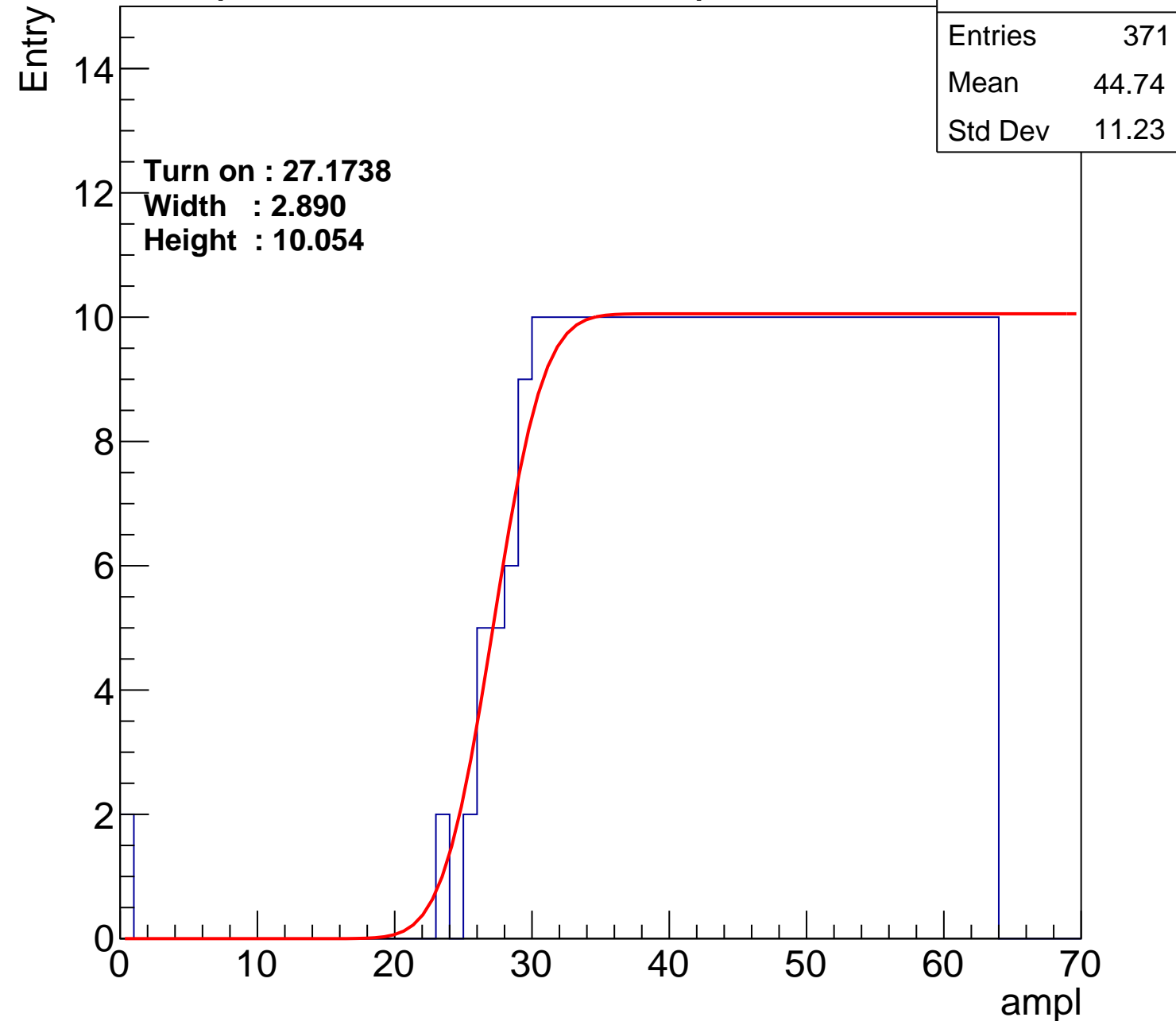
Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U24-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	357
Mean	45.39
Std Dev	10.93

Turn on : 28.2485

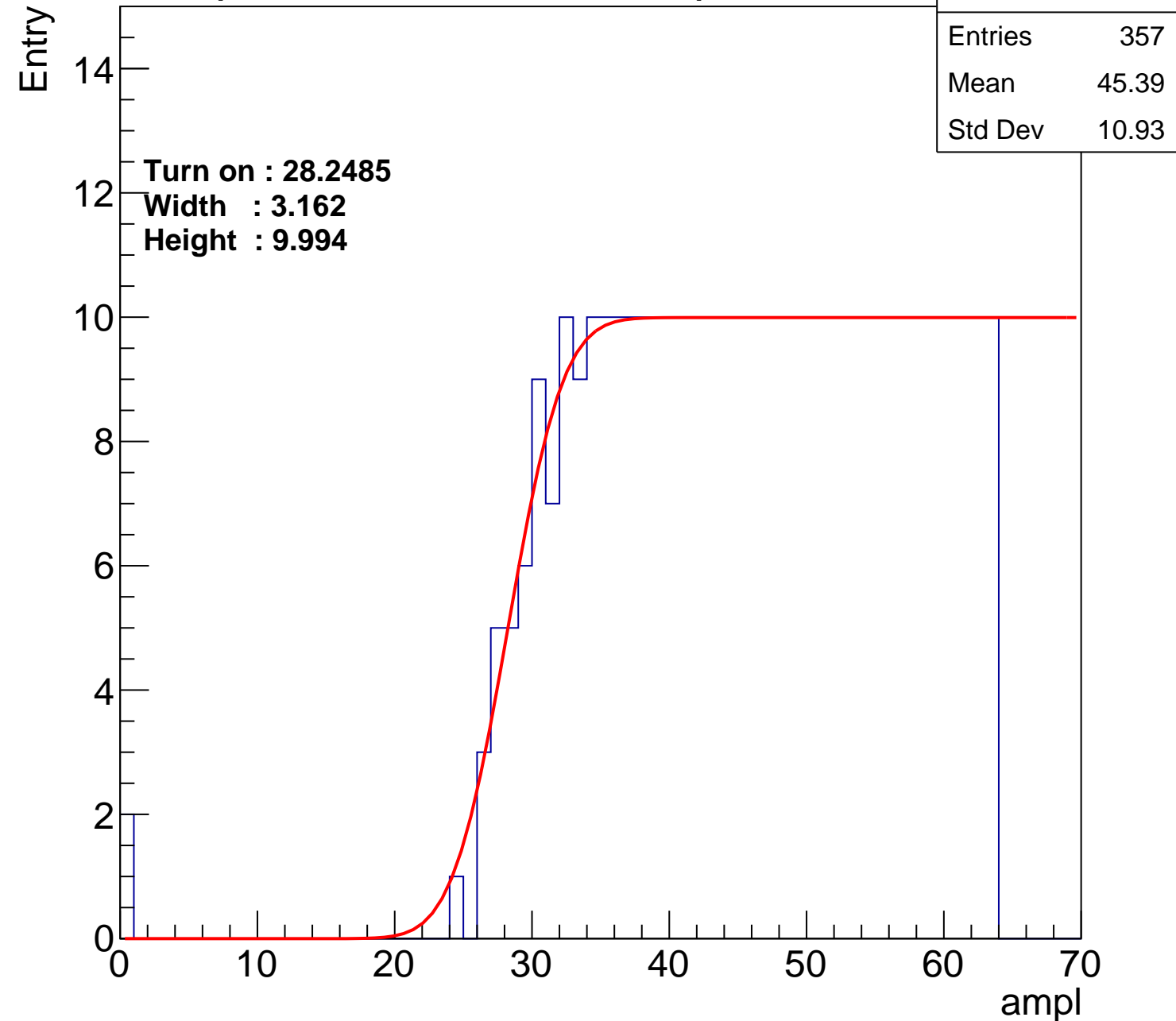
Width : 3.162

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	350
Mean	45.79
Std Dev	10.68

Turn on : 29.2379

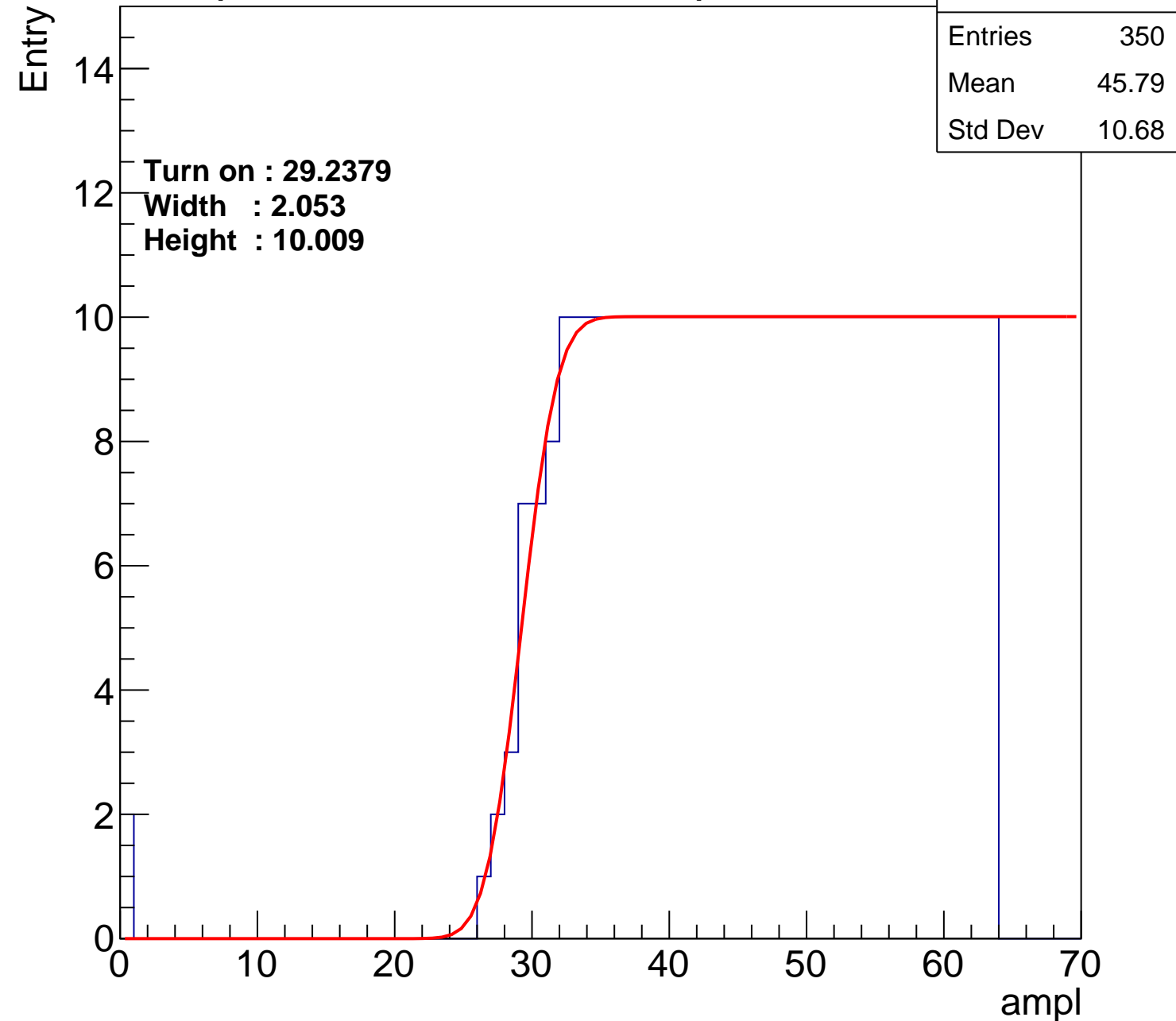
Width : 2.053

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.75
Std Dev	11.4

Turn on : 27.5188

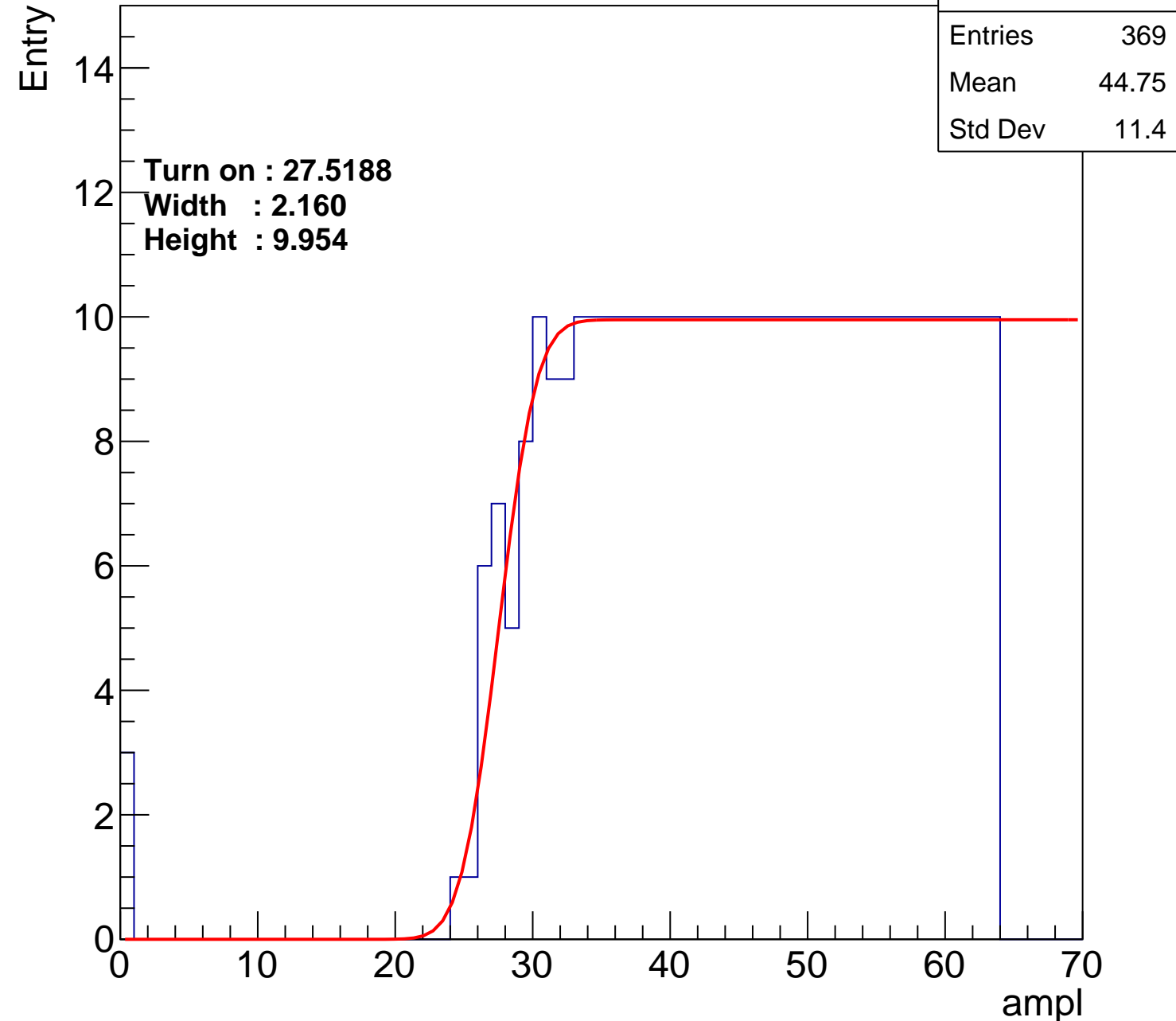
Width : 2.160

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 27.1132

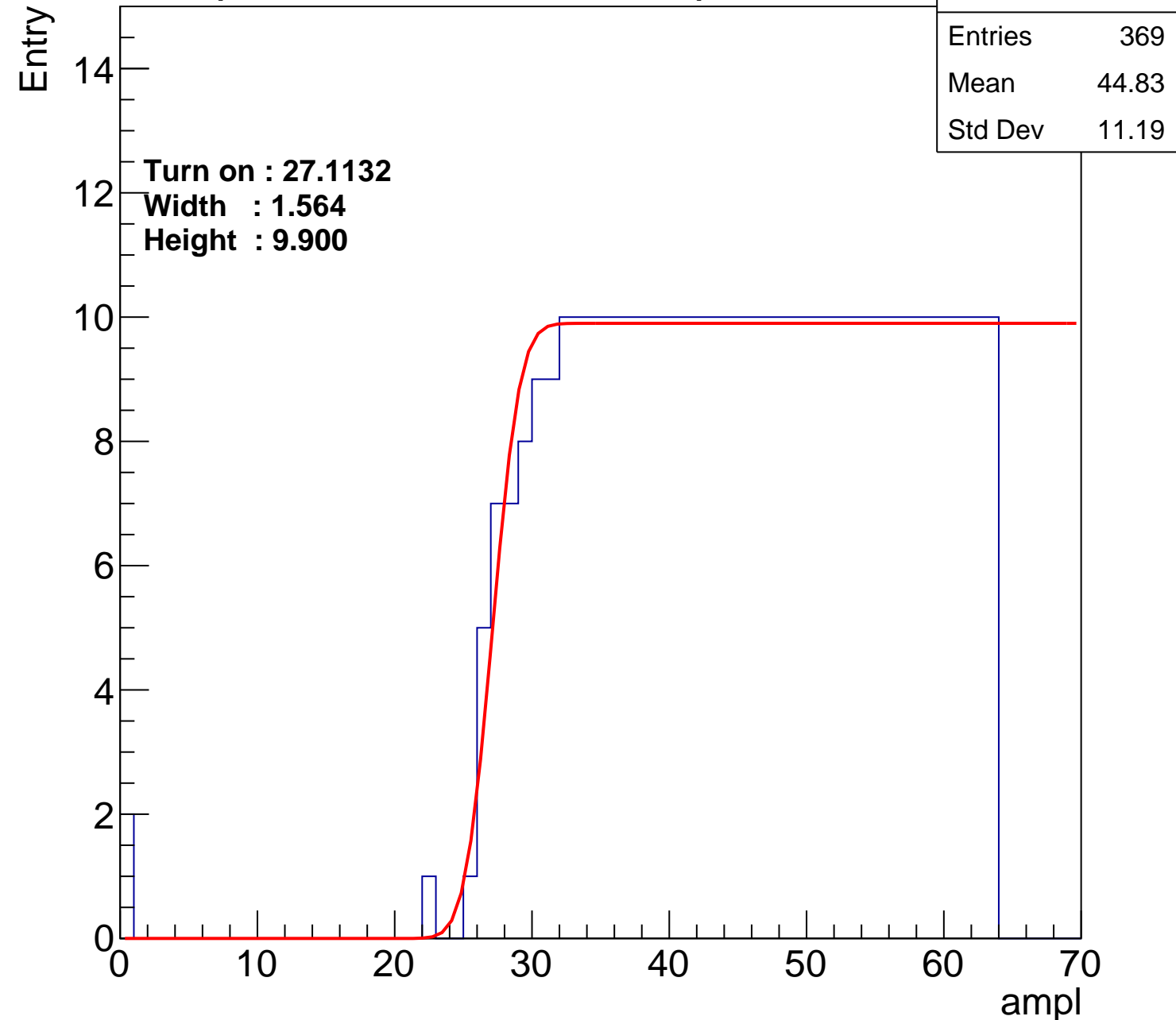
Width : 1.564

Height : 9.900

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch25

calib_packv5_042523_0143.root, FC#3, port B3

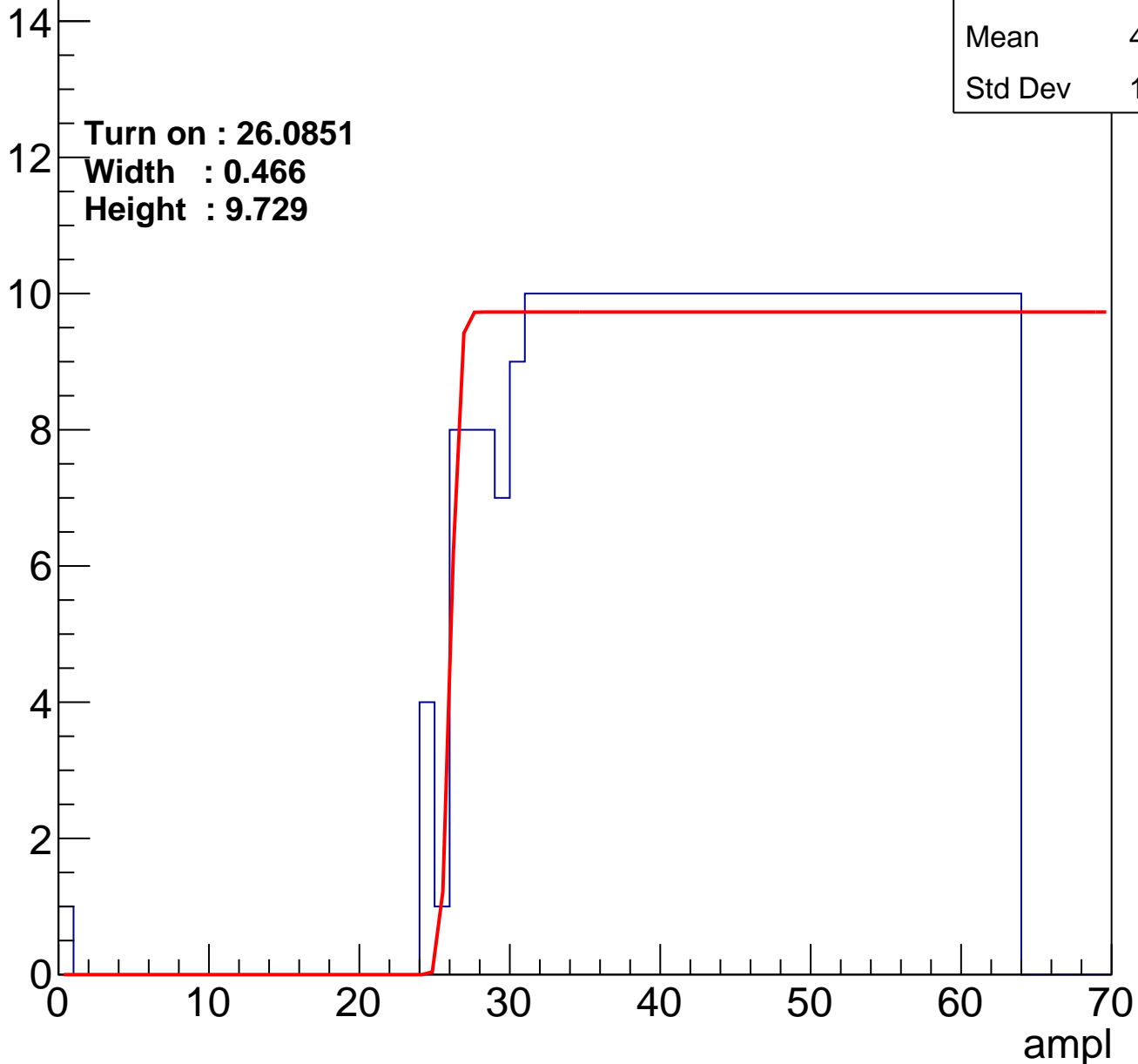
Entries	376
Mean	44.55
Std Dev	11.18

Turn on : 26.0851

Width : 0.466

Height : 9.729

Entry



B1L000S, U24-ch26

calib_packv5_042523_0143.root, FC#3, port B3

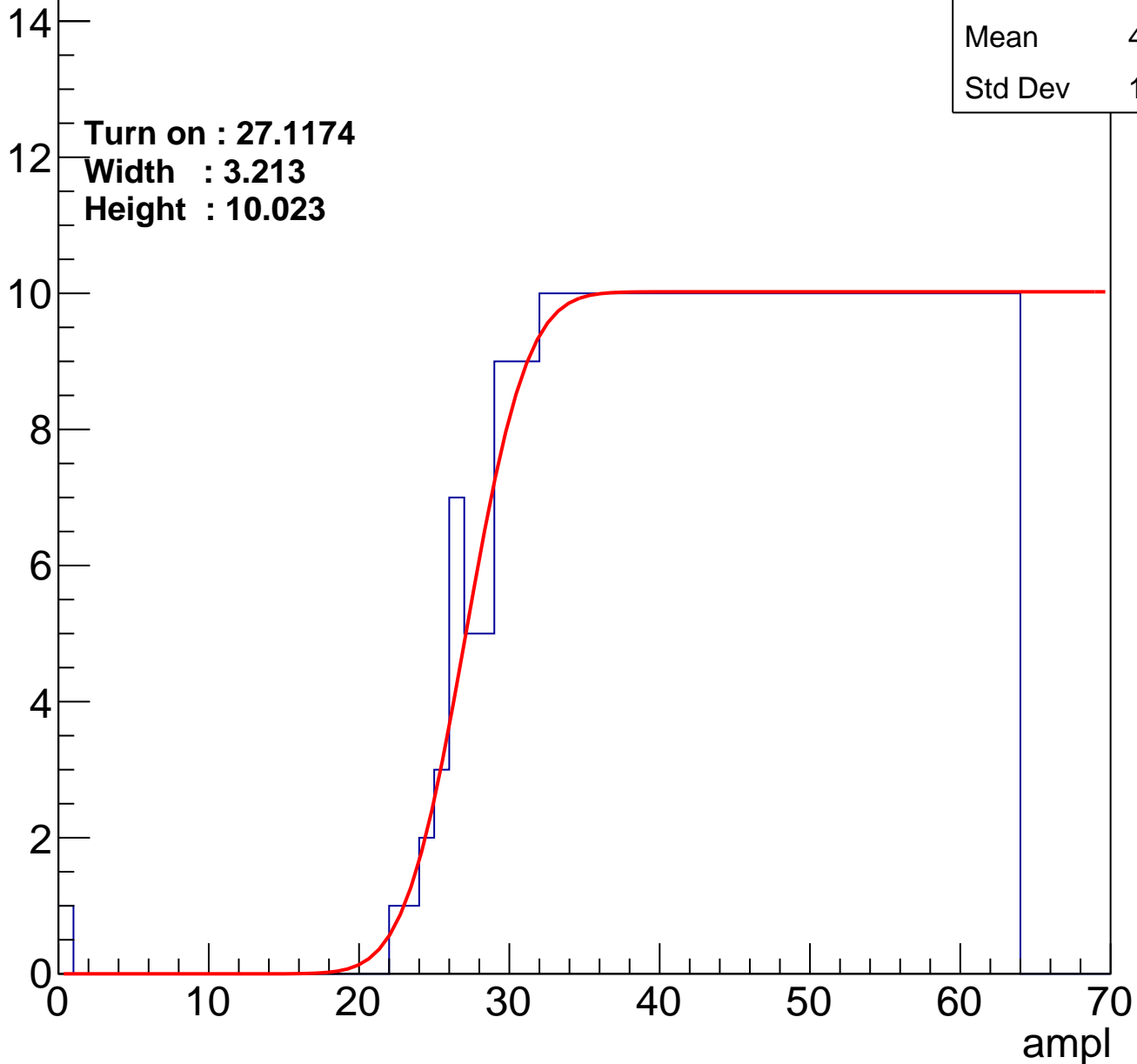
Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 27.1174

Width : 3.213

Height : 10.023

Entry



B1L000S, U24-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.3
Std Dev	11.33

Turn on : 26.3869

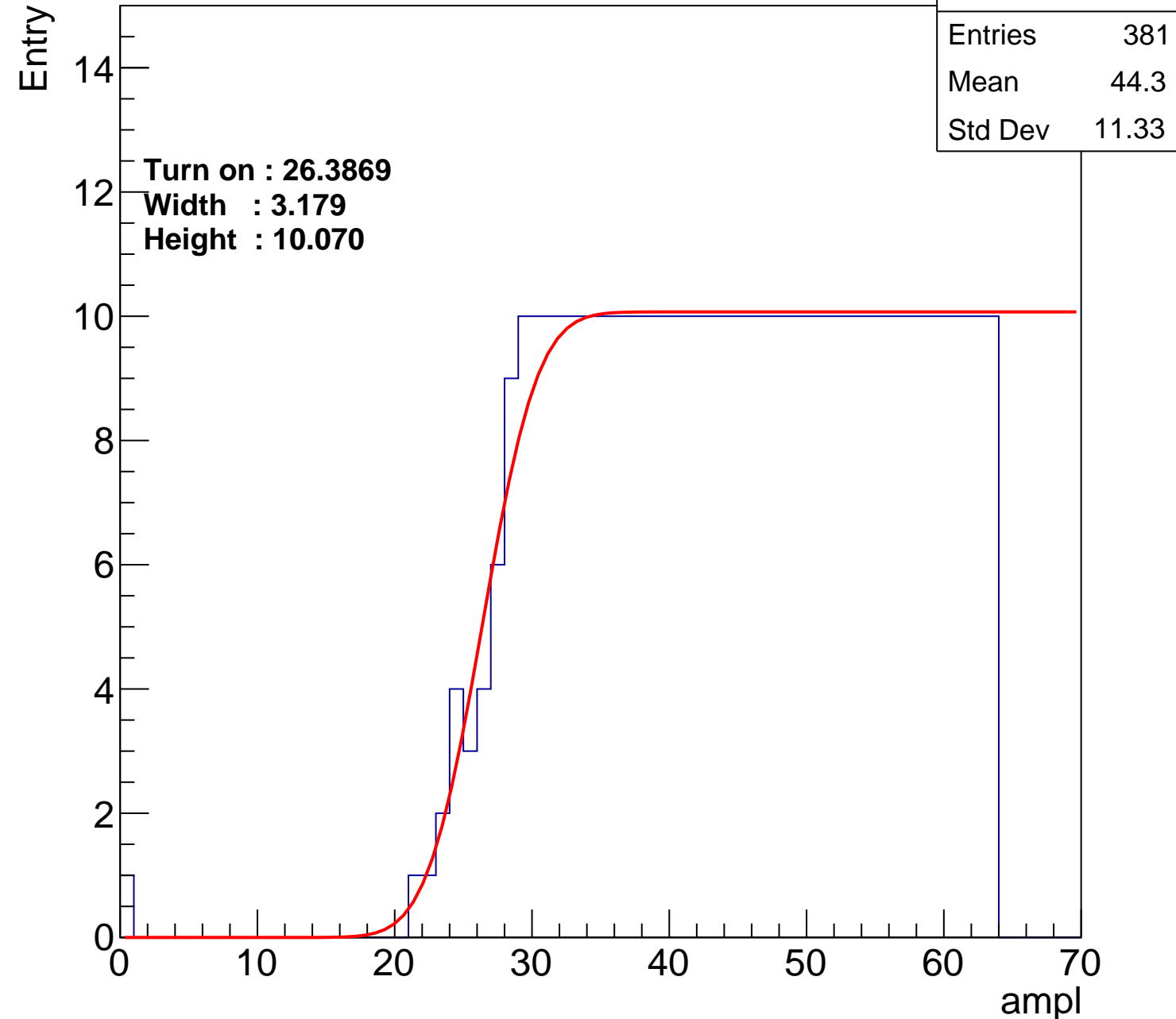
Width : 3.179

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.09
Std Dev	10.89

Turn on : 27.6844

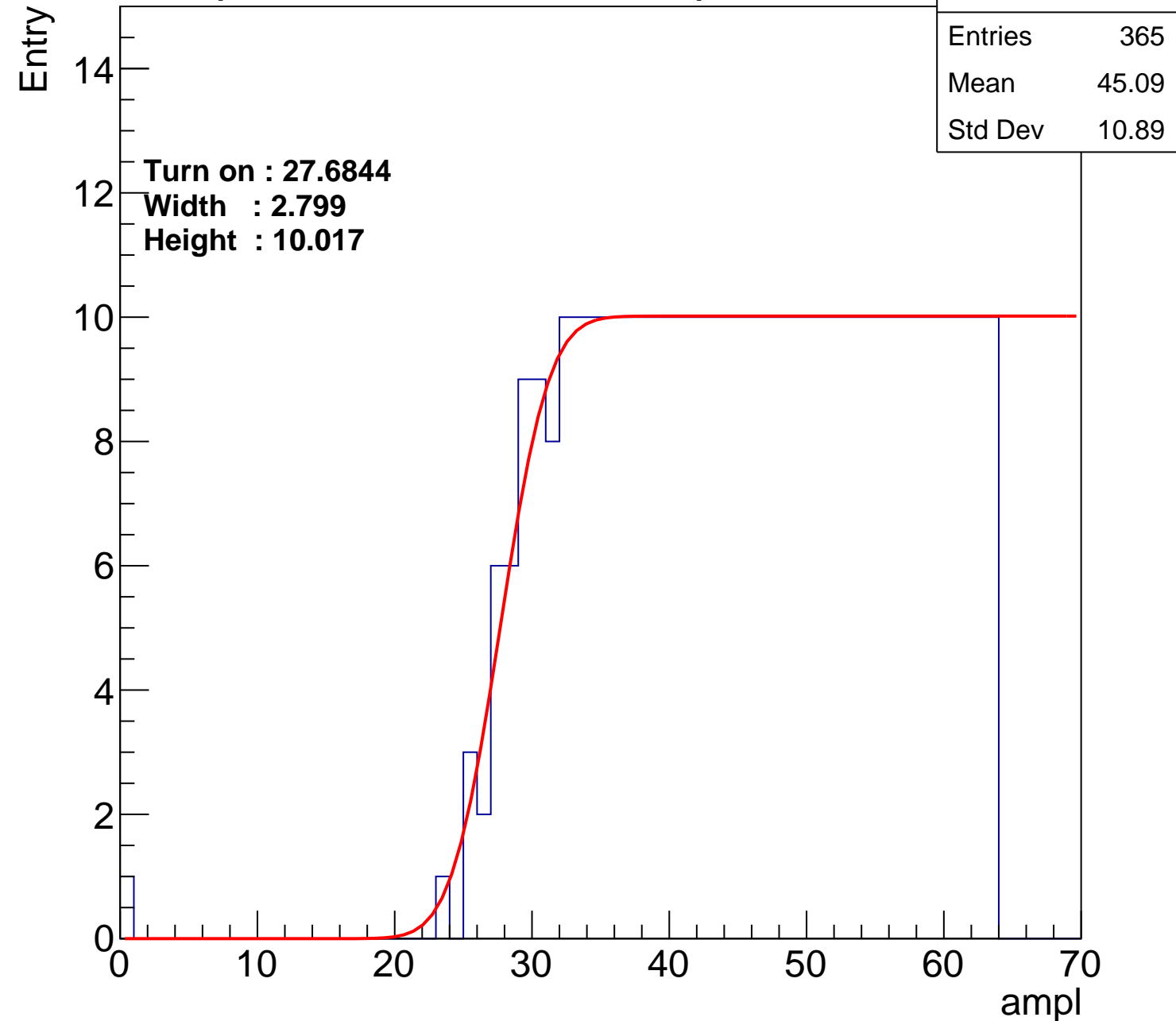
Width : 2.799

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch29

calib_packv5_042523_0143.root, FC#3, port B3

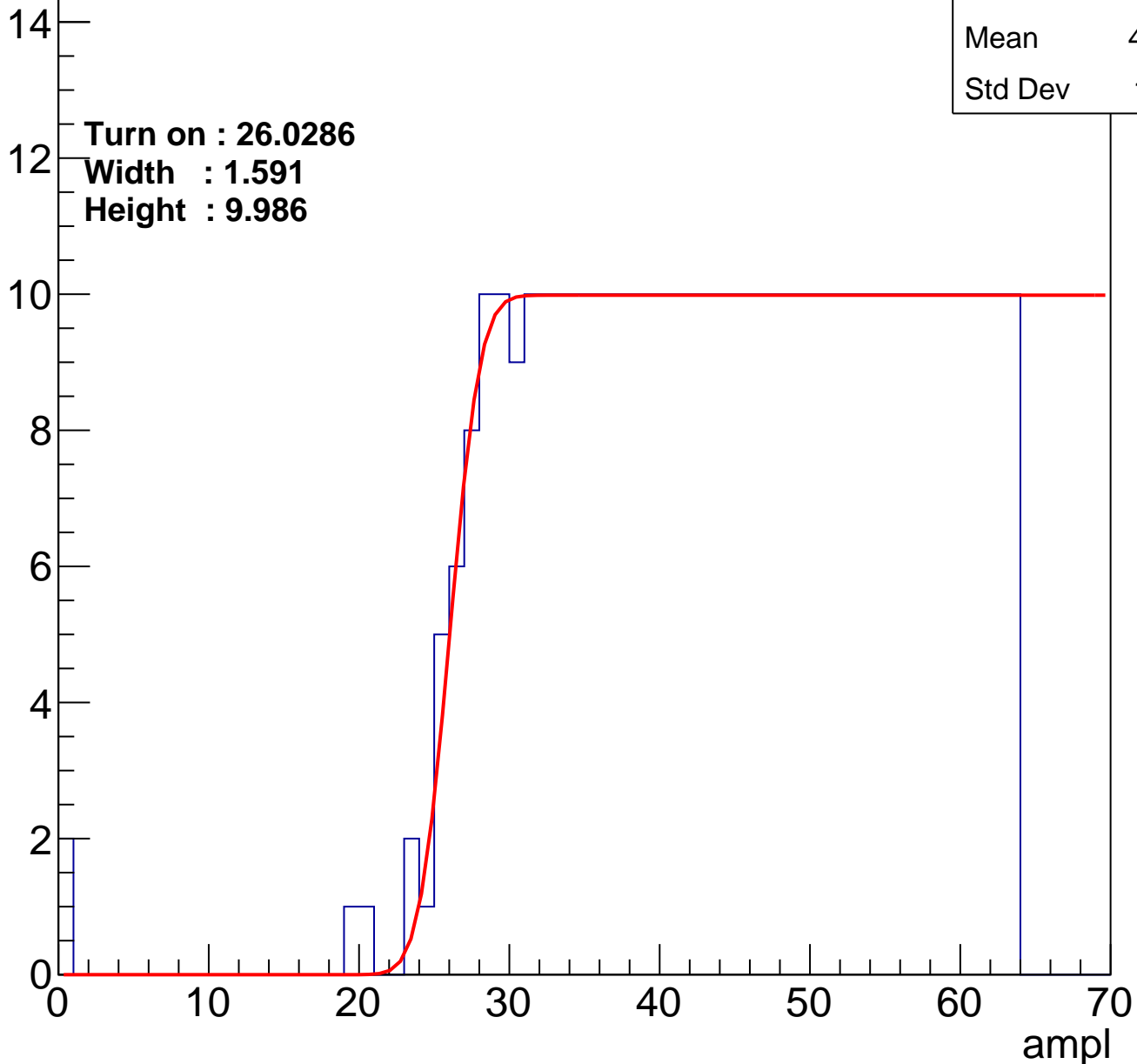
Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 26.0286

Width : 1.591

Height : 9.986

Entry



B1L000S, U24-ch30

calib_packv5_042523_0143.root, FC#3, port B3

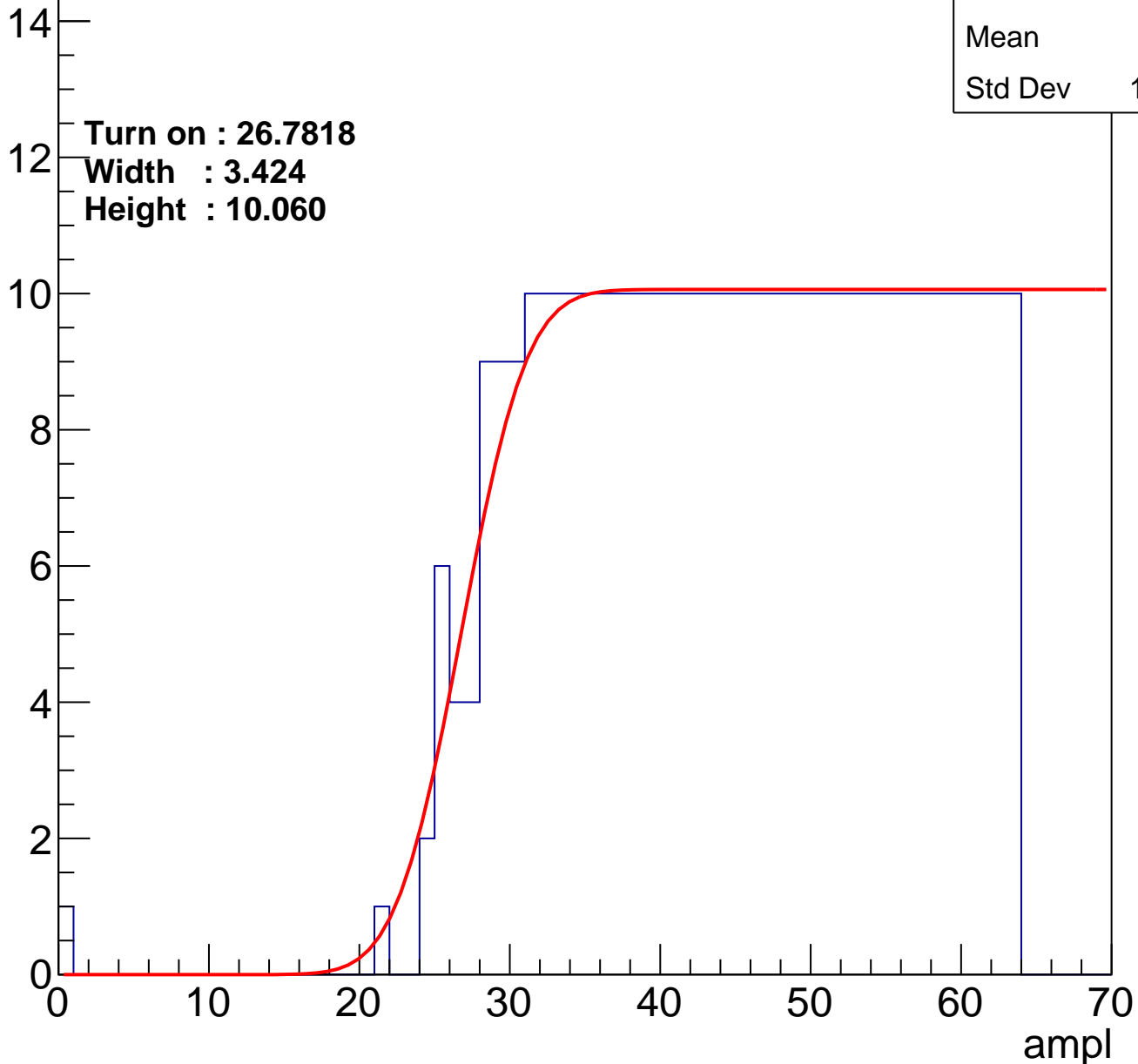
Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.7818

Width : 3.424

Height : 10.060

Entry



B1L000S, U24-ch31

calib_packv5_042523_0143.root, FC#3, port B3

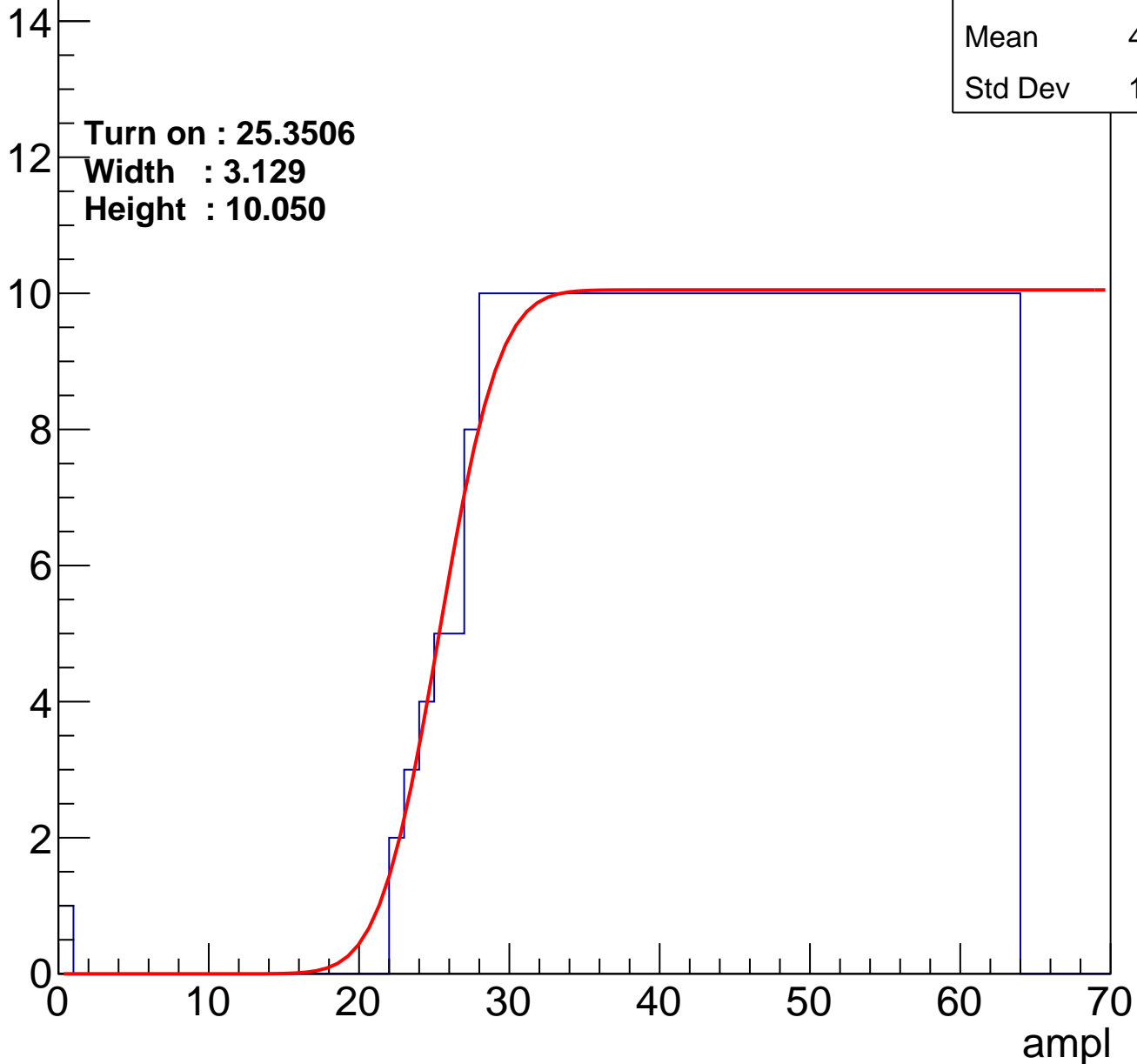
Entries	388
Mean	43.97
Std Dev	11.49

Turn on : 25.3506

Width : 3.129

Height : 10.050

Entry



B1L000S, U24-ch32

calib_packv5_042523_0143.root, FC#3, port B3

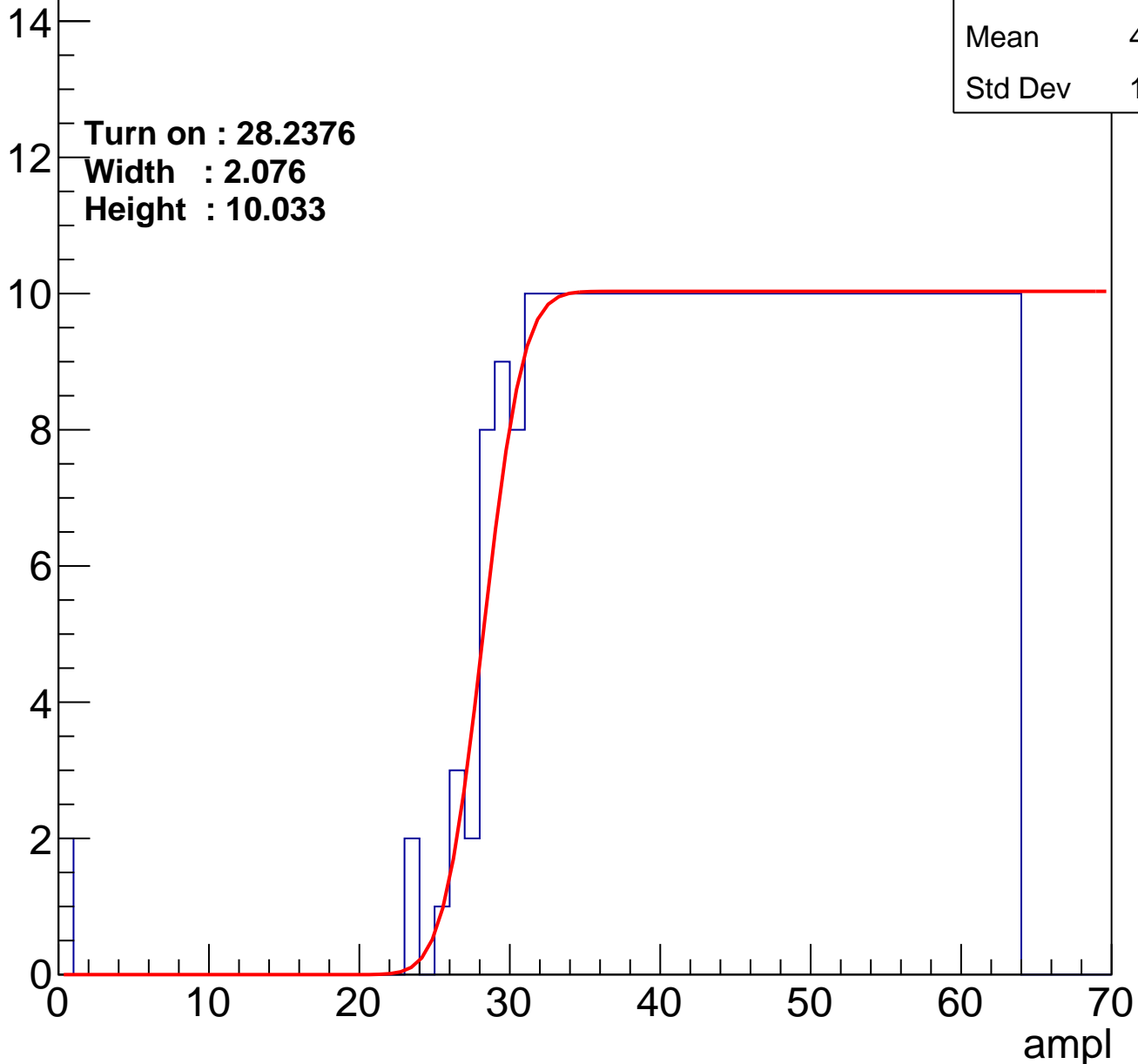
Entries	365
Mean	45.04
Std Dev	11.08

Turn on : 28.2376

Width : 2.076

Height : 10.033

Entry



B1L000S, U24-ch33

calib_packv5_042523_0143.root, FC#3, port B3

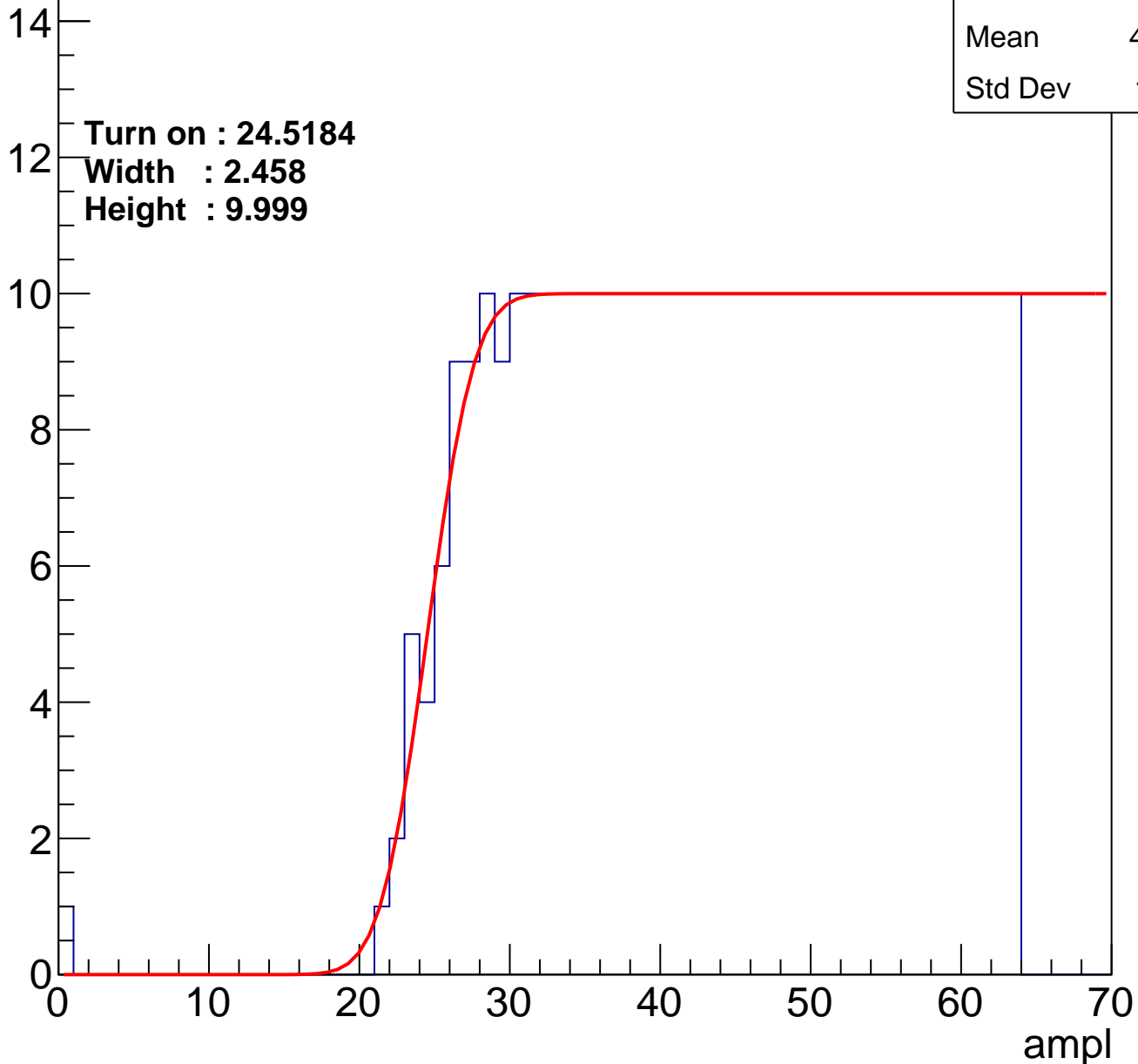
Entries	396
Mean	43.57
Std Dev	11.71

Turn on : 24.5184

Width : 2.458

Height : 9.999

Entry



B1L000S, U24-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.62
Std Dev	11.19

Turn on : 27.2587

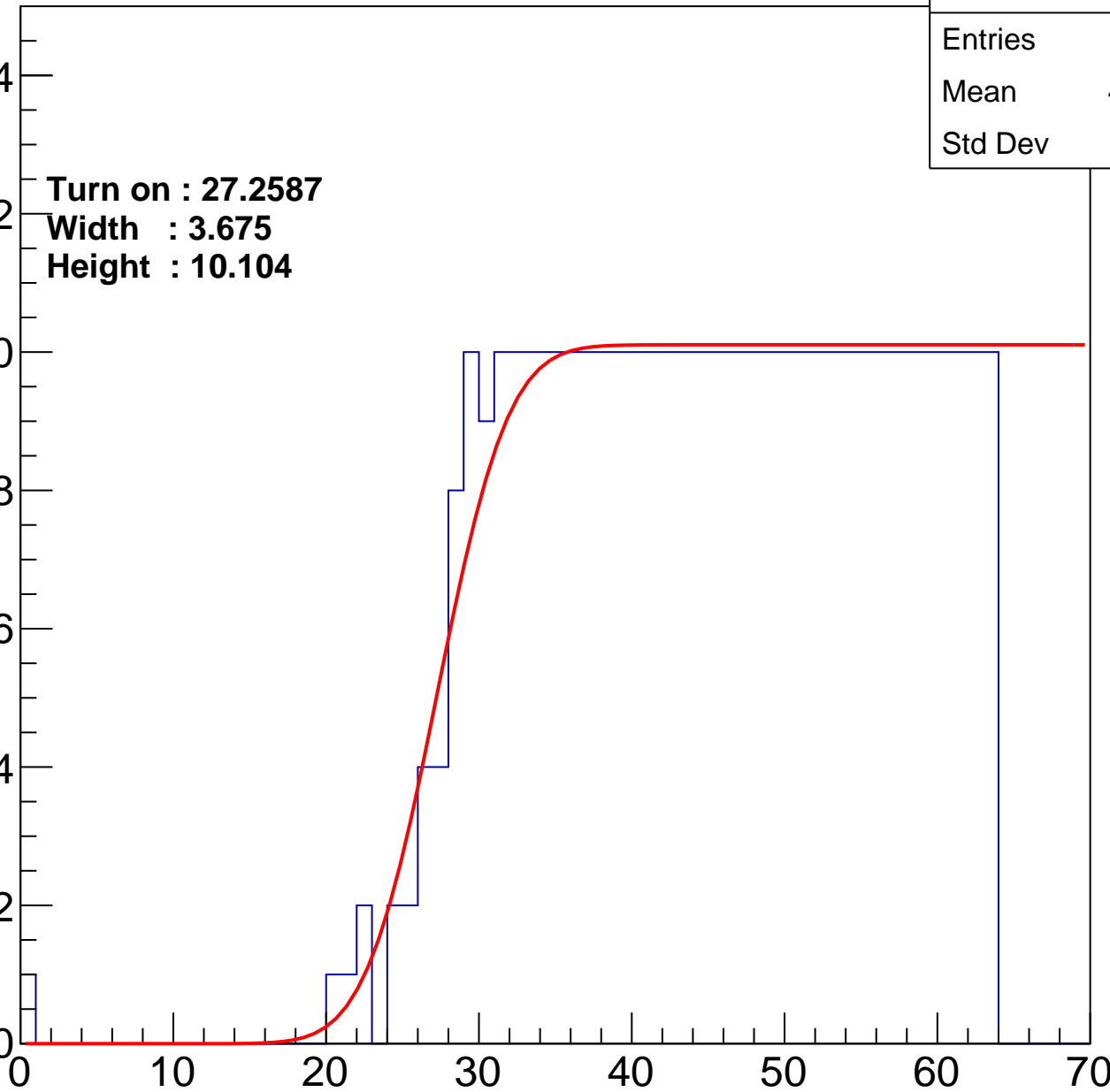
Width : 3.675

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.23
Std Dev	10.84

Turn on : 28.5167

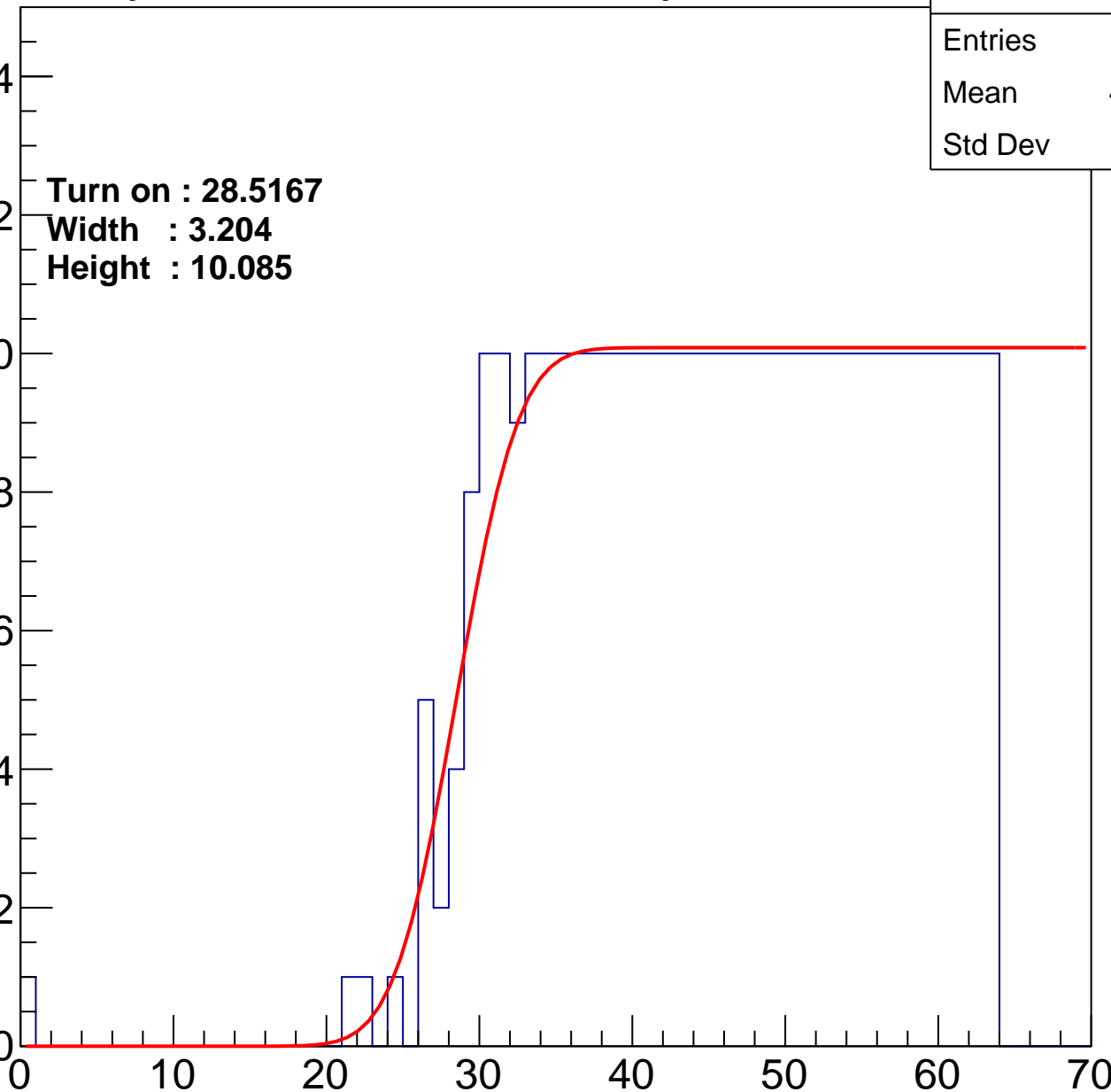
Width : 3.204

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.41
Std Dev	11.26

Turn on : 26.4466

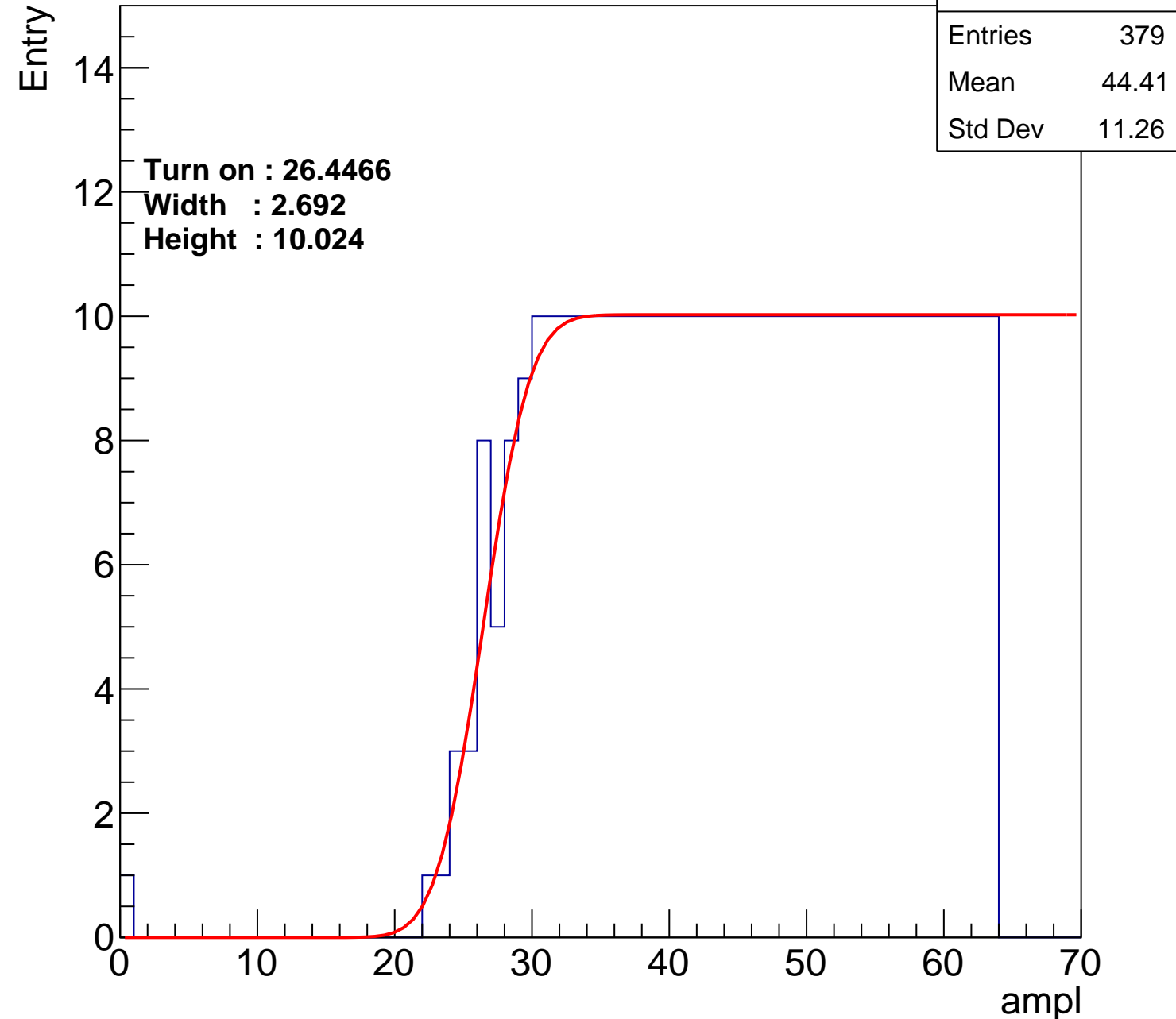
Width : 2.692

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.2707

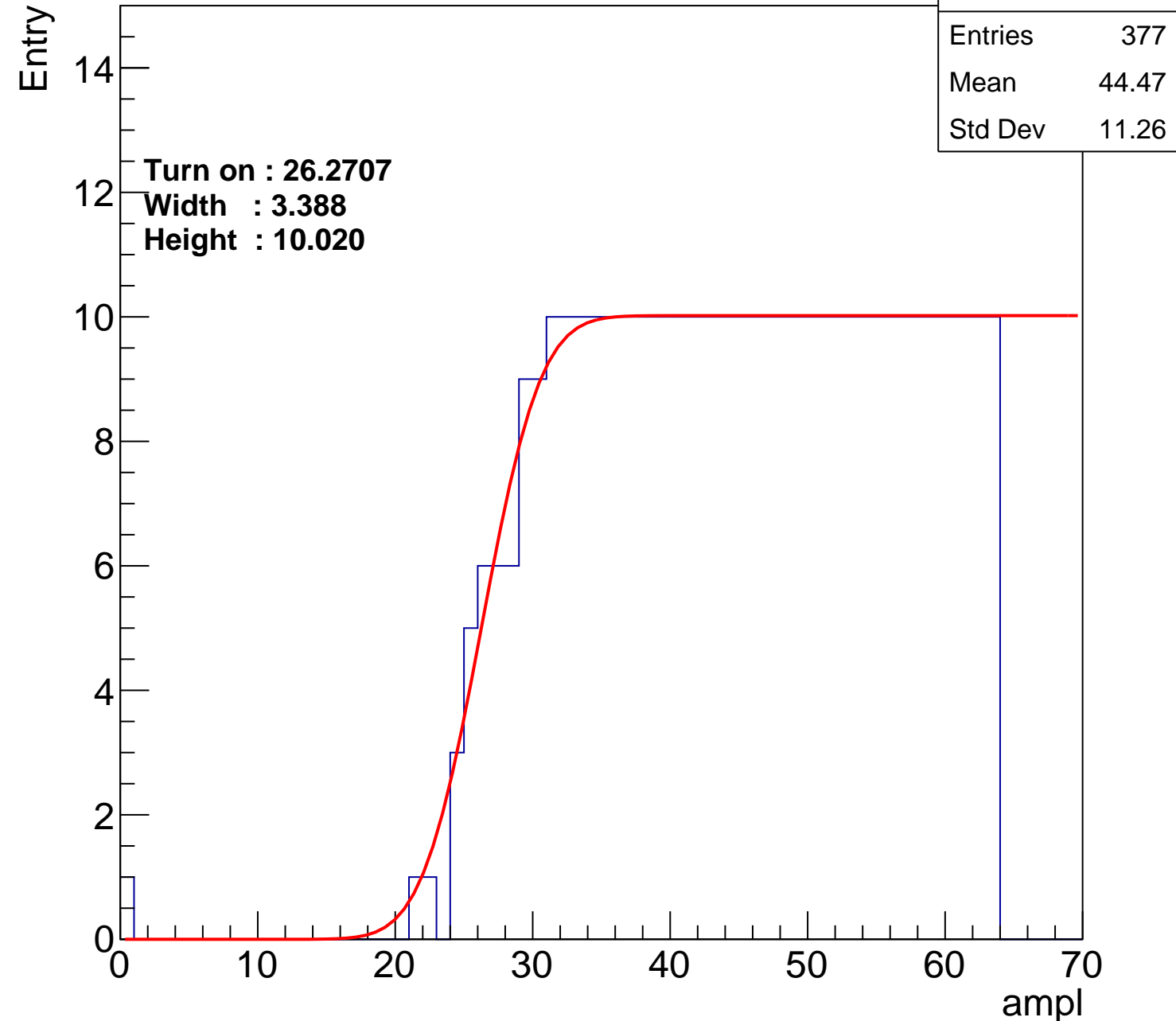
Width : 3.388

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.72
Std Dev	11.21

Turn on : 27.0624

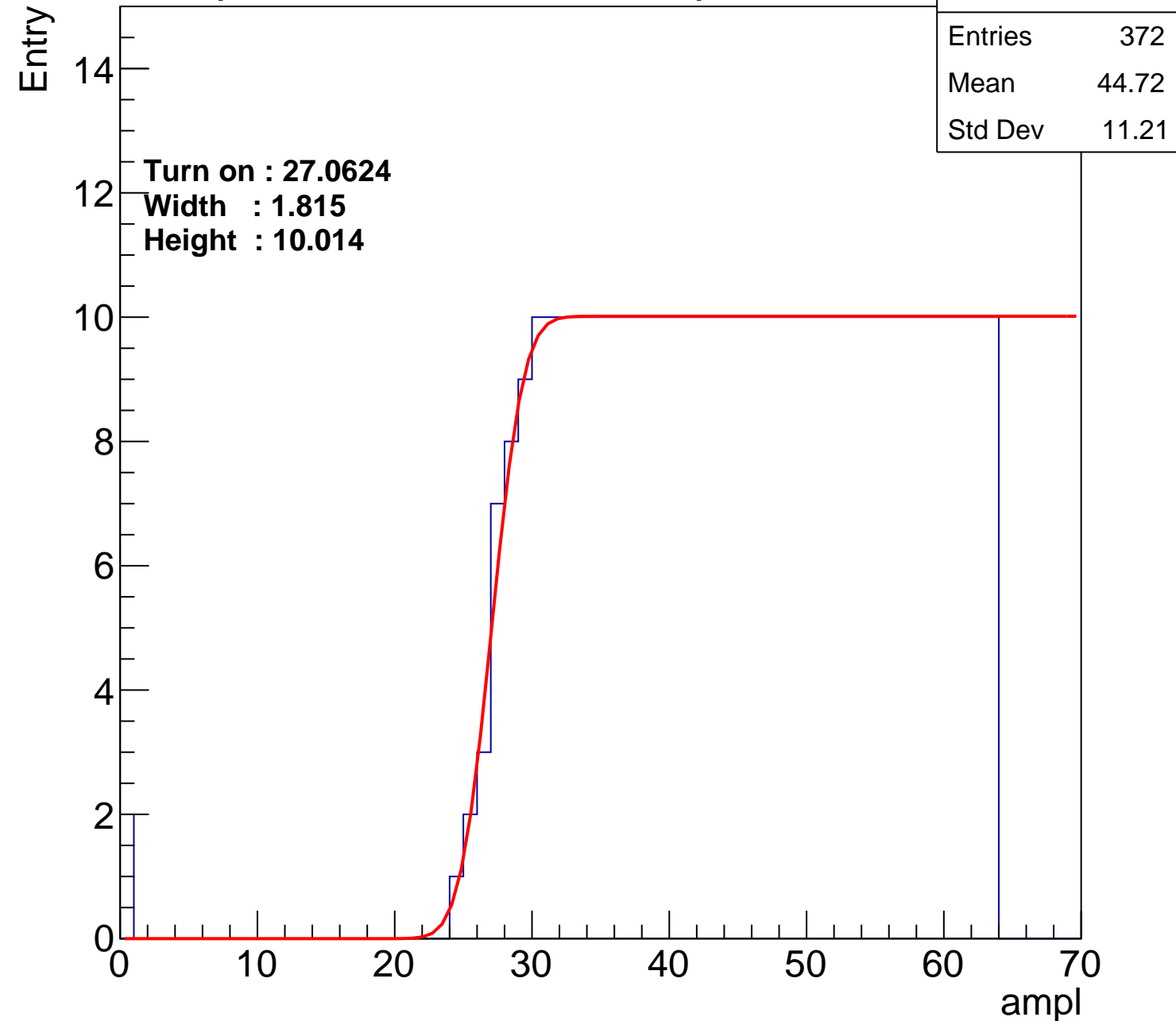
Width : 1.815

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch39

calib_packv5_042523_0143.root, FC#3, port B3

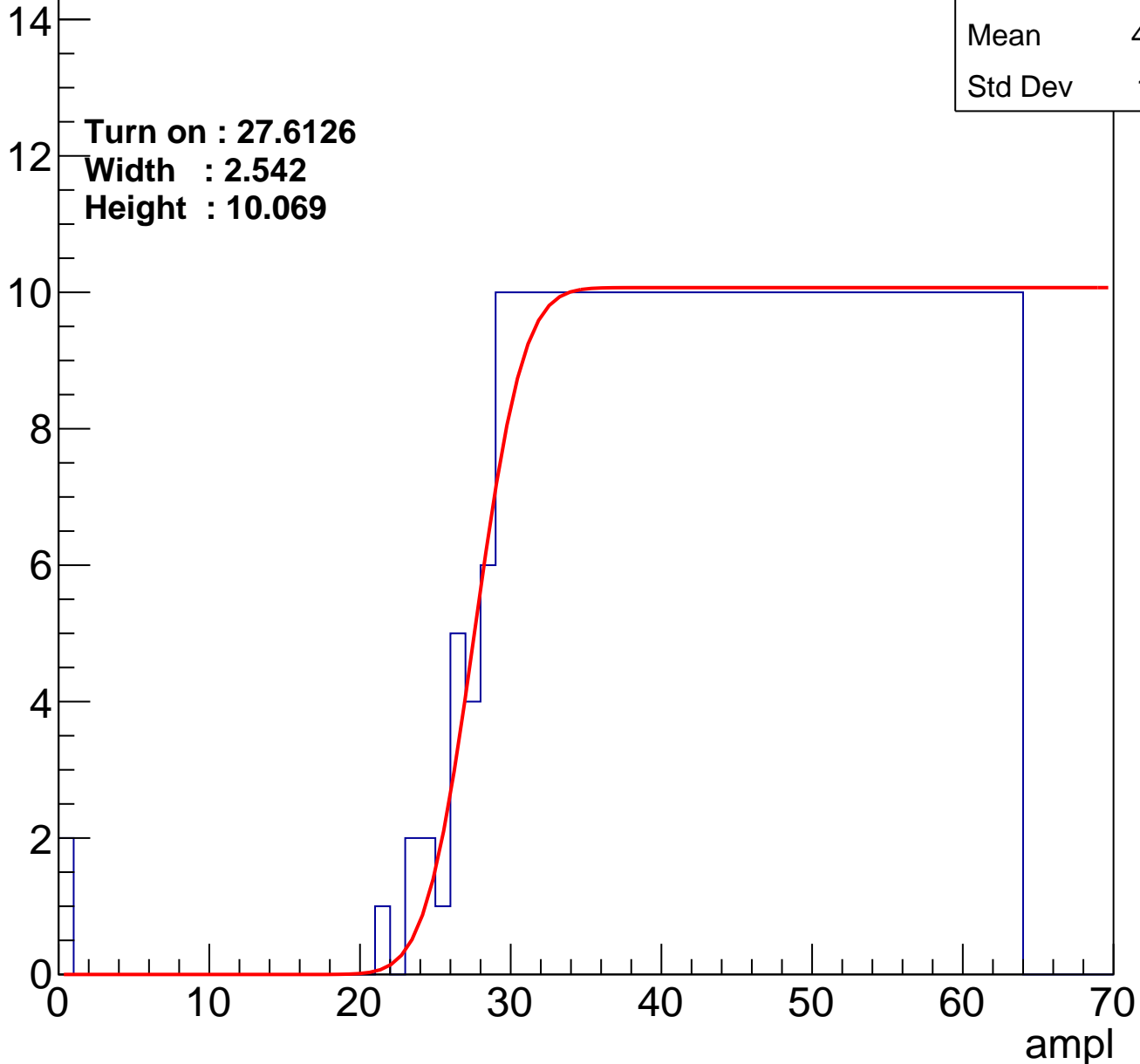
Entries	373
Mean	44.63
Std Dev	11.31

Turn on : 27.6126

Width : 2.542

Height : 10.069

Entry



B1L000S, U24-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.7
Std Dev	11.11

Turn on : 26.6840

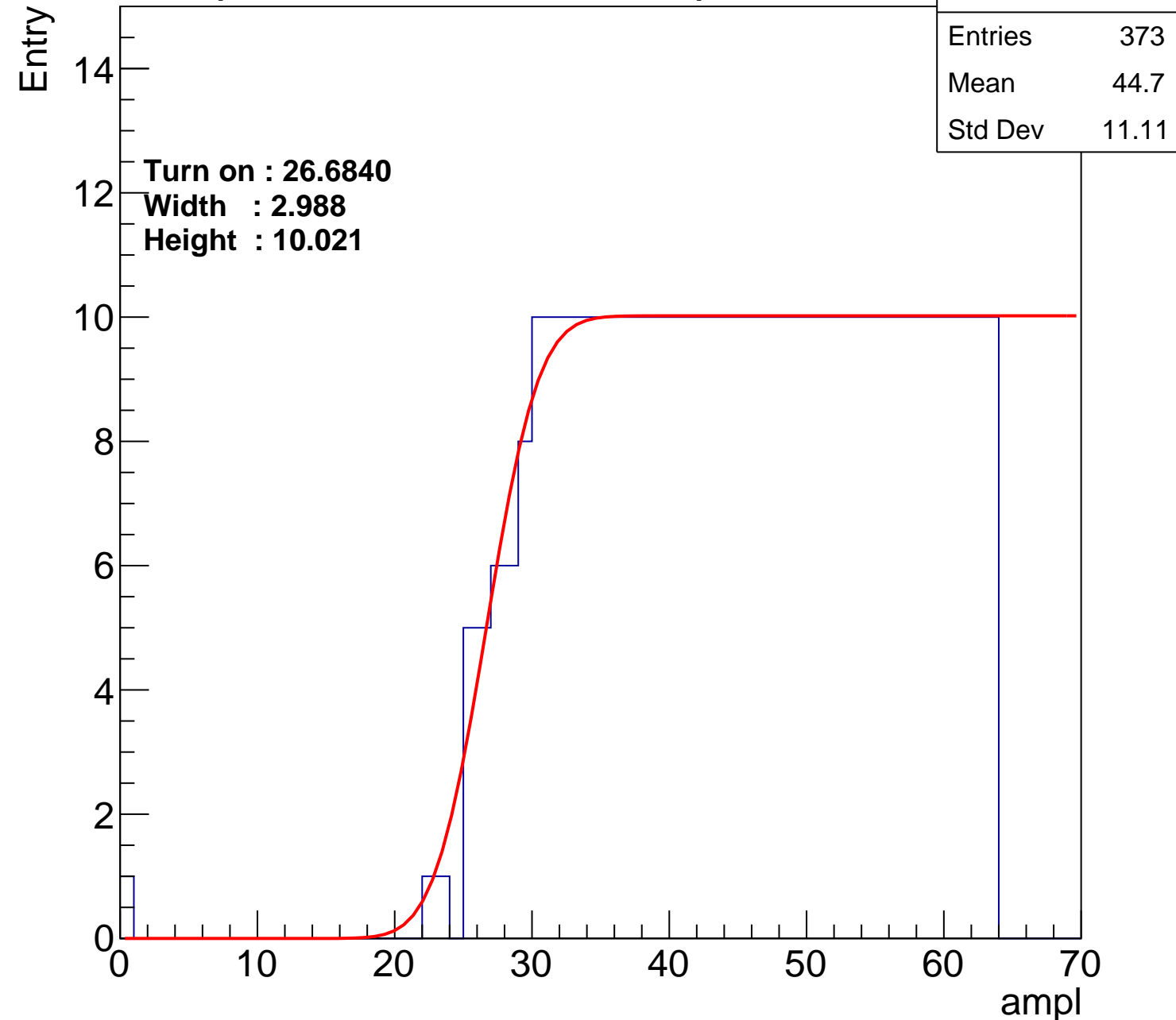
Width : 2.988

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch41

calib_packv5_042523_0143.root, FC#3, port B3

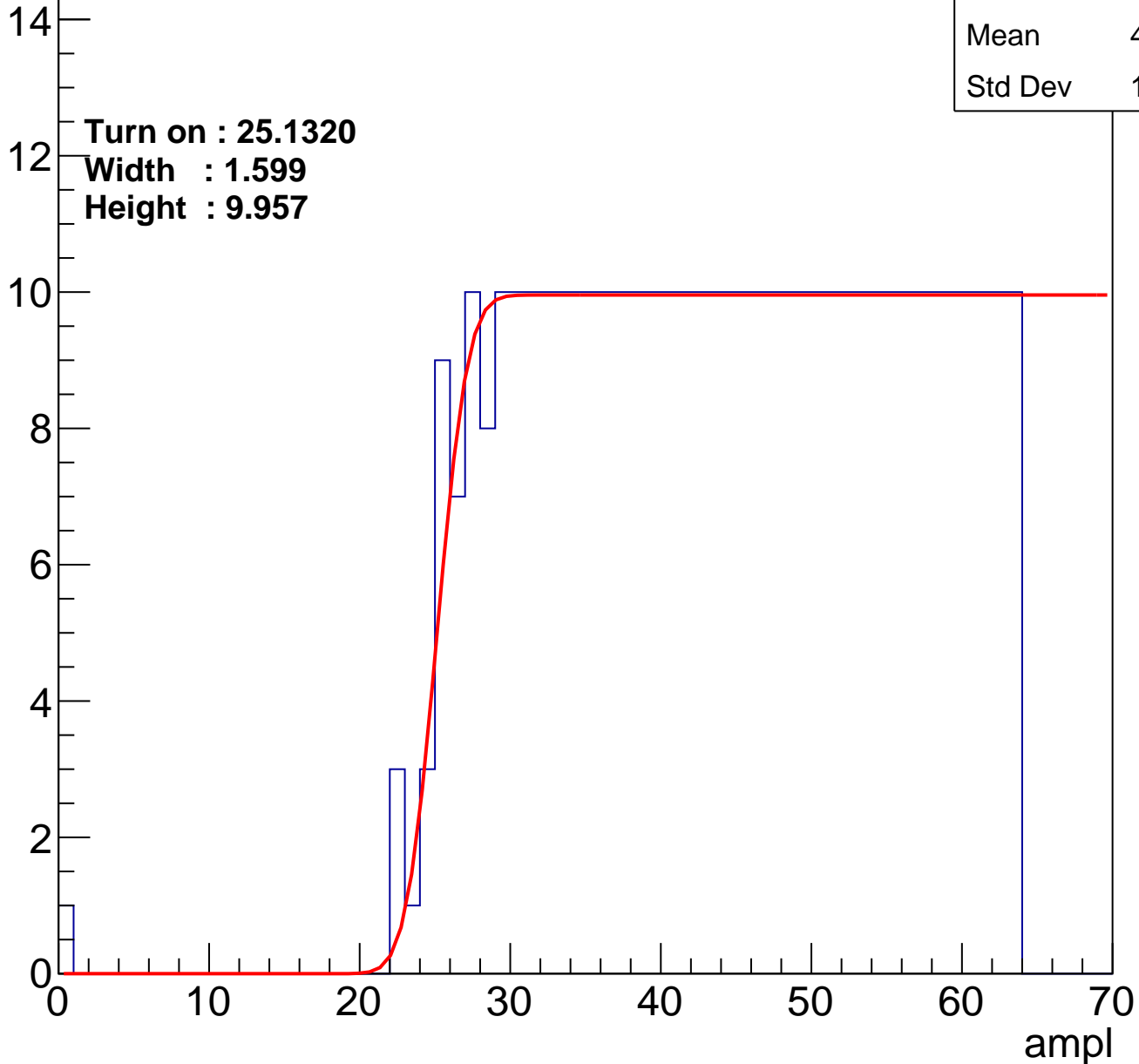
Entries	392
Mean	43.78
Std Dev	11.58

Turn on : 25.1320

Width : 1.599

Height : 9.957

Entry



B1L000S, U24-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.61
Std Dev	11.32

Turn on : 26.6258

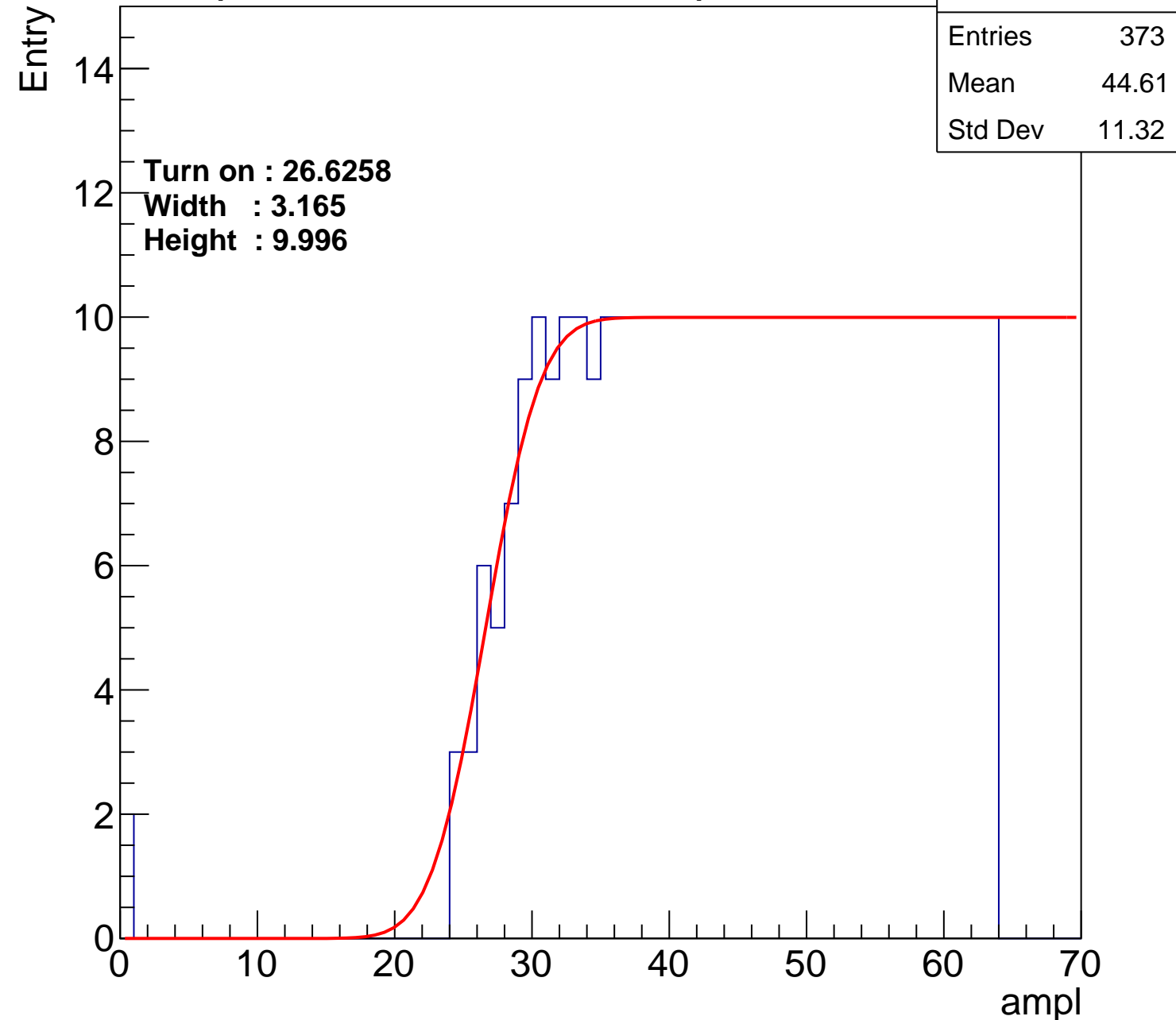
Width : 3.165

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.89
Std Dev	11.22

Turn on : 28.0460

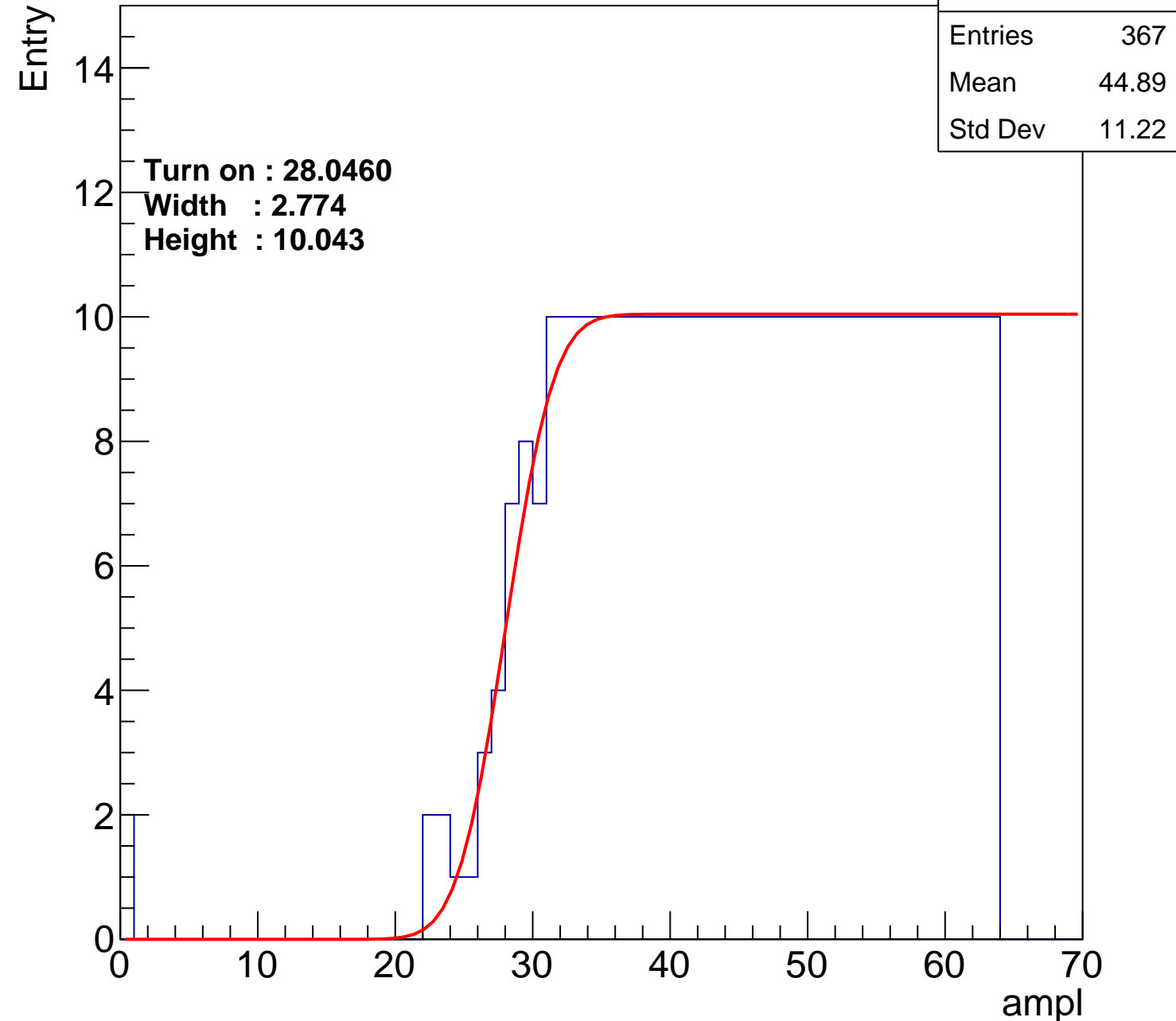
Width : 2.774

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.6409

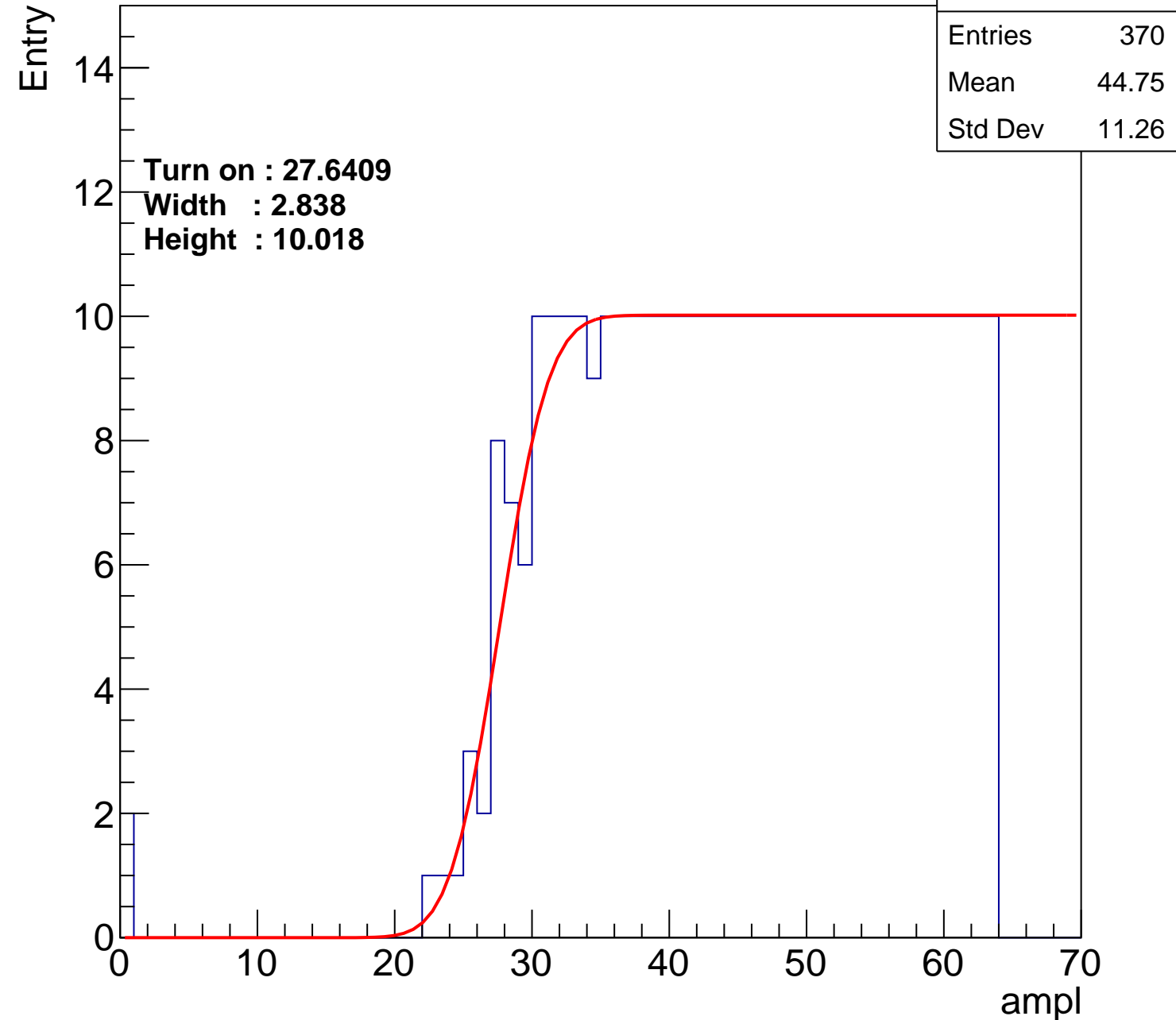
Width : 2.838

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch45

calib_packv5_042523_0143.root, FC#3, port B3

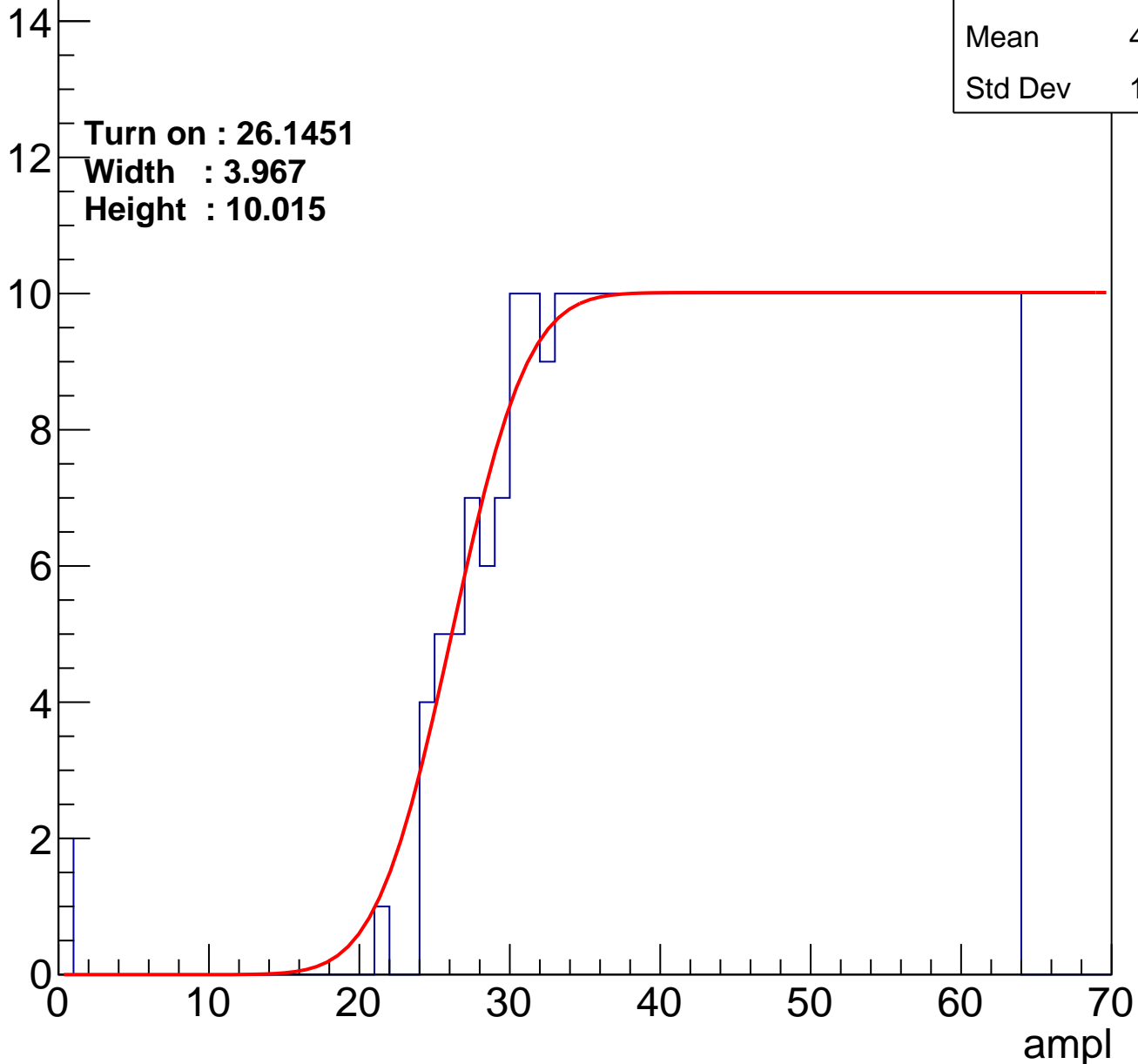
Entries	376
Mean	44.44
Std Dev	11.44

Turn on : 26.1451

Width : 3.967

Height : 10.015

Entry



B1L000S, U24-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.22
Std Dev	11.72

Turn on : 27.2797

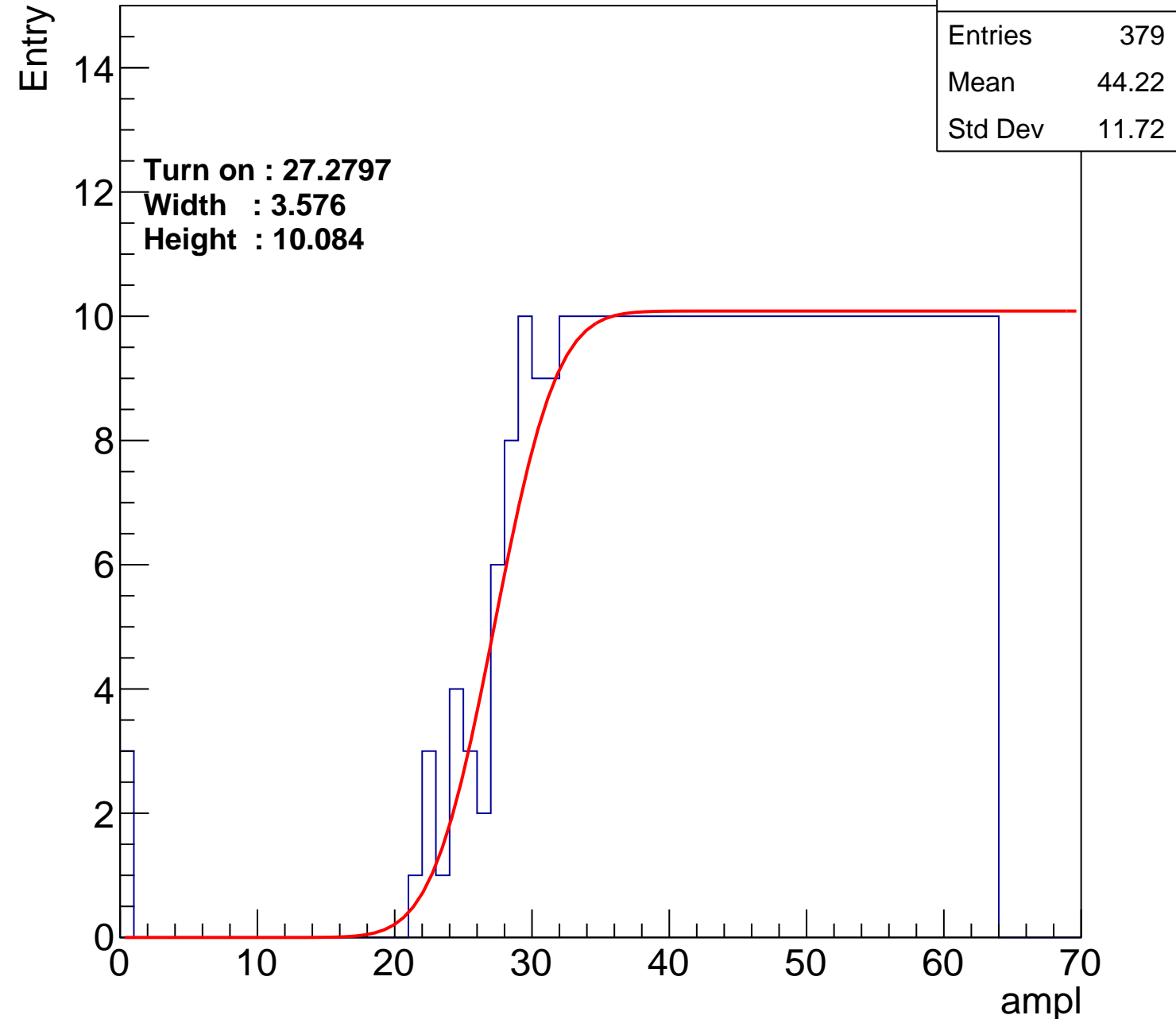
Width : 3.576

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.84
Std Dev	11.05

Turn on : 26.6471

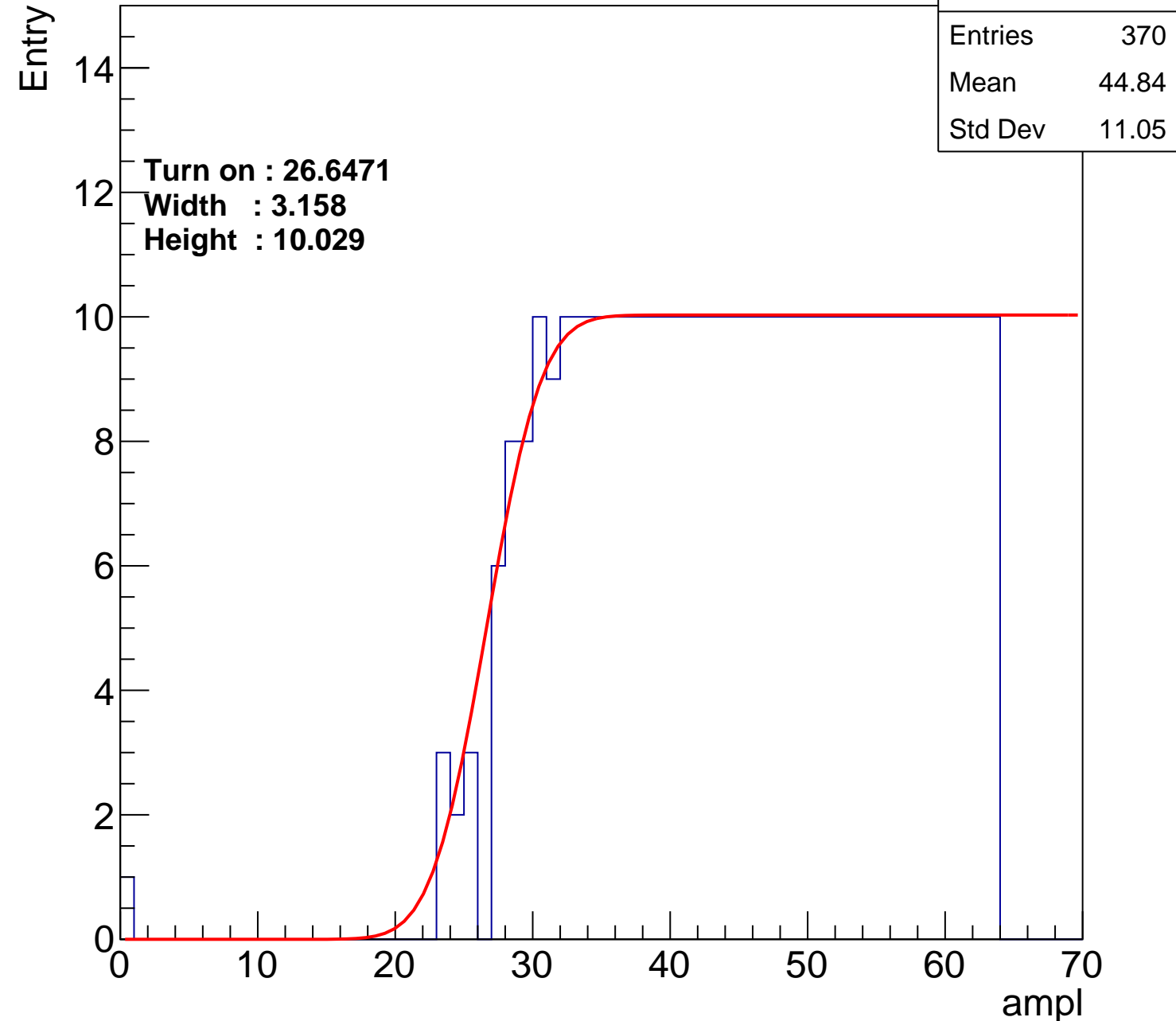
Width : 3.158

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.3926

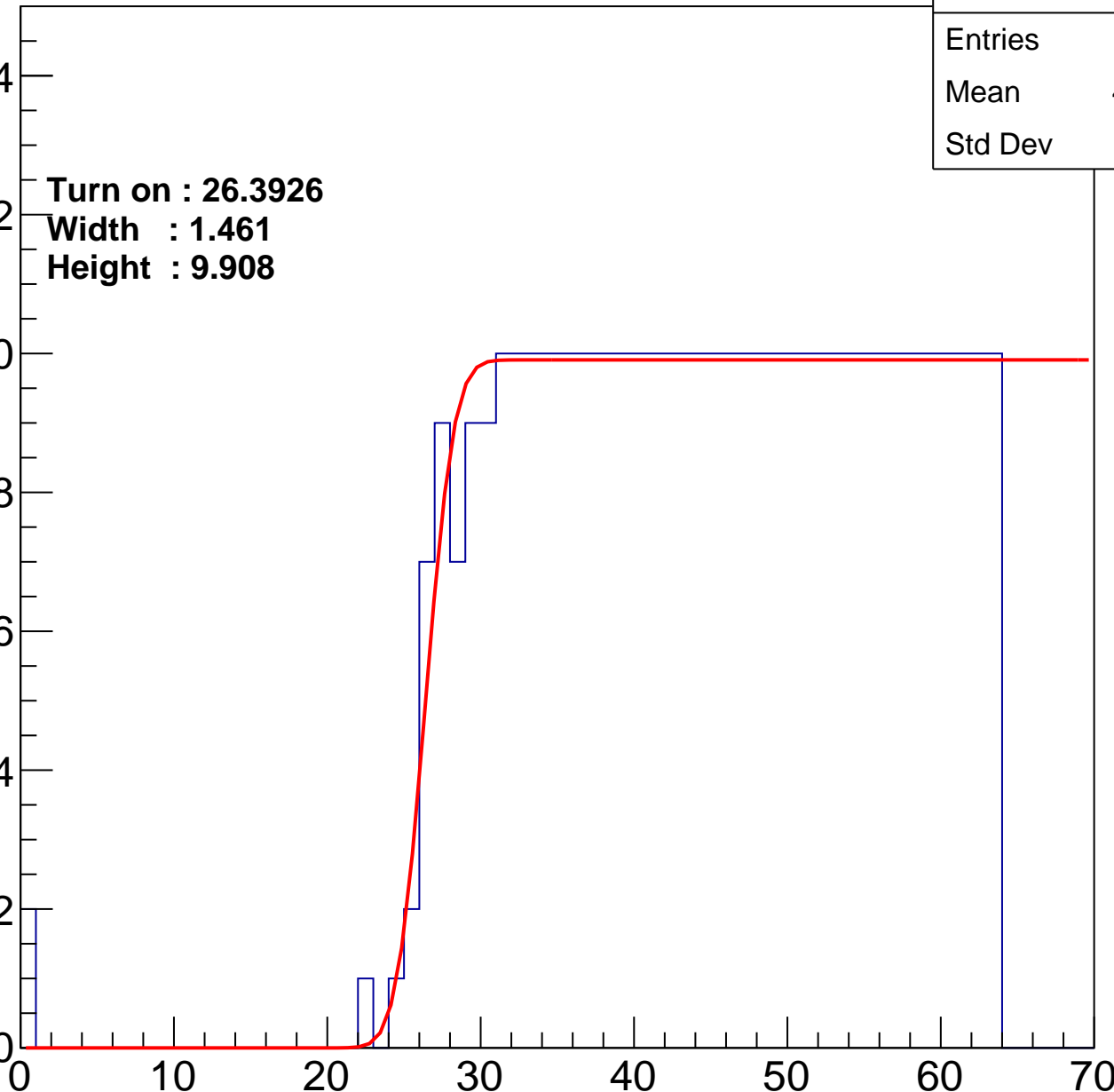
Width : 1.461

Height : 9.908

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 26.7379

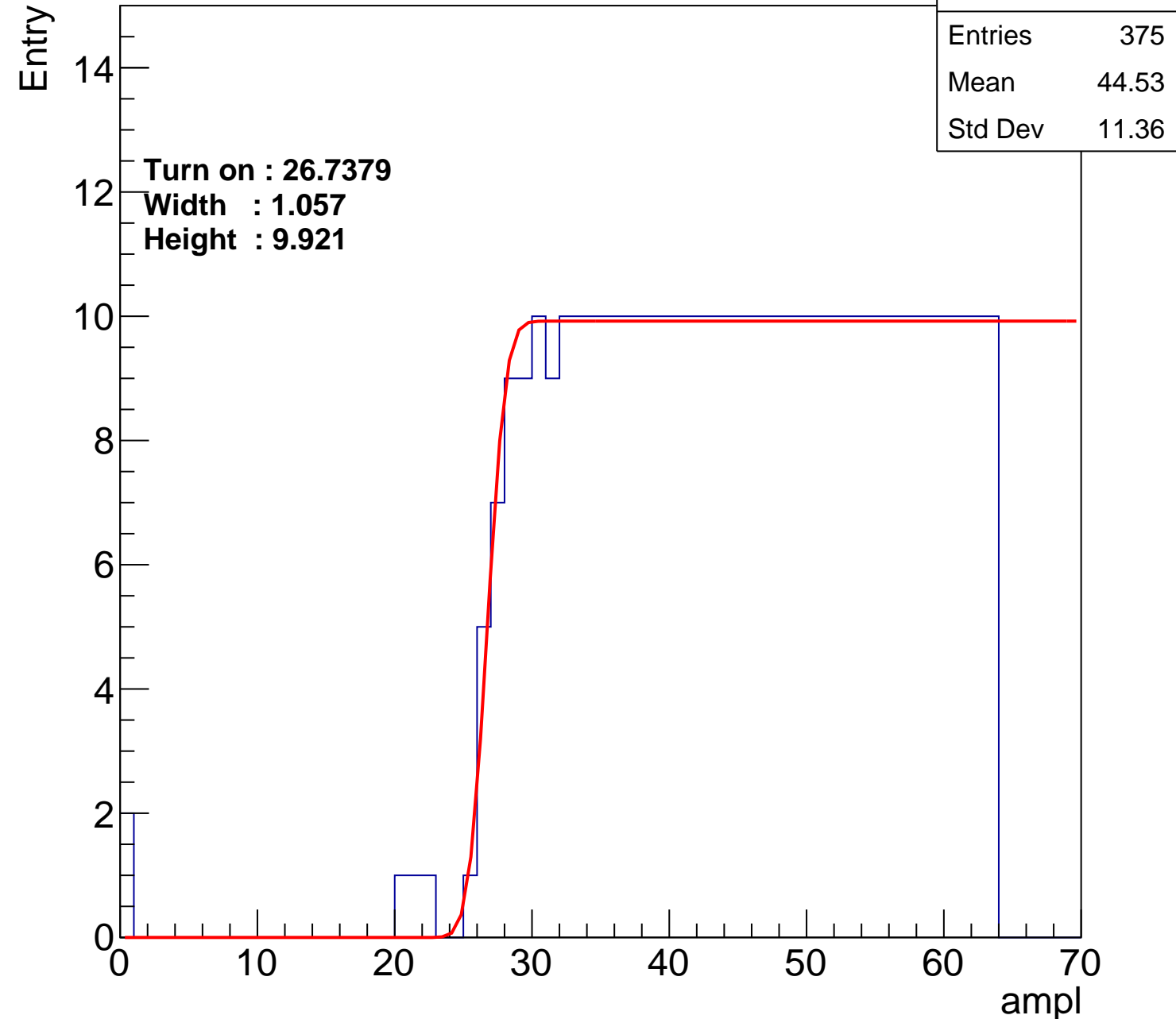
Width : 1.057

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch50

calib_packv5_042523_0143.root, FC#3, port B3

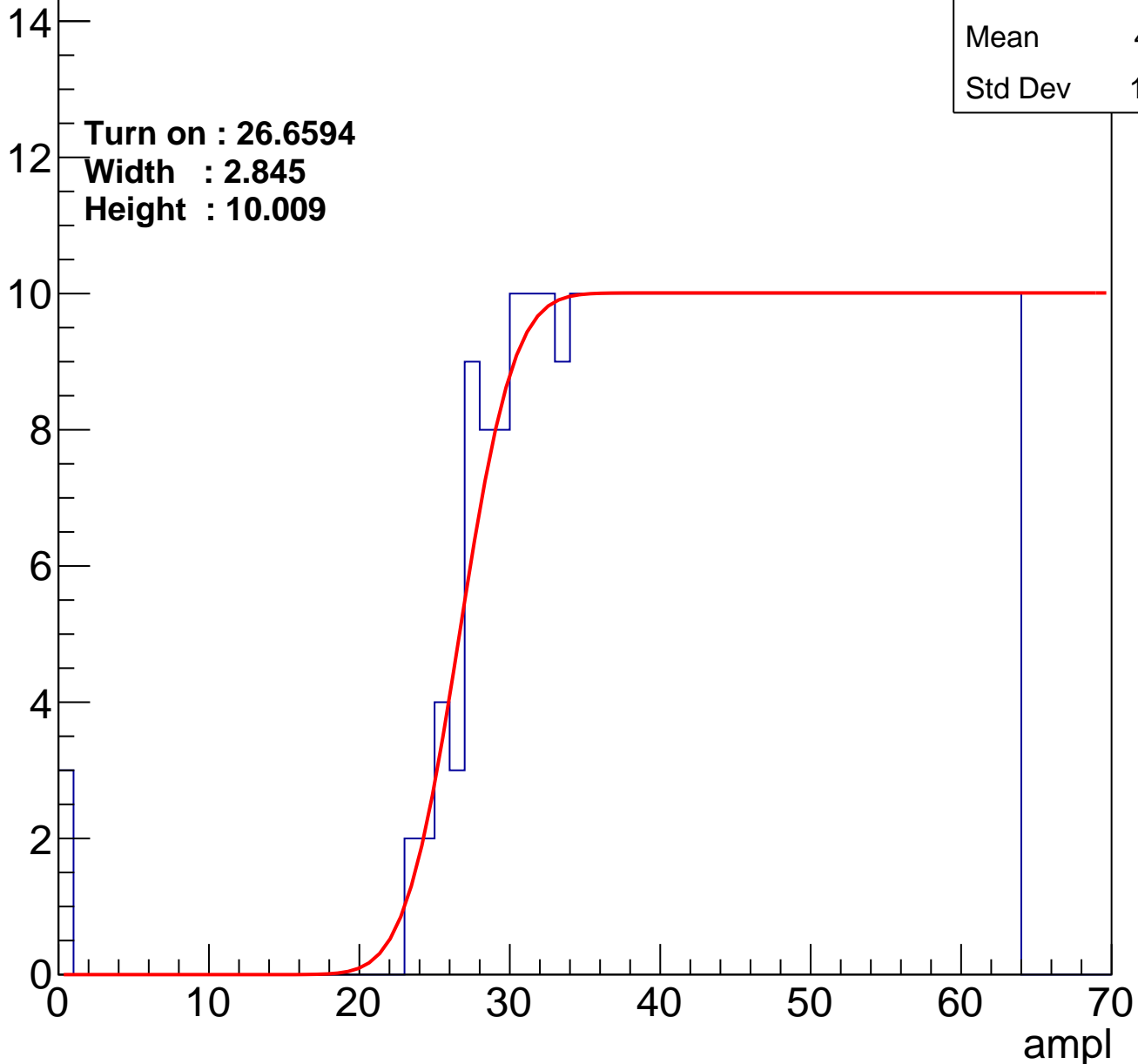
Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.6594

Width : 2.845

Height : 10.009

Entry



B1L000S, U24-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.8659

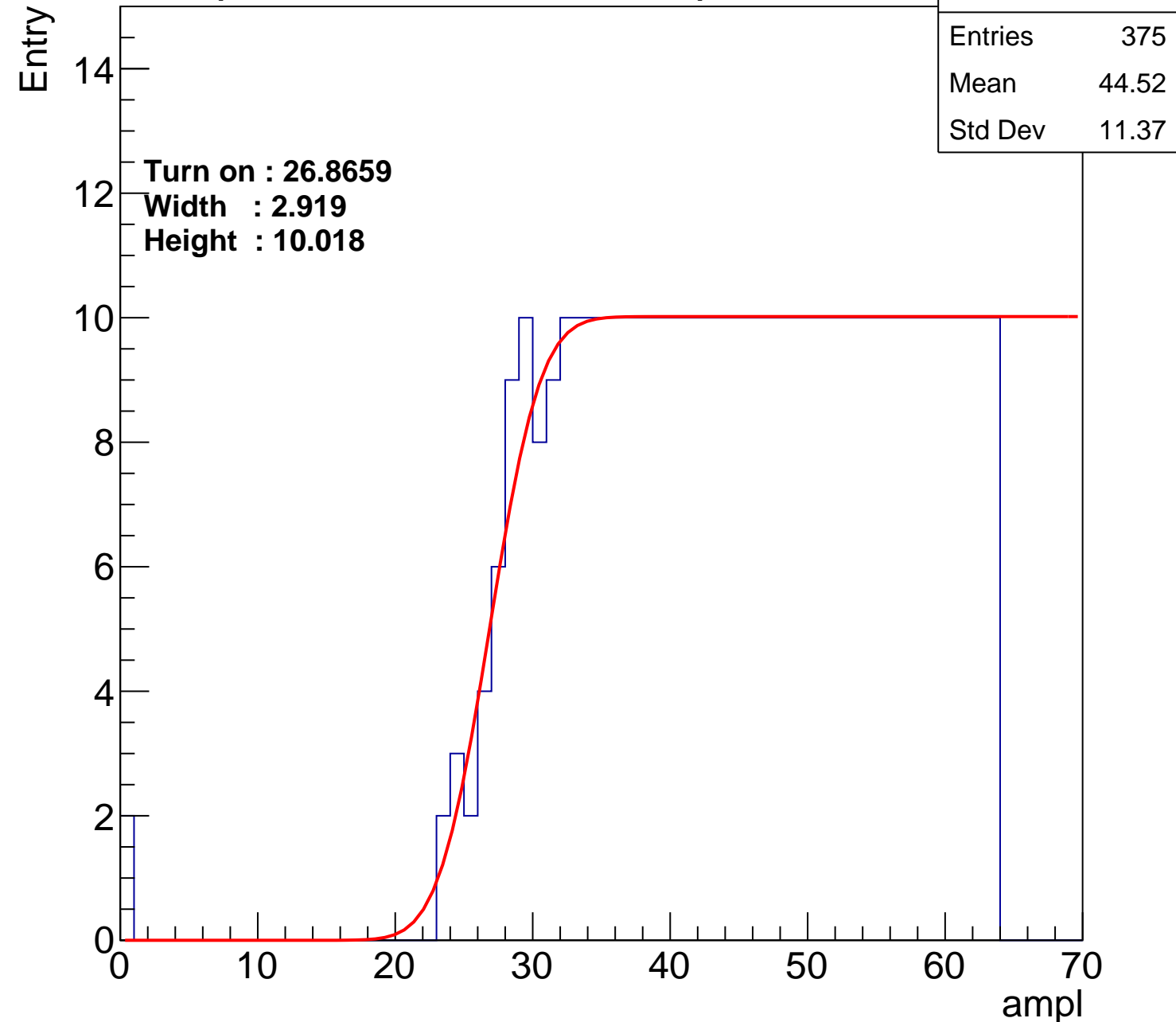
Width : 2.919

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.97
Std Dev	10.93

Turn on : 27.4894

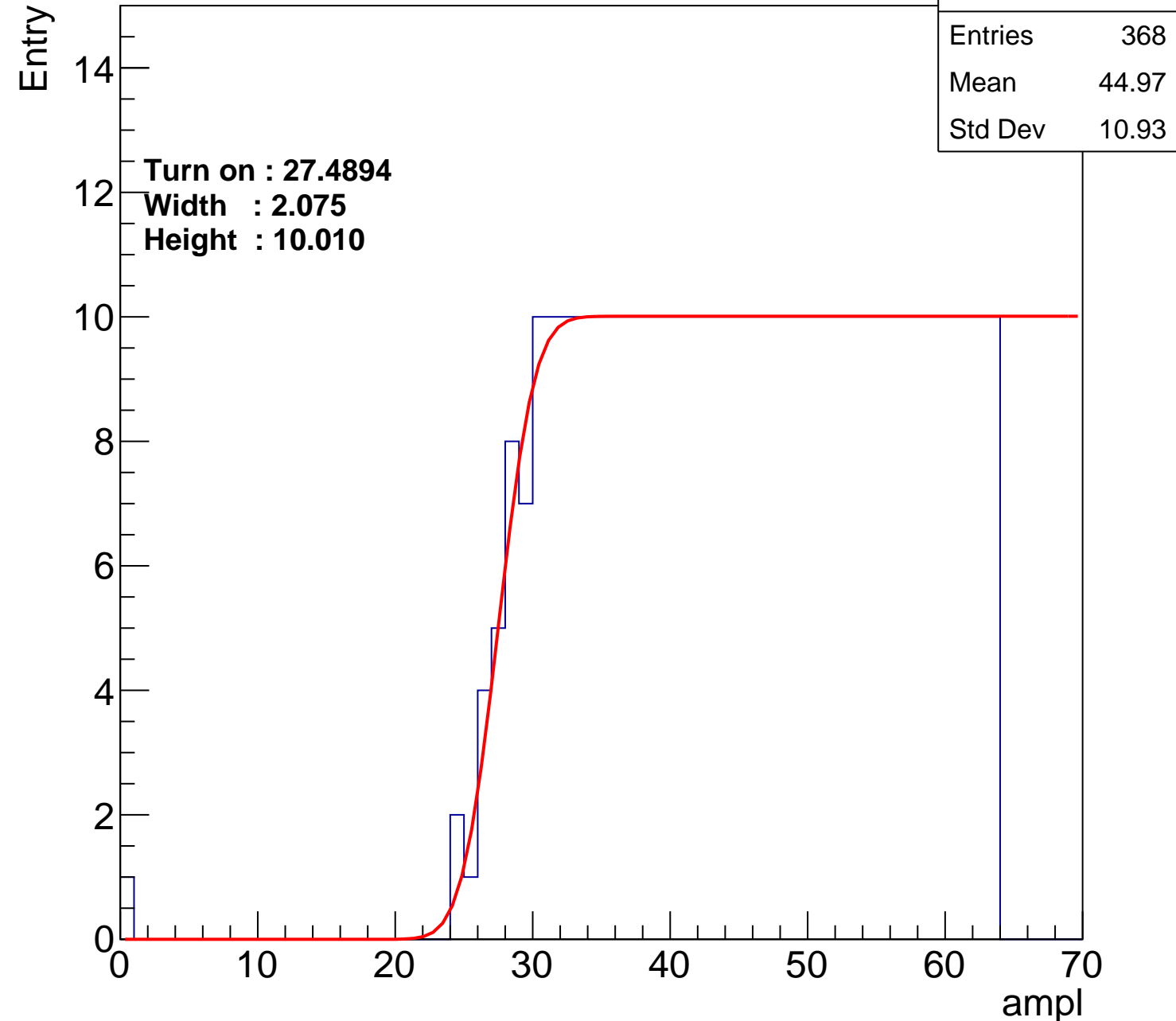
Width : 2.075

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.29
Std Dev	11.47

Turn on : 26.3458

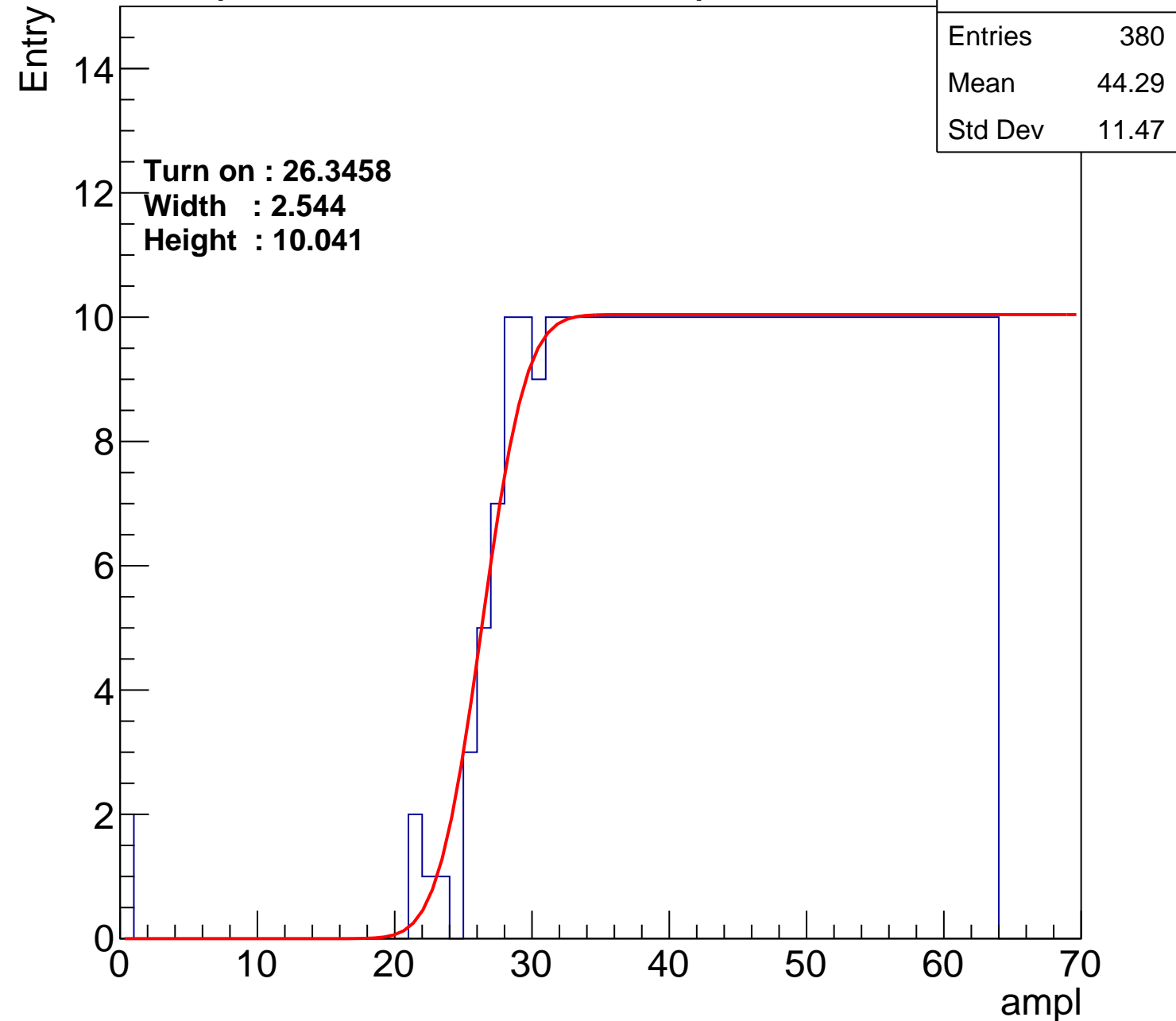
Width : 2.544

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.54

Turn on : 26.4361

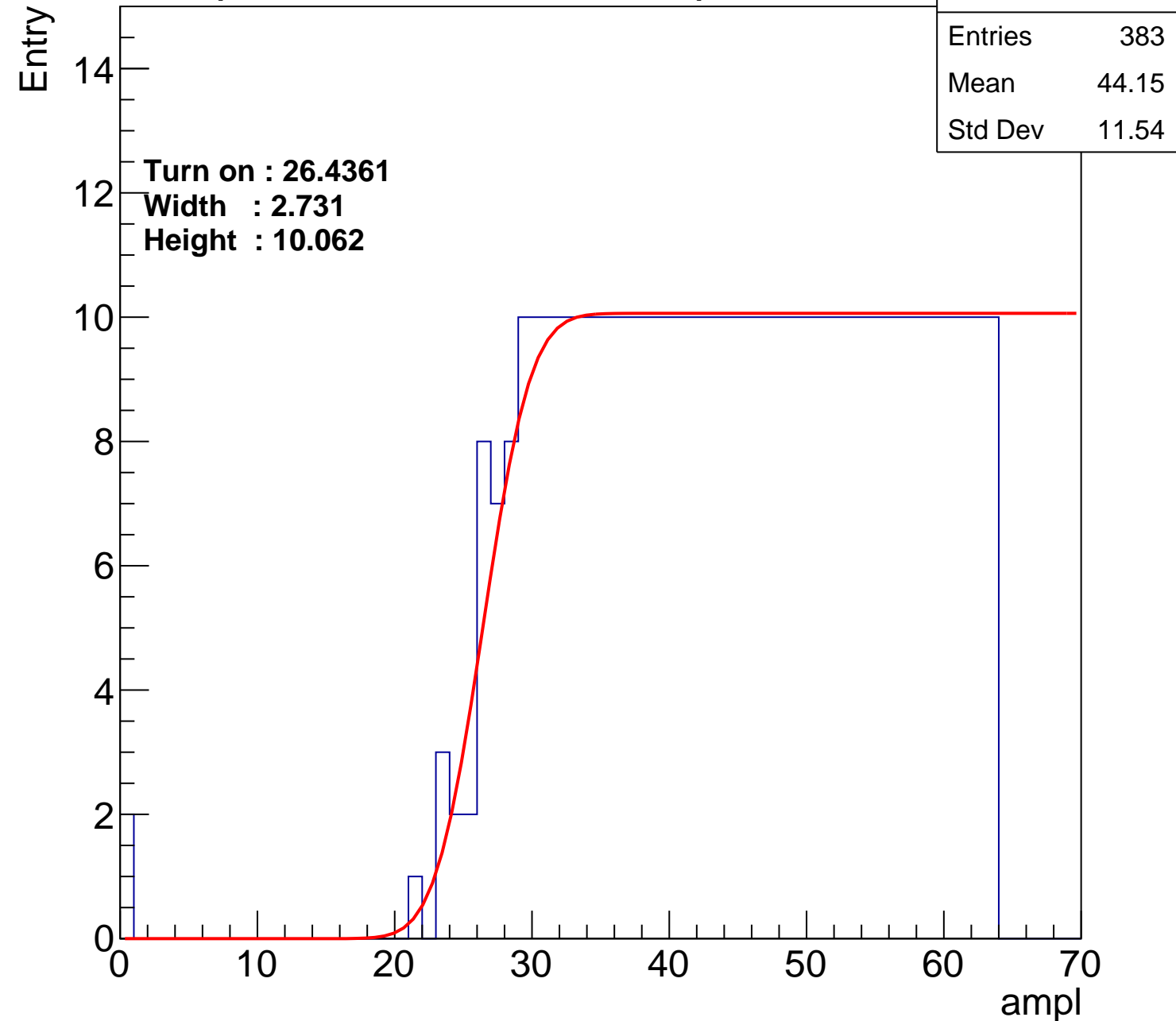
Width : 2.731

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch55

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.9
Std Dev	11.79

Turn on : 25.8885

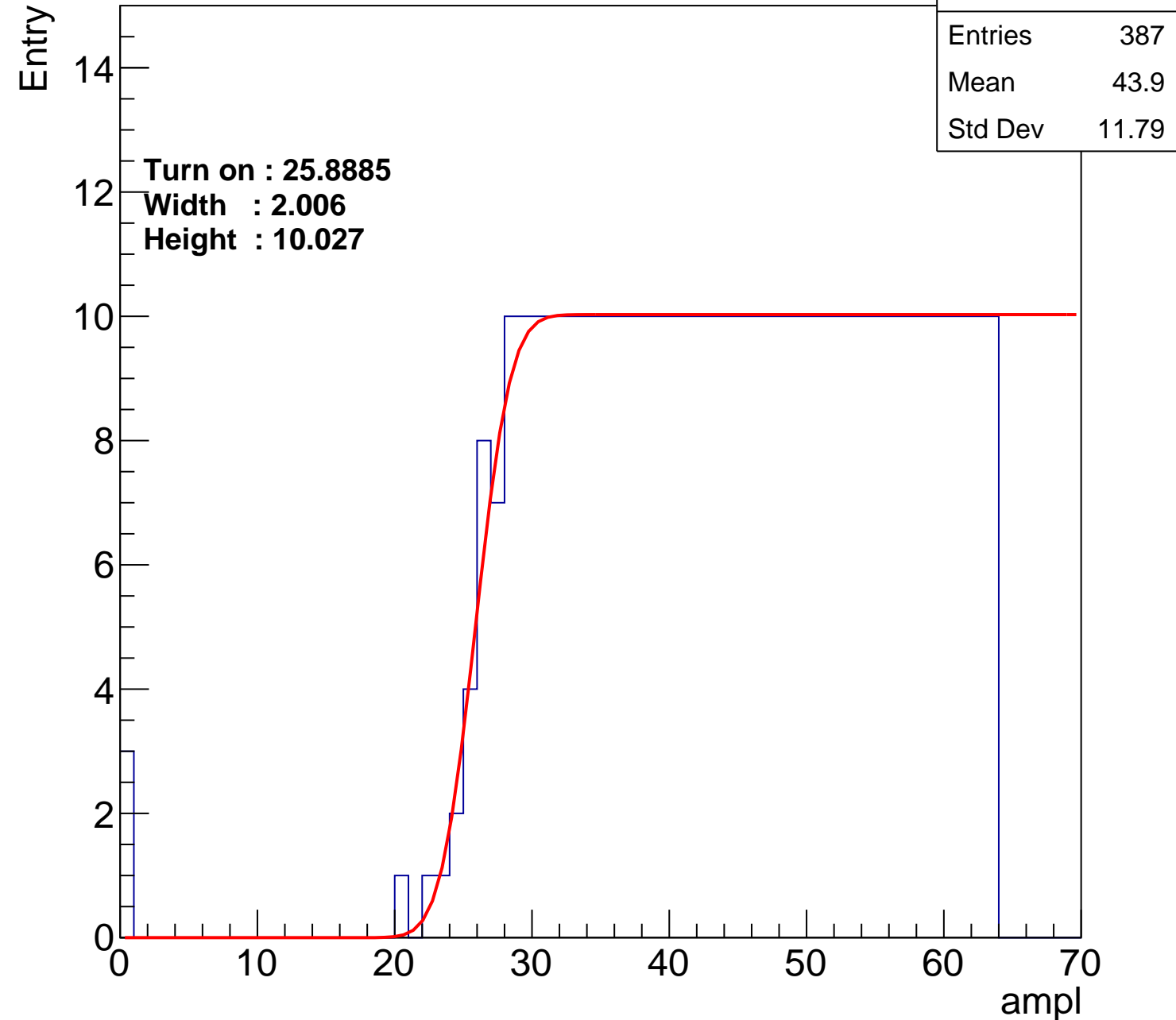
Width : 2.006

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch56

calib_packv5_042523_0143.root, FC#3, port B3

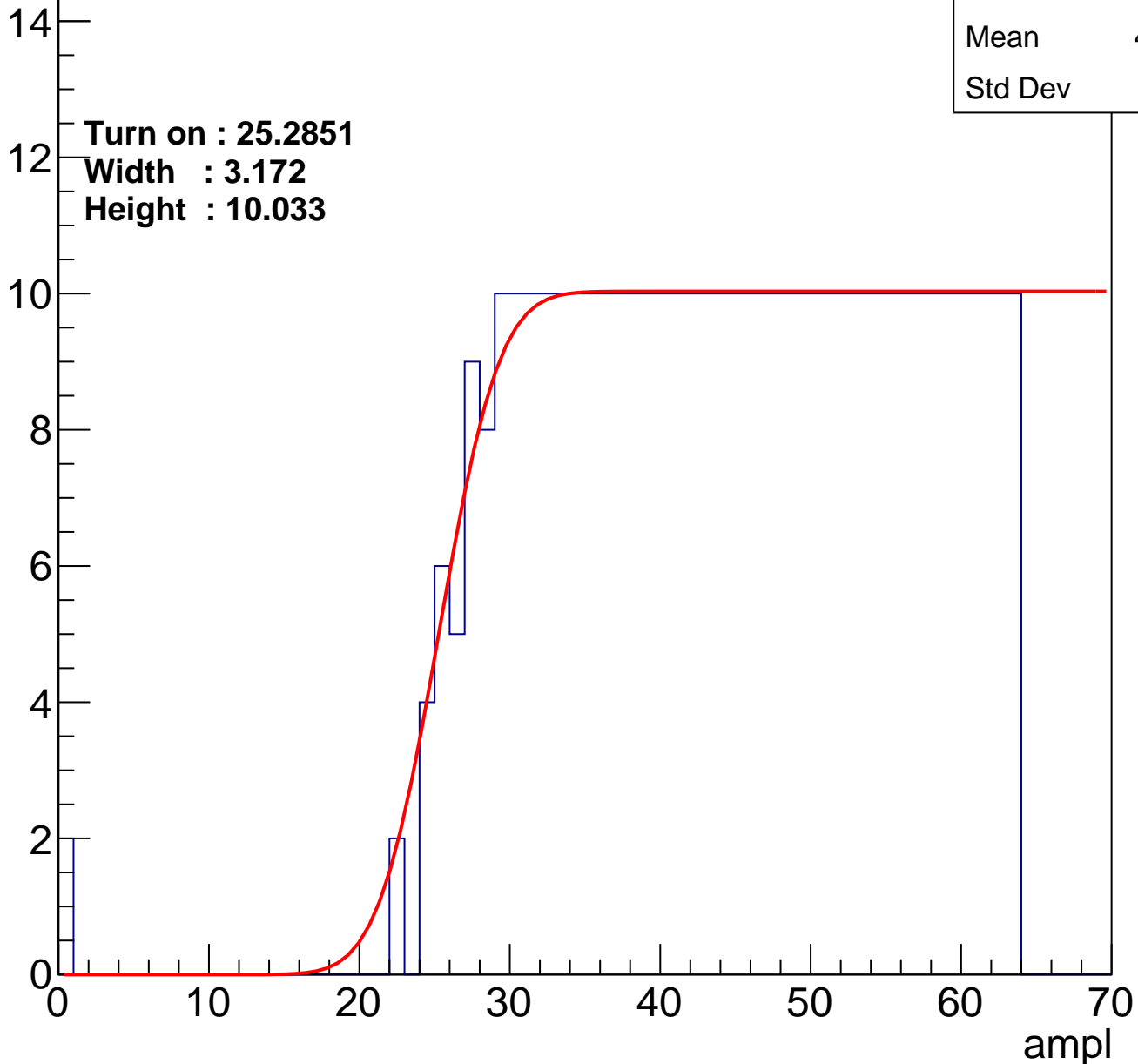
Entries	386
Mean	44.01
Std Dev	11.6

Turn on : 25.2851

Width : 3.172

Height : 10.033

Entry



B1L000S, U24-ch57

calib_packv5_042523_0143.root, FC#3, port B3

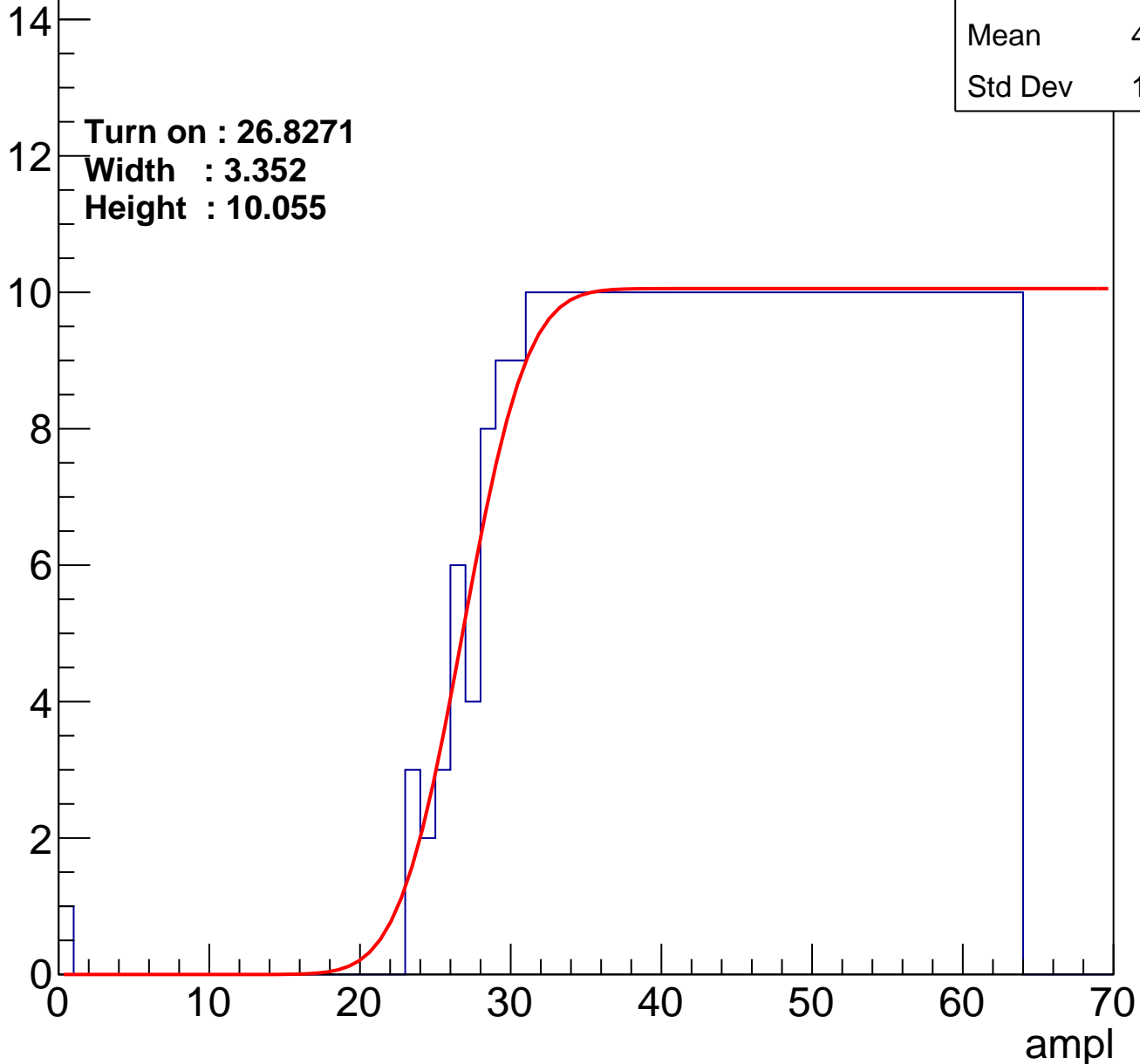
Entries	375
Mean	44.59
Std Dev	11.18

Turn on : 26.8271

Width : 3.352

Height : 10.055

Entry



B1L000S, U24-ch58

calib_packv5_042523_0143.root, FC#3, port B3

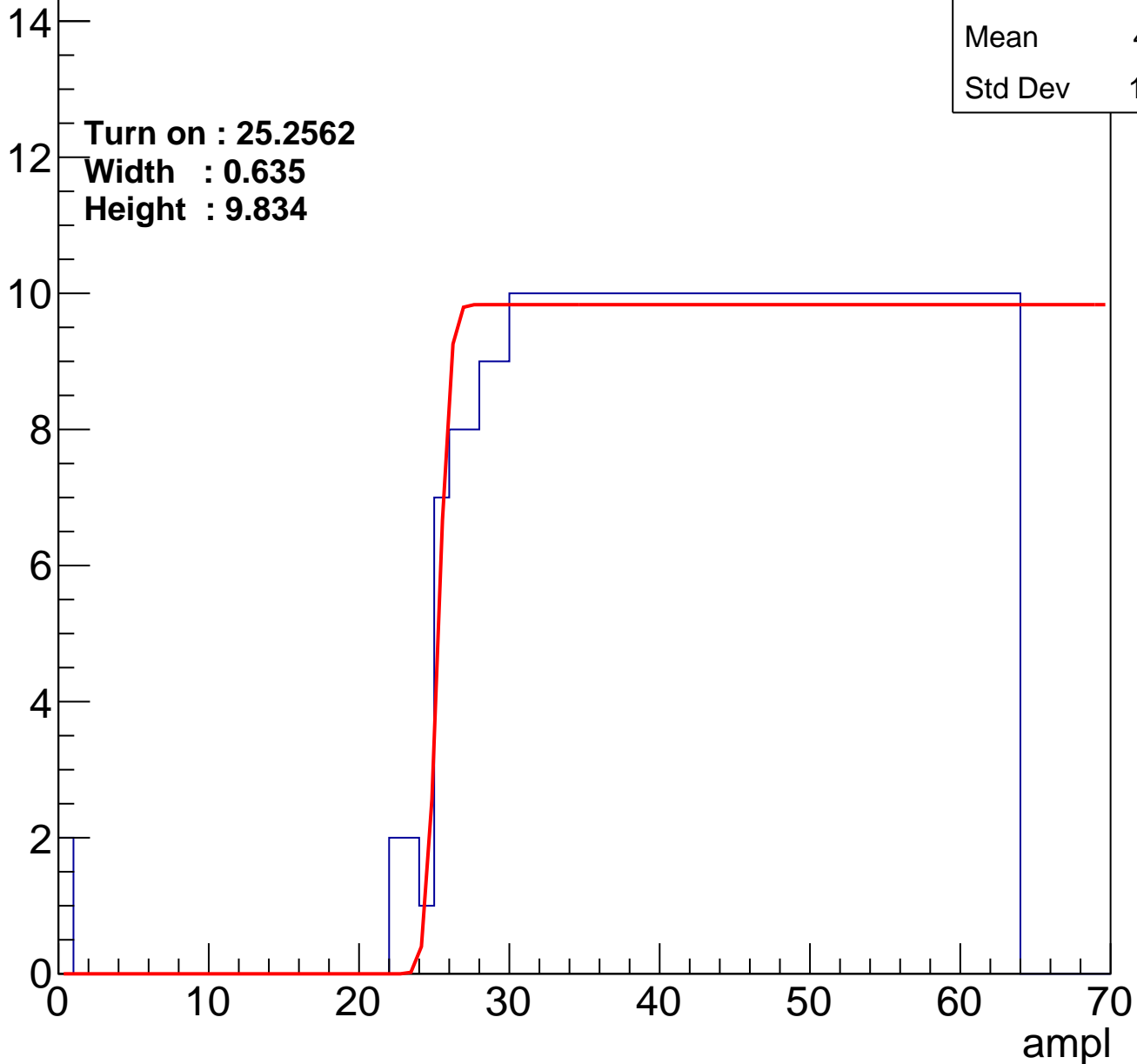
Entries	388
Mean	43.91
Std Dev	11.66

Turn on : 25.2562

Width : 0.635

Height : 9.834

Entry



B1L000S, U24-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.09
Std Dev	11.1

Turn on : 27.8232

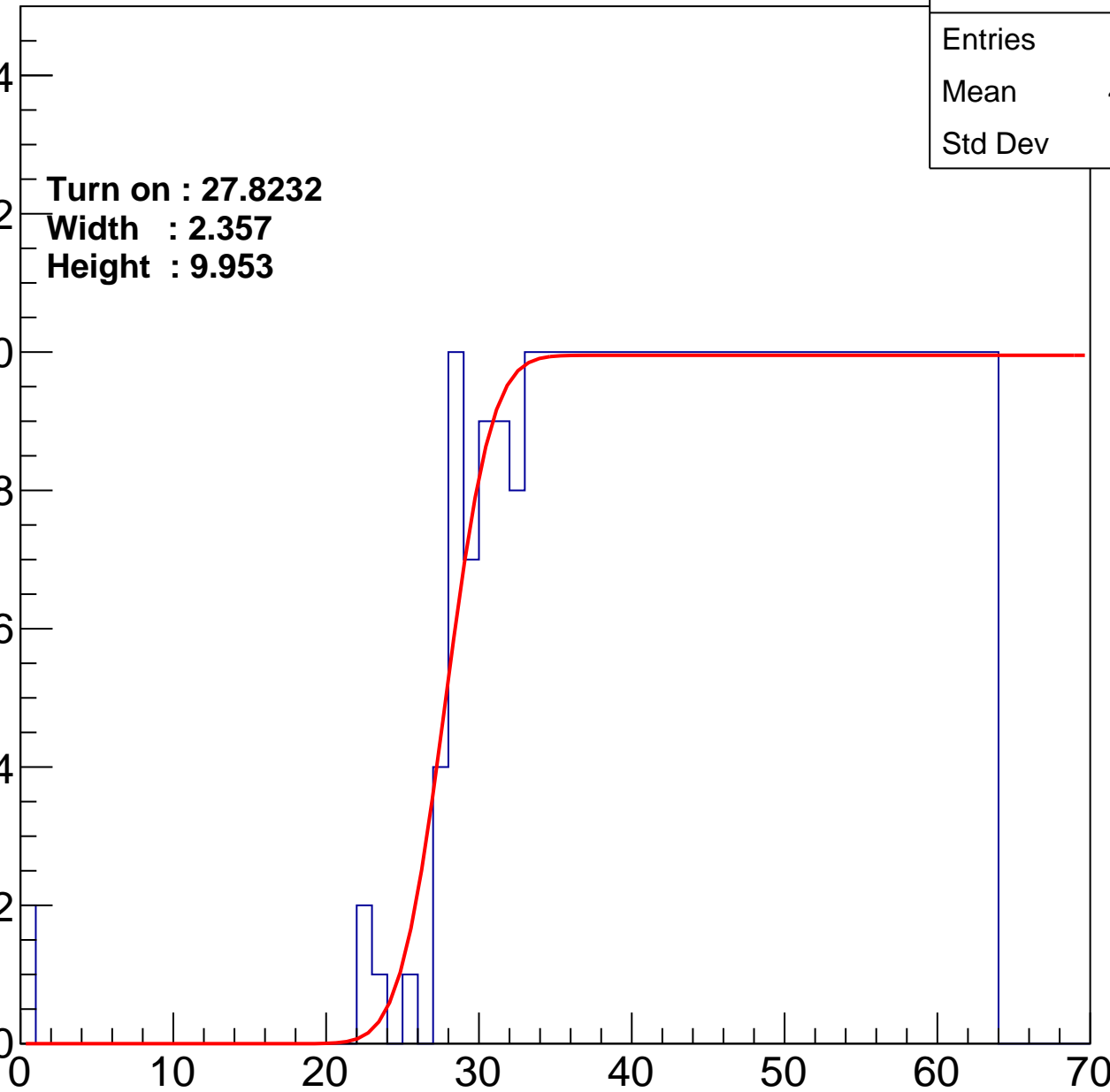
Width : 2.357

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch60

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.49
Std Dev	11.5

Turn on : 26.7094

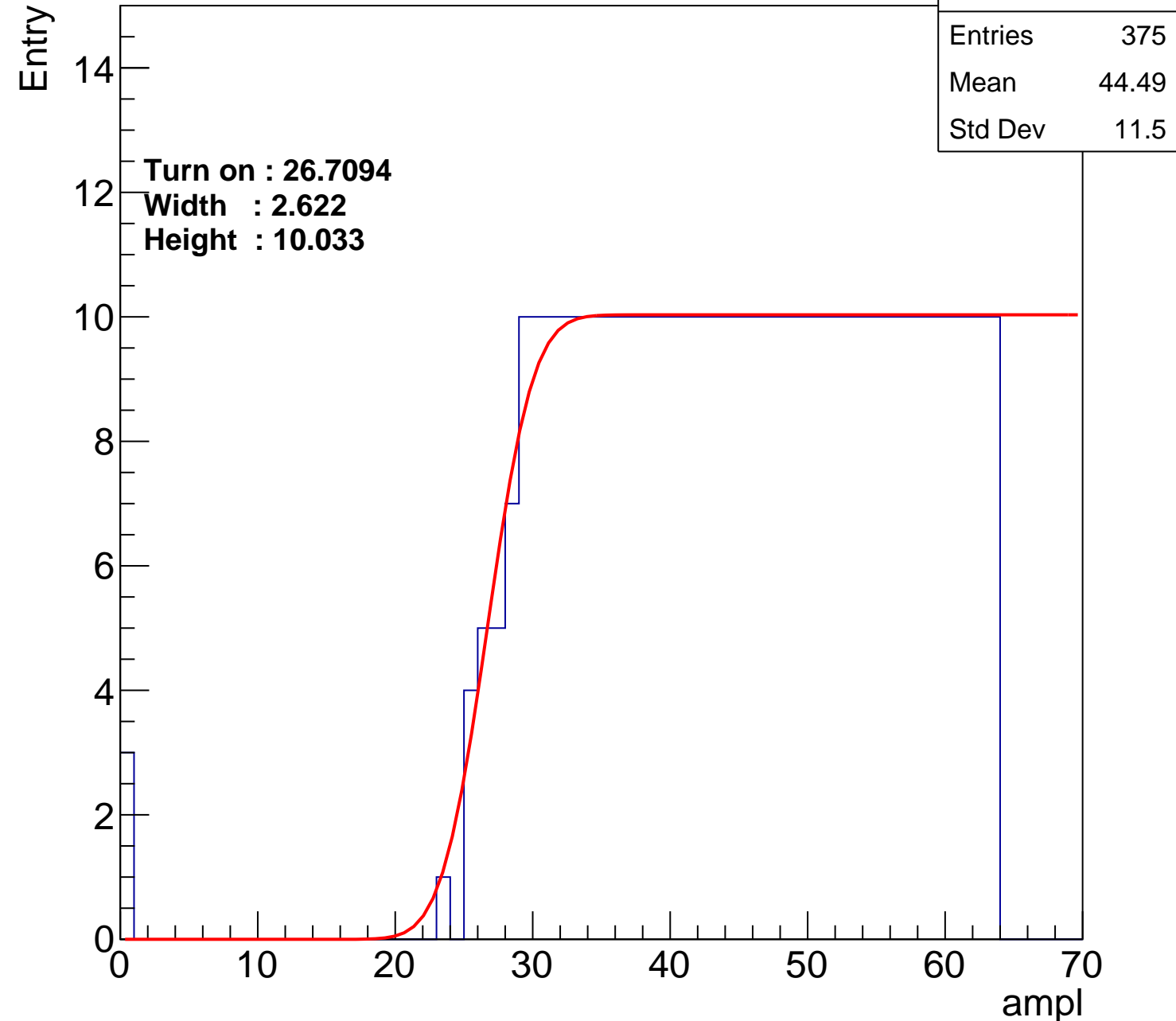
Width : 2.622

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	353
Mean	45.62
Std Dev	10.79

Turn on : 28.9341

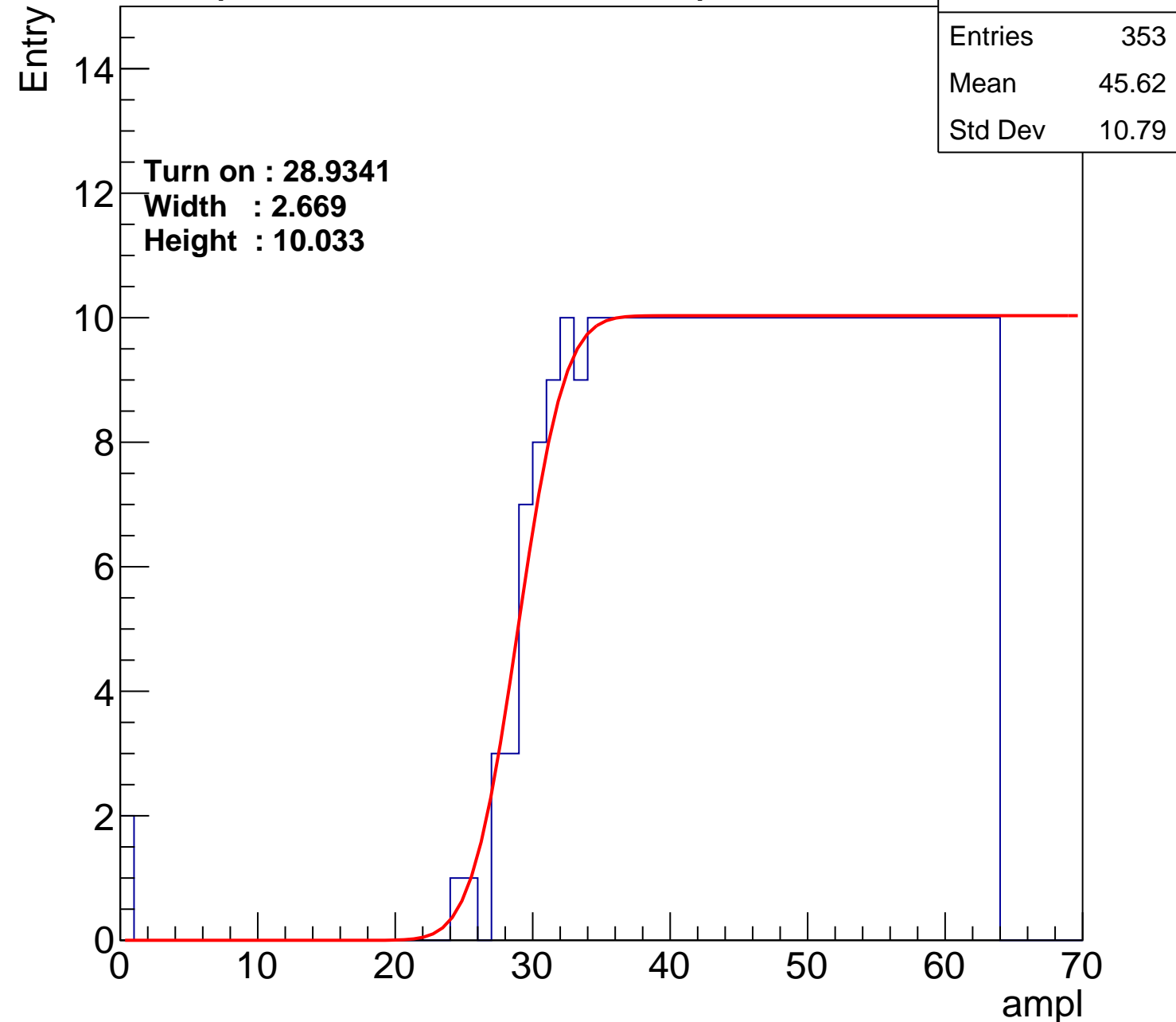
Width : 2.669

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.24
Std Dev	11.02

Turn on : 28.3643

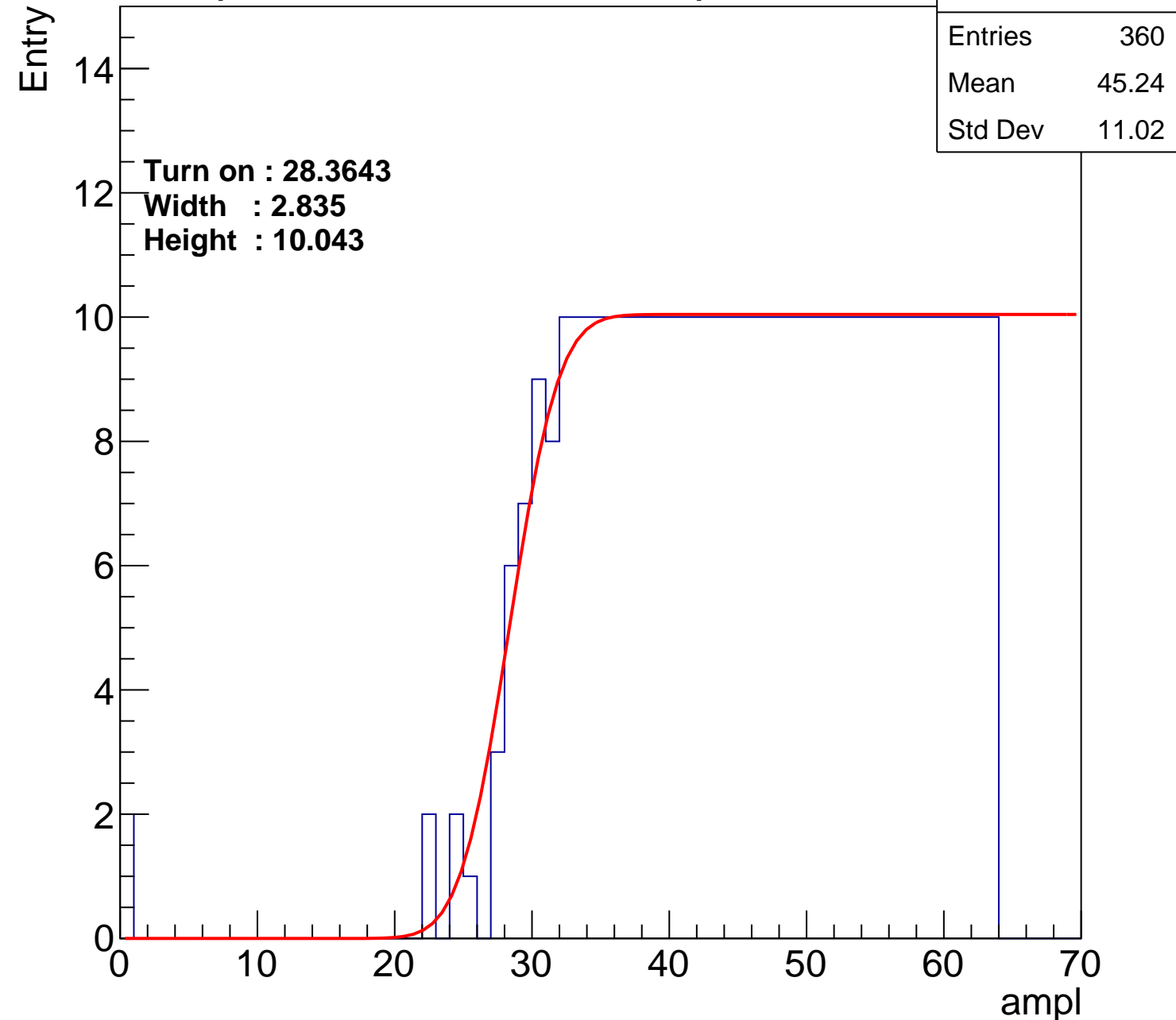
Width : 2.835

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch63

calib_packv5_042523_0143.root, FC#3, port B3

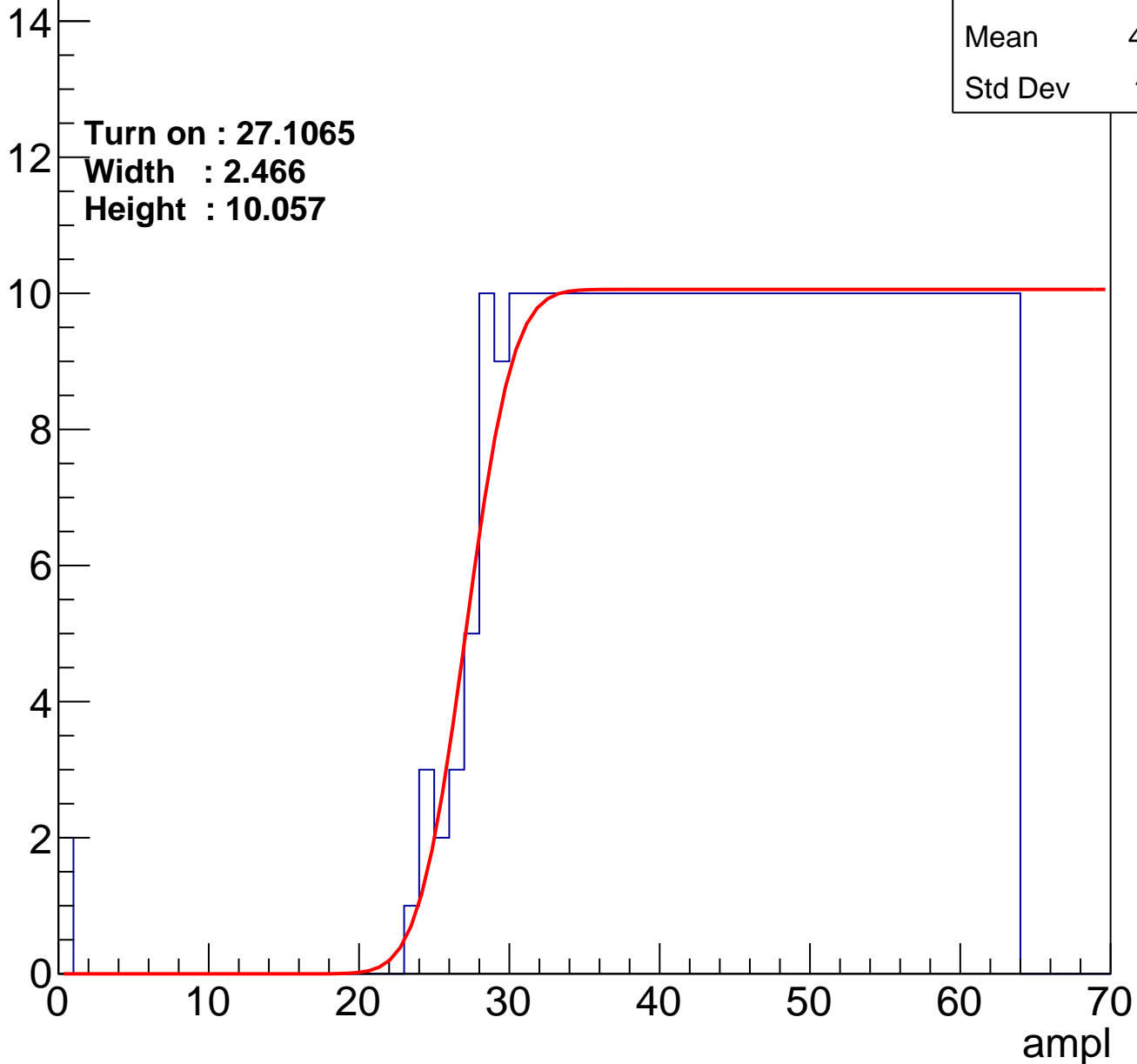
Entries	375
Mean	44.56
Std Dev	11.31

Turn on : 27.1065

Width : 2.466

Height : 10.057

Entry



B1L000S, U24-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 26.9763

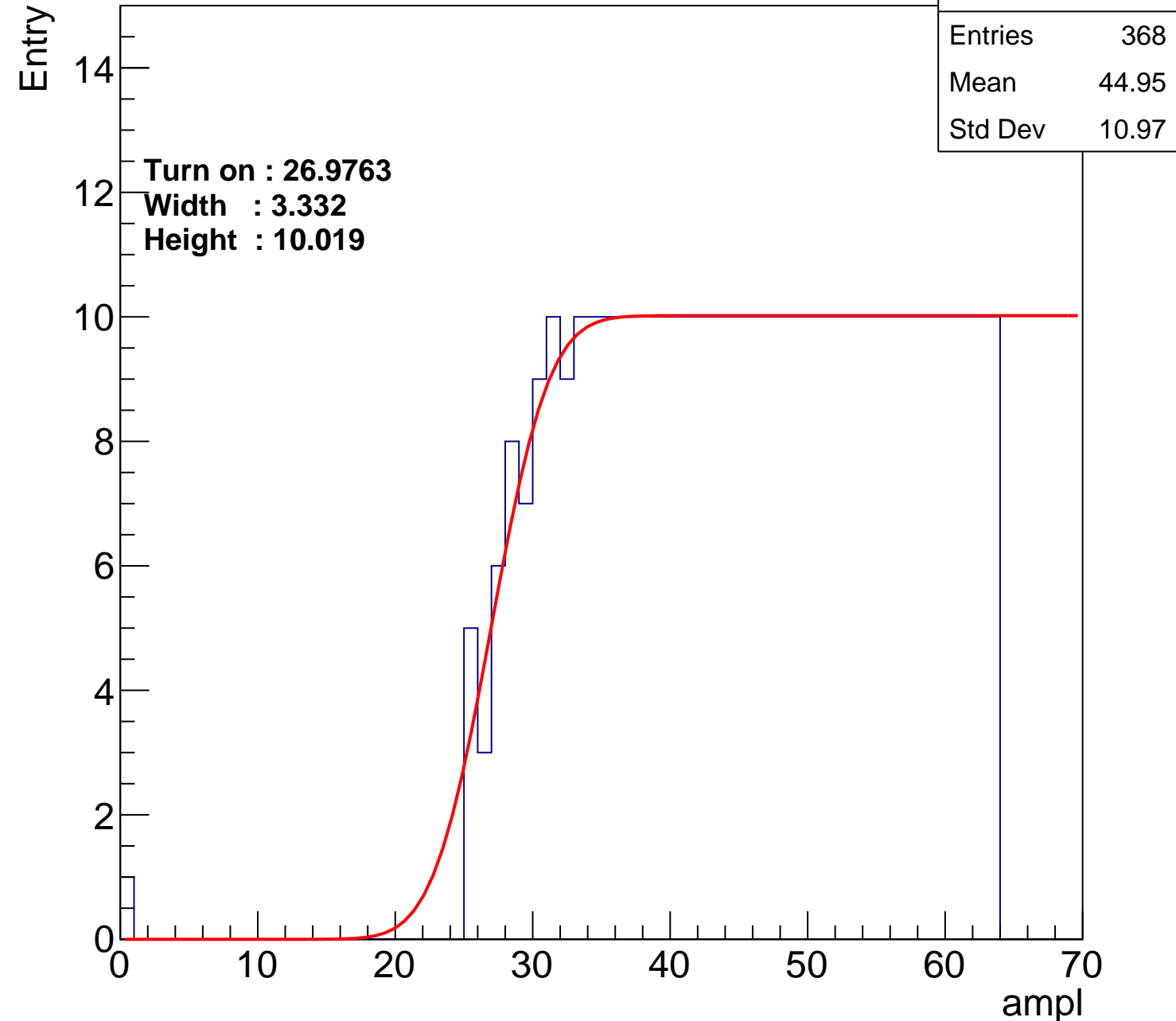
Width : 3.332

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.11
Std Dev	11.86

Turn on : 26.8443

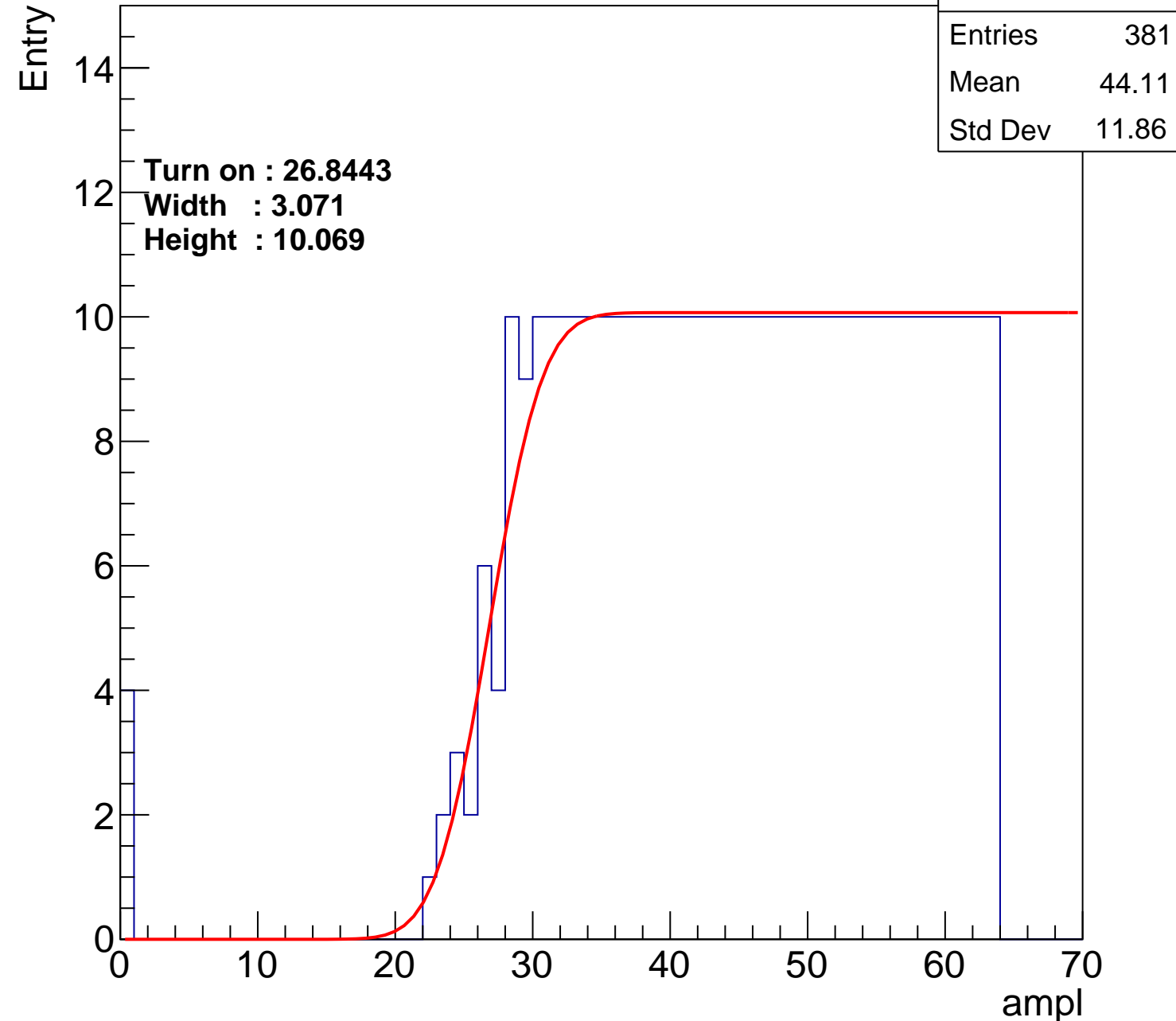
Width : 3.071

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch66

calib_packv5_042523_0143.root, FC#3, port B3

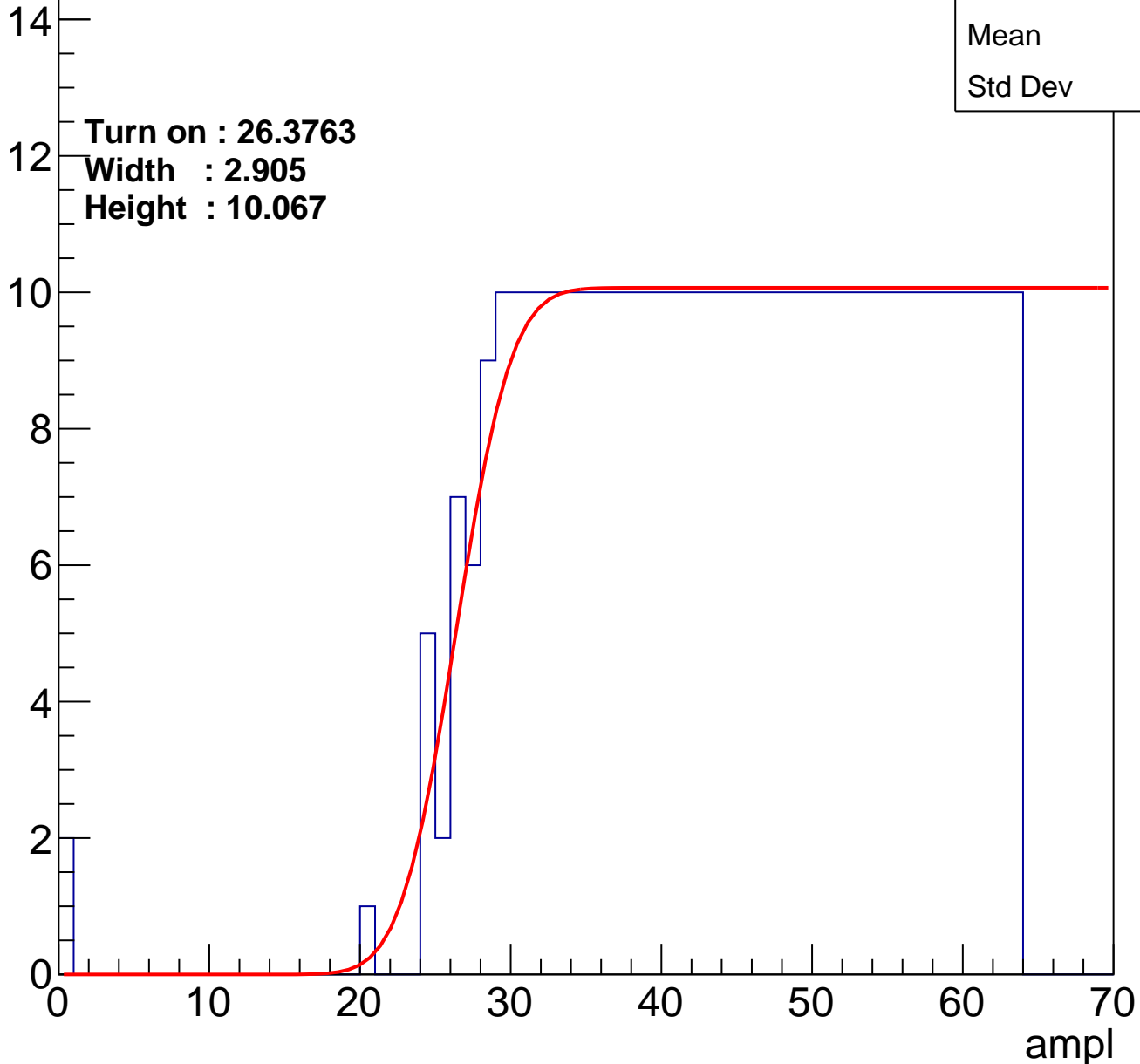
Entries	382
Mean	44.2
Std Dev	11.5

Turn on : 26.3763

Width : 2.905

Height : 10.067

Entry



B1L000S, U24-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.78
Std Dev	12.12

Turn on : 25.7239

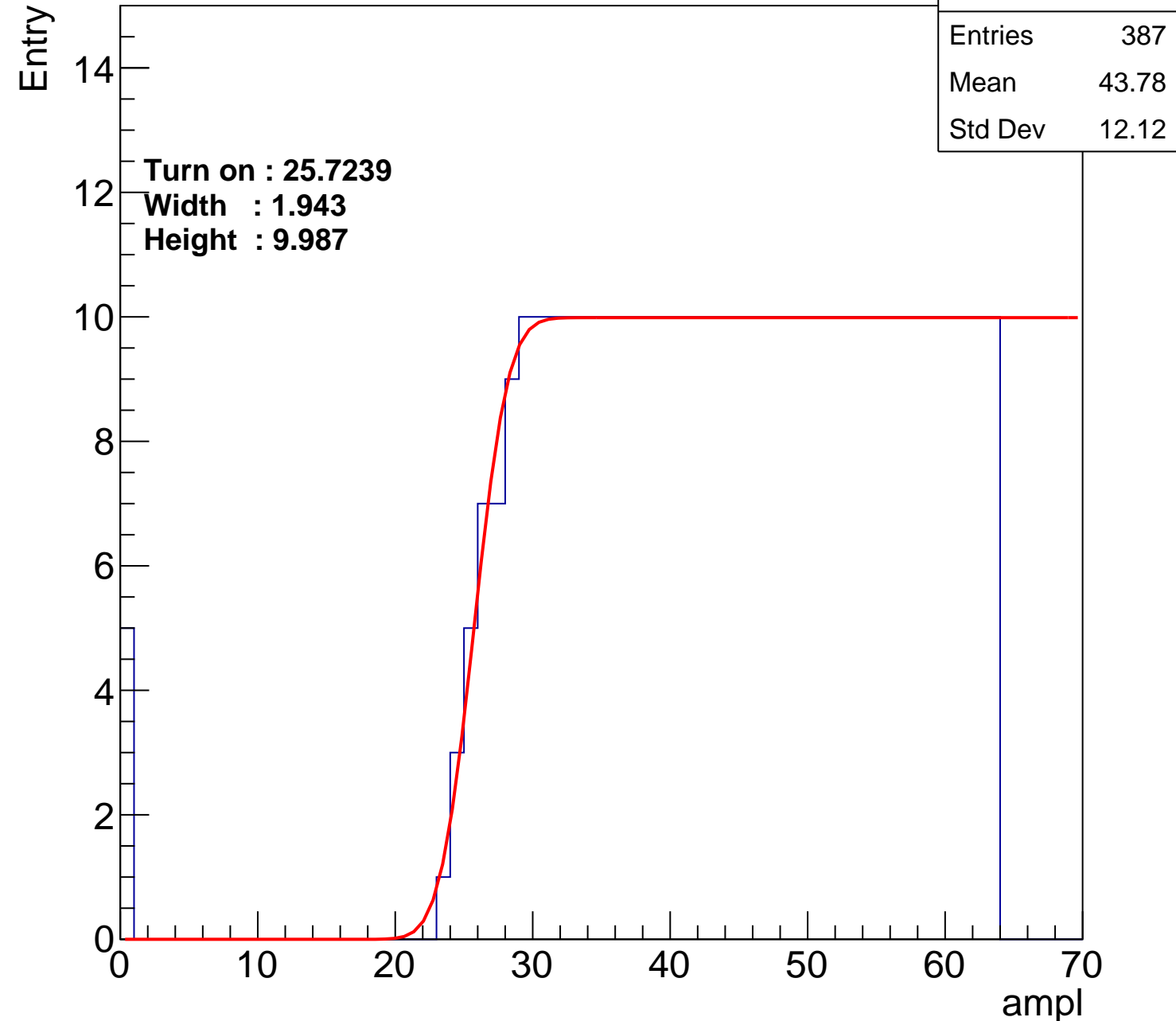
Width : 1.943

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.71
Std Dev	11.42

Turn on : 27.3158

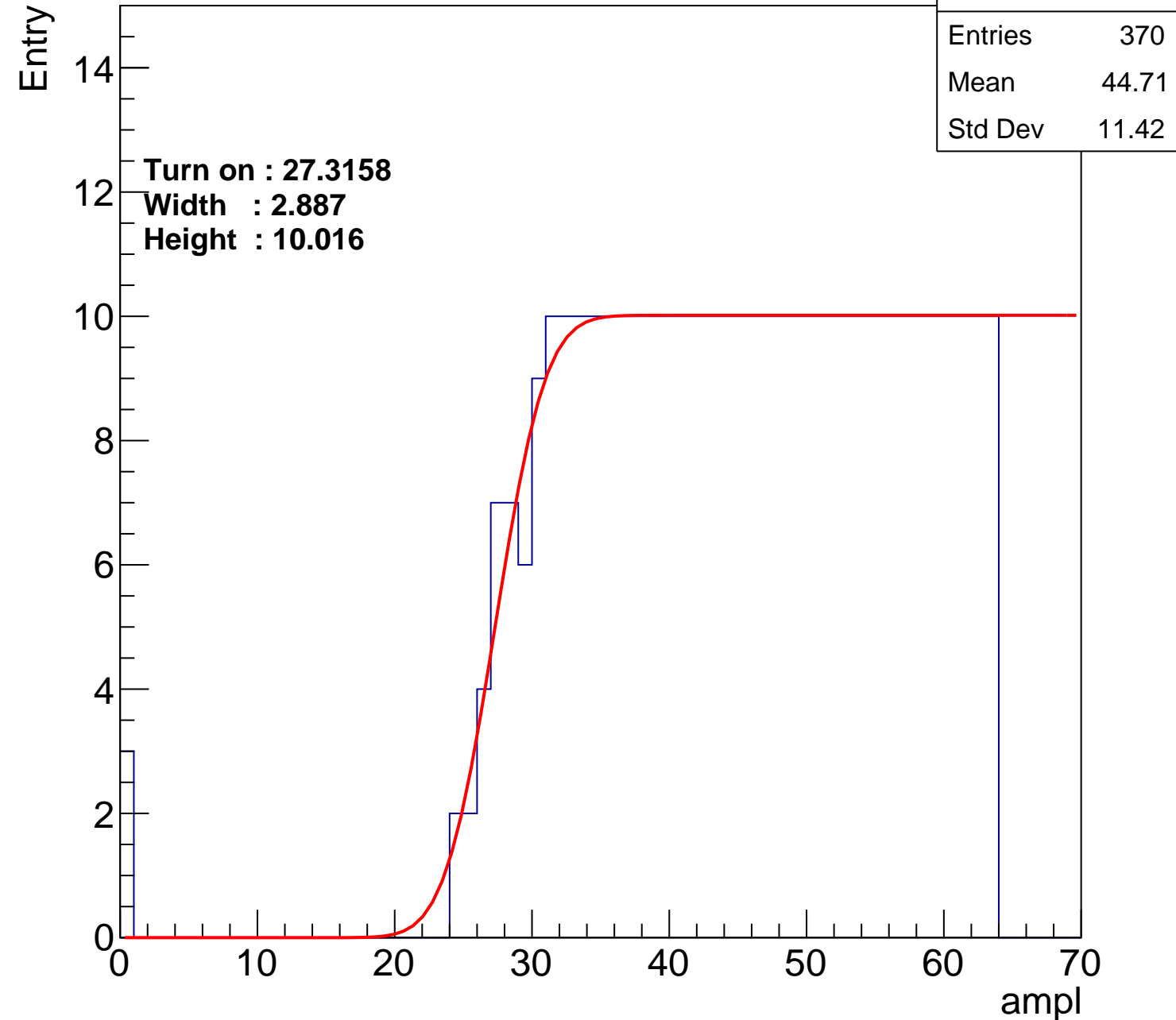
Width : 2.887

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch69

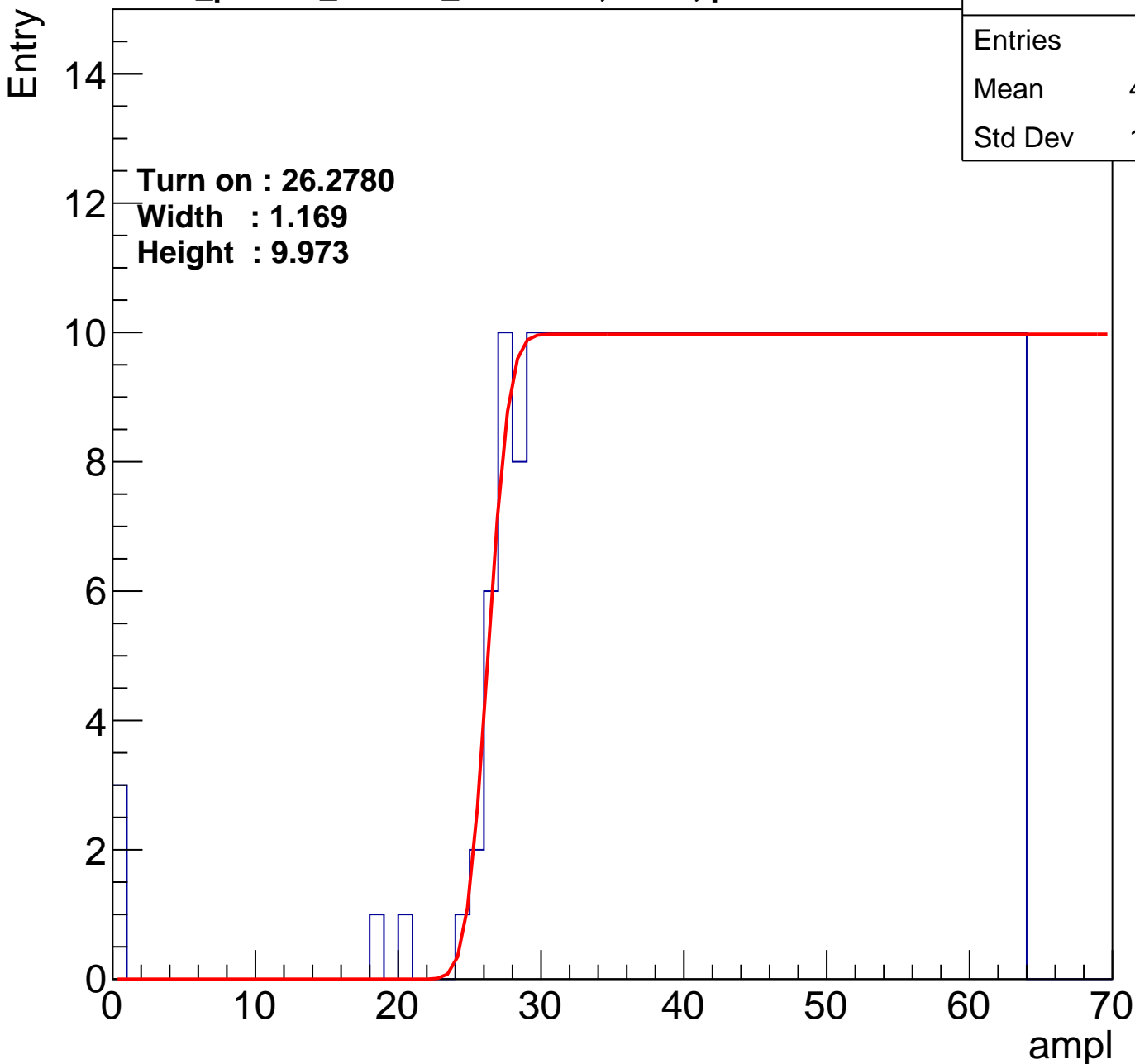
calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.14
Std Dev	11.68

Turn on : 26.2780

Width : 1.169

Height : 9.973



B1L000S, U24-ch70

calib_packv5_042523_0143.root, FC#3, port B3

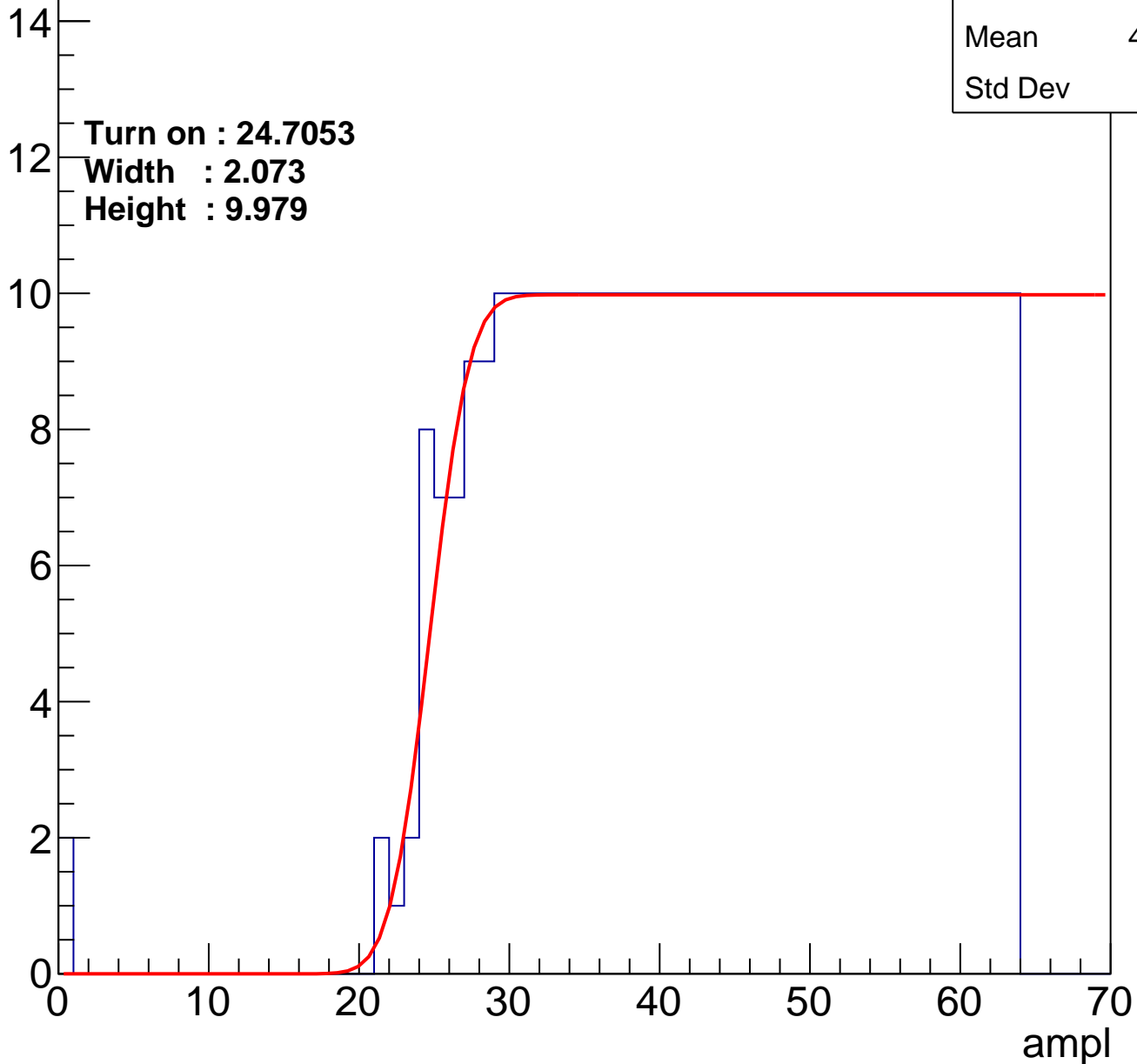
Entries	397
Mean	43.46
Std Dev	11.9

Turn on : 24.7053

Width : 2.073

Height : 9.979

Entry



B1L000S, U24-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 27.4352

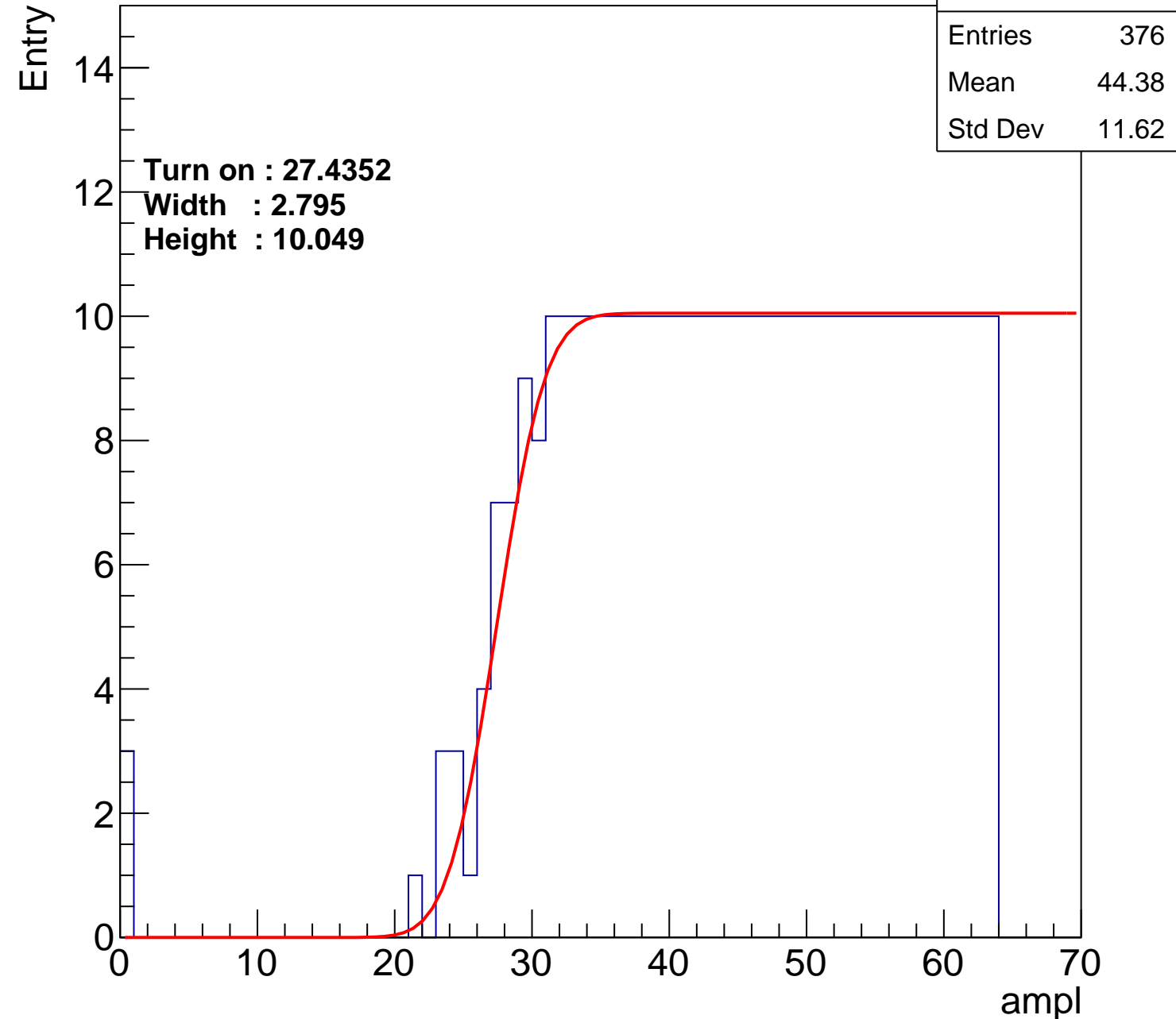
Width : 2.795

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.86
Std Dev	11.01

Turn on : 27.1362

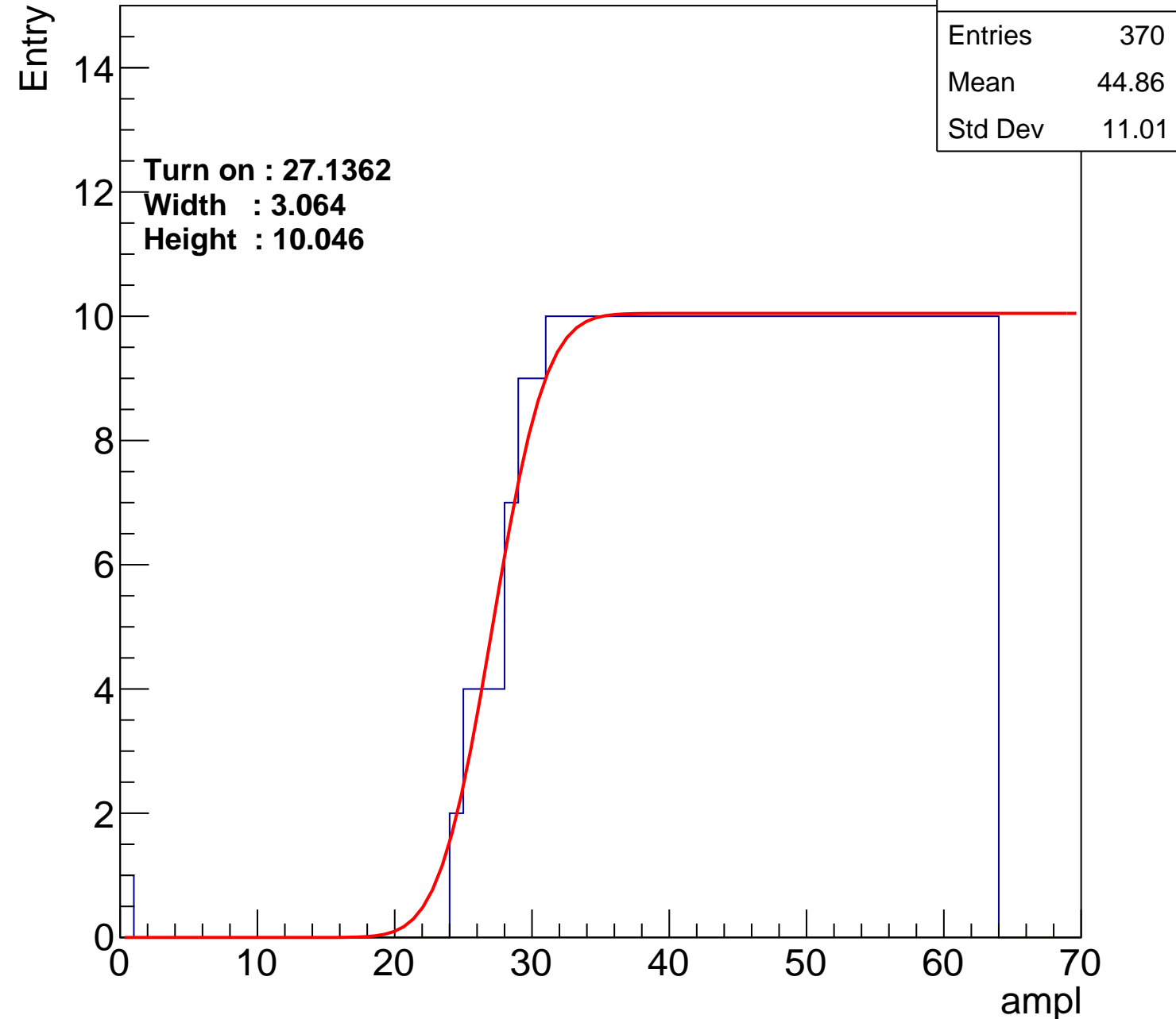
Width : 3.064

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.65
Std Dev	11.46

Turn on : 27.5937

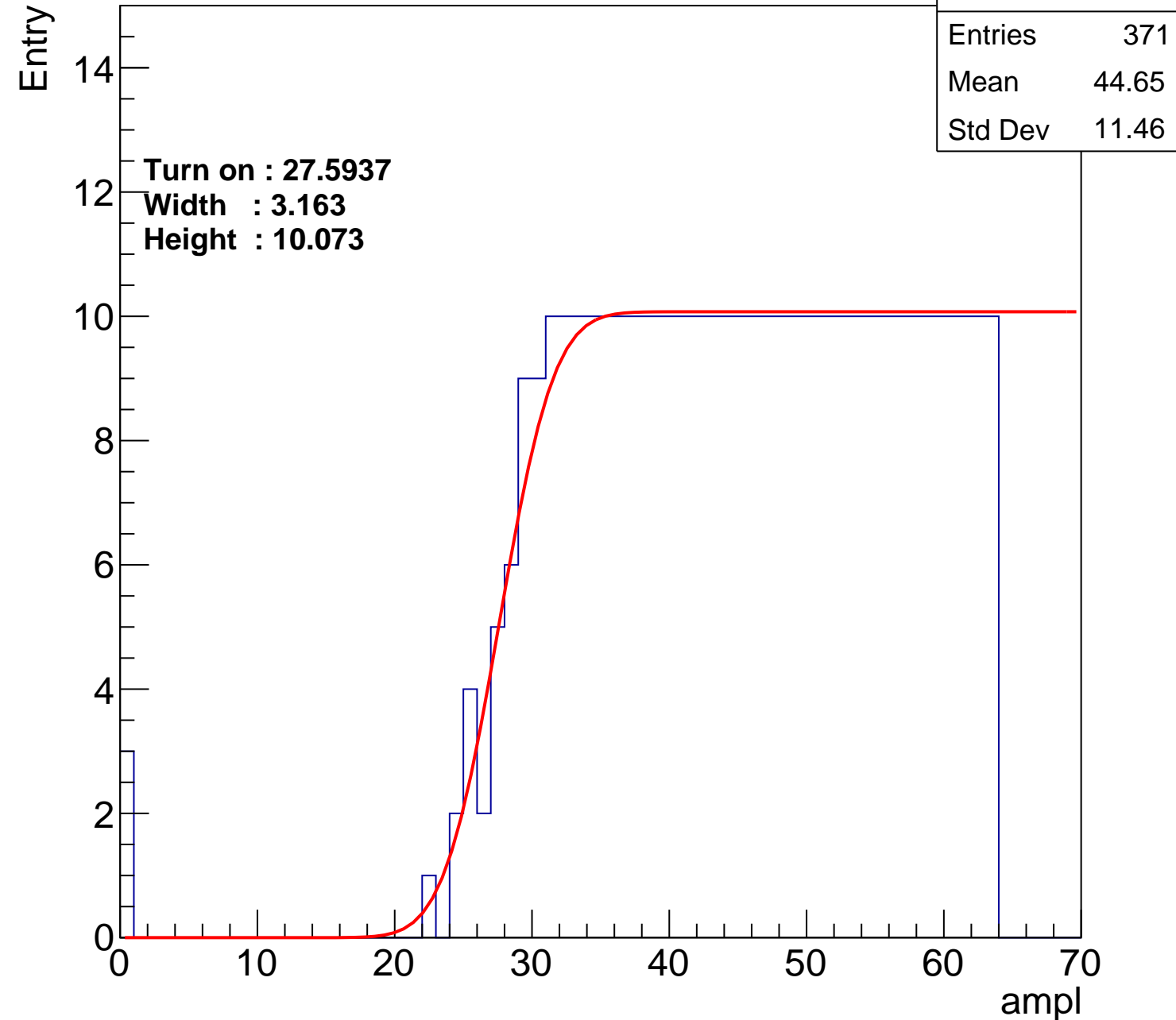
Width : 3.163

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.0358

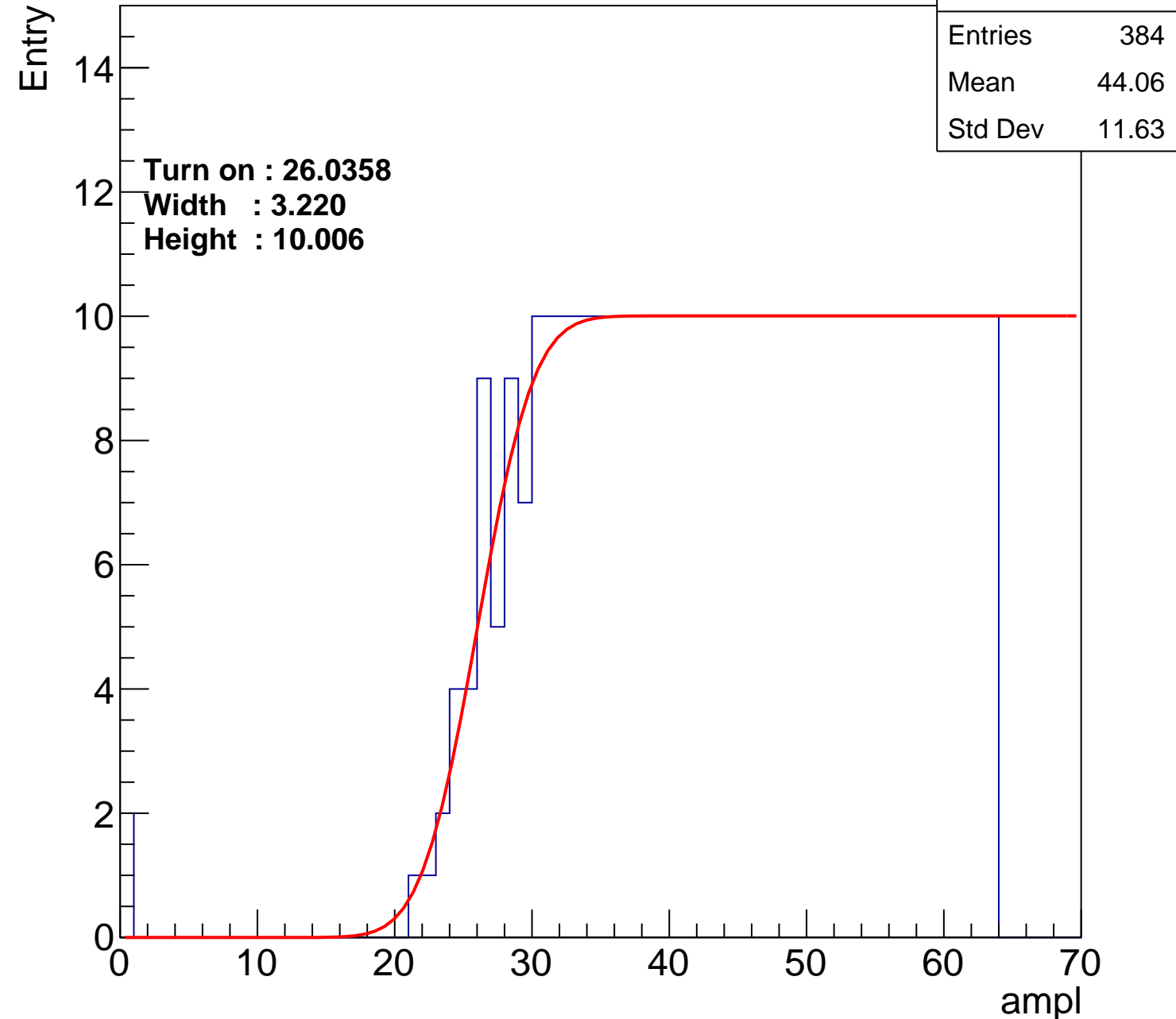
Width : 3.220

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.09
Std Dev	11.04

Turn on : 27.5897

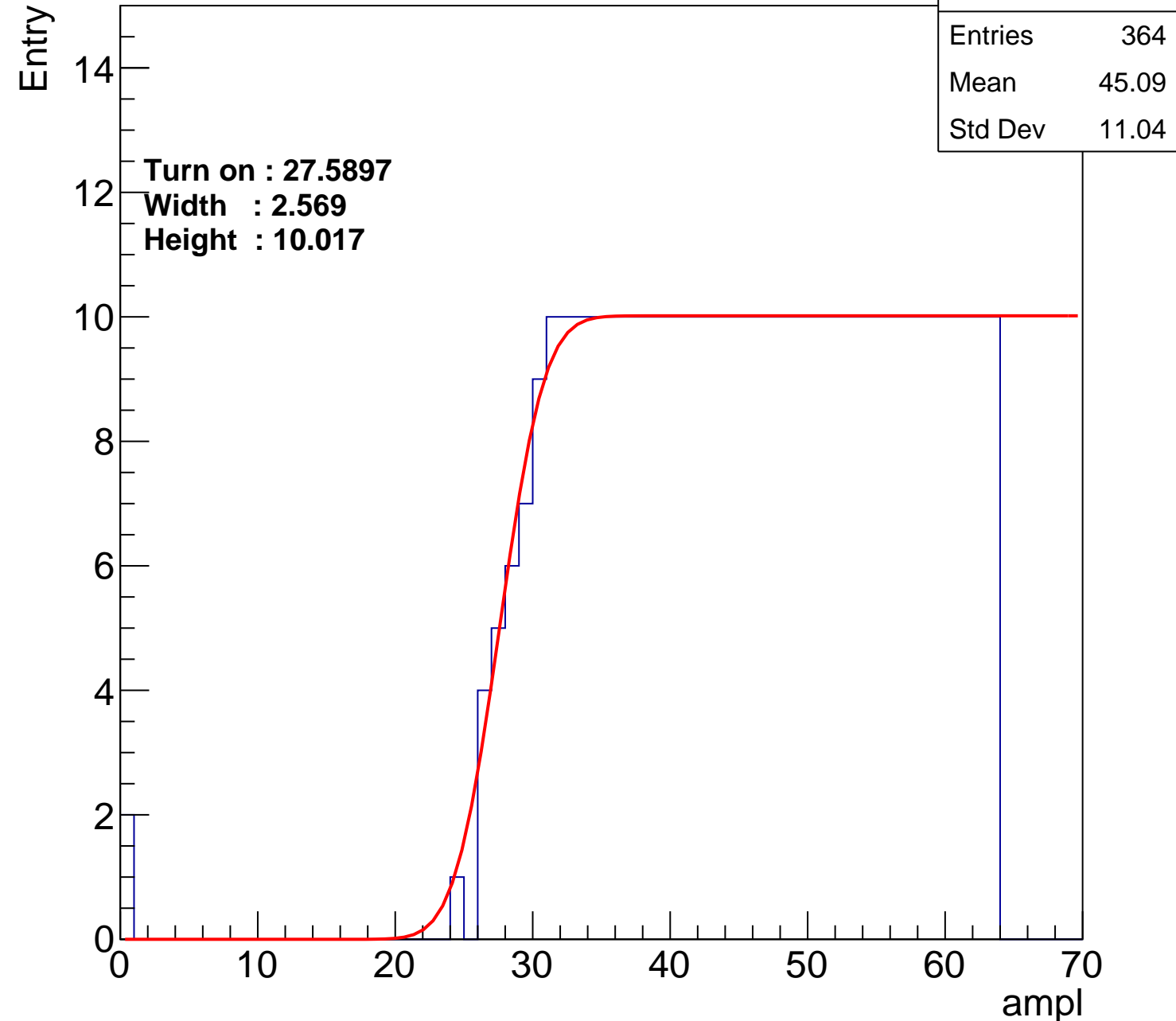
Width : 2.569

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch76

calib_packv5_042523_0143.root, FC#3, port B3

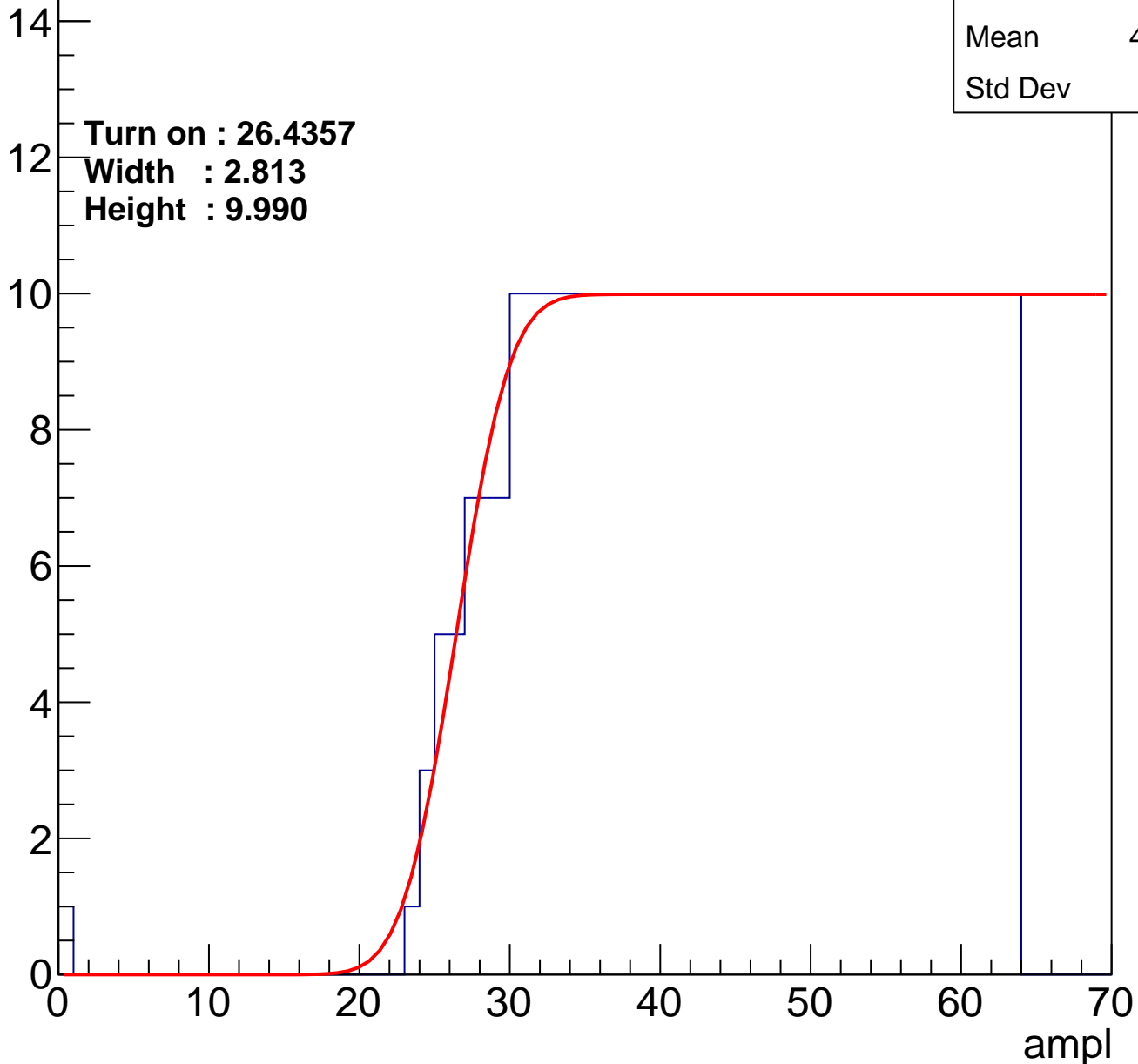
Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.4357

Width : 2.813

Height : 9.990

Entry



B1L000S, U24-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.5
Std Dev	11.89

Turn on : 24.9086

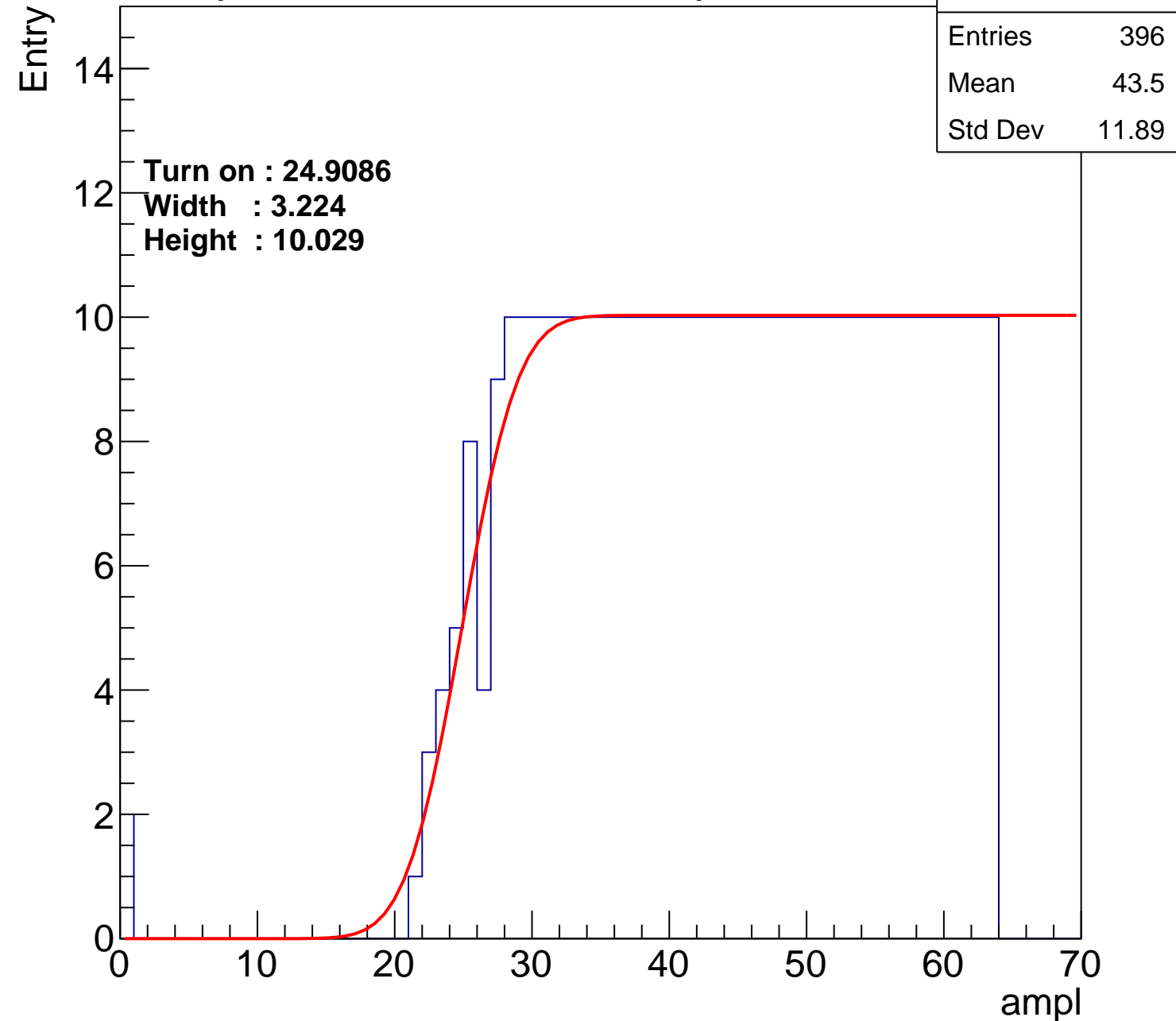
Width : 3.224

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.48
Std Dev	11.39

Turn on : 26.9186

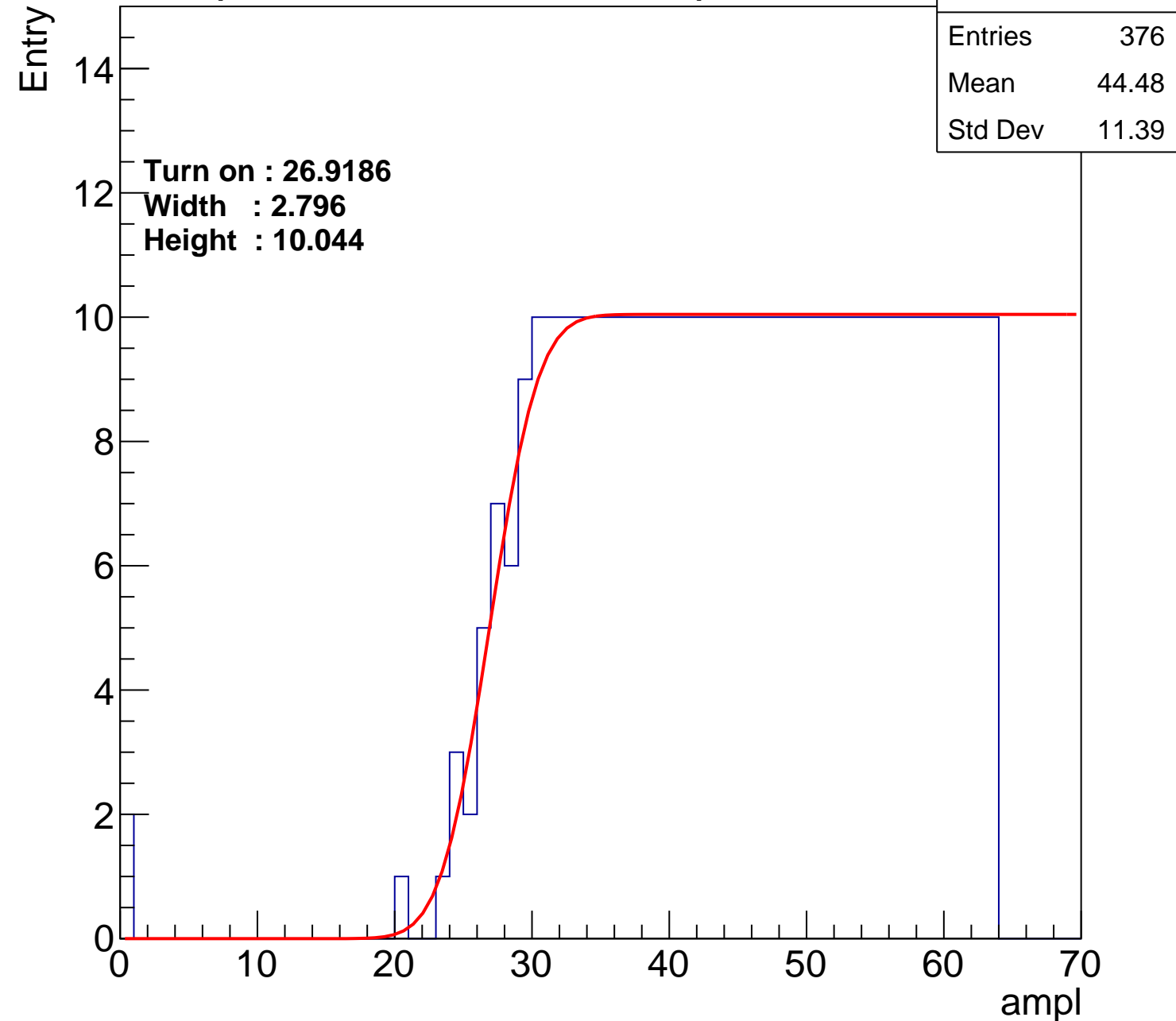
Width : 2.796

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.64
Std Dev	11.47

Turn on : 27.2689

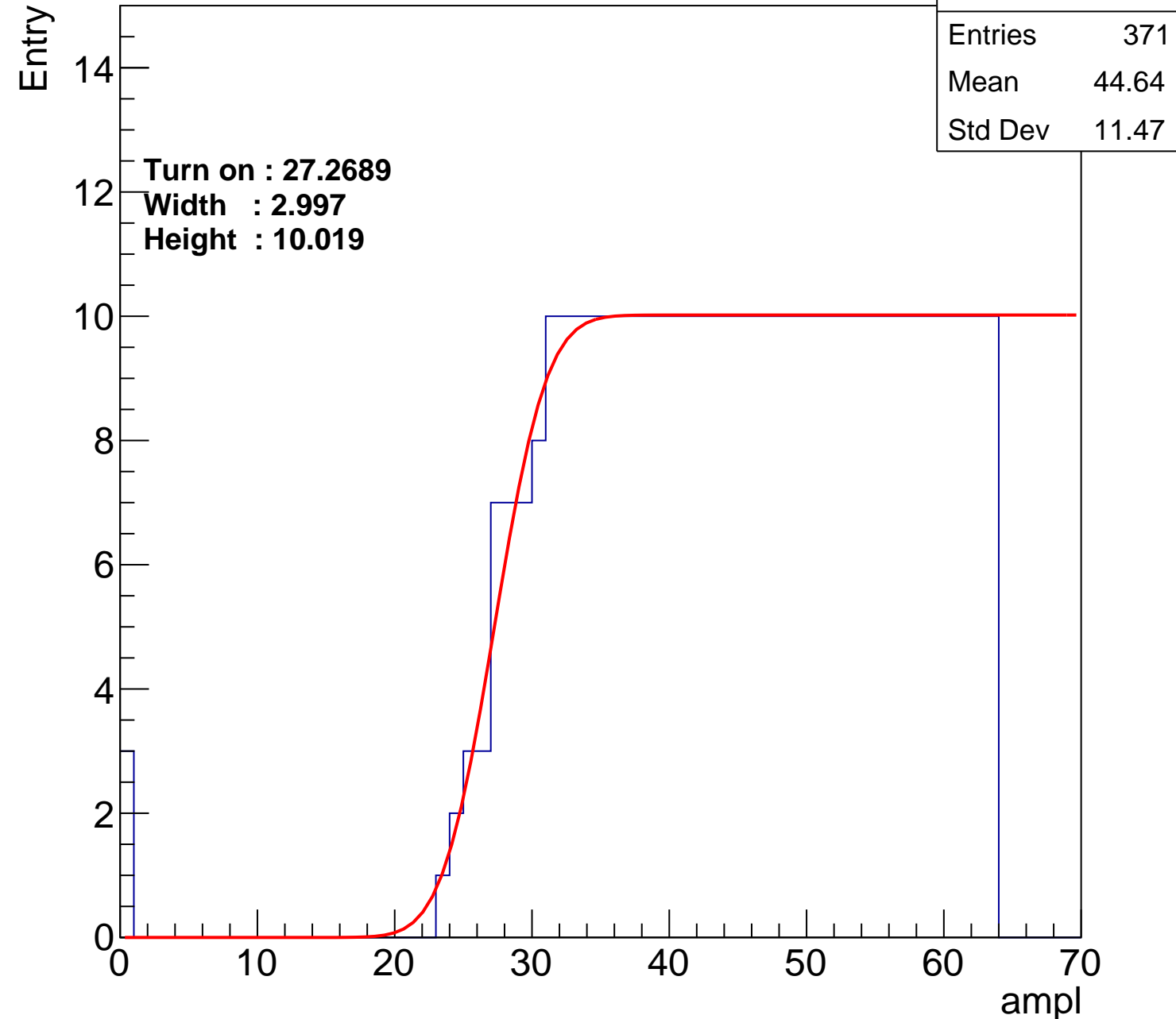
Width : 2.997

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch80

calib_packv5_042523_0143.root, FC#3, port B3

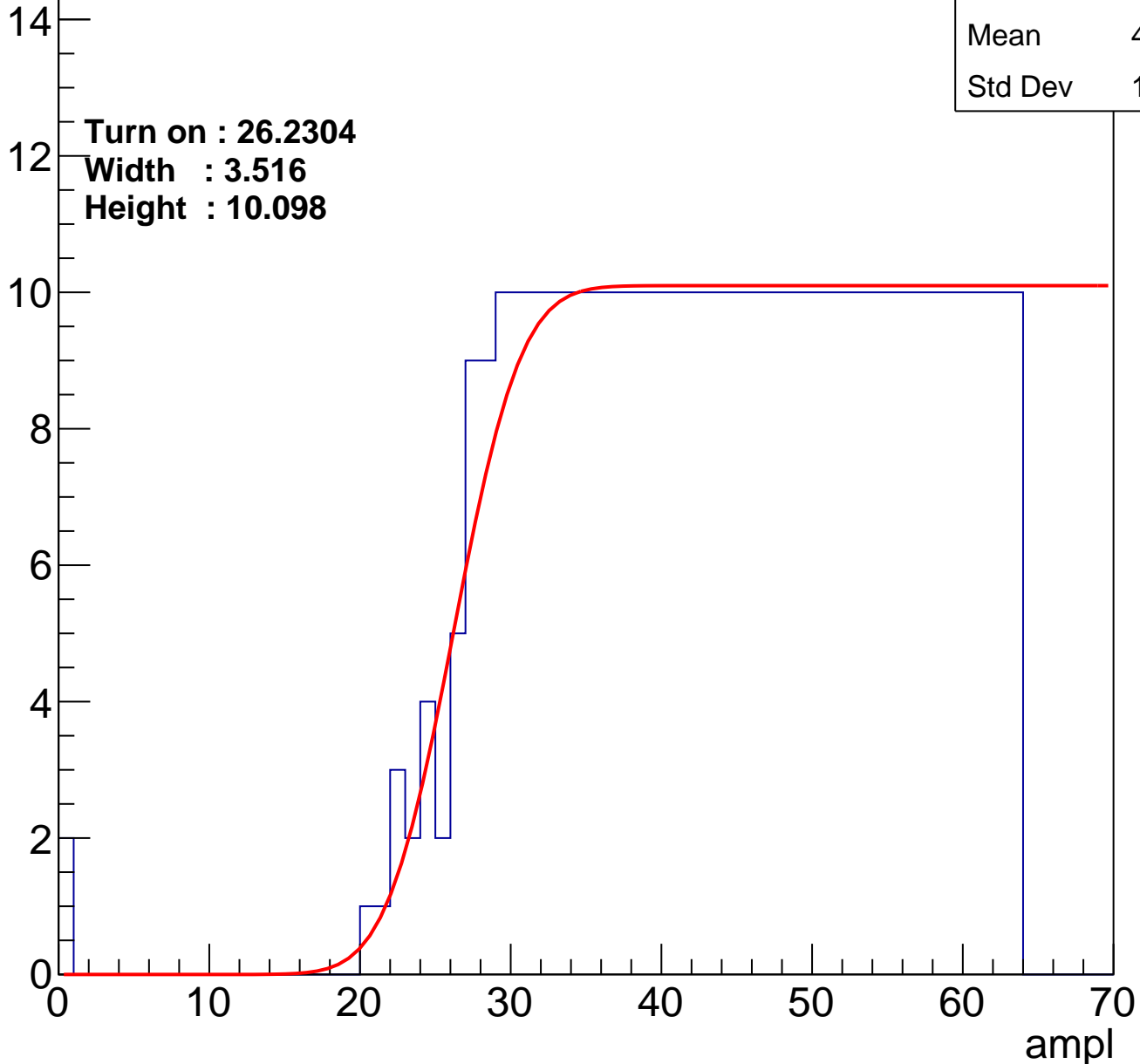
Entries	388
Mean	43.88
Std Dev	11.72

Turn on : 26.2304

Width : 3.516

Height : 10.098

Entry



B1L000S, U24-ch81

calib_packv5_042523_0143.root, FC#3, port B3

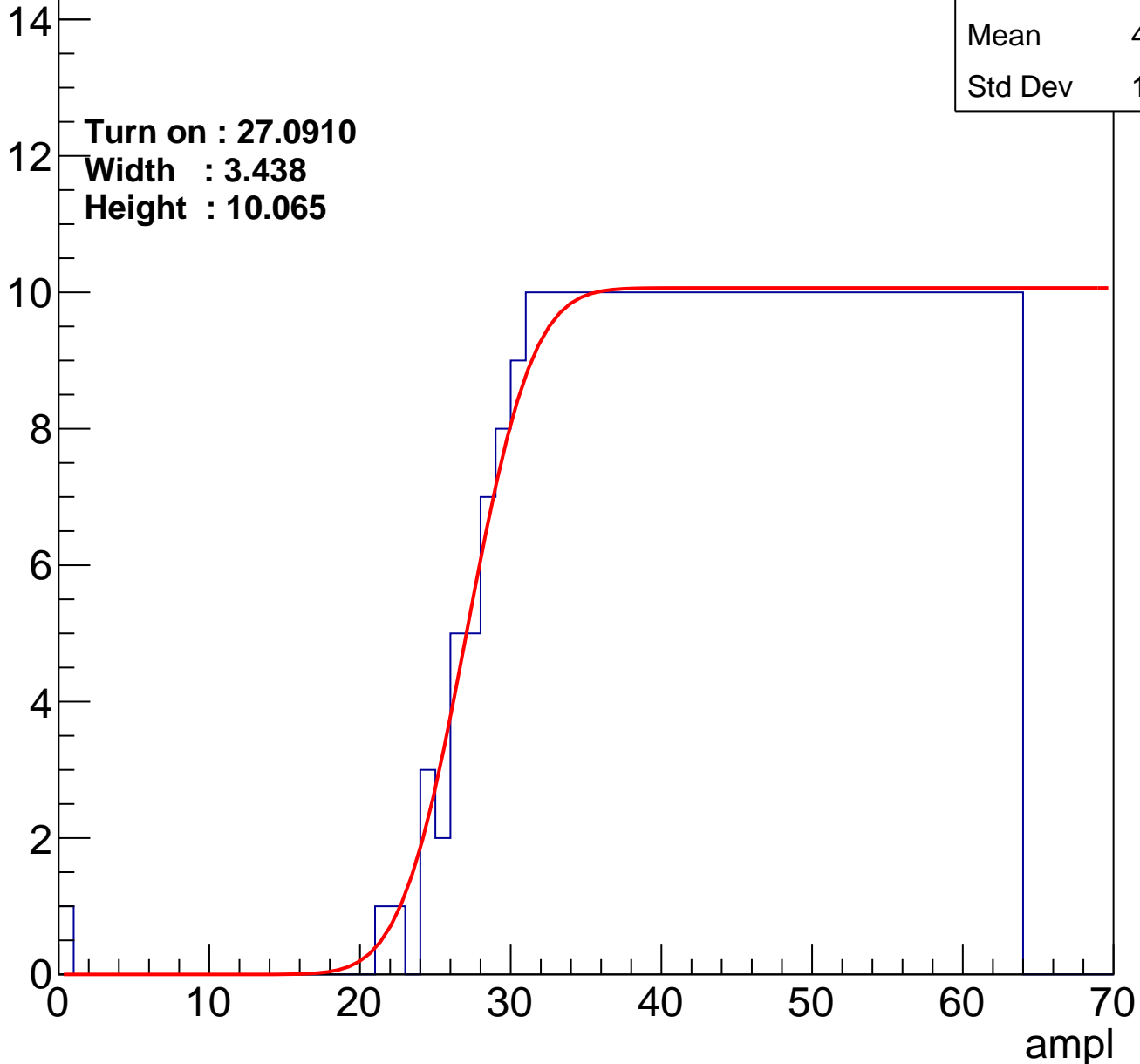
Entries	372
Mean	44.73
Std Dev	11.12

Turn on : 27.0910

Width : 3.438

Height : 10.065

Entry



B1L000S, U24-ch82

calib_packv5_042523_0143.root, FC#3, port B3

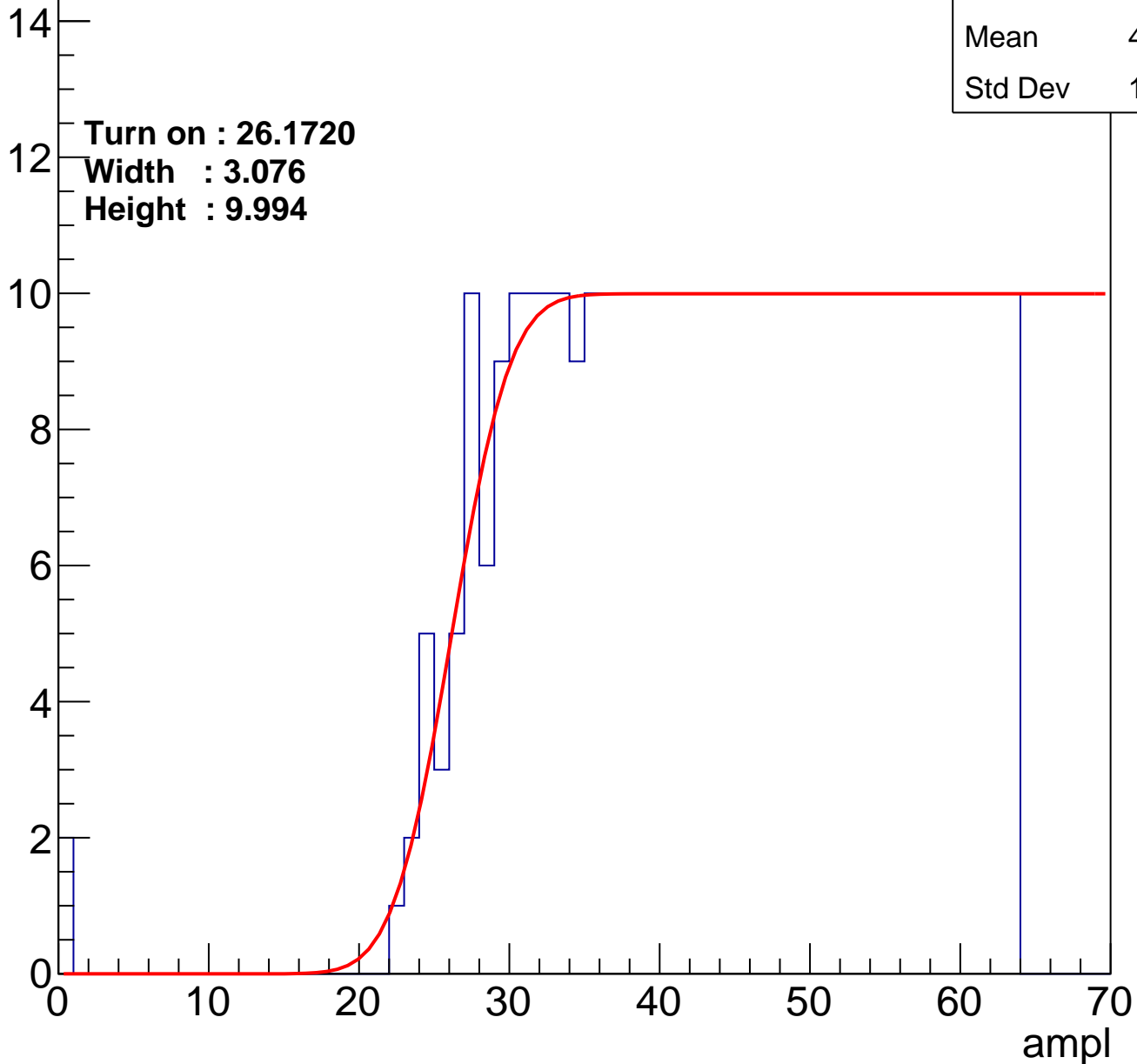
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1720

Width : 3.076

Height : 9.994

Entry



B1L000S, U24-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.7404

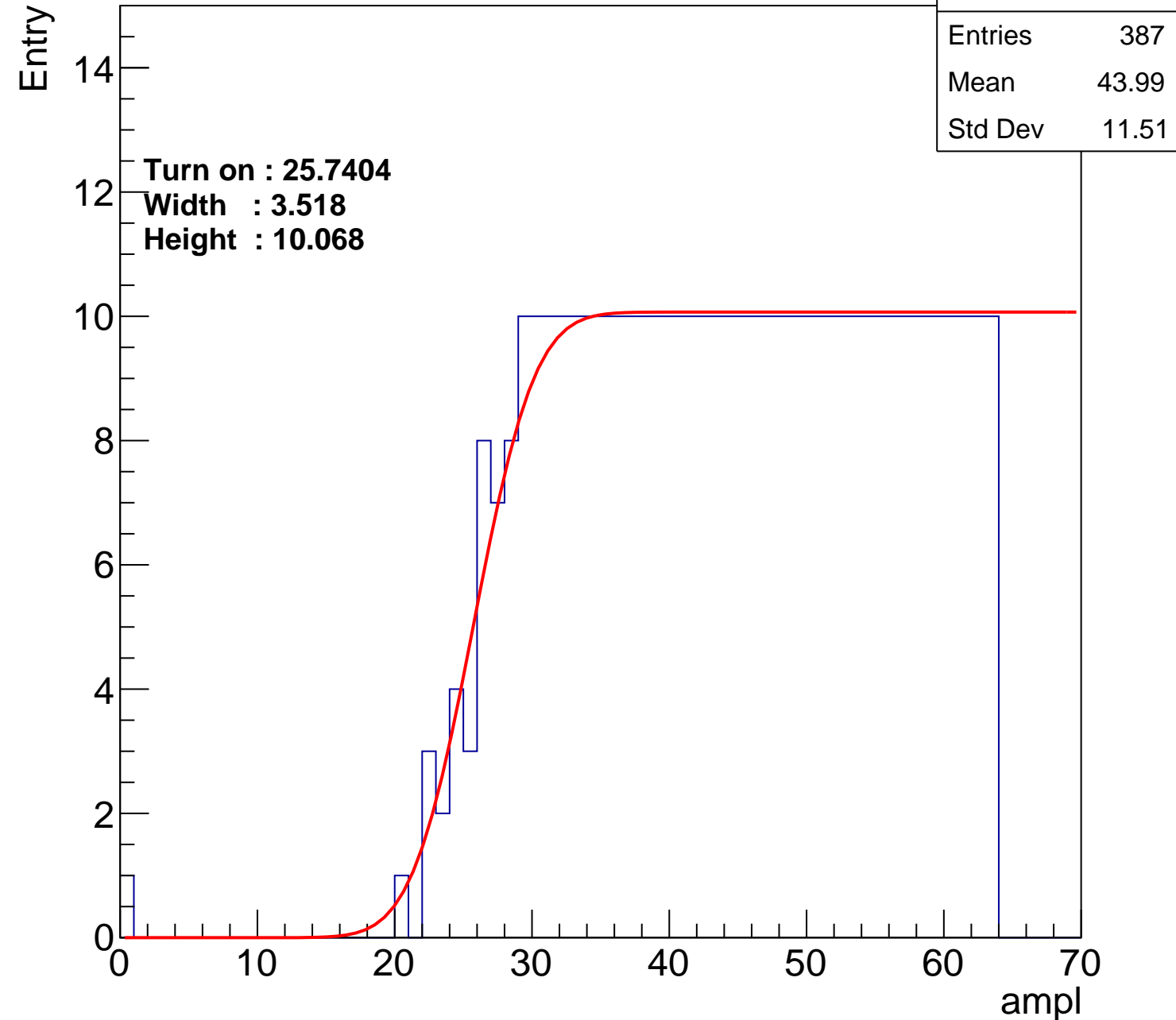
Width : 3.518

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.21
Std Dev	11.02

Turn on : 28.2198

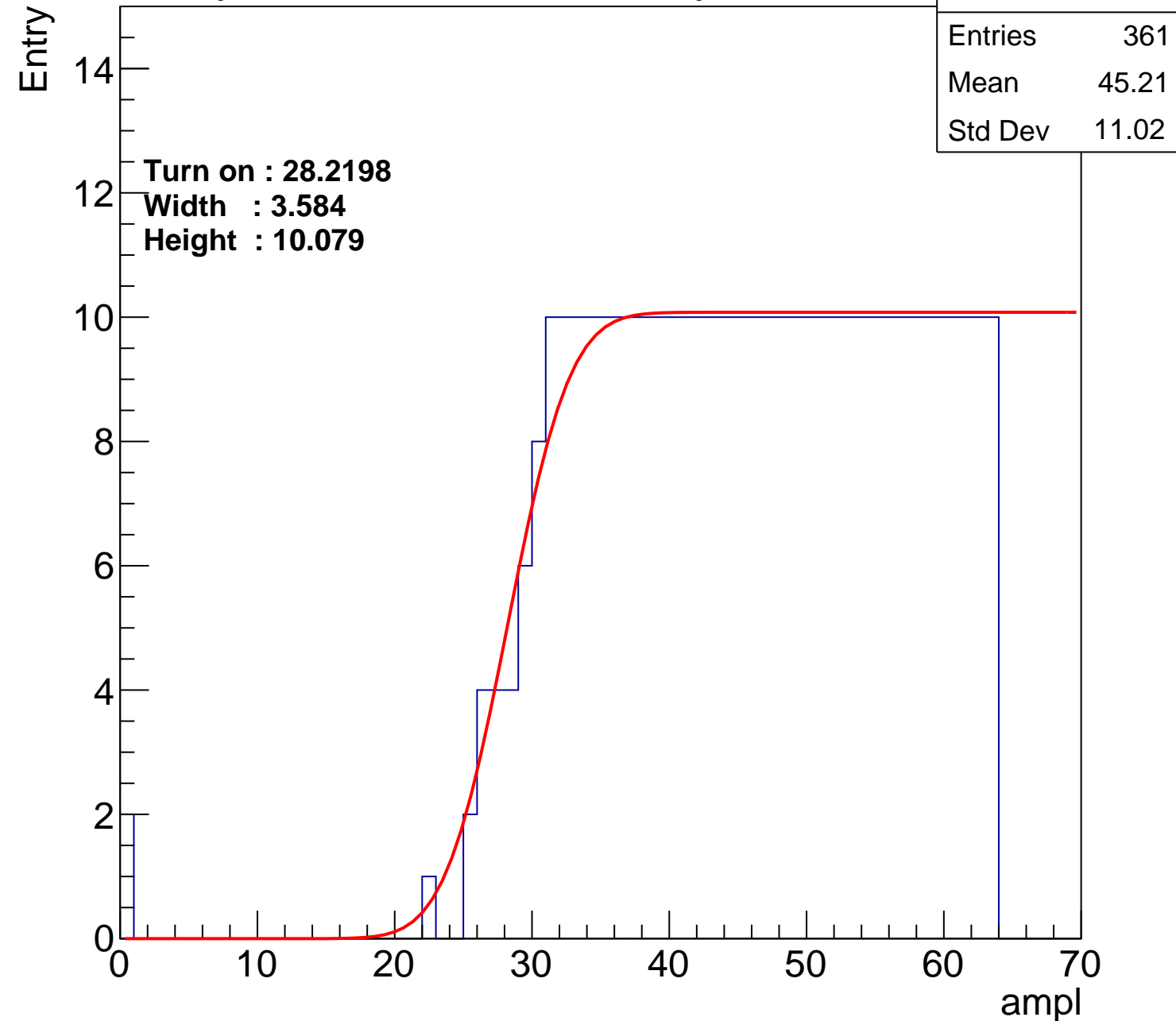
Width : 3.584

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.1408

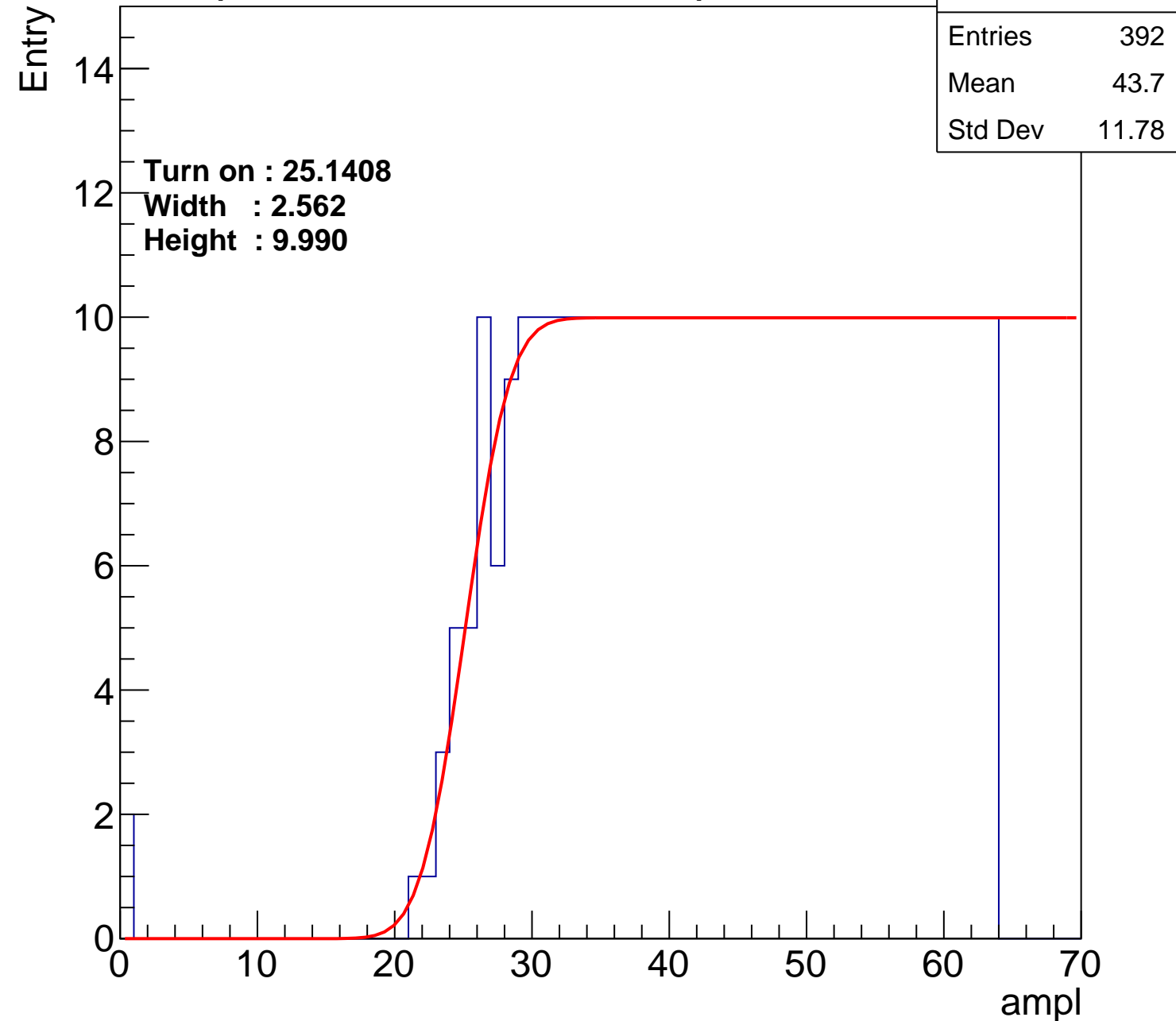
Width : 2.562

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch86

calib_packv5_042523_0143.root, FC#3, port B3

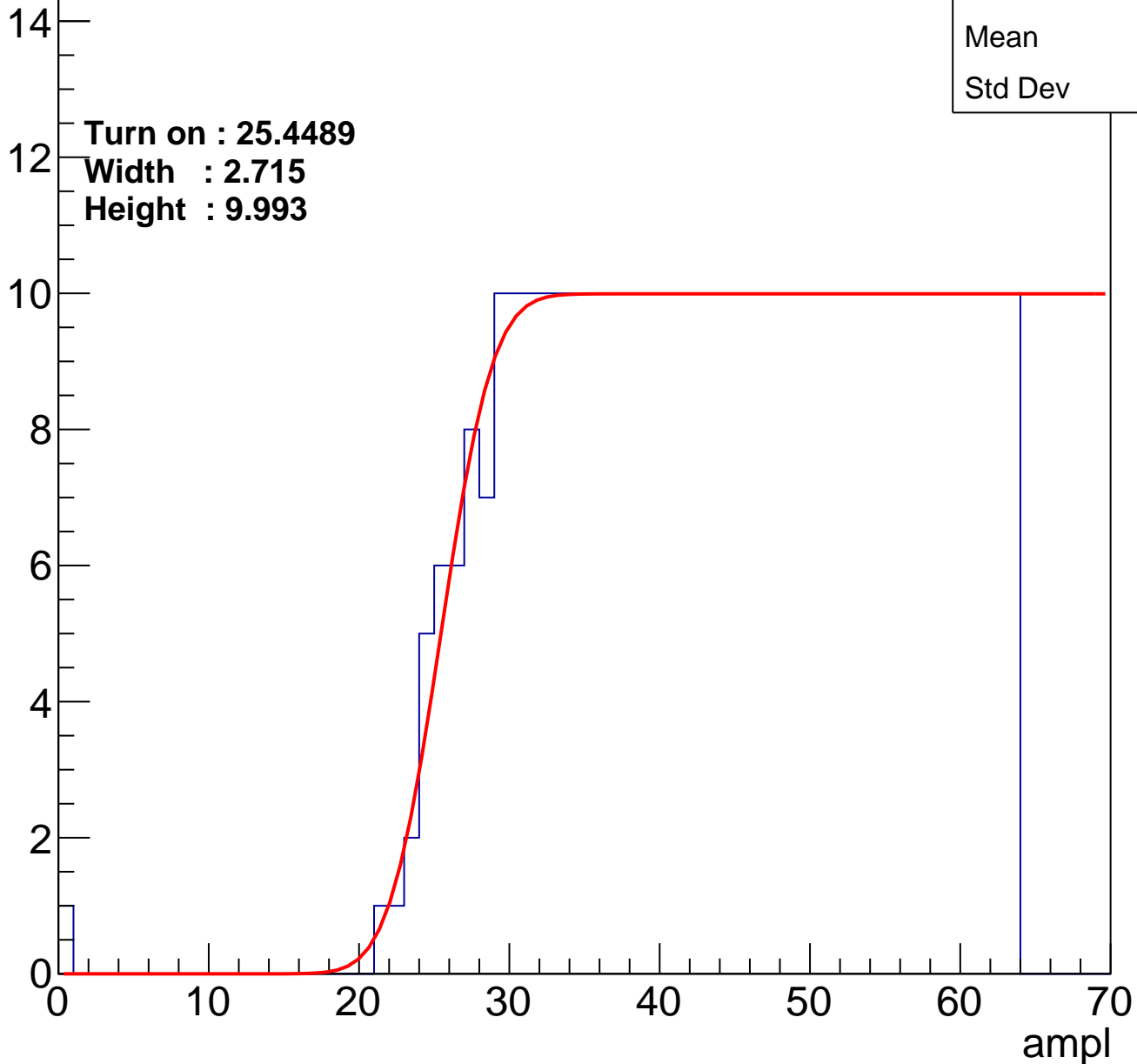
Entries	387
Mean	44
Std Dev	11.5

Turn on : 25.4489

Width : 2.715

Height : 9.993

Entry



B1L000S, U24-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.77
Std Dev	11.57

Turn on : 27.9103

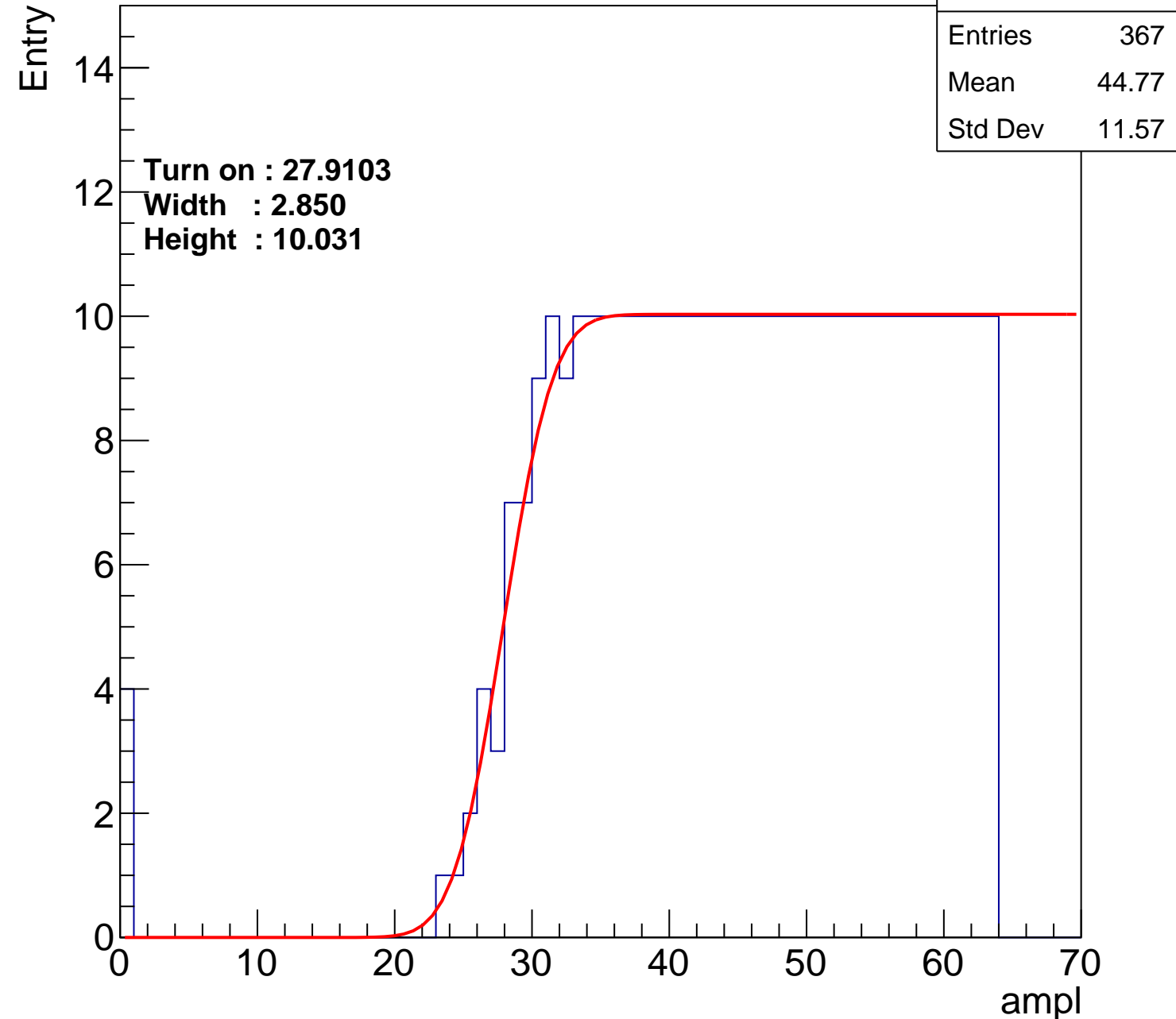
Width : 2.850

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch88

calib_packv5_042523_0143.root, FC#3, port B3

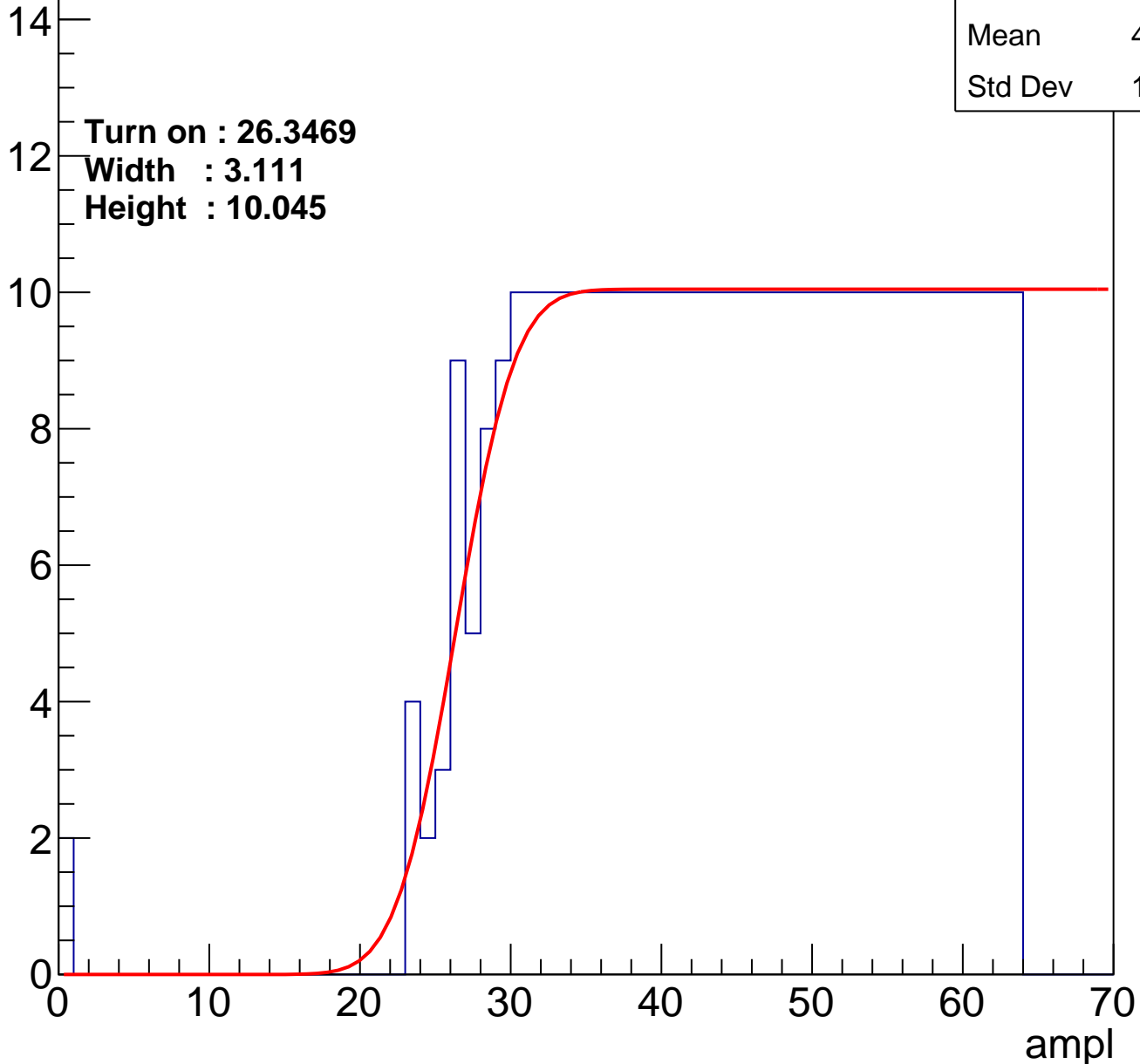
Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.3469

Width : 3.111

Height : 10.045

Entry



B1L000S, U24-ch89

calib_packv5_042523_0143.root, FC#3, port B3

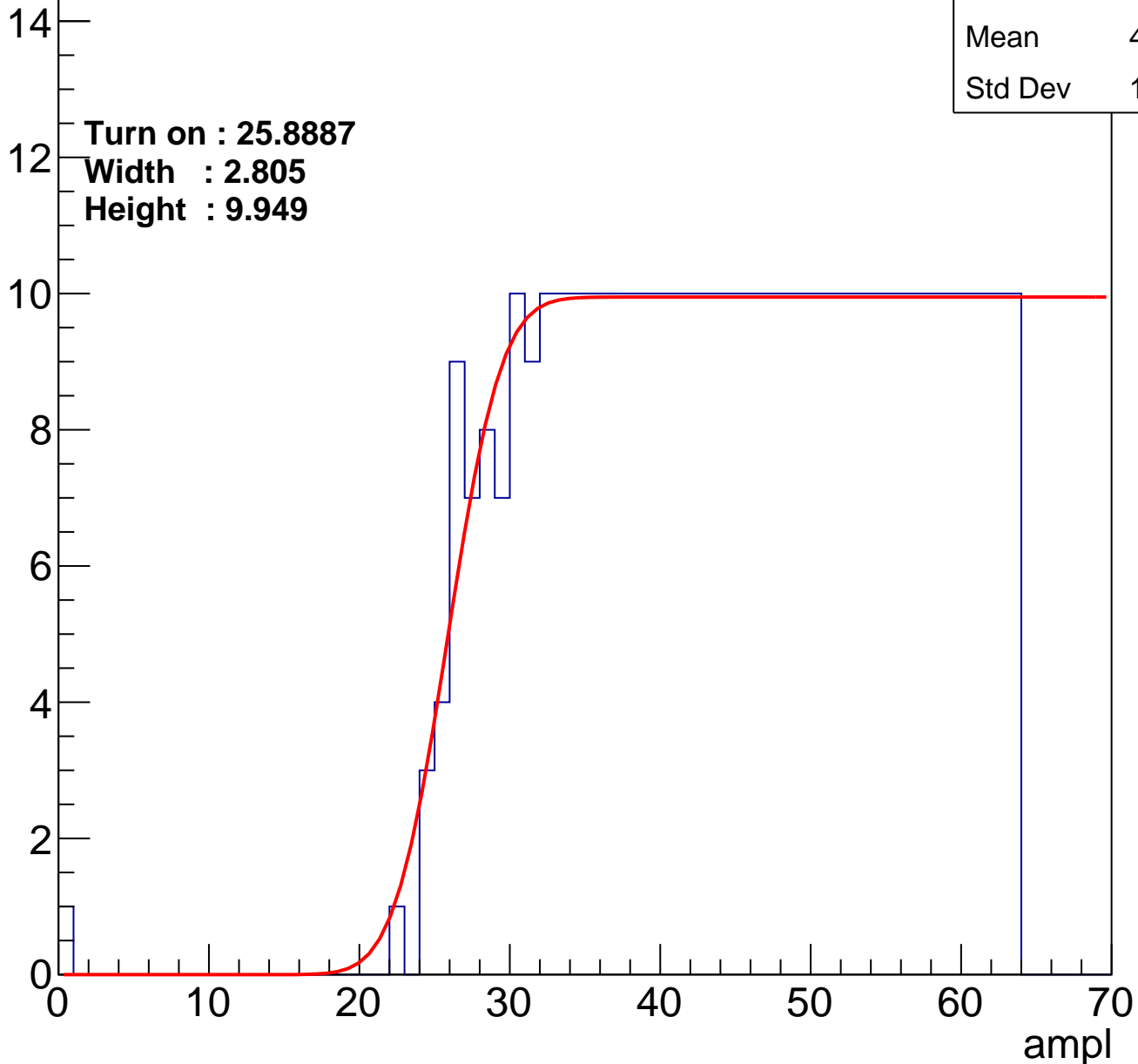
Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 25.8887

Width : 2.805

Height : 9.949

Entry



B1L000S, U24-ch90

calib_packv5_042523_0143.root, FC#3, port B3

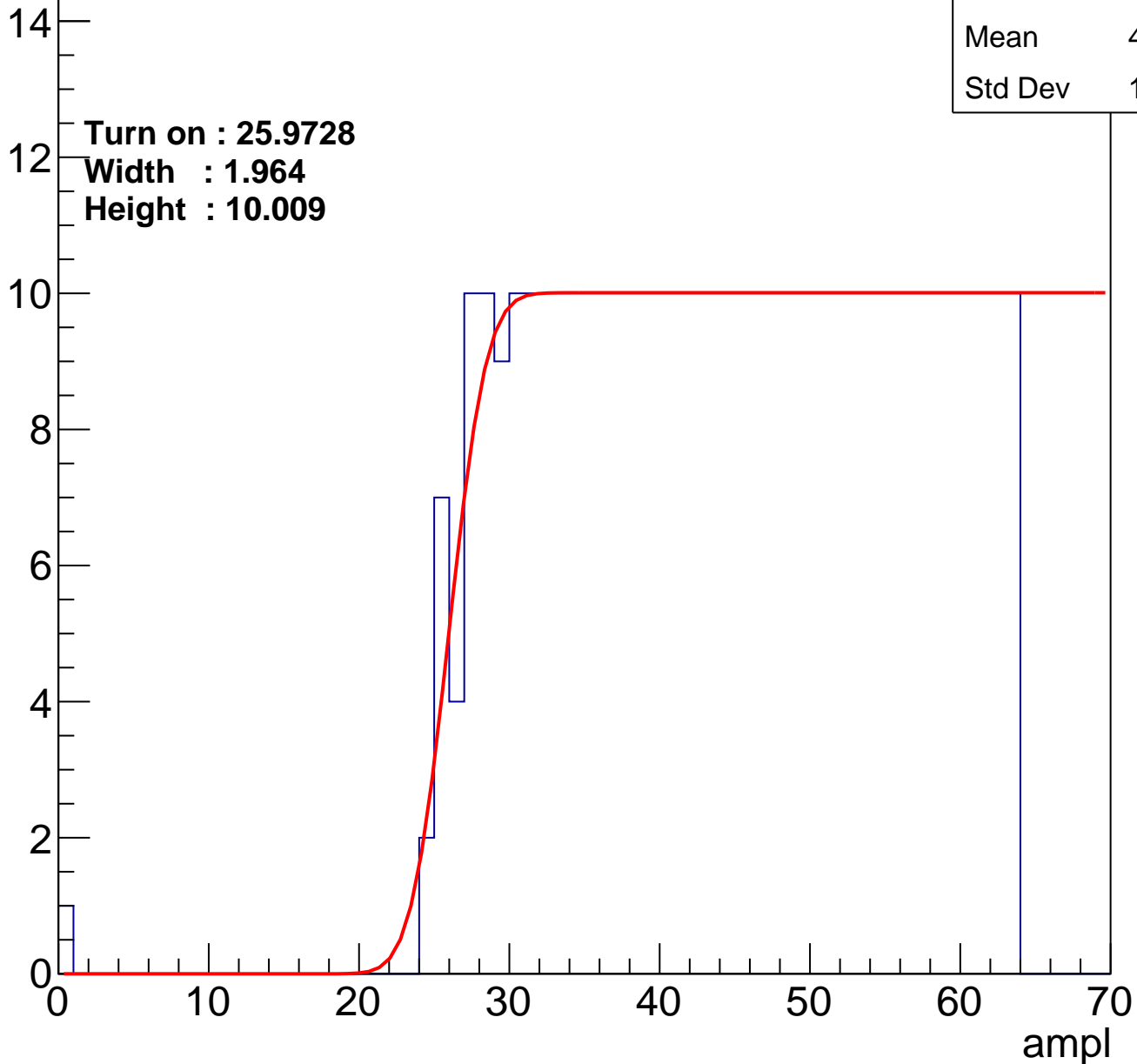
Entries	383
Mean	44.25
Std Dev	11.29

Turn on : 25.9728

Width : 1.964

Height : 10.009

Entry



B1L000S, U24-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 25.6493

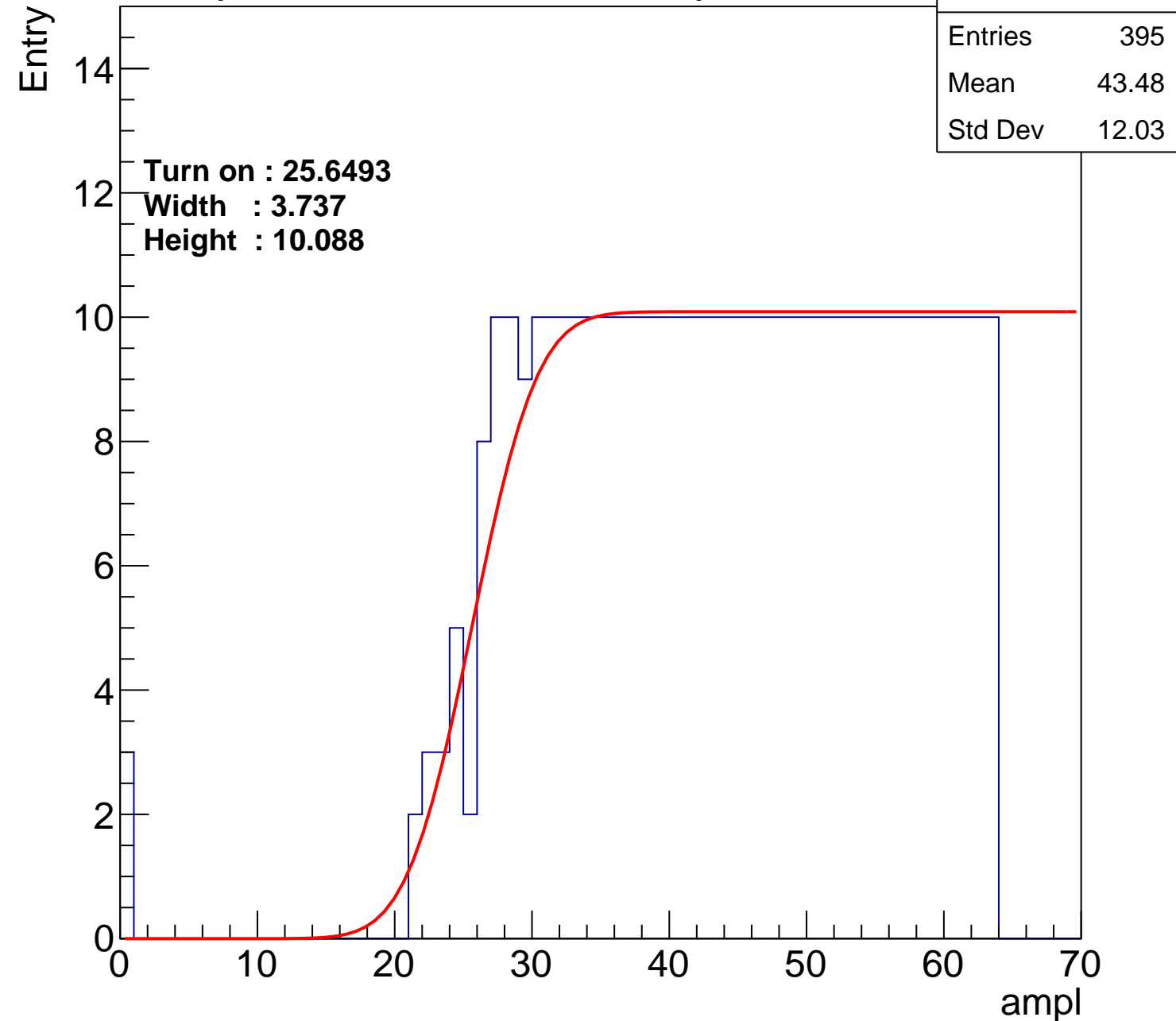
Width : 3.737

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.7
Std Dev	11.67

Turn on : 25.4588

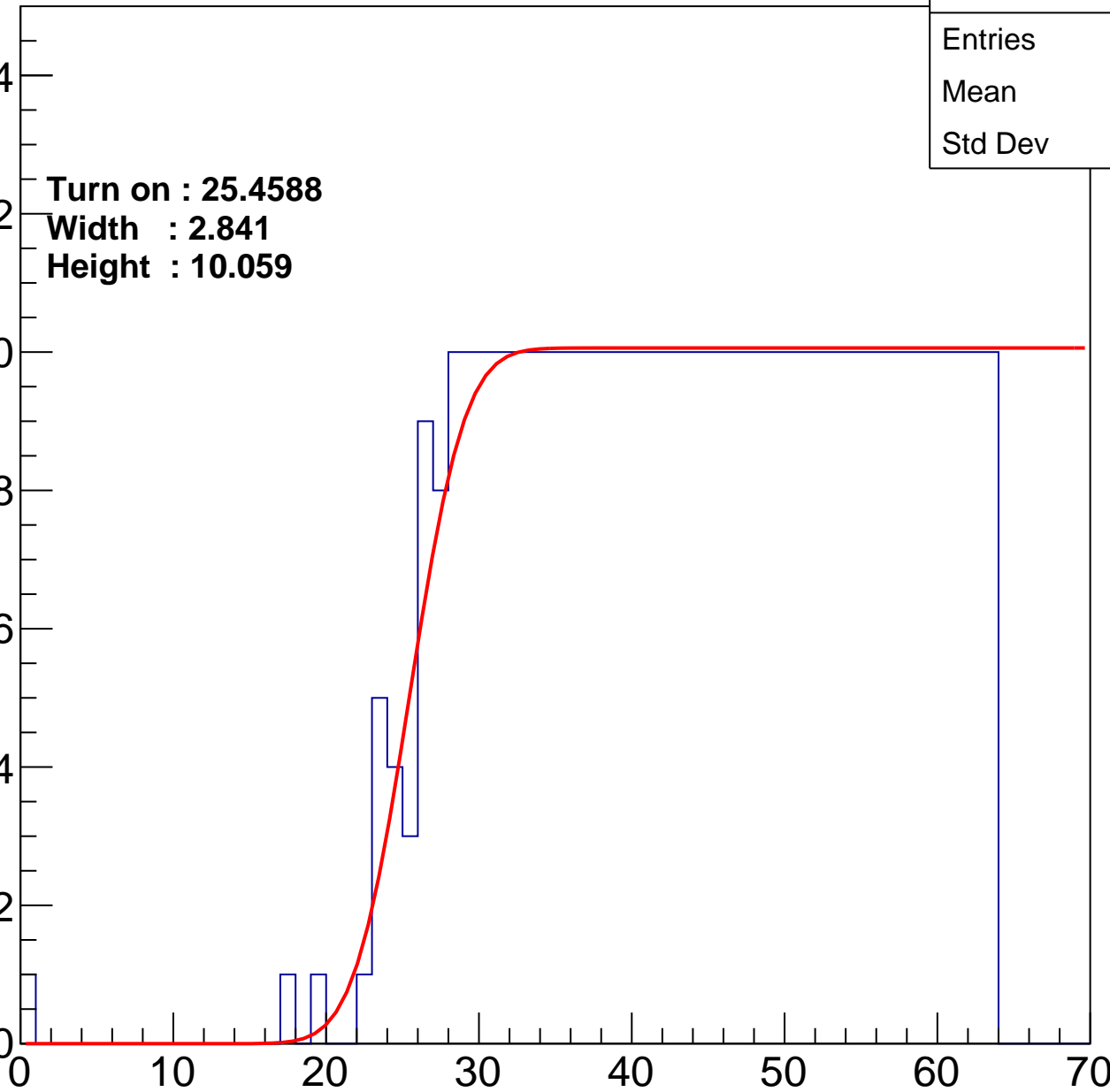
Width : 2.841

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch93

calib_packv5_042523_0143.root, FC#3, port B3

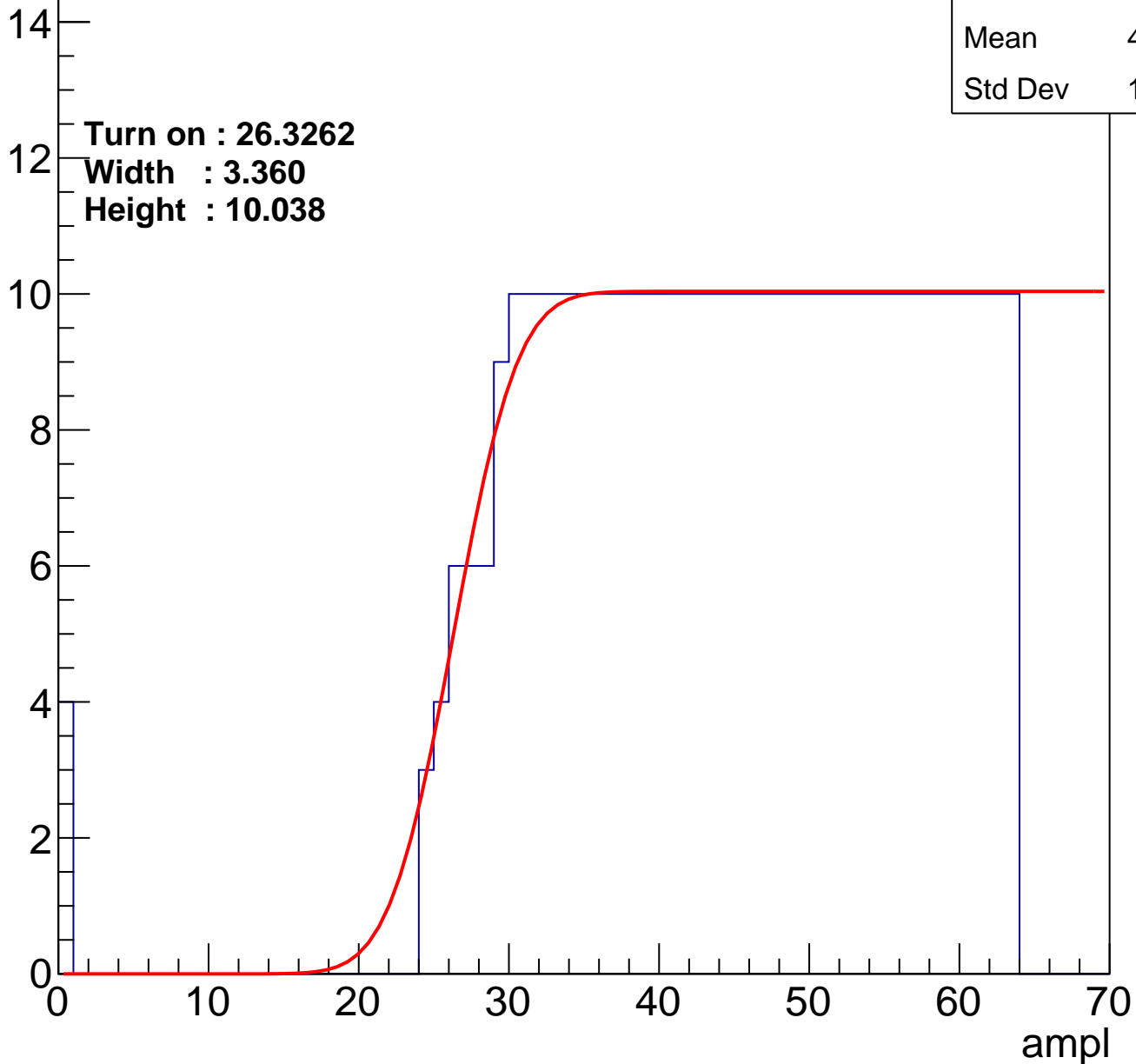
Entries	378
Mean	44.26
Std Dev	11.78

Turn on : 26.3262

Width : 3.360

Height : 10.038

Entry



B1L000S, U24-ch94

calib_packv5_042523_0143.root, FC#3, port B3

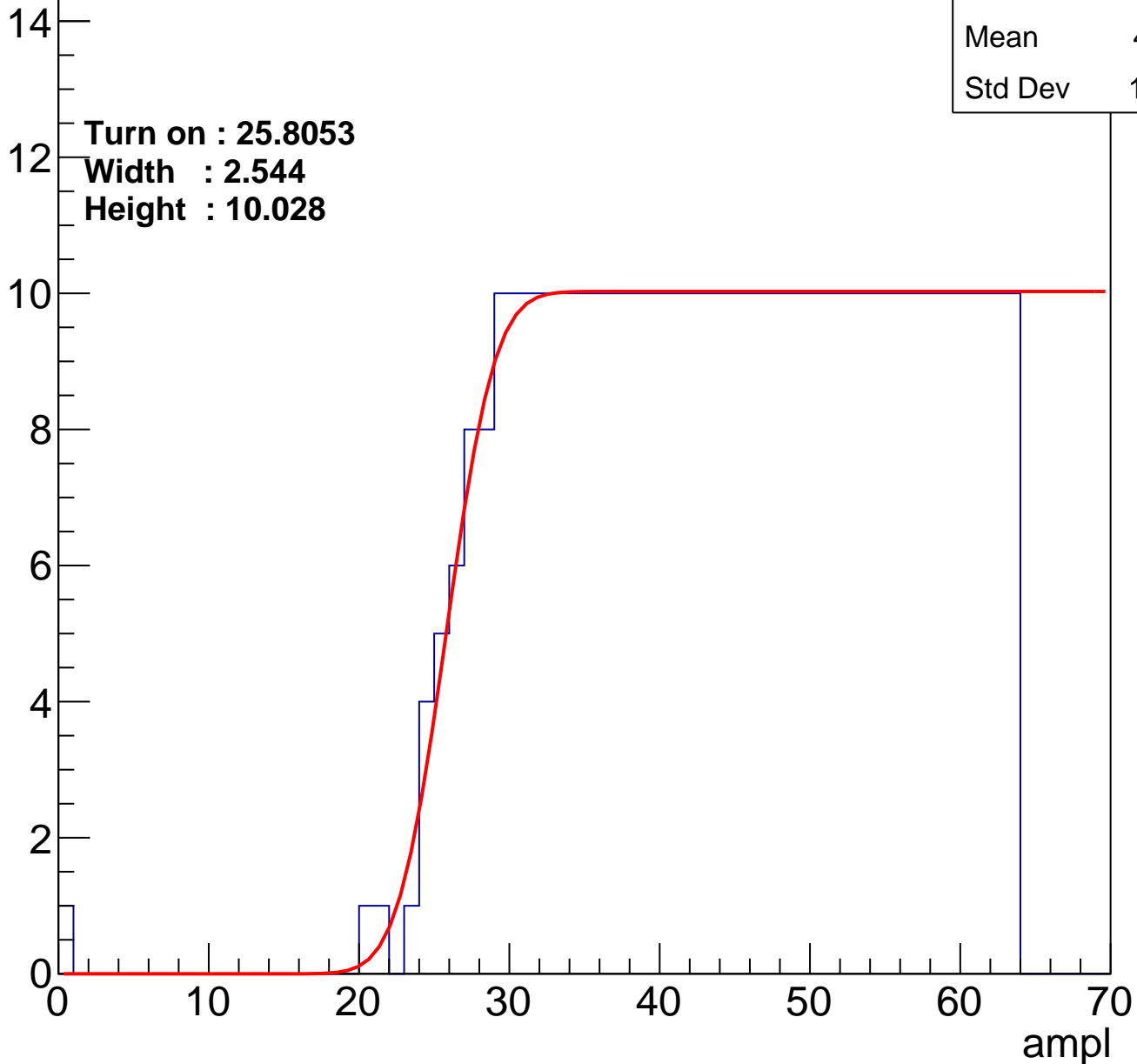
Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.8053

Width : 2.544

Height : 10.028

Entry



B1L000S, U24-ch95

calib_packv5_042523_0143.root, FC#3, port B3

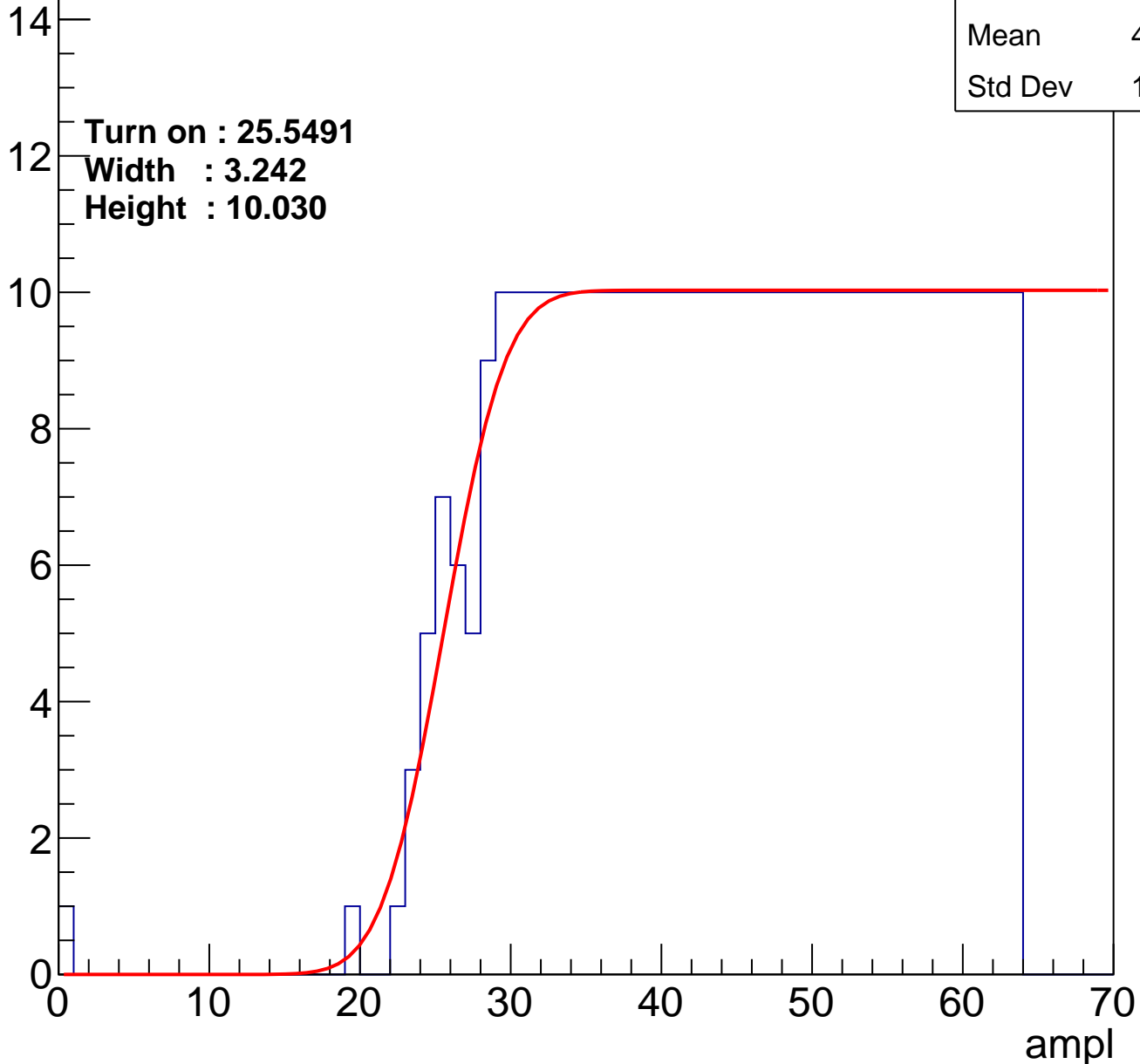
Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.5491

Width : 3.242

Height : 10.030

Entry



B1L000S, U24-ch96

calib_packv5_042523_0143.root, FC#3, port B3

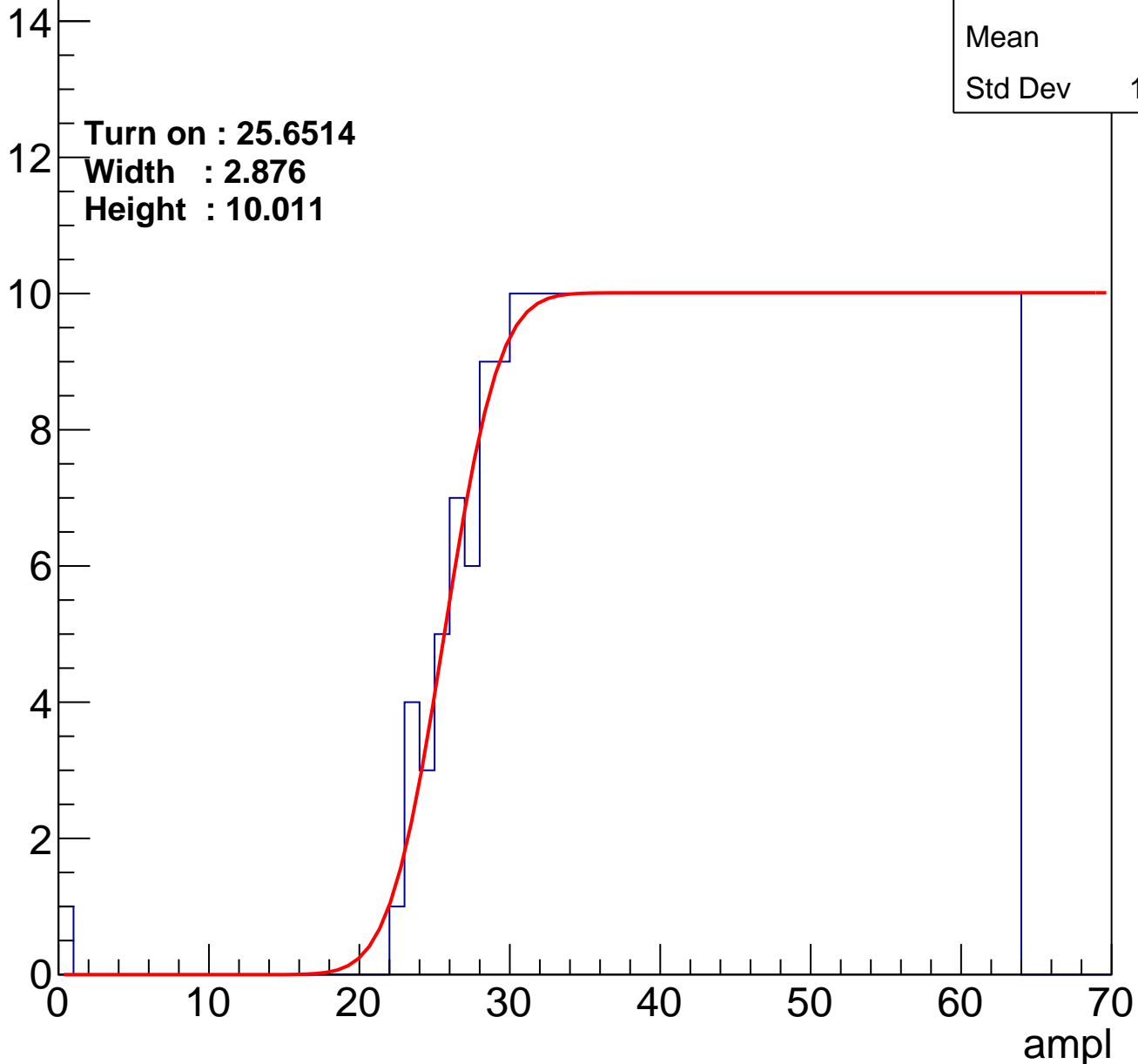
Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.6514

Width : 2.876

Height : 10.011

Entry



B1L000S, U24-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.6719

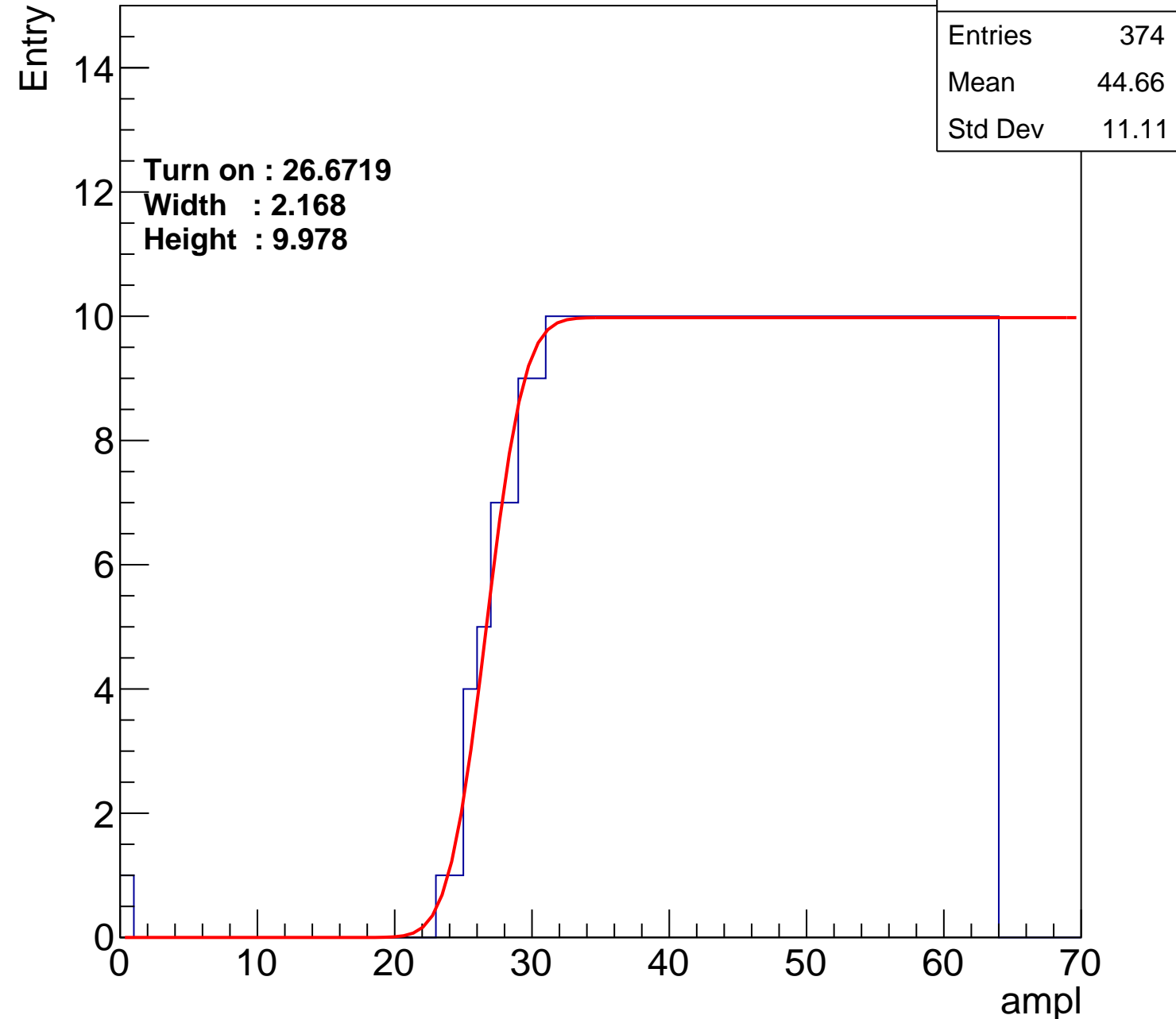
Width : 2.168

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch98

calib_packv5_042523_0143.root, FC#3, port B3

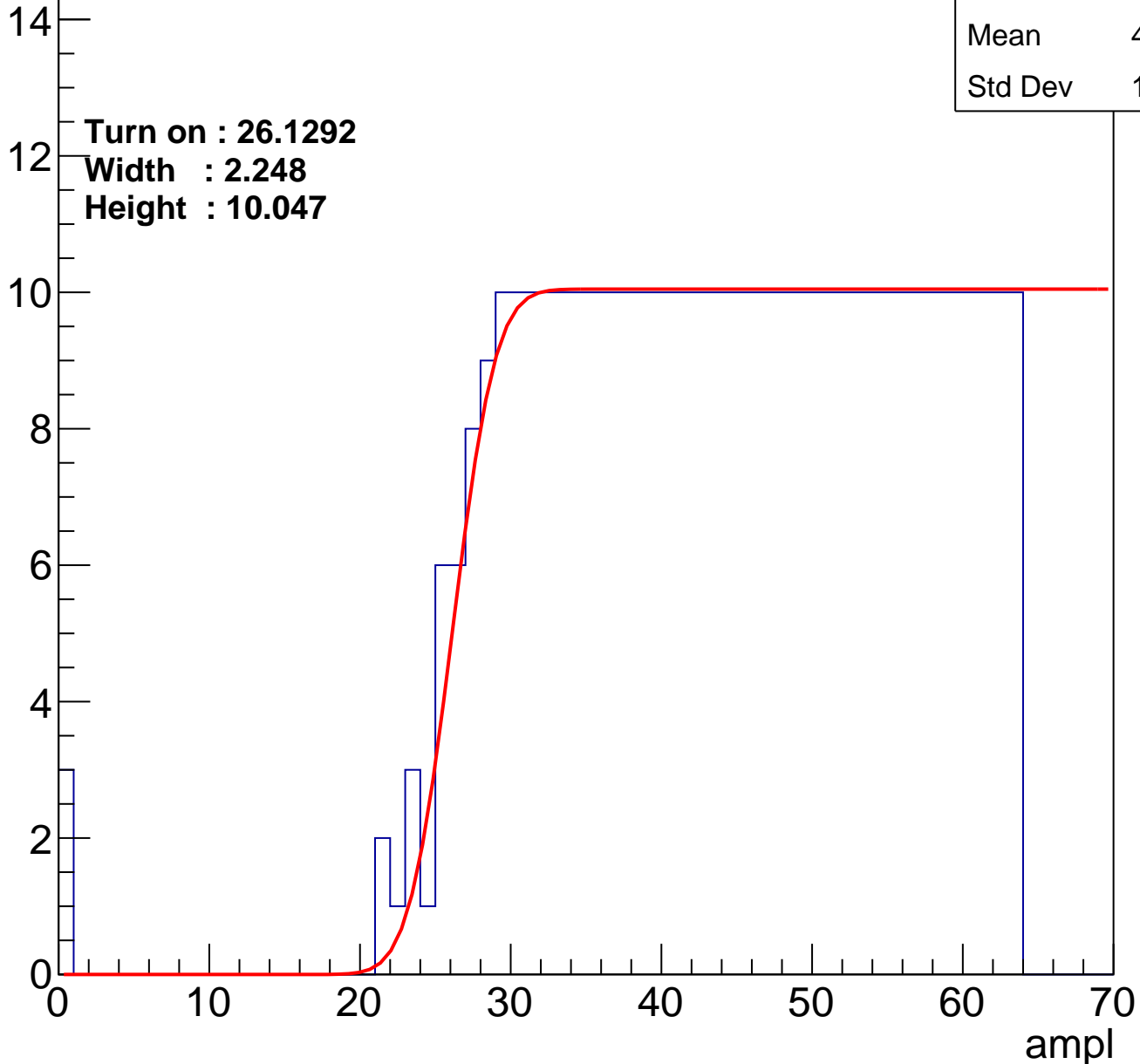
Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 26.1292

Width : 2.248

Height : 10.047

Entry



B1L000S, U24-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.11
Std Dev	11.72

Turn on : 26.3154

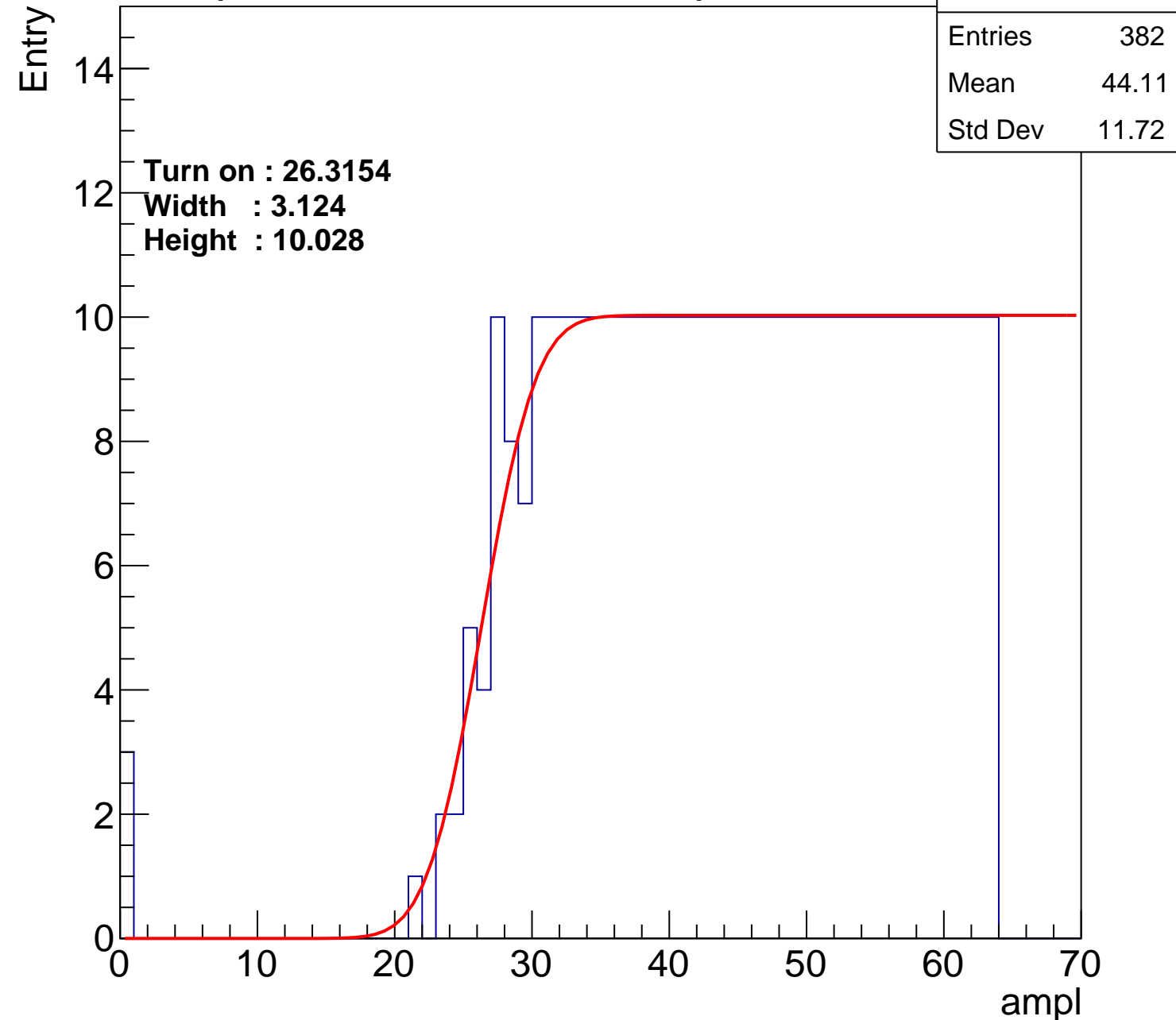
Width : 3.124

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 26.7698

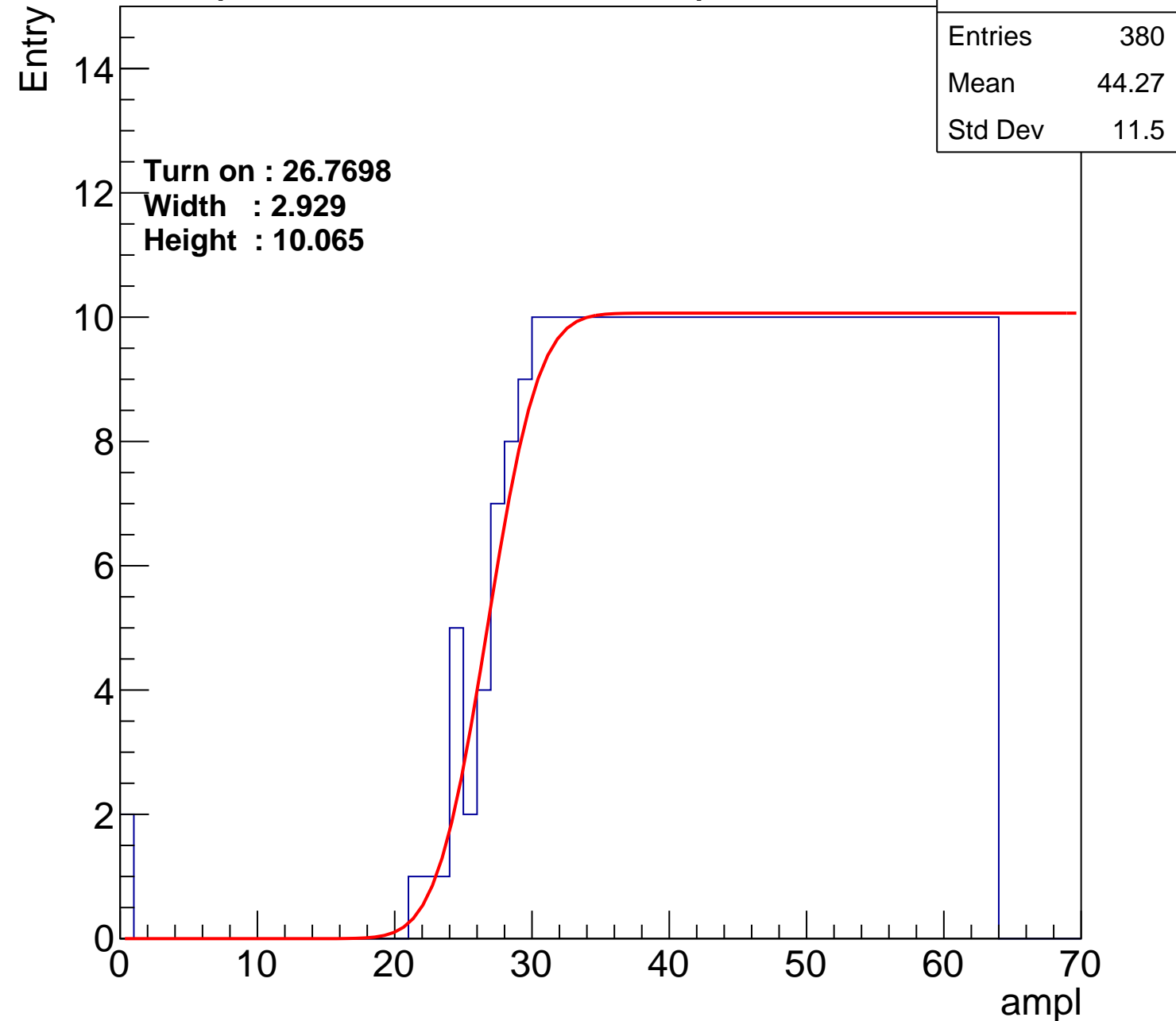
Width : 2.929

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.61
Std Dev	12.11

Turn on : 25.7072

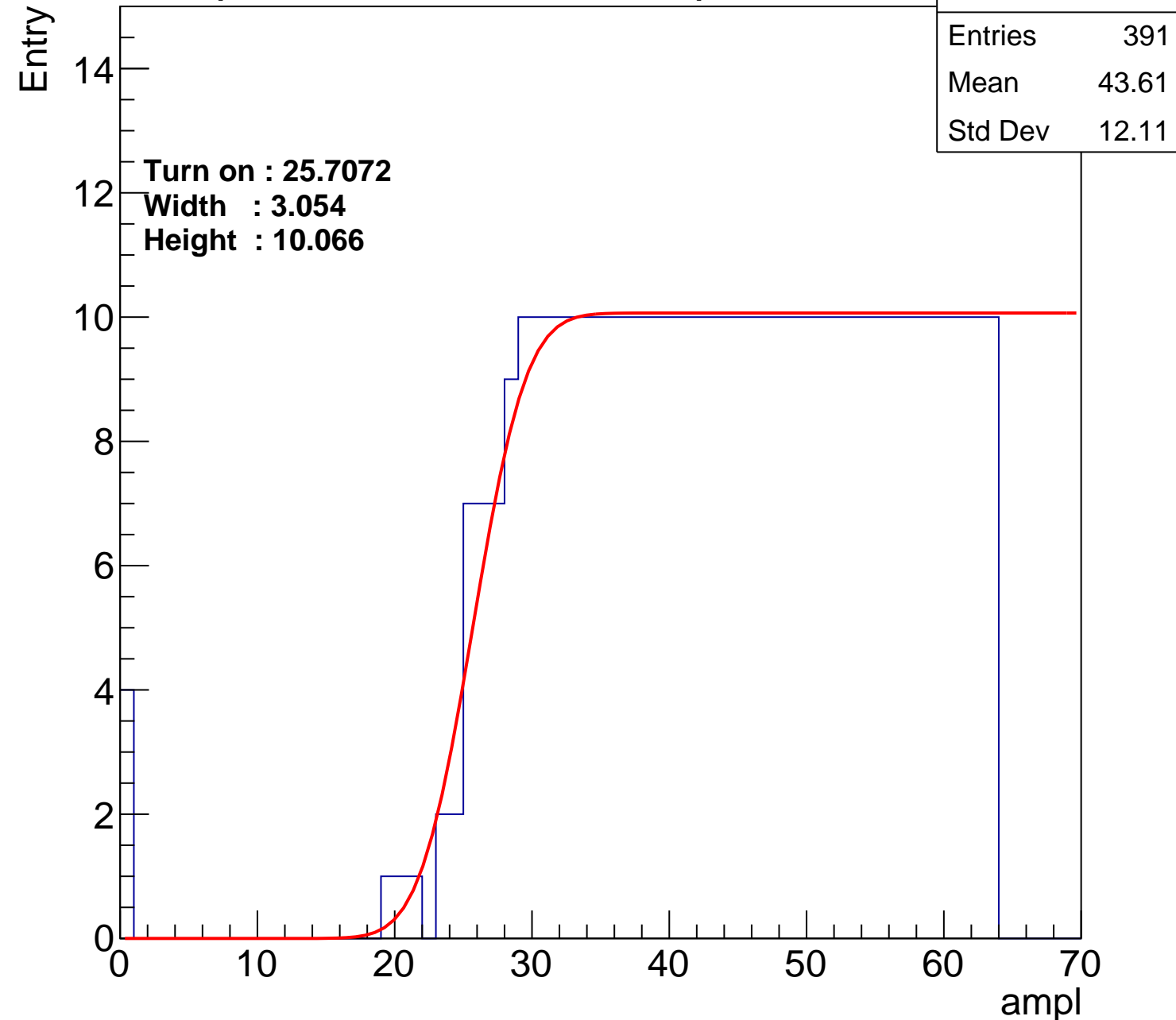
Width : 3.054

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.5532

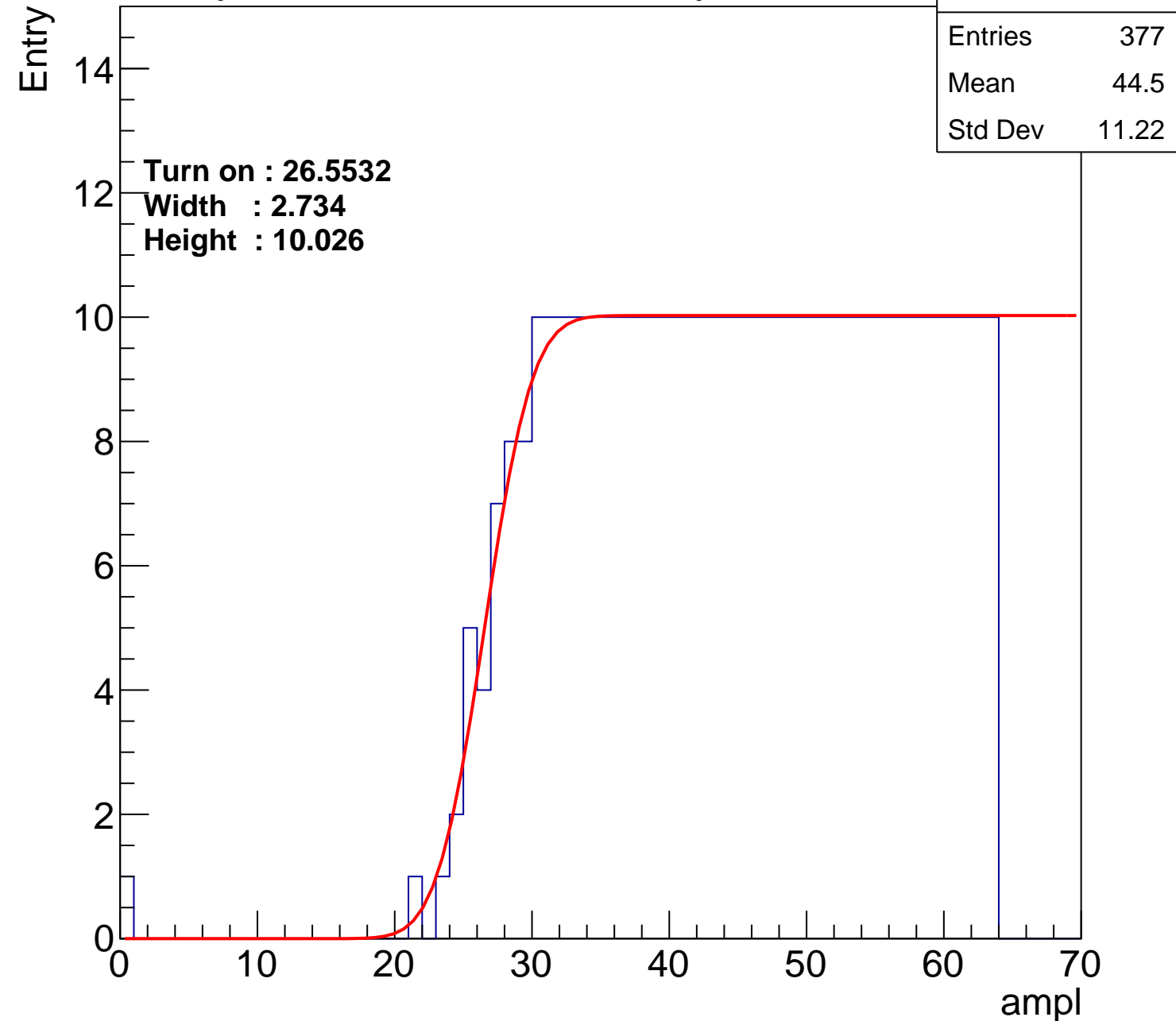
Width : 2.734

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch103

calib_packv5_042523_0143.root, FC#3, port B3

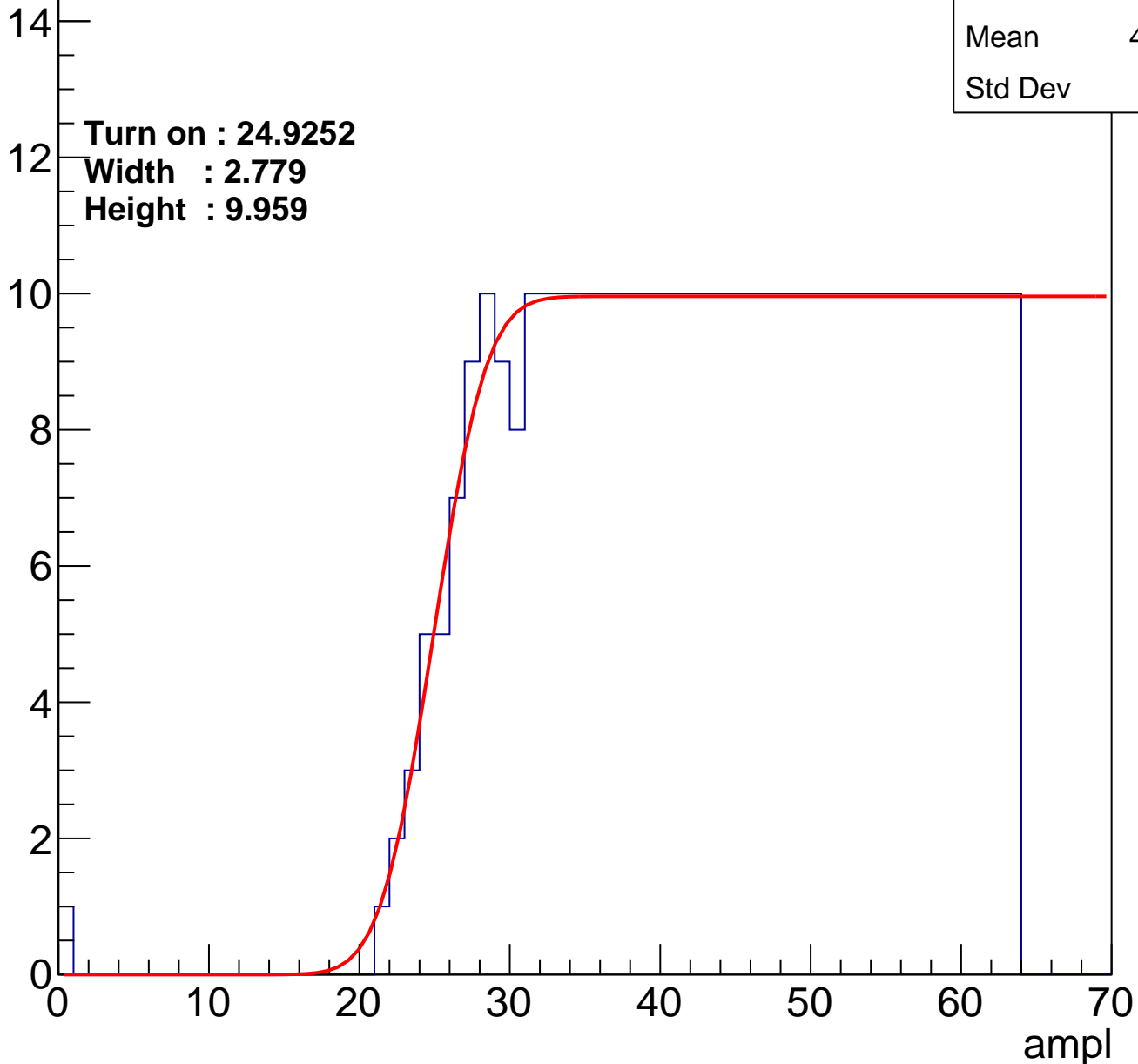
Entries	390
Mean	43.83
Std Dev	11.6

Turn on : 24.9252

Width : 2.779

Height : 9.959

Entry



B1L000S, U24-ch104

calib_packv5_042523_0143.root, FC#3, port B3

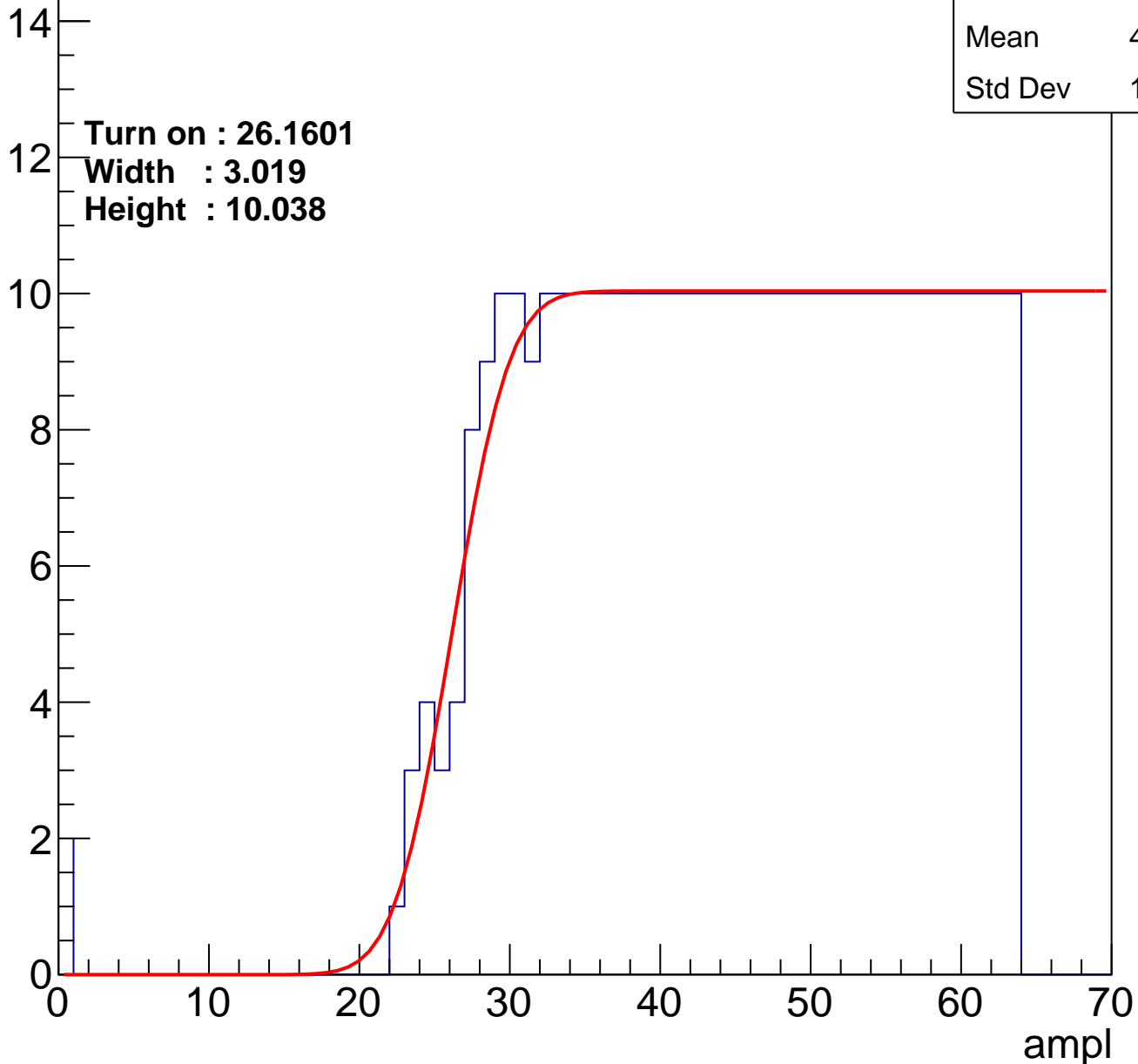
Entries	383
Mean	44.13
Std Dev	11.56

Turn on : 26.1601

Width : 3.019

Height : 10.038

Entry



B1L000S, U24-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.91
Std Dev	11.54

Turn on : 25.7358

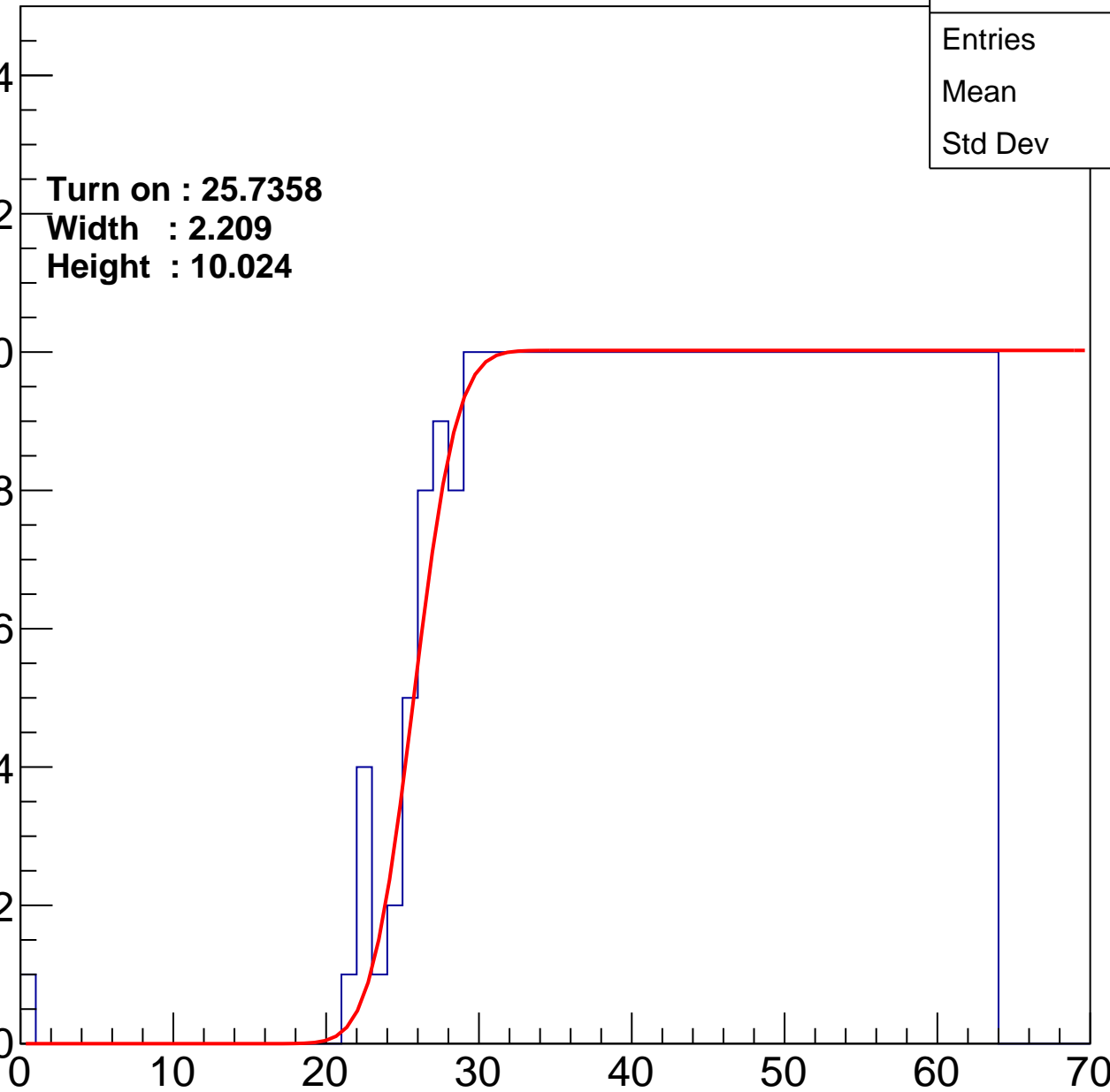
Width : 2.209

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch106

calib_packv5_042523_0143.root, FC#3, port B3

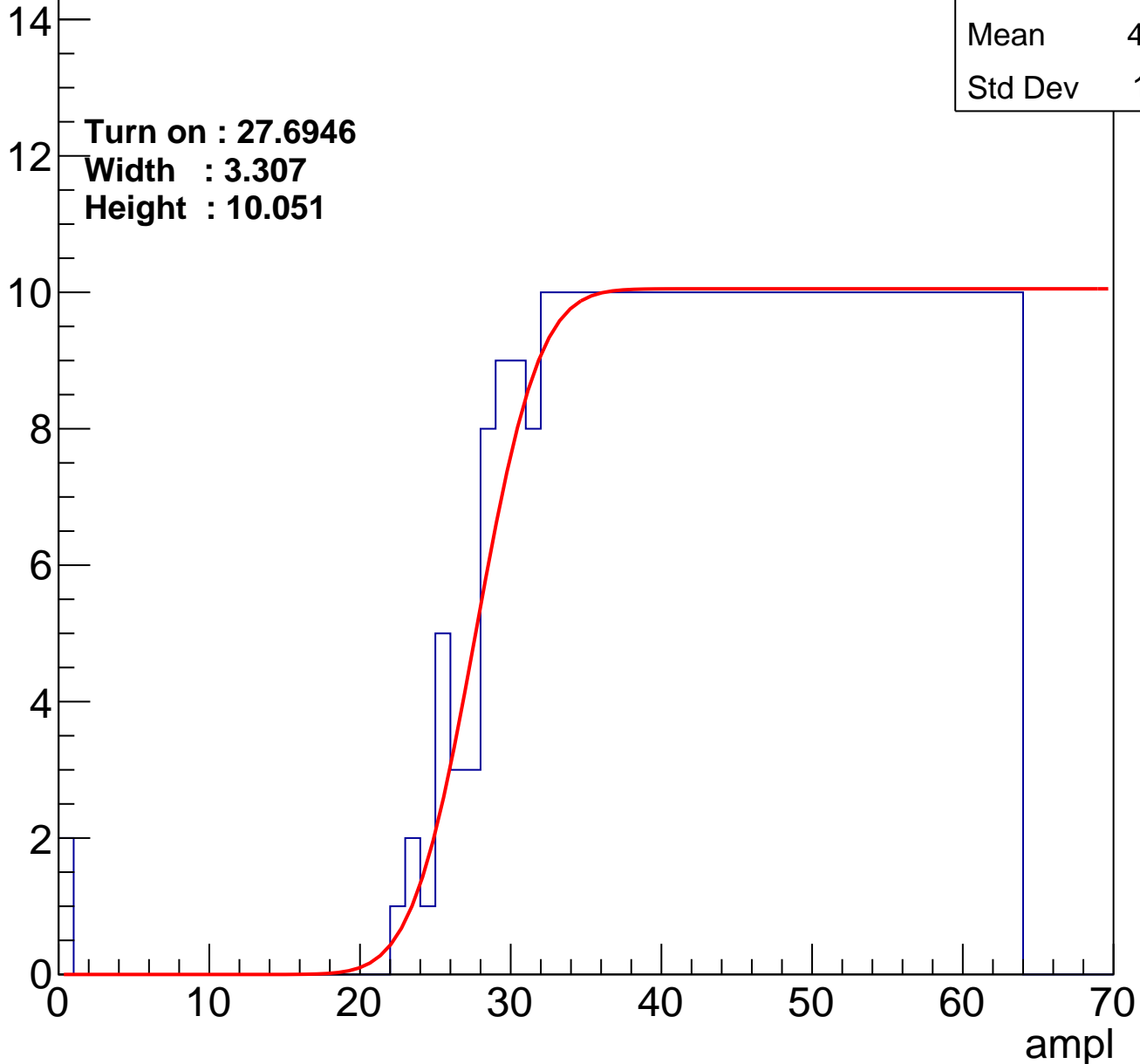
Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 27.6946

Width : 3.307

Height : 10.051

Entry



B1L000S, U24-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	44
Std Dev	11.48

Turn on : 25.1905

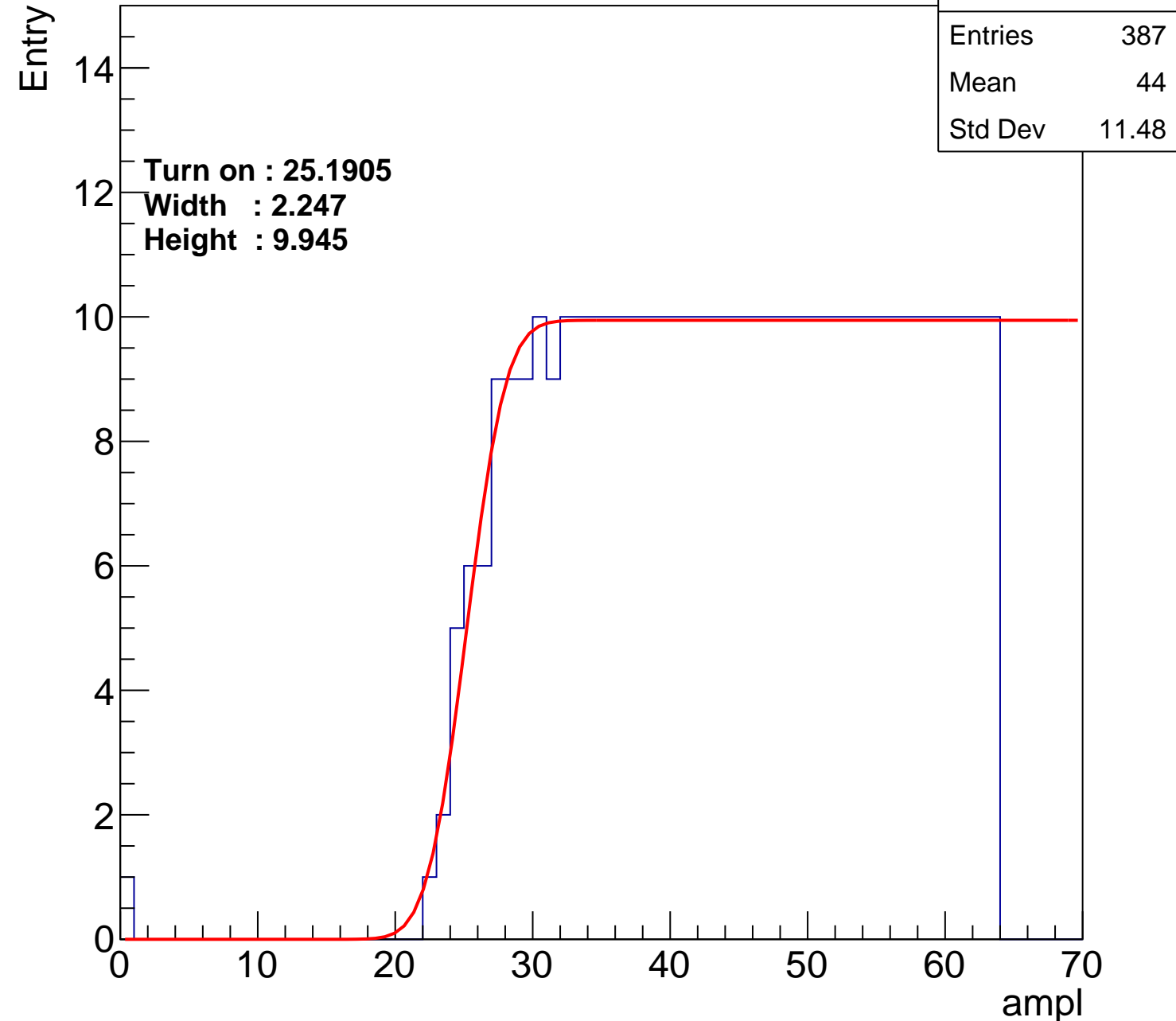
Width : 2.247

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.47
Std Dev	11.33

Turn on : 26.7368

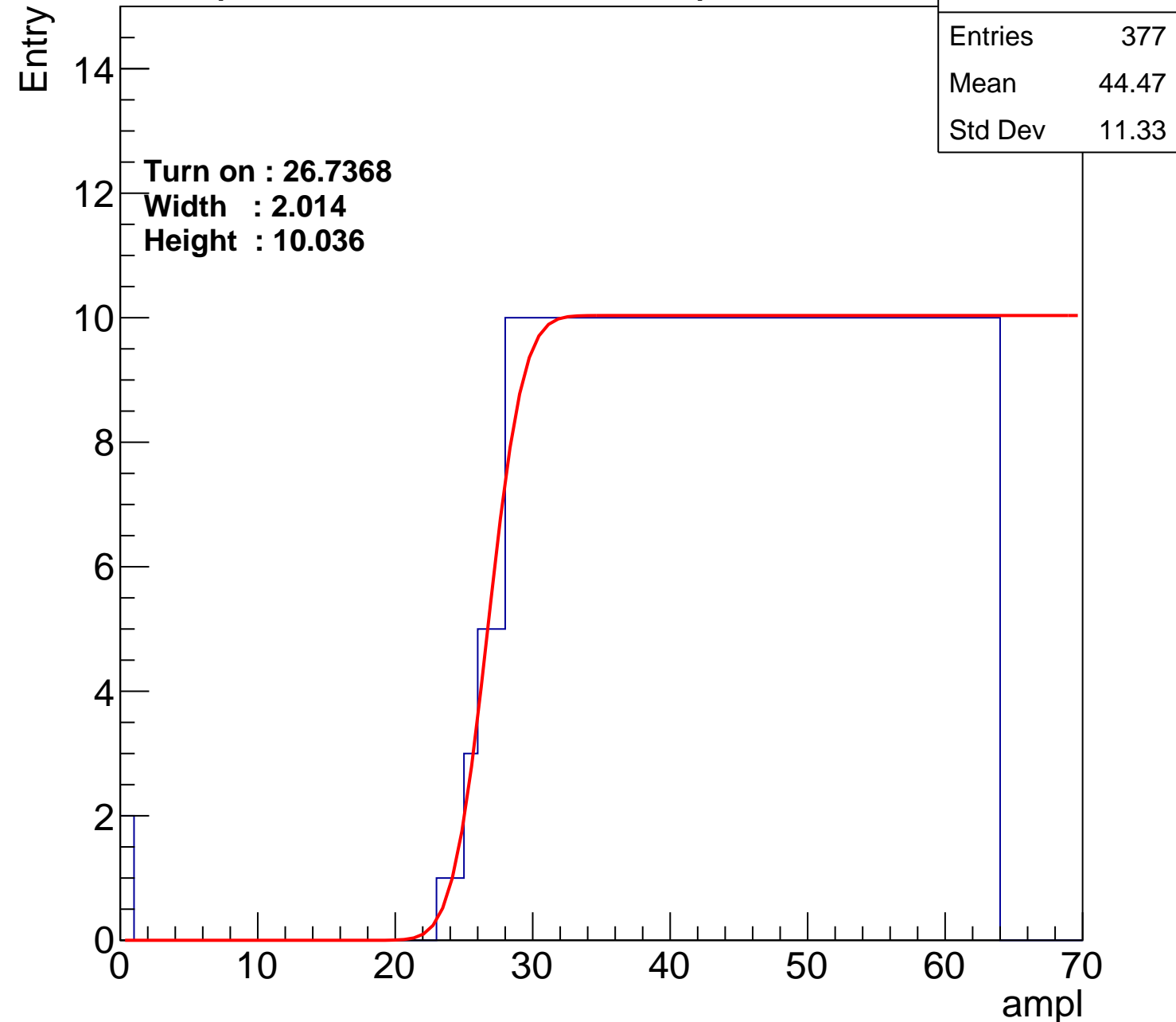
Width : 2.014

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch109

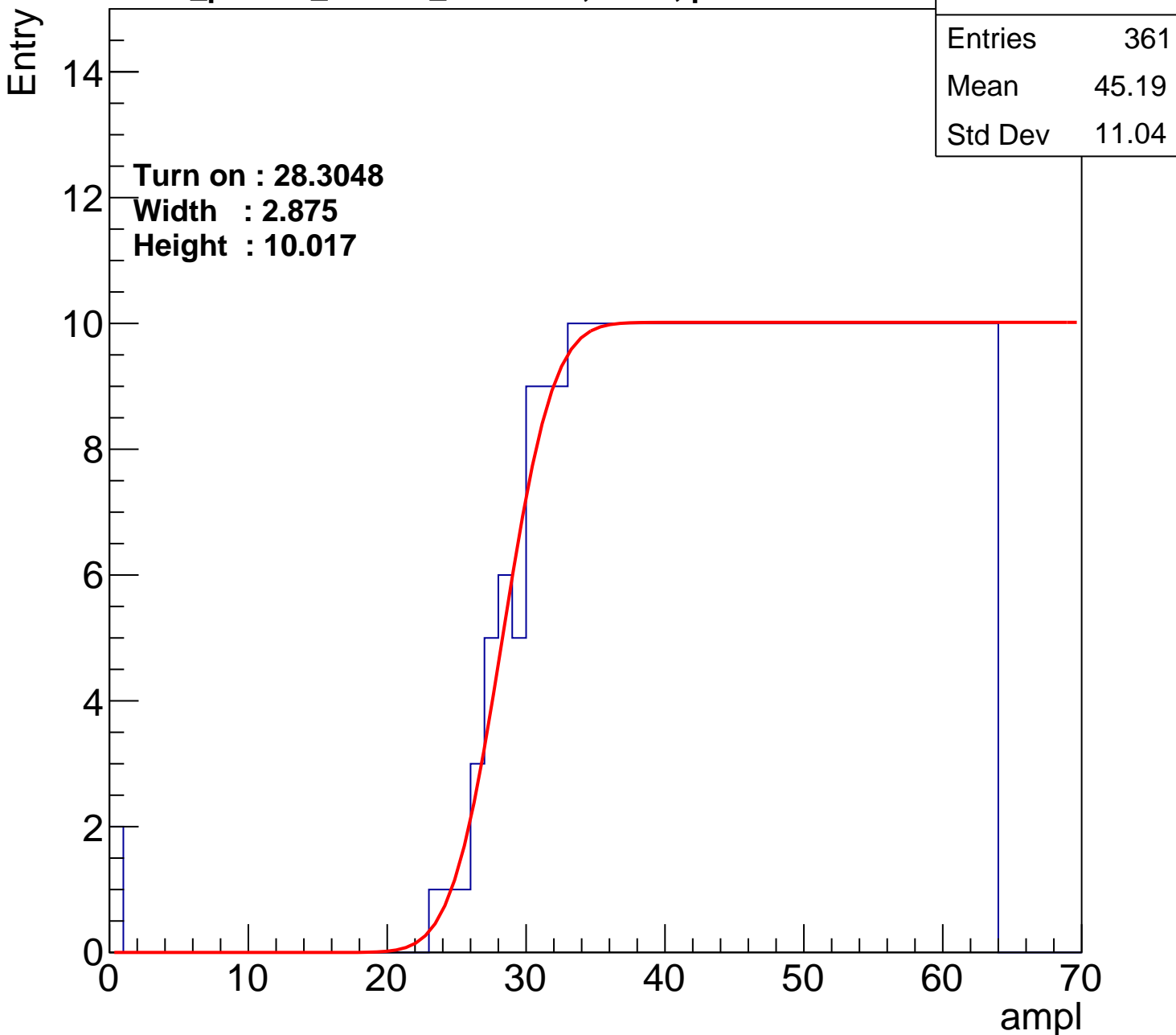
calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 28.3048

Width : 2.875

Height : 10.017

Entries	361
Mean	45.19
Std Dev	11.04



B1L000S, U24-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 24.8598

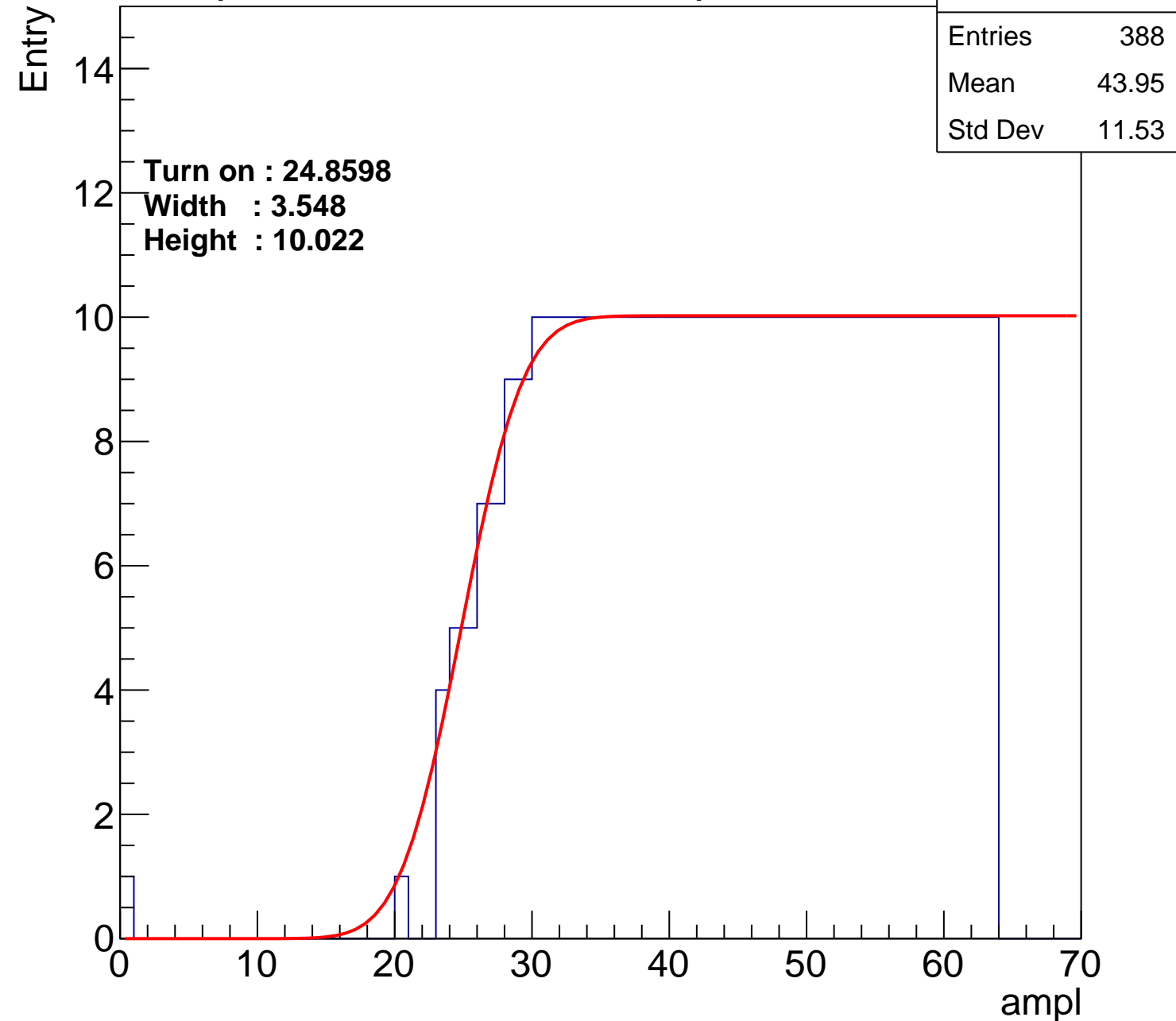
Width : 3.548

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.65
Std Dev	11.97

Turn on : 25.2947

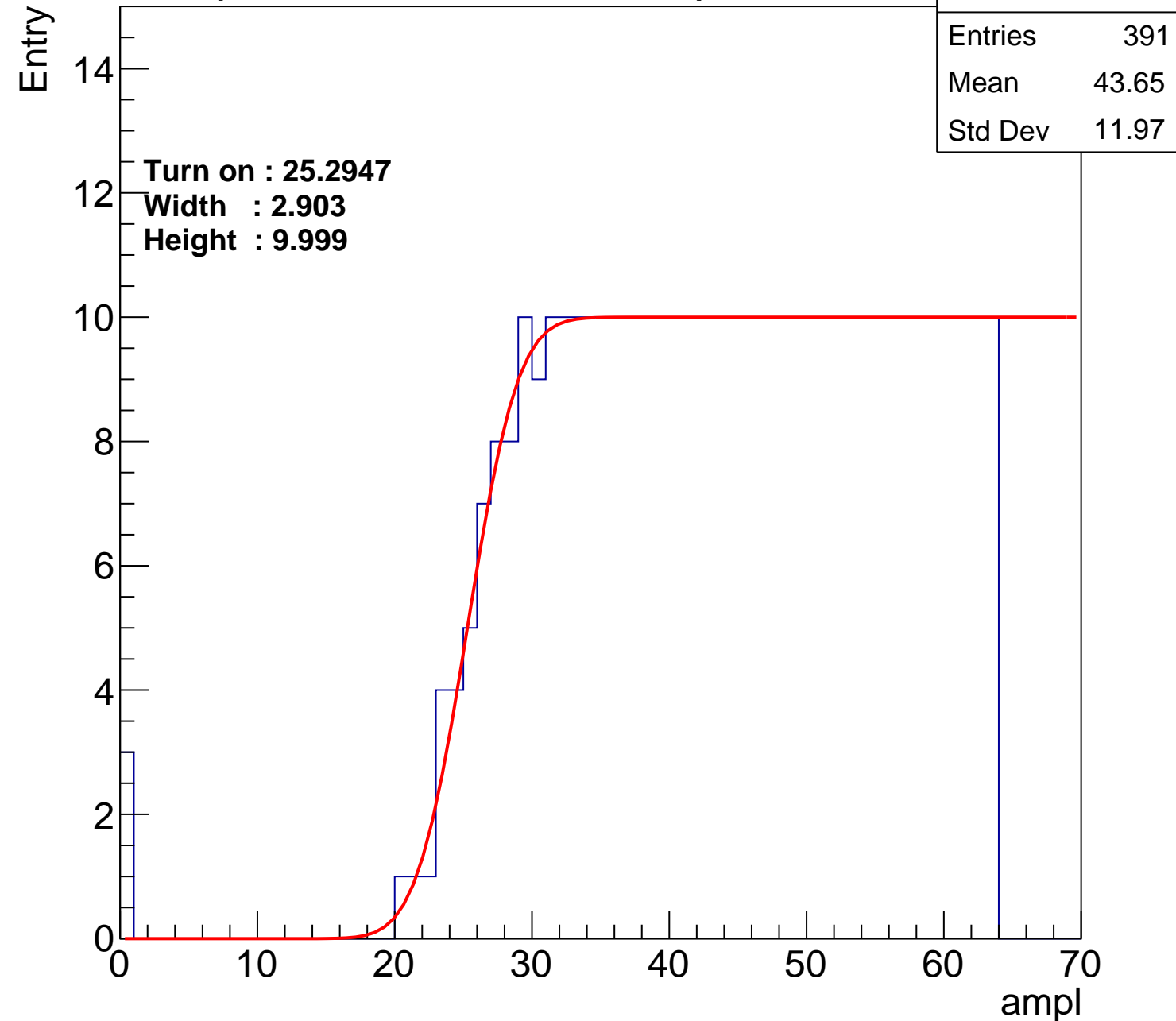
Width : 2.903

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.89
Std Dev	11.8

Turn on : 25.2619

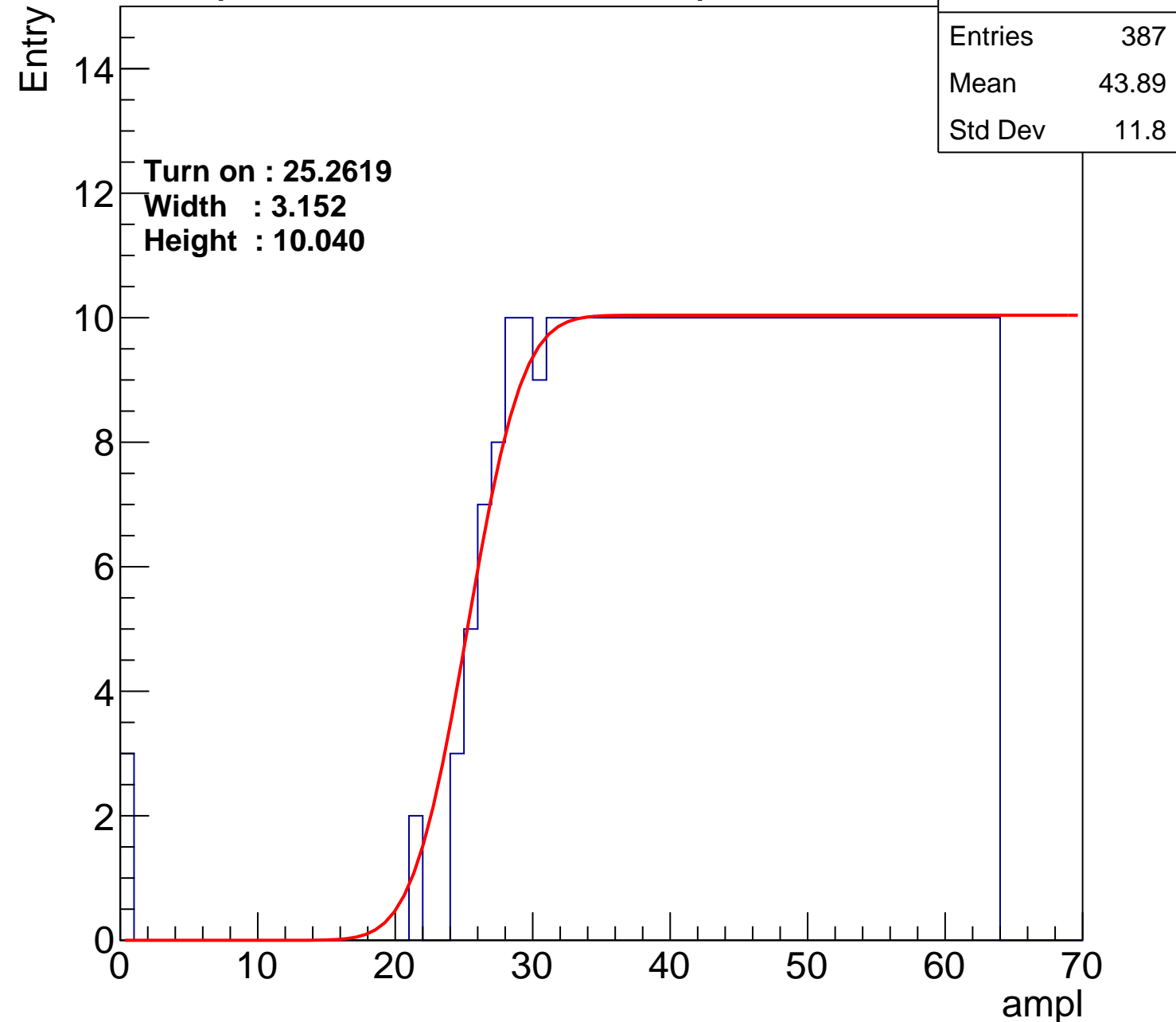
Width : 3.152

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.0663

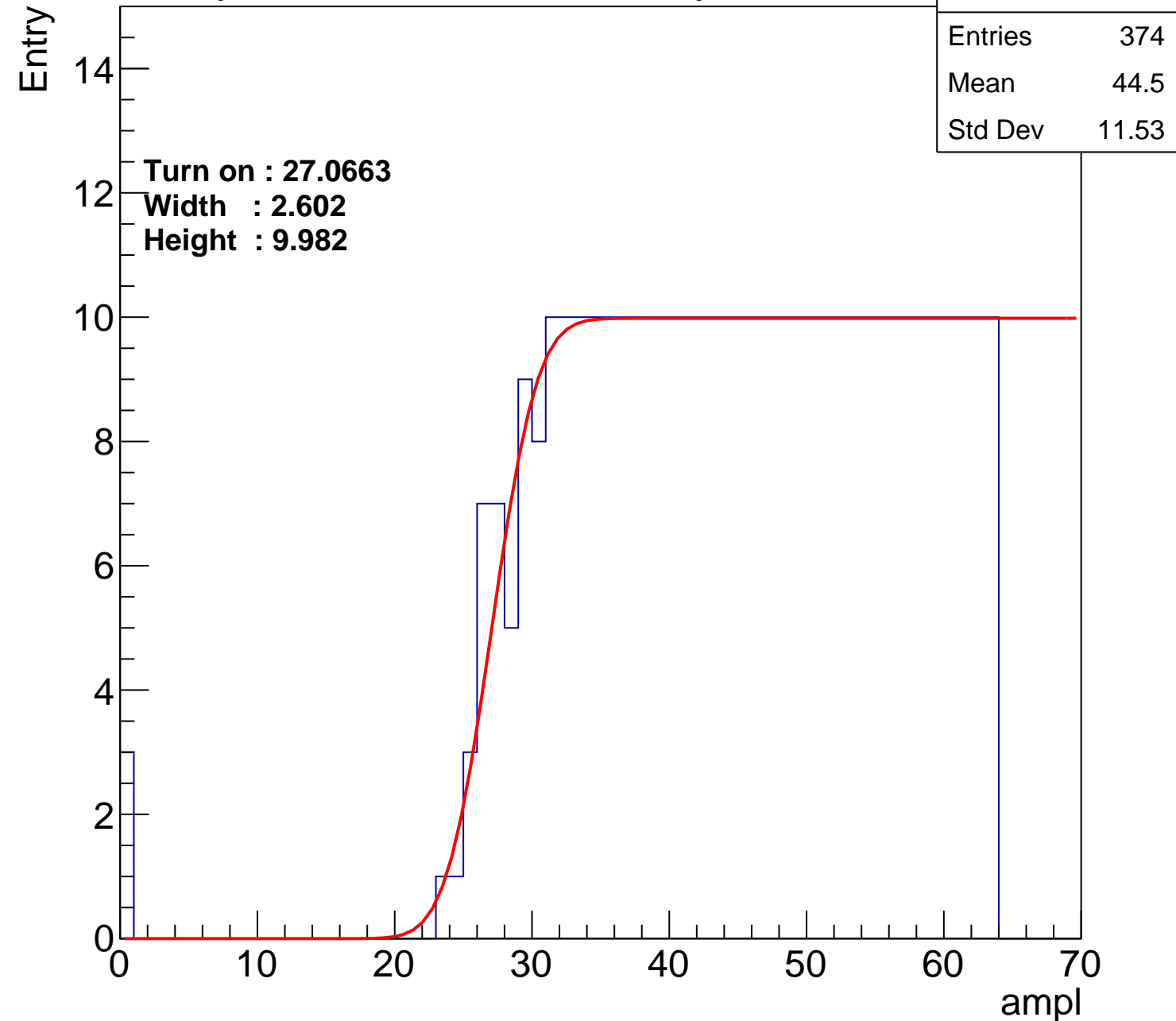
Width : 2.602

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.3764

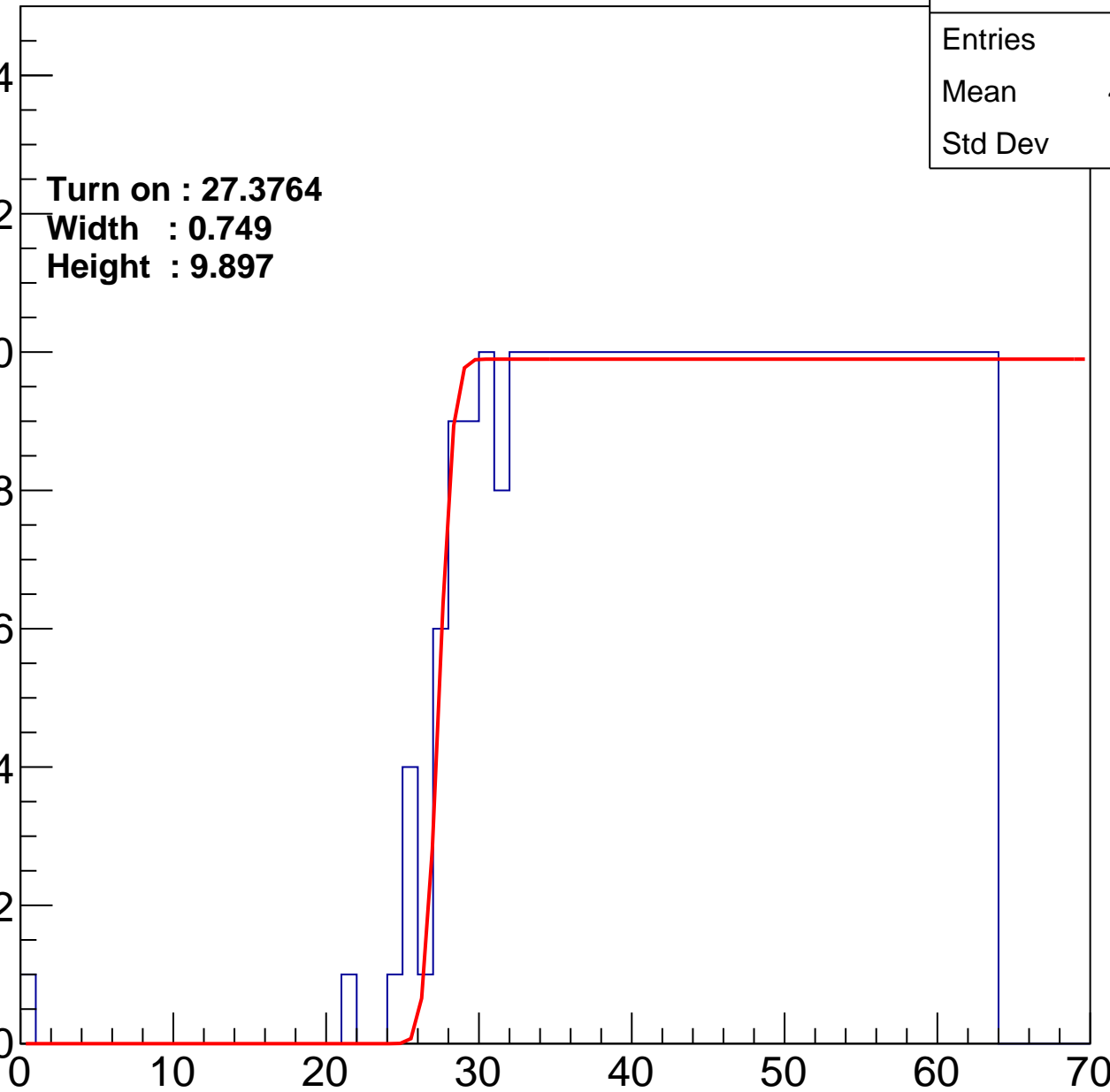
Width : 0.749

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.93
Std Dev	11.97

Turn on : 26.0400

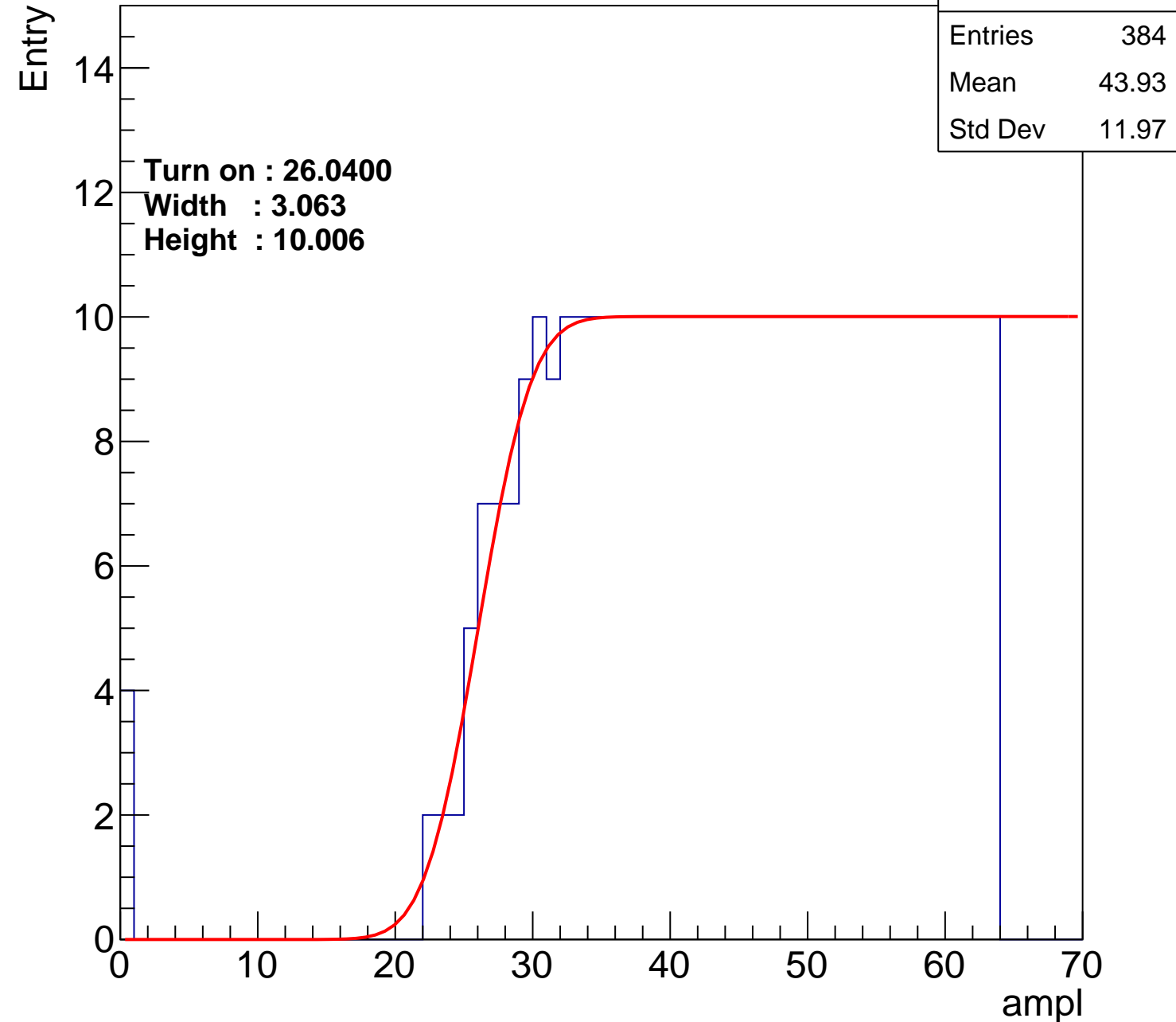
Width : 3.063

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.9172

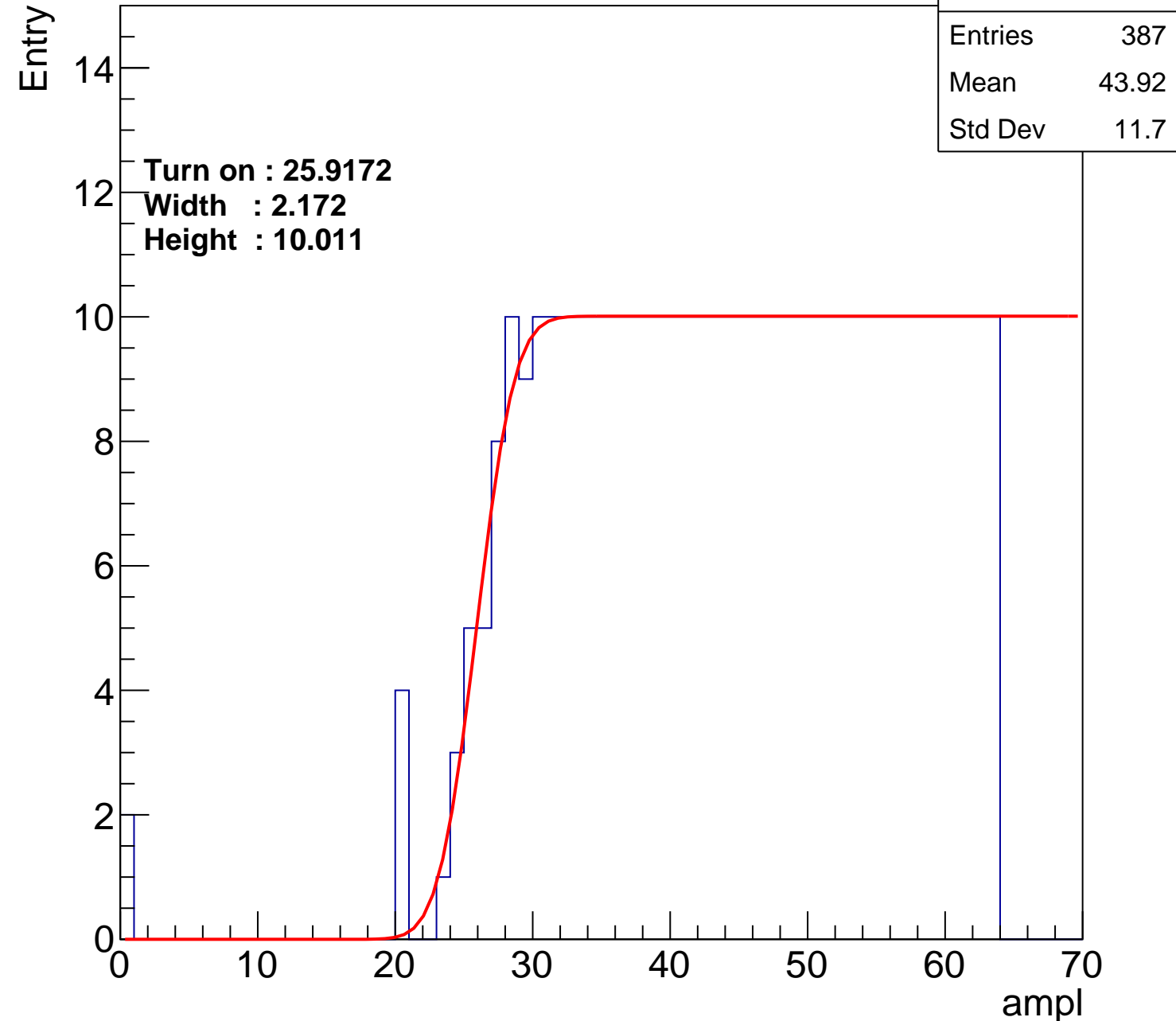
Width : 2.172

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.98
Std Dev	11.77

Turn on : 25.5510

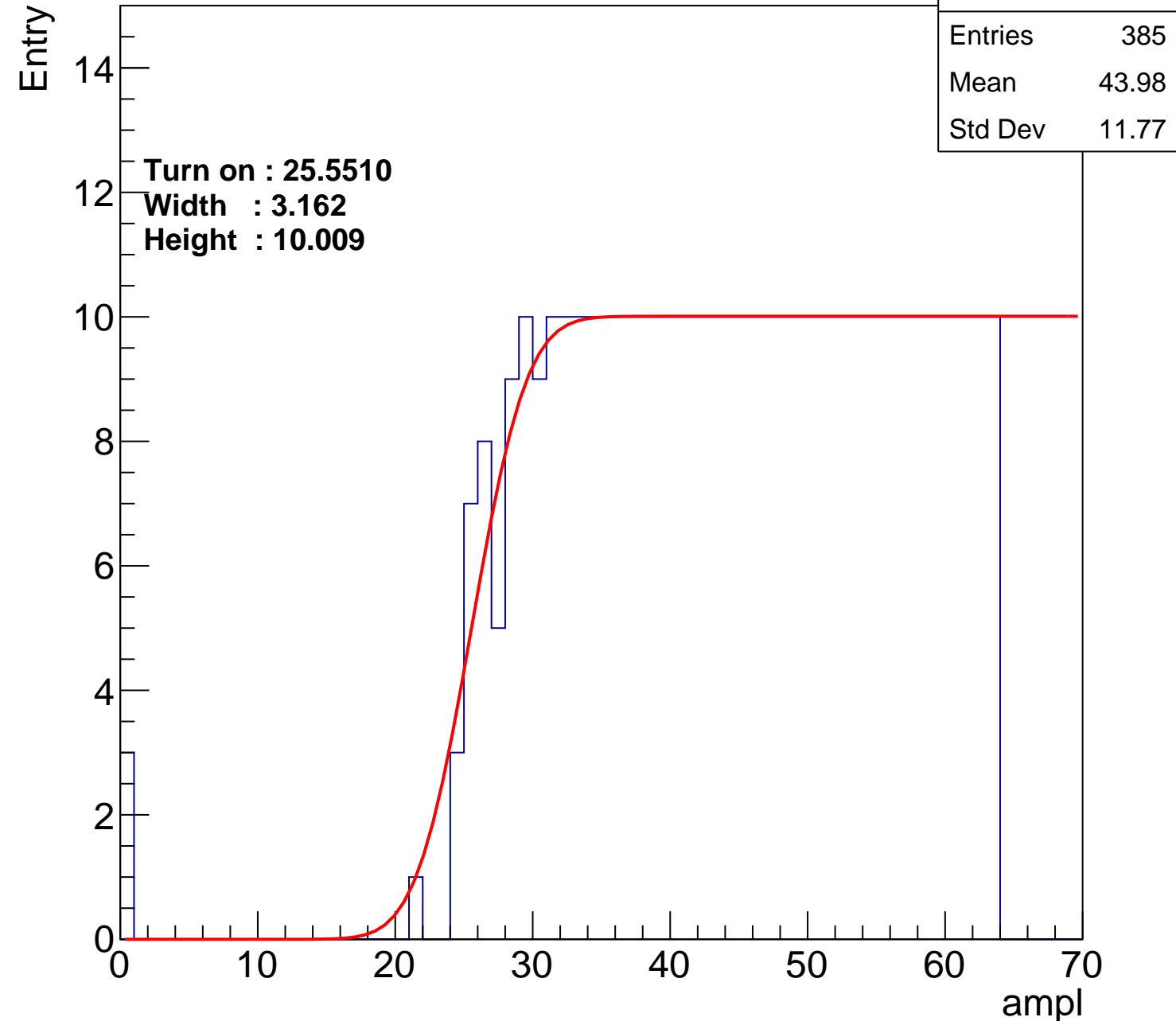
Width : 3.162

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.05
Std Dev	11.72

Turn on : 26.1045

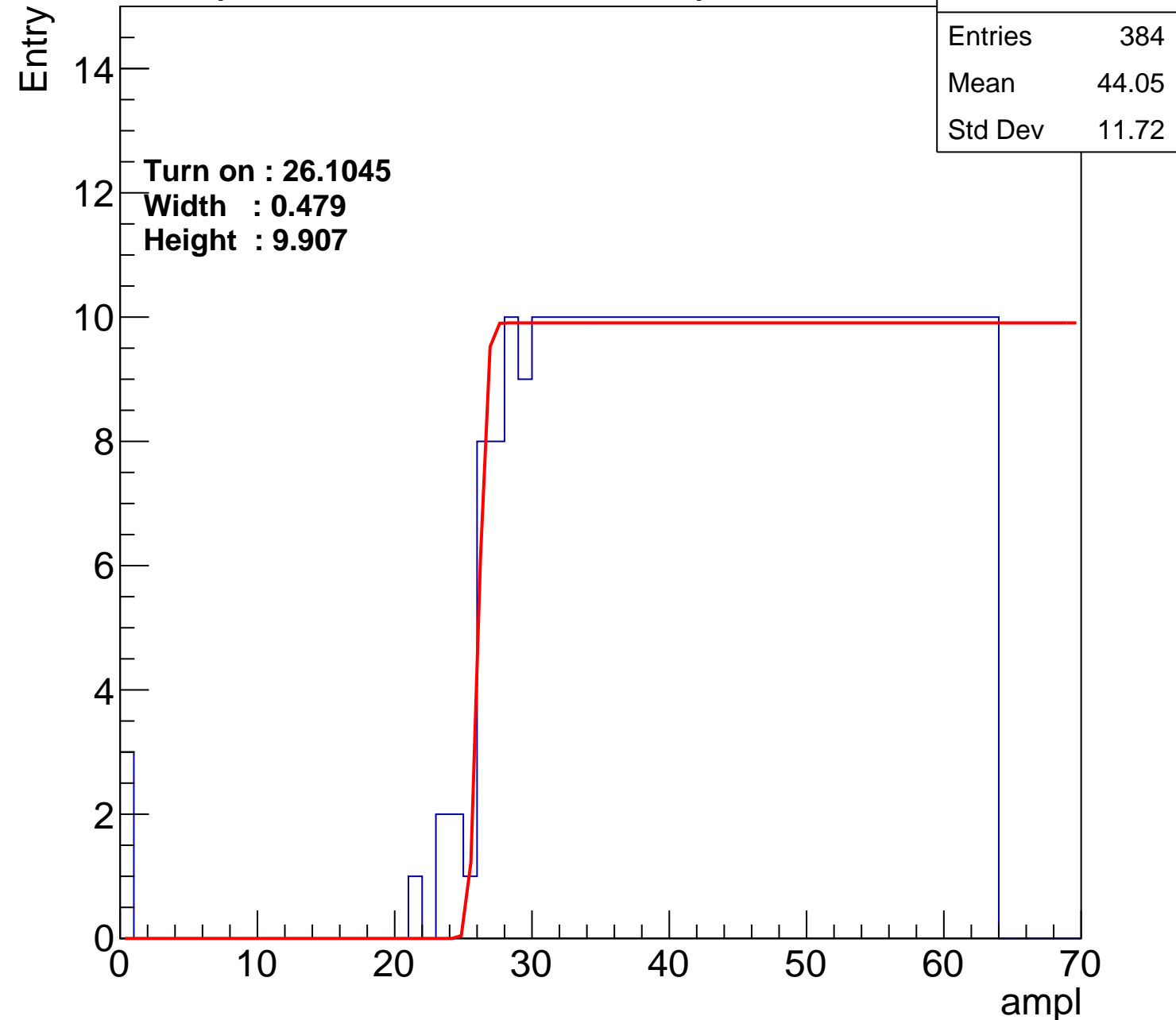
Width : 0.479

Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.51
Std Dev	11.46

Turn on : 27.9740

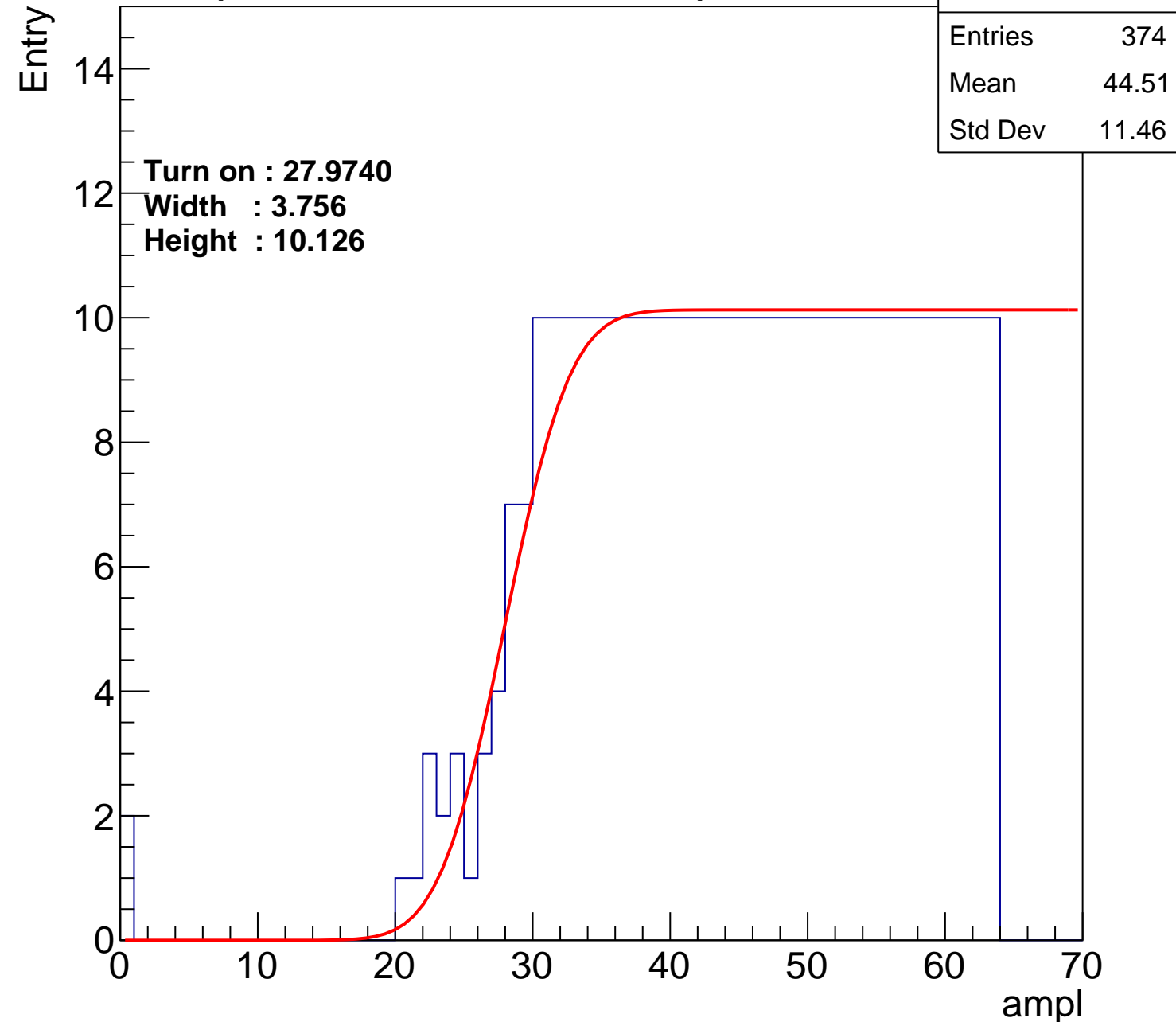
Width : 3.756

Height : 10.126

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.37
Std Dev	11.93

Turn on : 24.2417

Width : 1.811

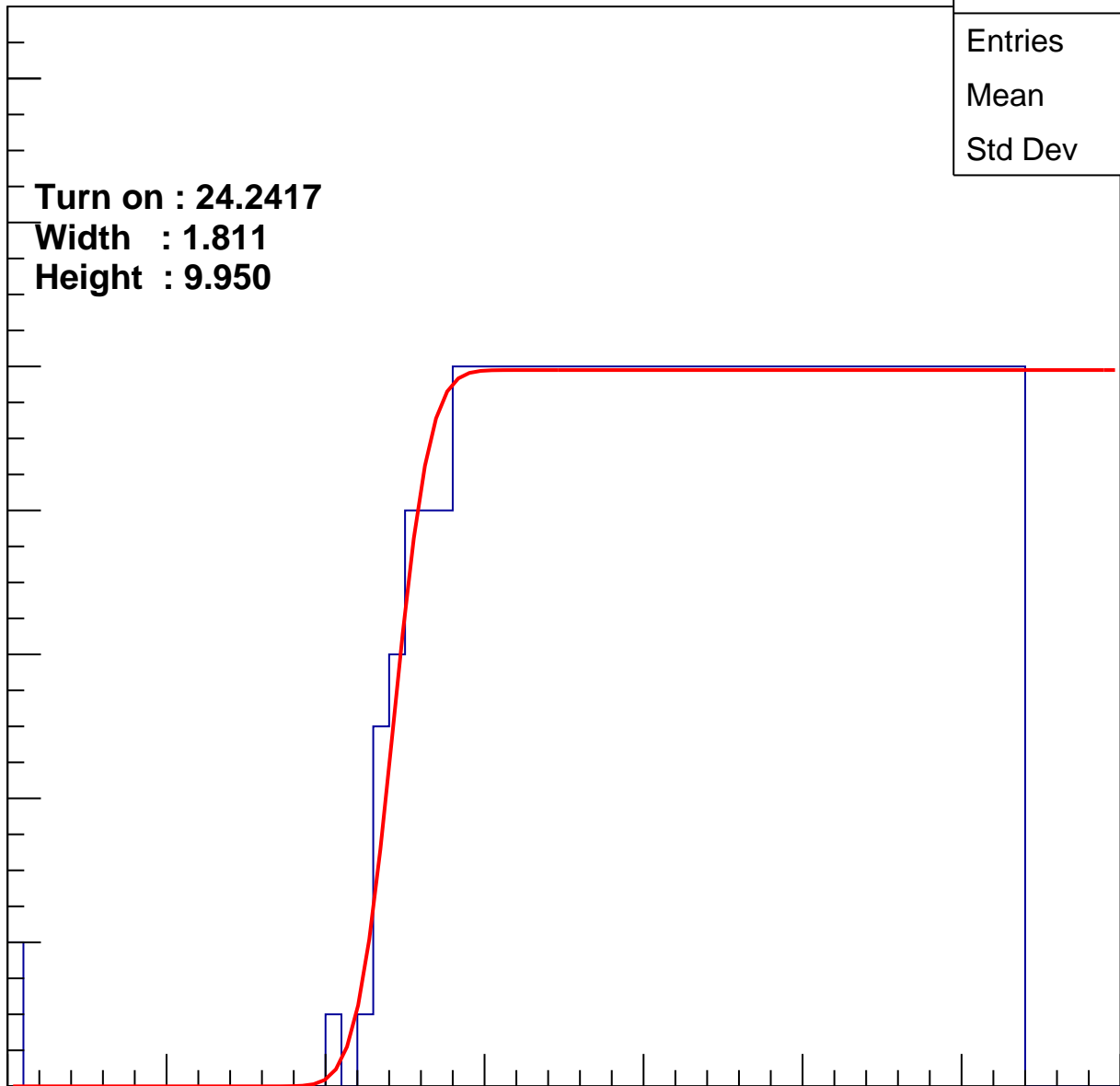
Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U24-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.2511

Width : 3.092

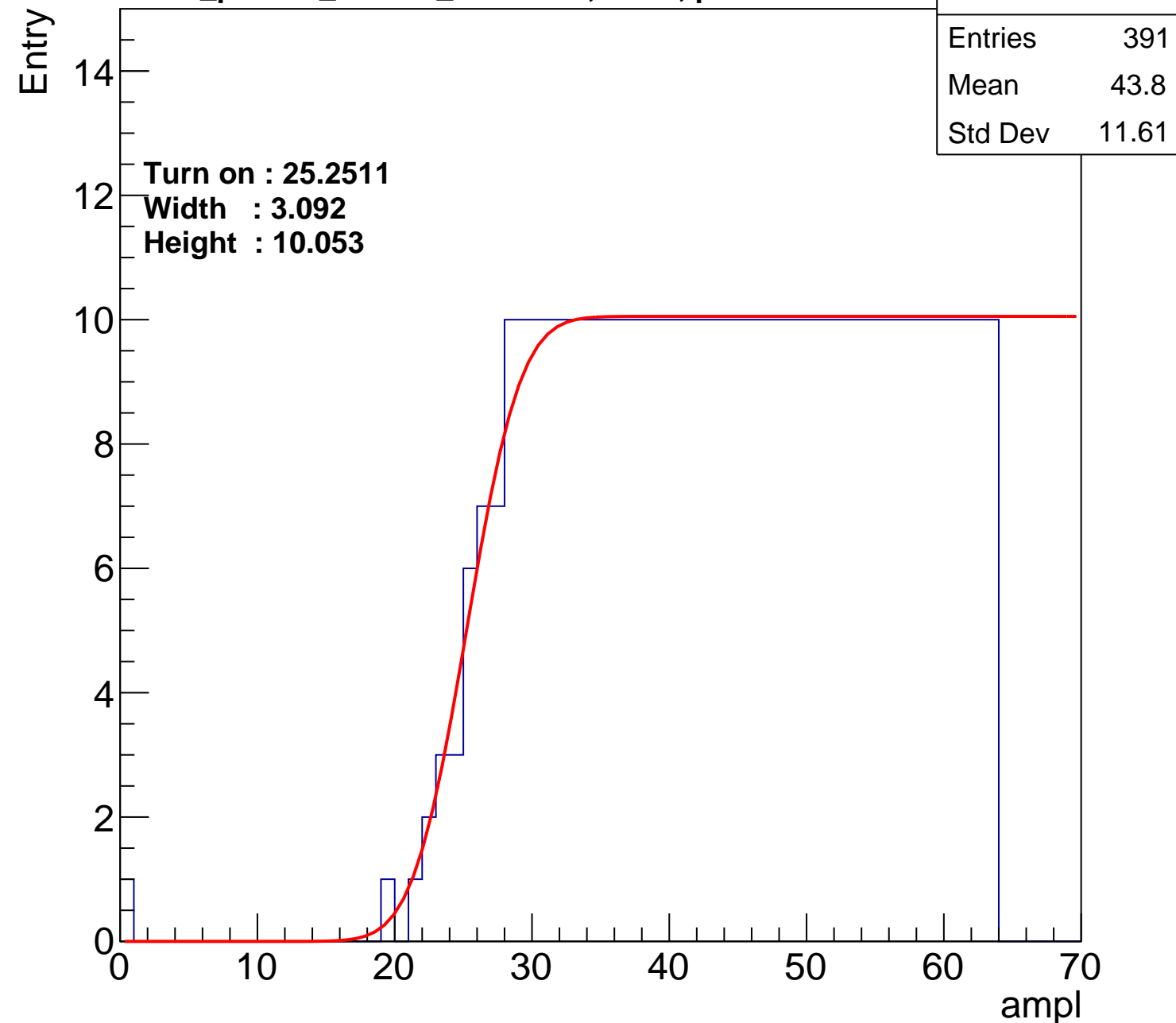
Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U24-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.2
Std Dev	11.68

Turn on : 26.3595

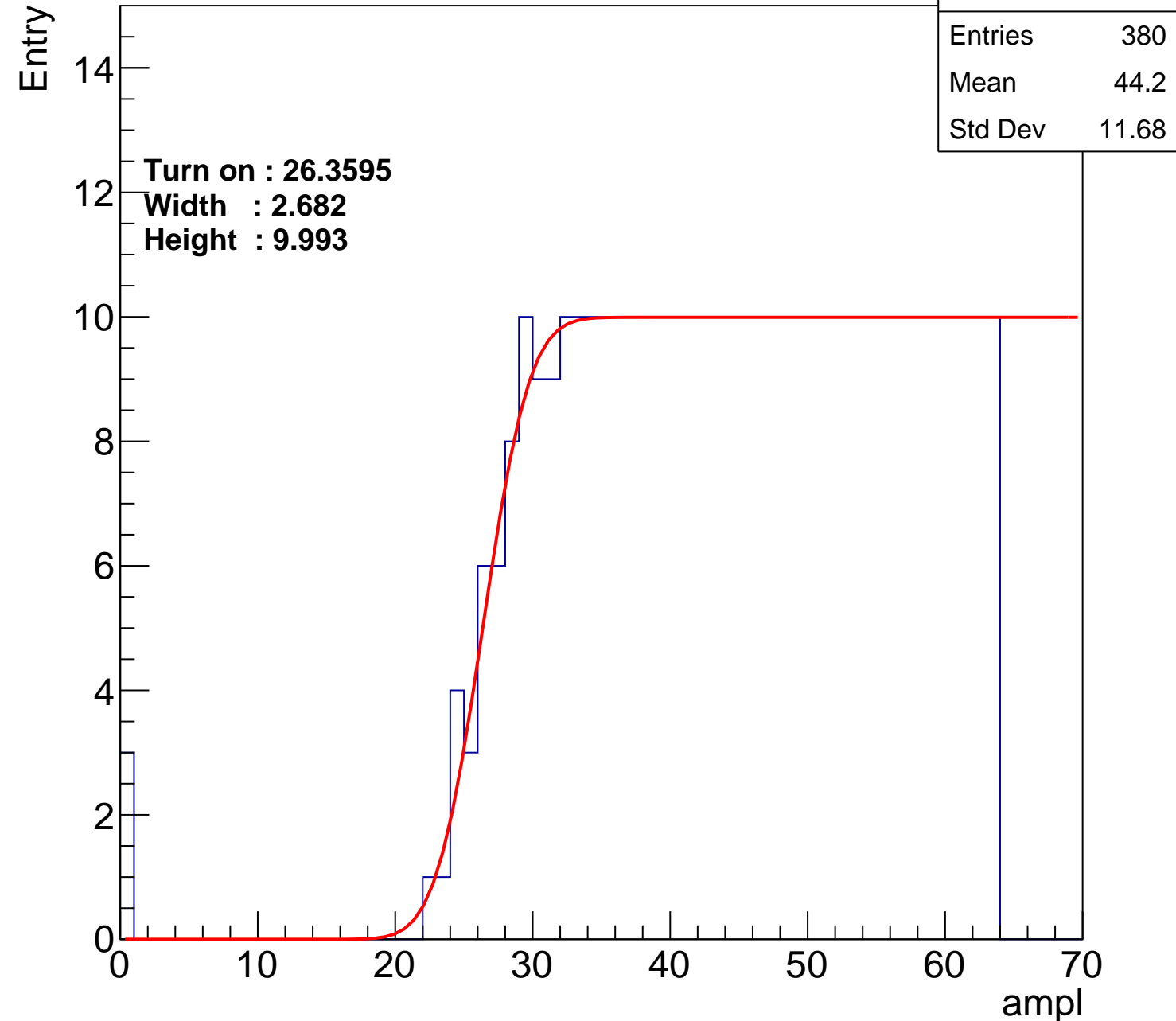
Width : 2.682

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.51
Std Dev	12.27

Turn on : 25.1774

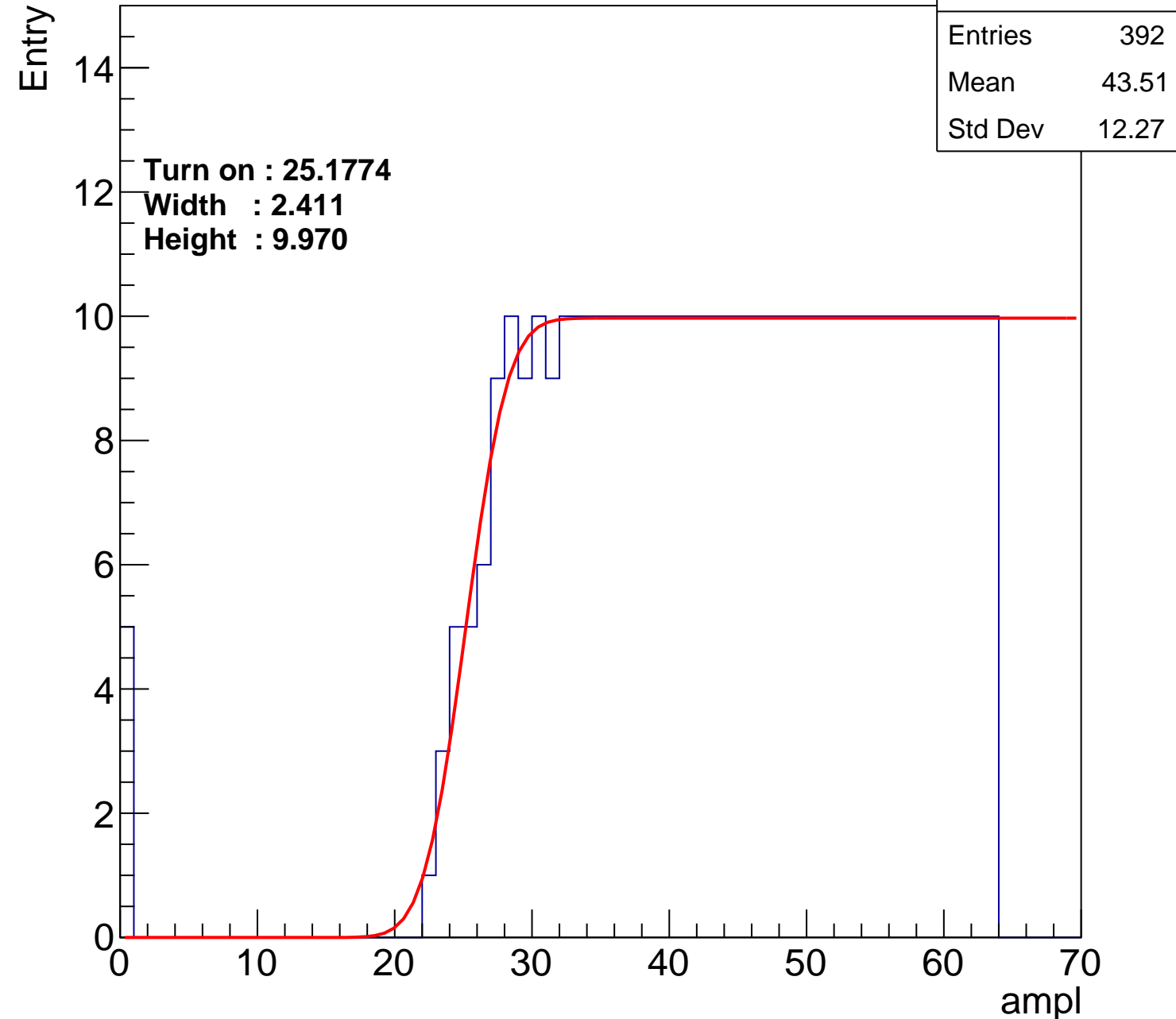
Width : 2.411

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch124

calib_packv5_042523_0143.root, FC#3, port B3

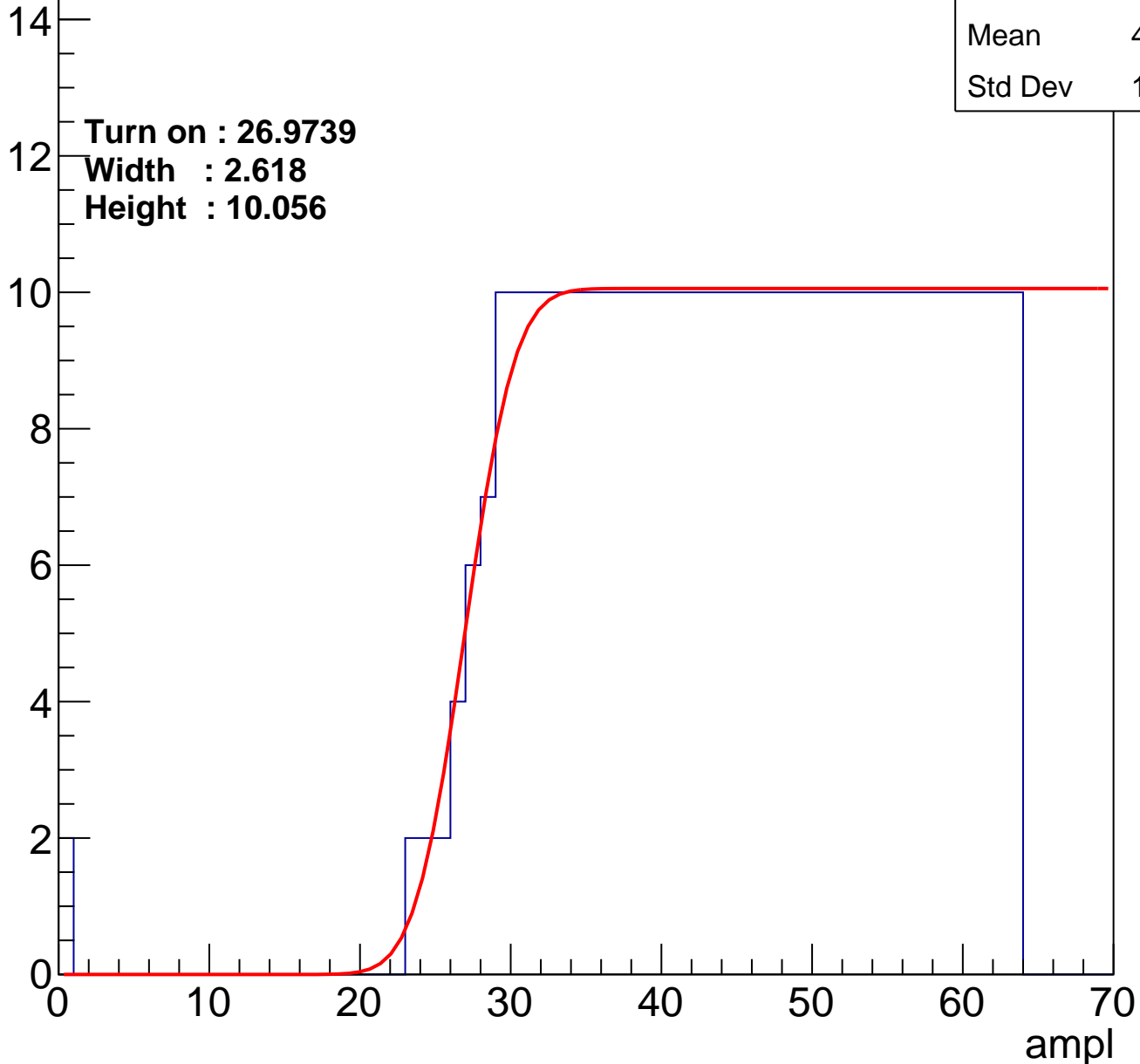
Entries	375
Mean	44.55
Std Dev	11.32

Turn on : 26.9739

Width : 2.618

Height : 10.056

Entry



B1L000S, U24-ch125

calib_packv5_042523_0143.root, FC#3, port B3

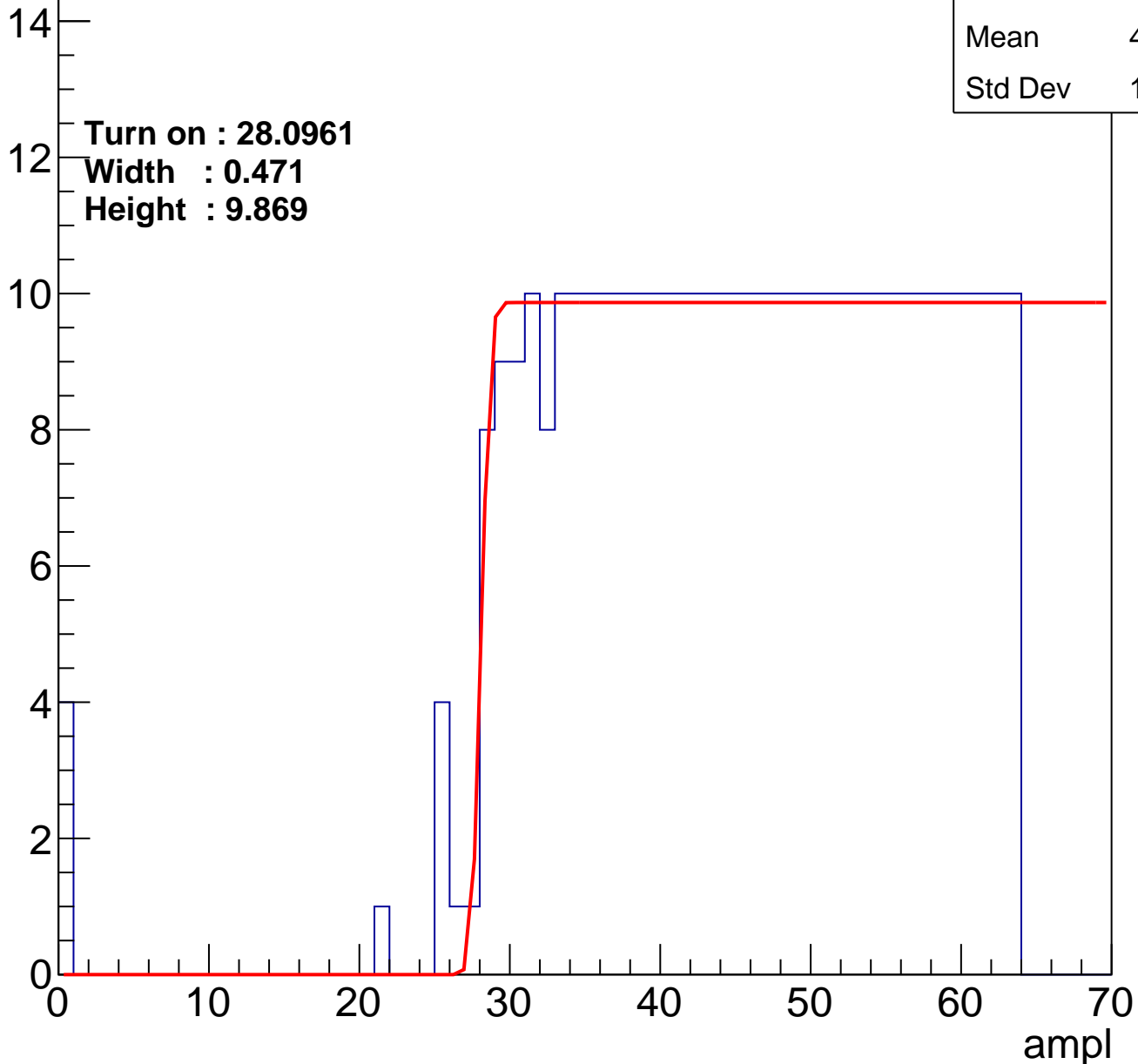
Entries	365
Mean	44.86
Std Dev	11.53

Turn on : 28.0961

Width : 0.471

Height : 9.869

Entry



B1L000S, U24-ch126

calib_packv5_042523_0143.root, FC#3, port B3

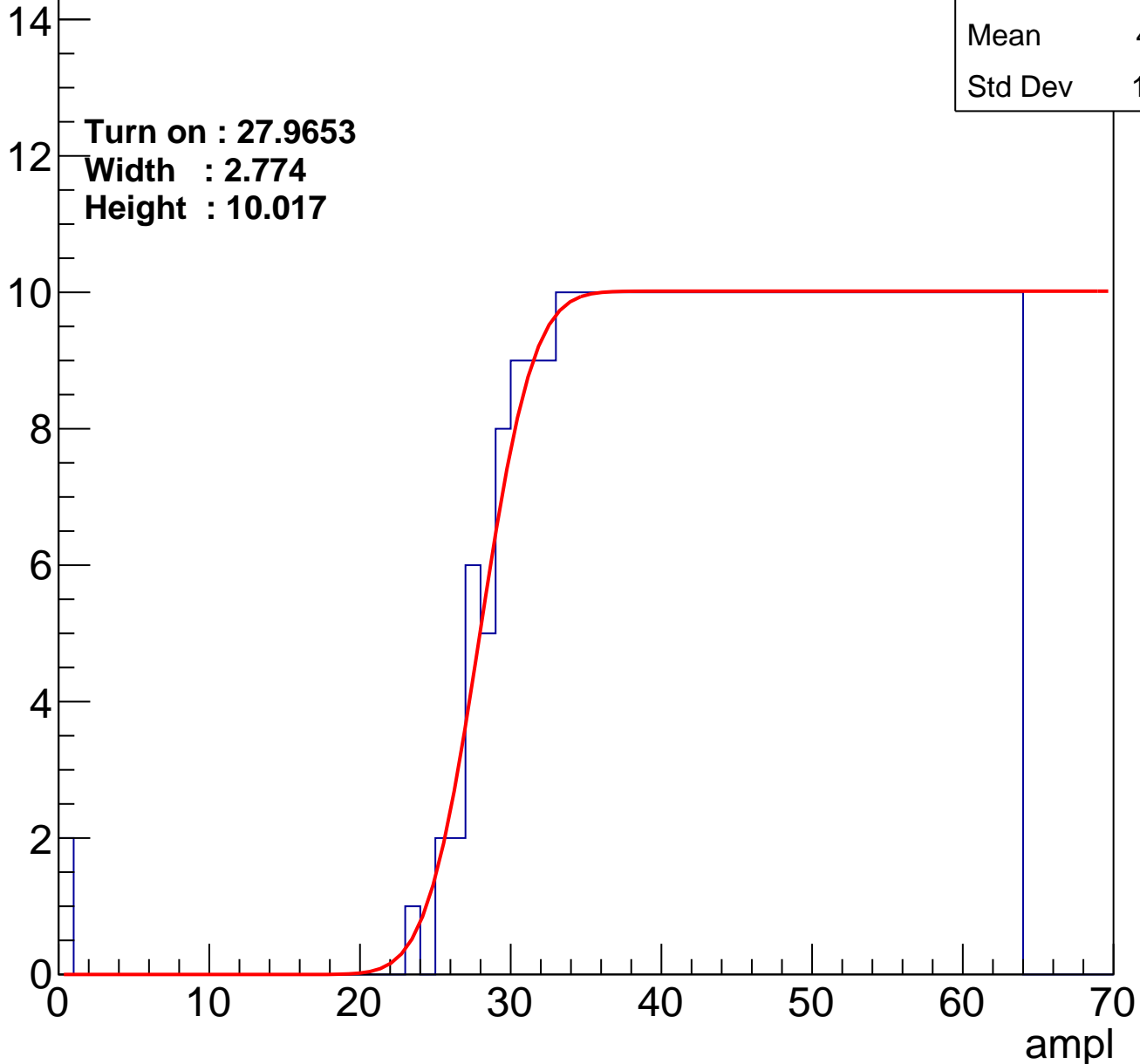
Entries	363
Mean	45.11
Std Dev	11.06

Turn on : 27.9653

Width : 2.774

Height : 10.017

Entry



B1L000S, U24-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.25
Std Dev	10.8

Turn on : 28.2893

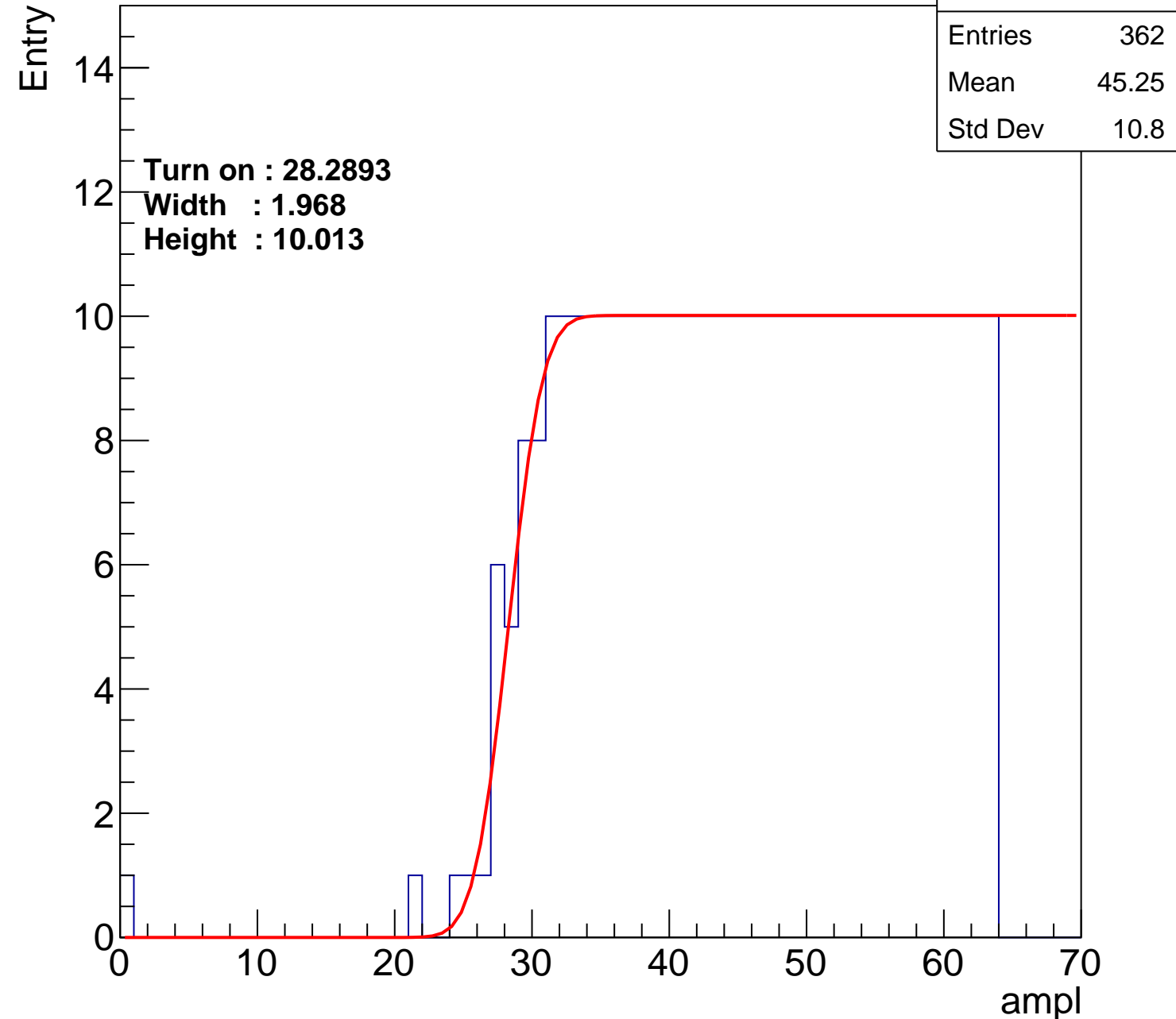
Width : 1.968

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U24-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.25
Std Dev	10.8

Turn on : 28.2893

Width : 1.968

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl

