

B1L104S, U23-ch0

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	29.46
Std Dev	28.64

Turn on : 53.7311

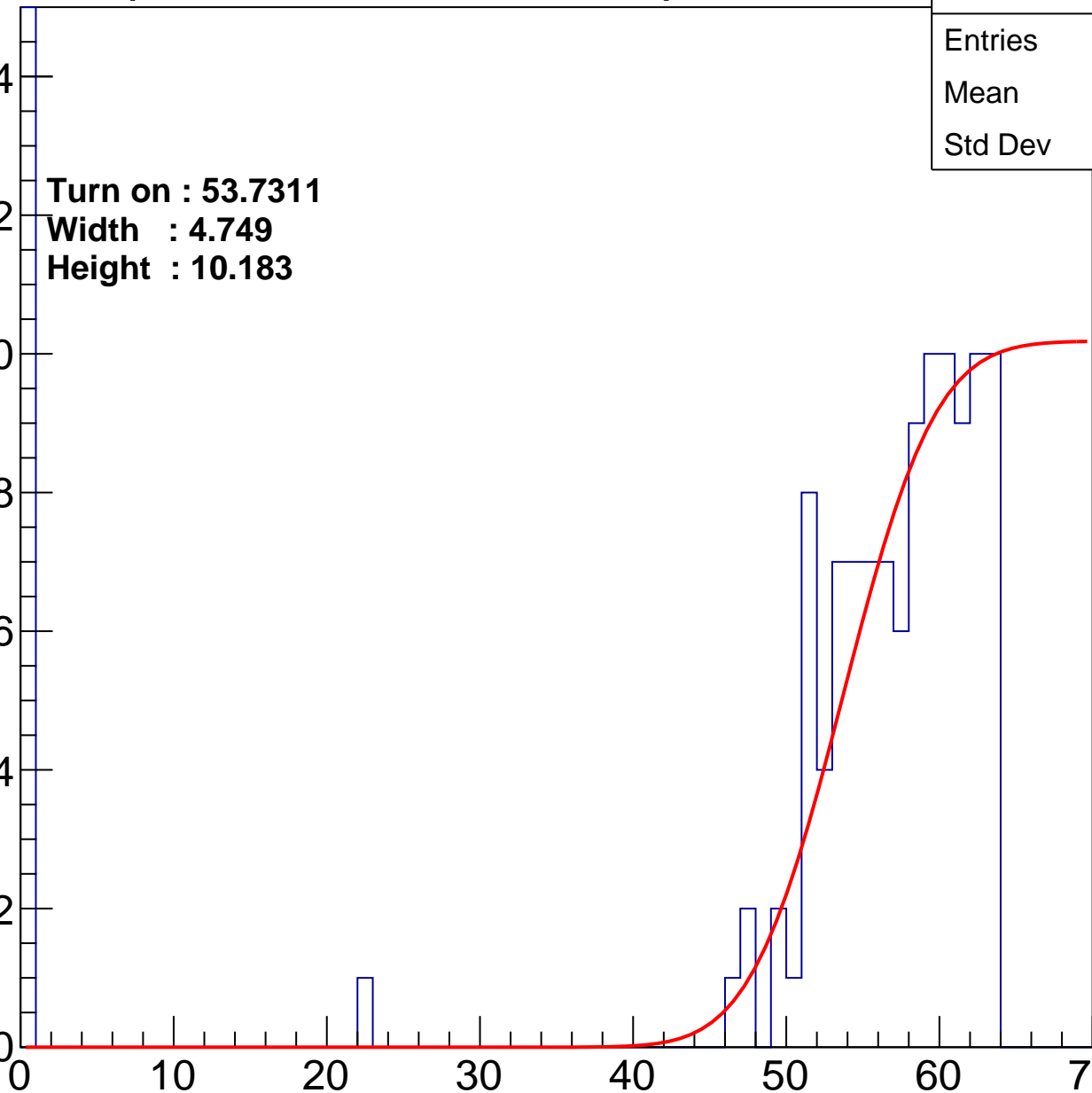
Width : 4.749

Height : 10.183

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	33.87
Std Dev	28.96

Turn on : 55.5839

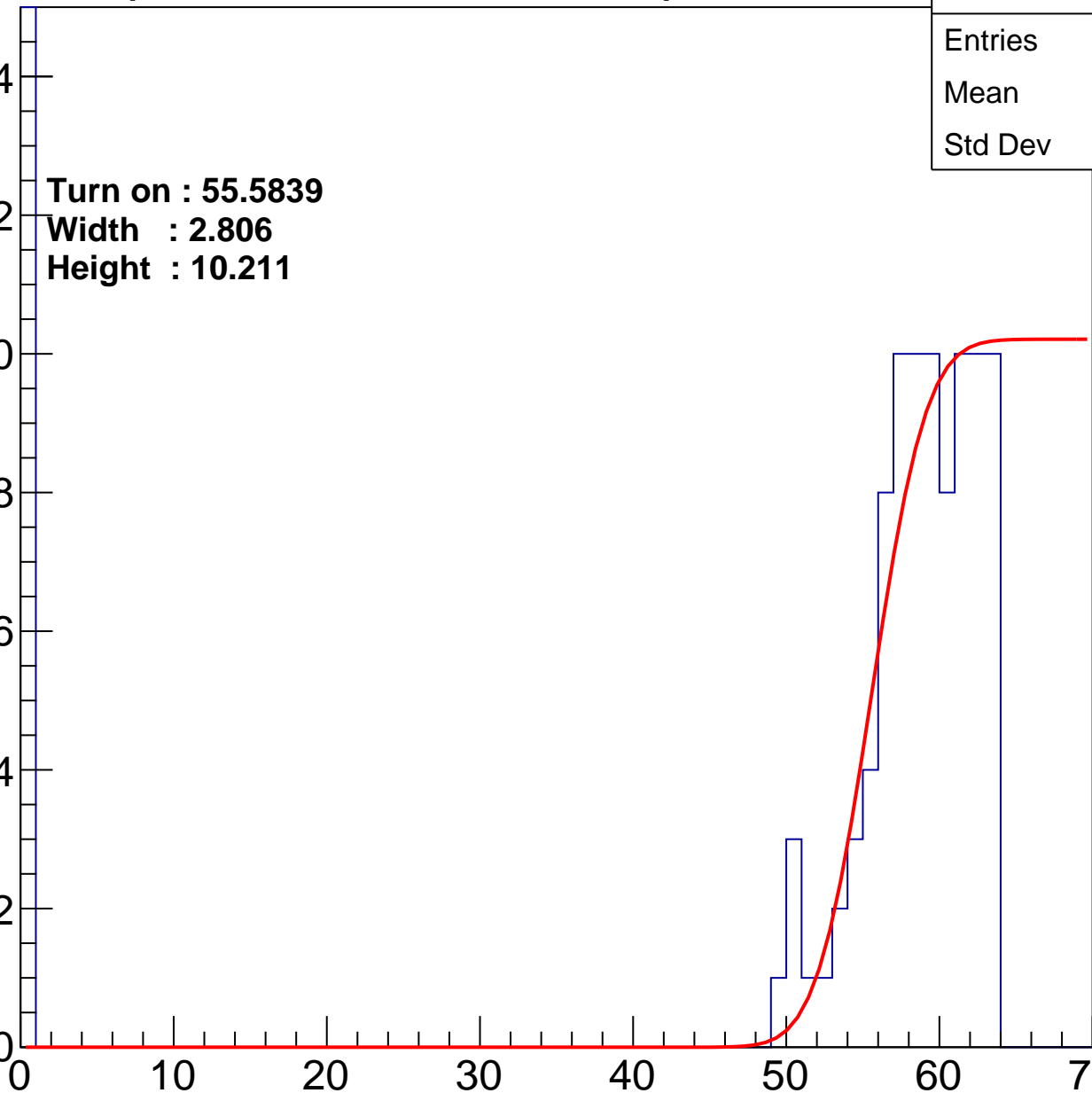
Width : 2.806

Height : 10.211

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	29.71
Std Dev	29.19

Turn on : 54.1735

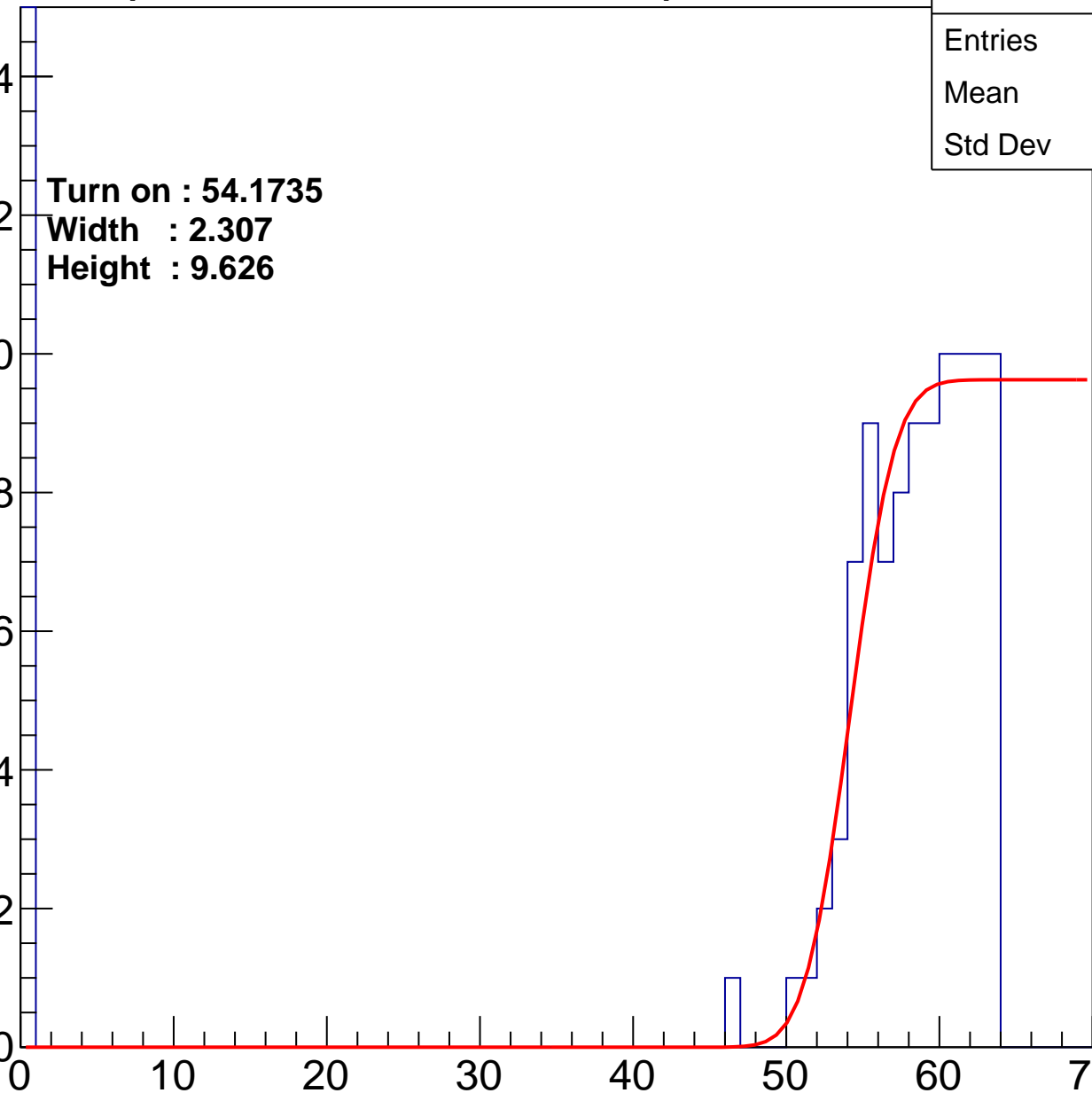
Width : 2.307

Height : 9.626

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	36.27
Std Dev	27.96

Turn on : 53.8421

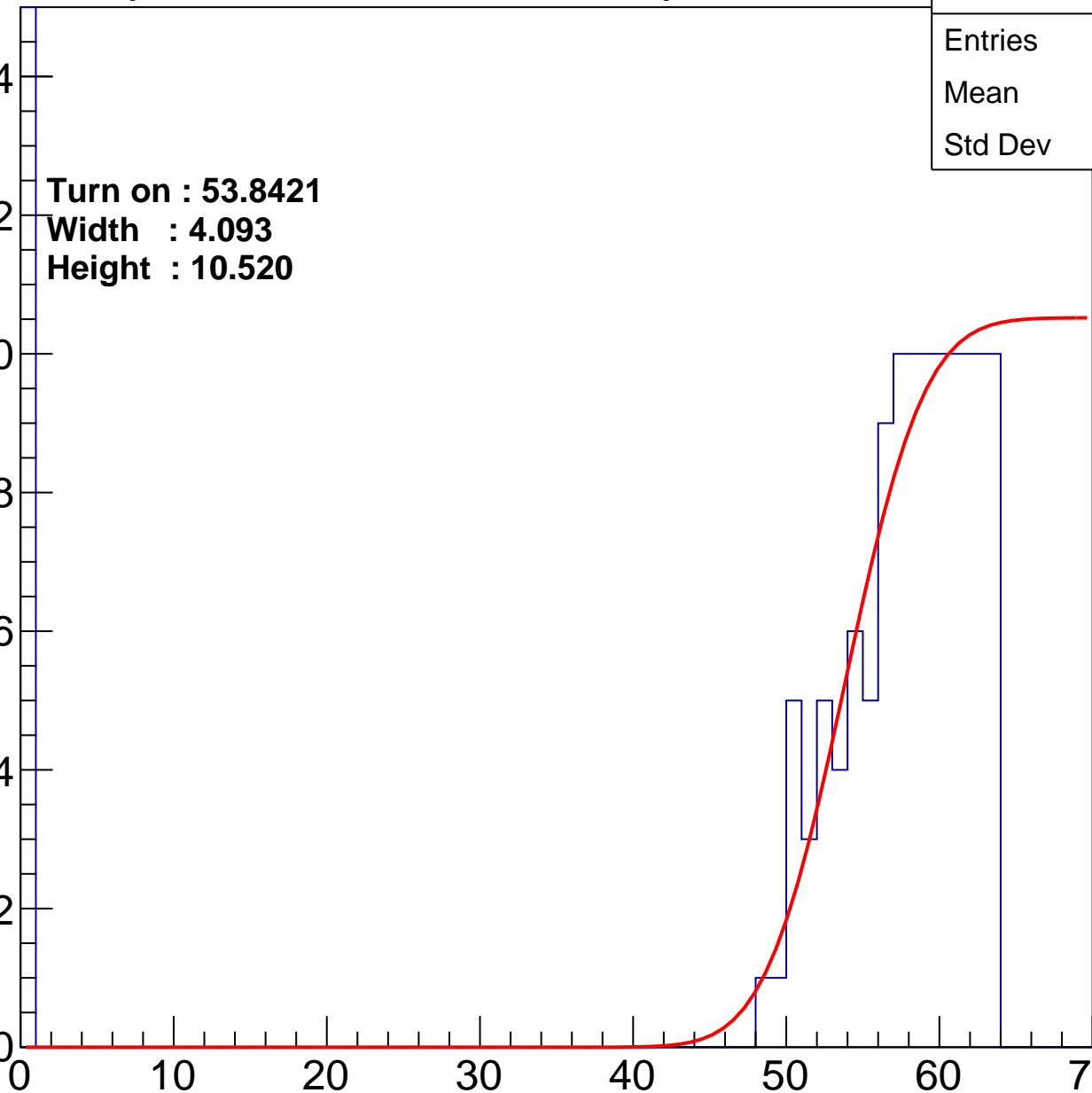
Width : 4.093

Height : 10.520

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	30.88
Std Dev	28.87

Turn on : 54.2310

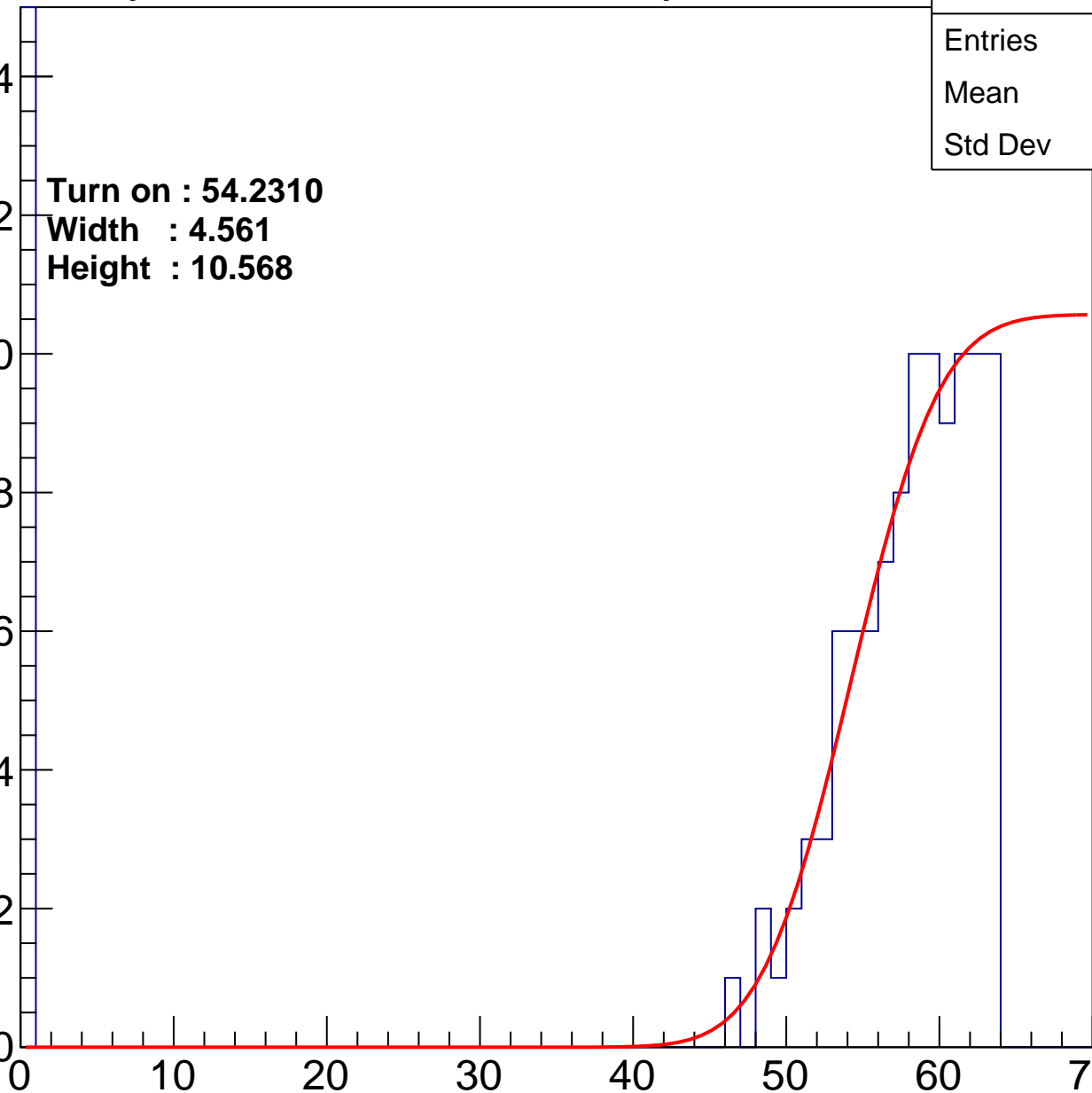
Width : 4.561

Height : 10.568

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch5

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	31.86
Std Dev	29.43

Turn on : 55.9584

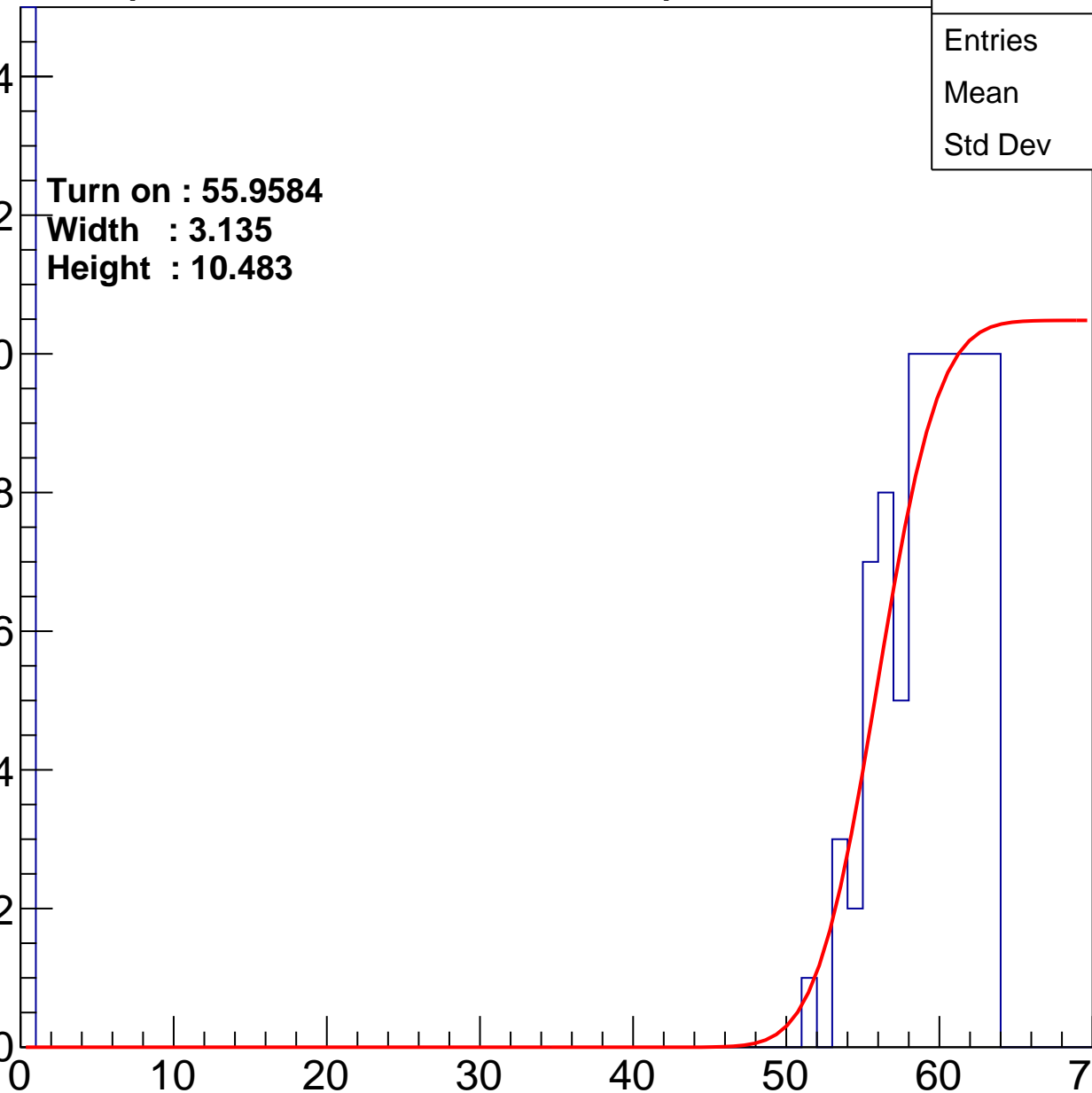
Width : 3.135

Height : 10.483

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch6

calib_packv5_033123_0516.root, FC#4, port A1

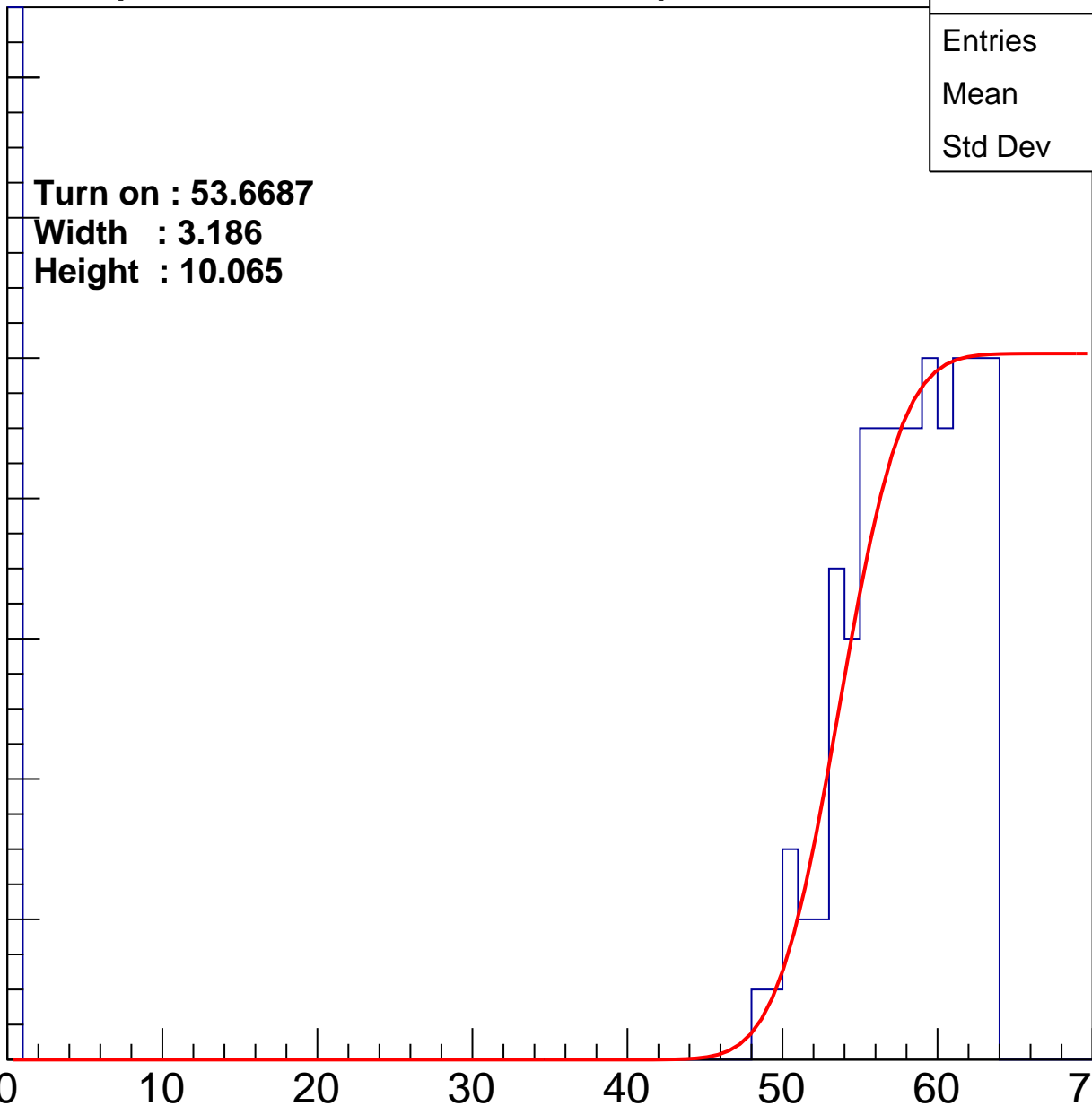
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6687
Width : 3.186
Height : 10.065

Entries	197
Mean	31.33
Std Dev	28.86

ampl



B1L104S, U23-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	33.47
Std Dev	28.9

Turn on : 54.8251

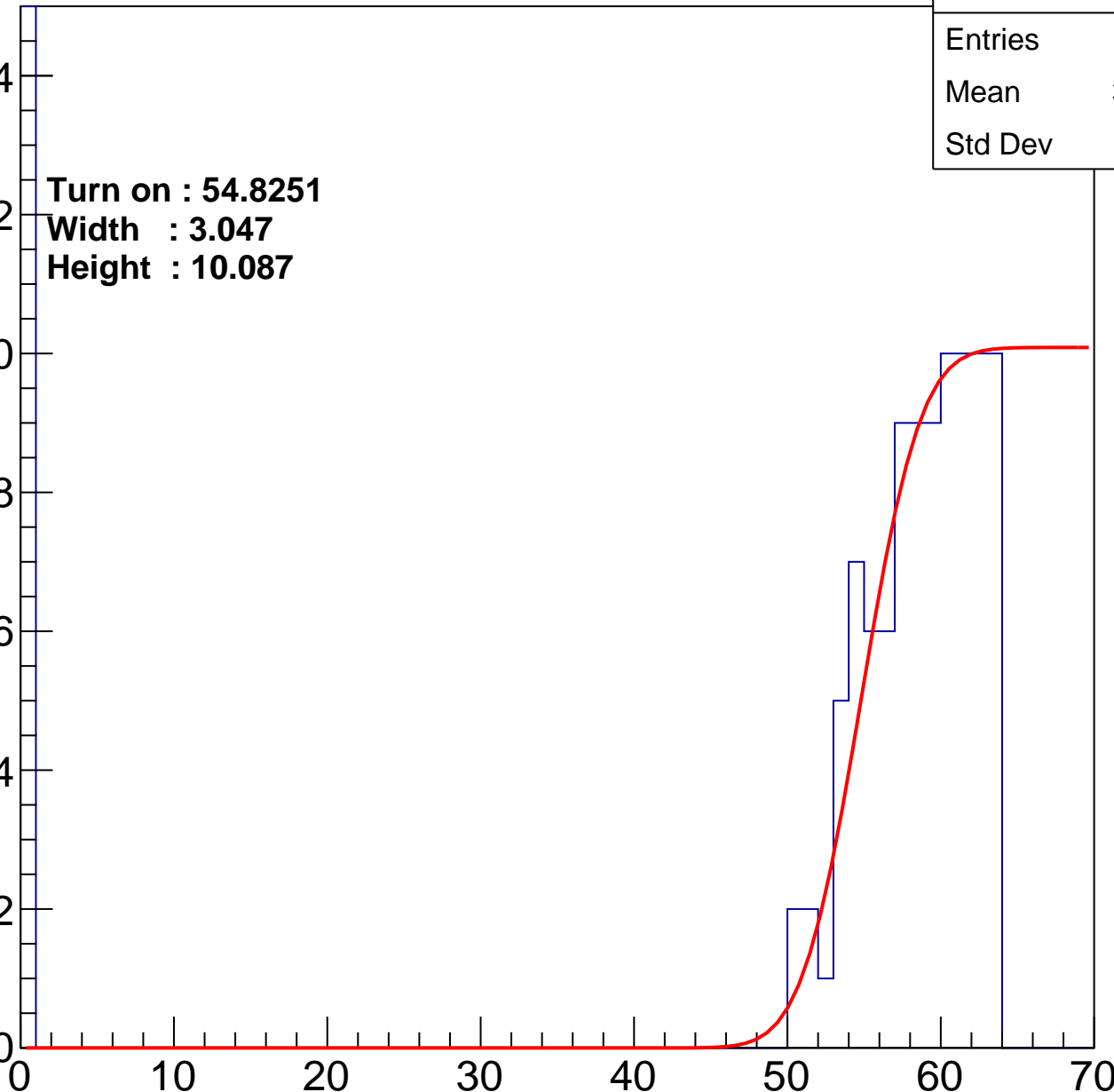
Width : 3.047

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch8

calib_packv5_033123_0516.root, FC#4, port A1

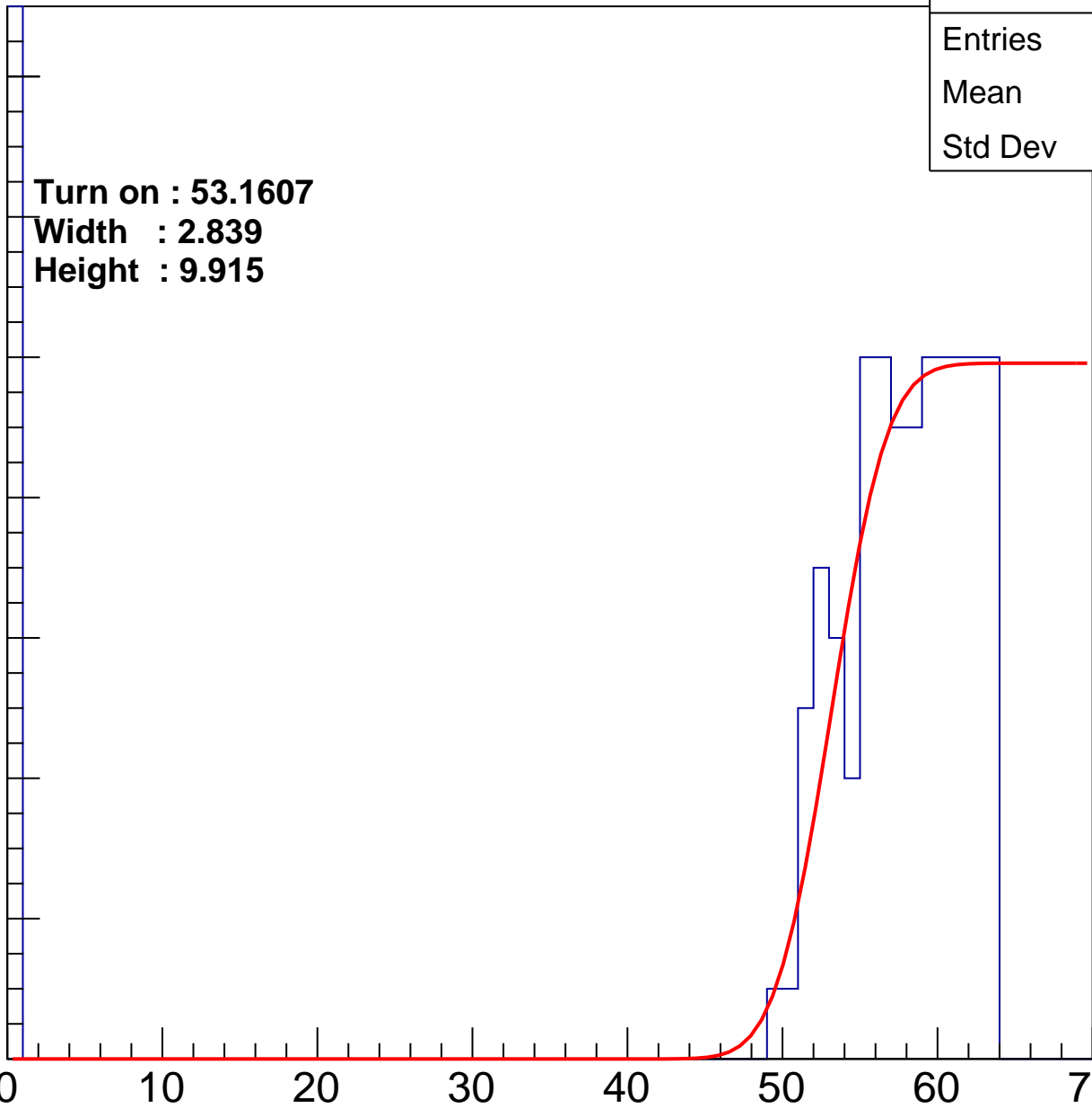
Entry

14
12
10
8
6
4
2
0

Turn on : 53.1607
Width : 2.839
Height : 9.915

Entries	191
Mean	33.75
Std Dev	28.49

ampl



B1L104S, U23-ch9

calib_packv5_033123_0516.root, FC#4, port A1

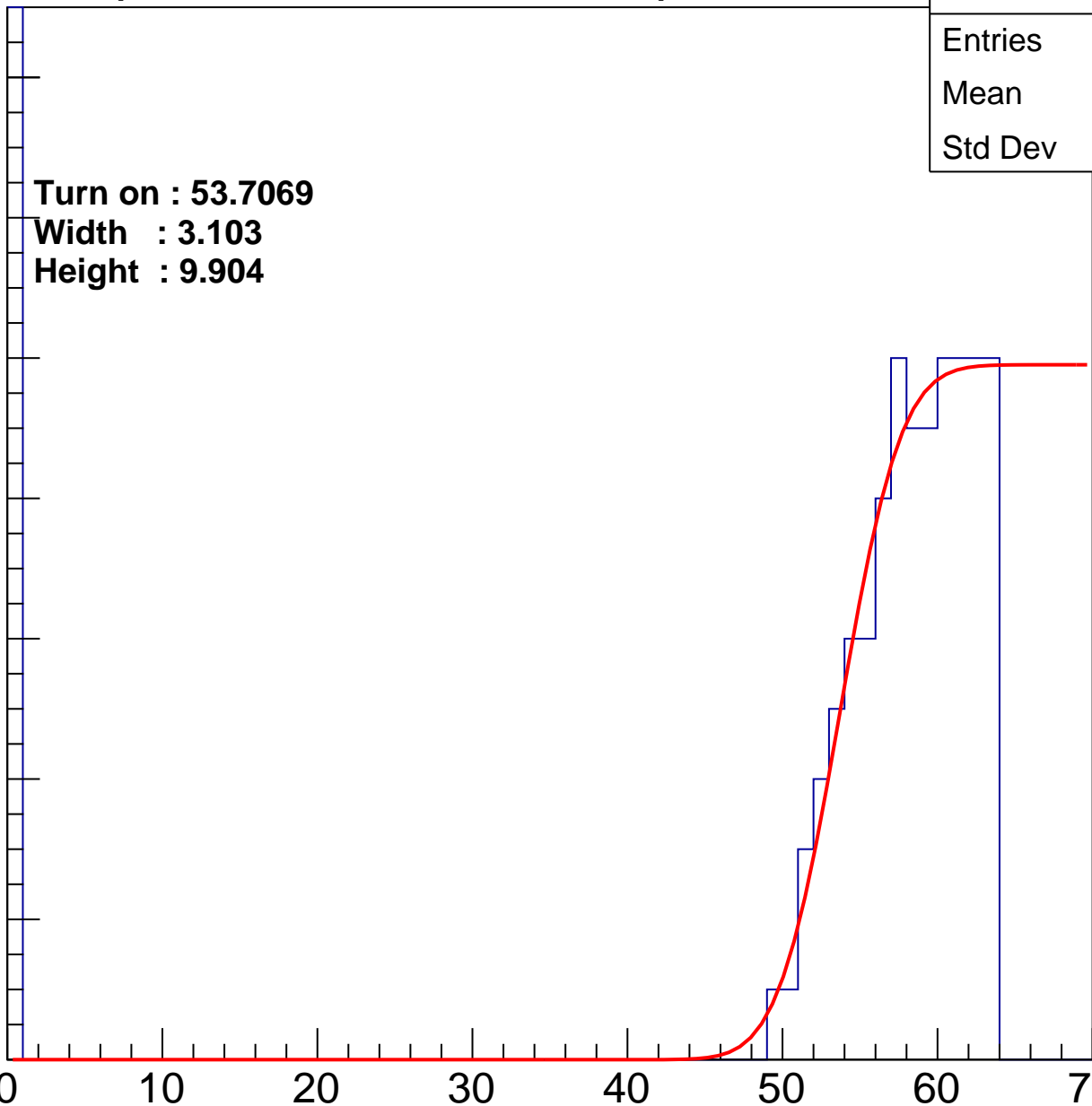
Entry

14
12
10
8
6
4
2
0

Turn on : 53.7069
Width : 3.103
Height : 9.904

Entries	141
Mean	41.91
Std Dev	26.1

ampl



B1L104S, U23-ch10

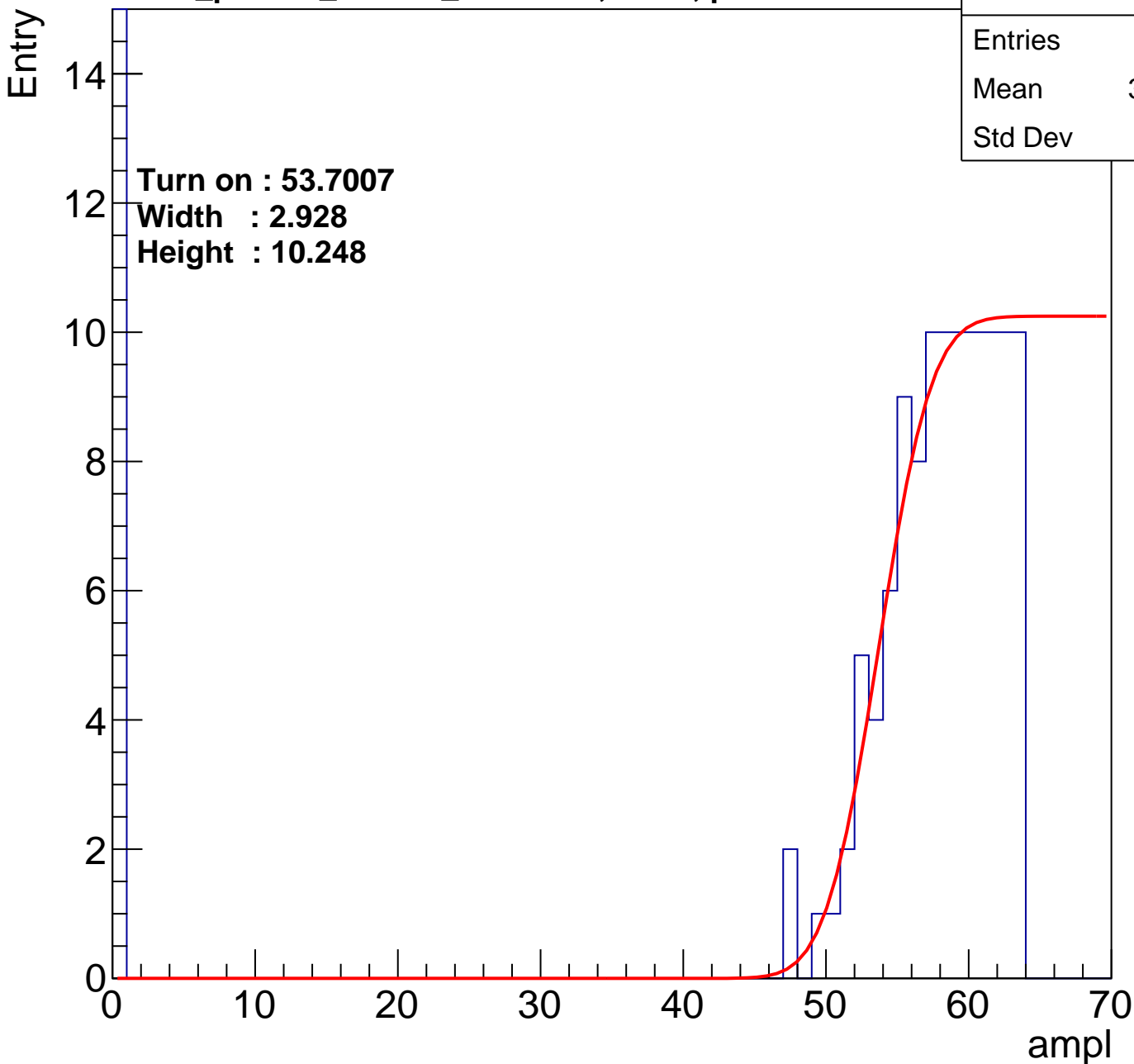
calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	31.17
Std Dev	28.9

Turn on : 53.7007

Width : 2.928

Height : 10.248



B1L104S, U23-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	31.45
Std Dev	28.91

Turn on : 53.9477

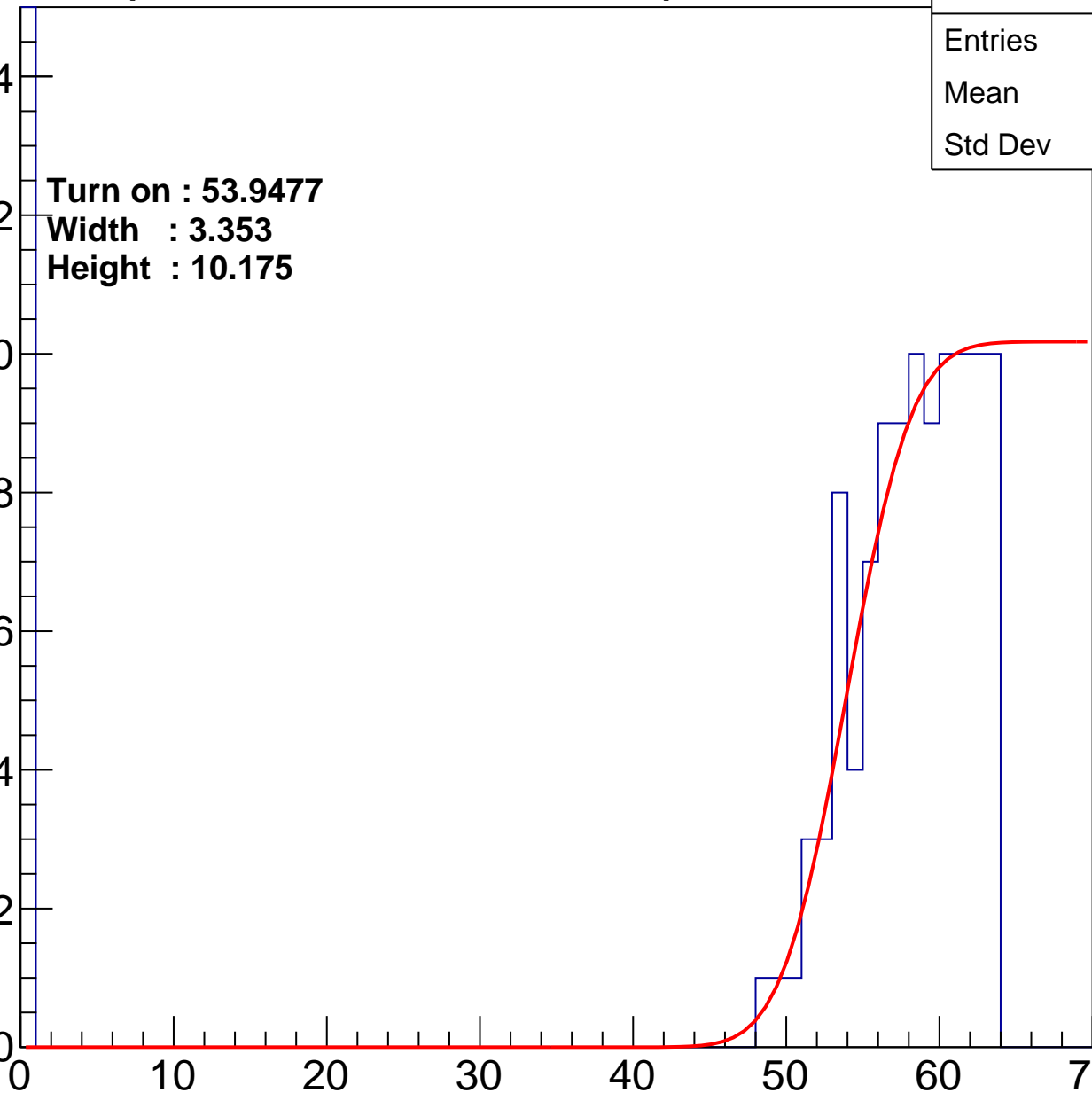
Width : 3.353

Height : 10.175

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	30.66
Std Dev	28.96

Turn on : 54.4184

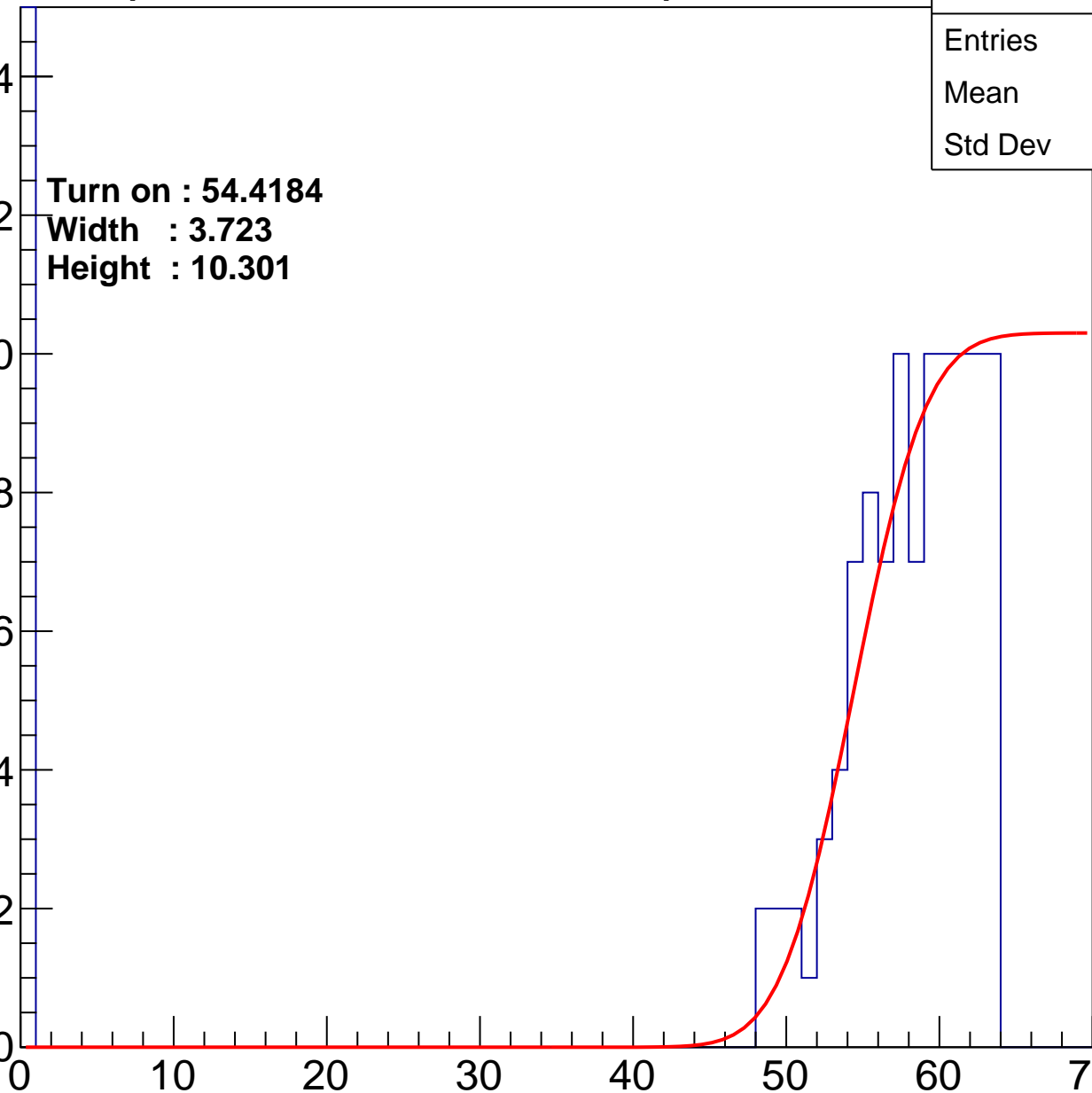
Width : 3.723

Height : 10.301

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	34.04
Std Dev	28.83

Turn on : 54.0155

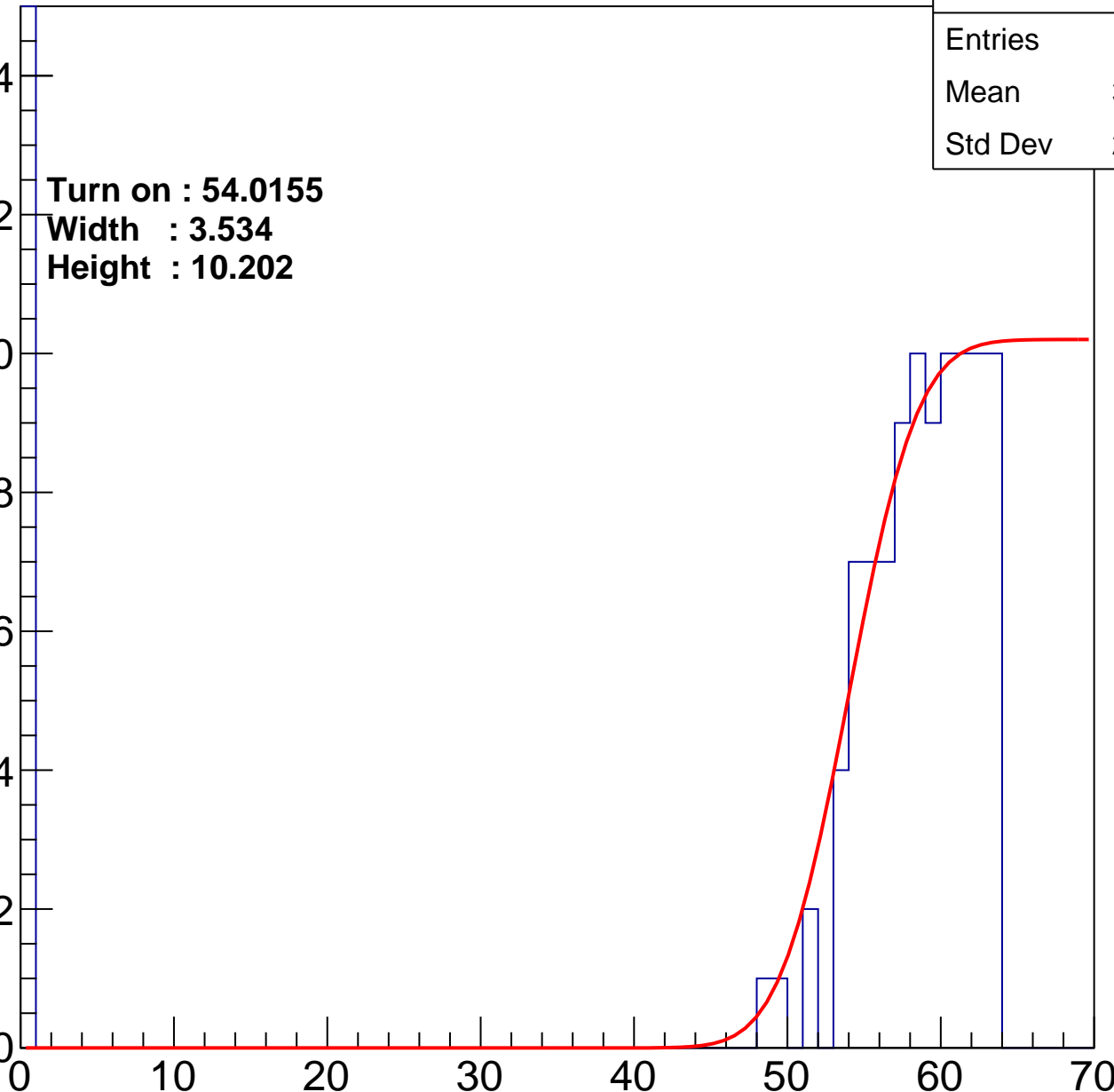
Width : 3.534

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch14

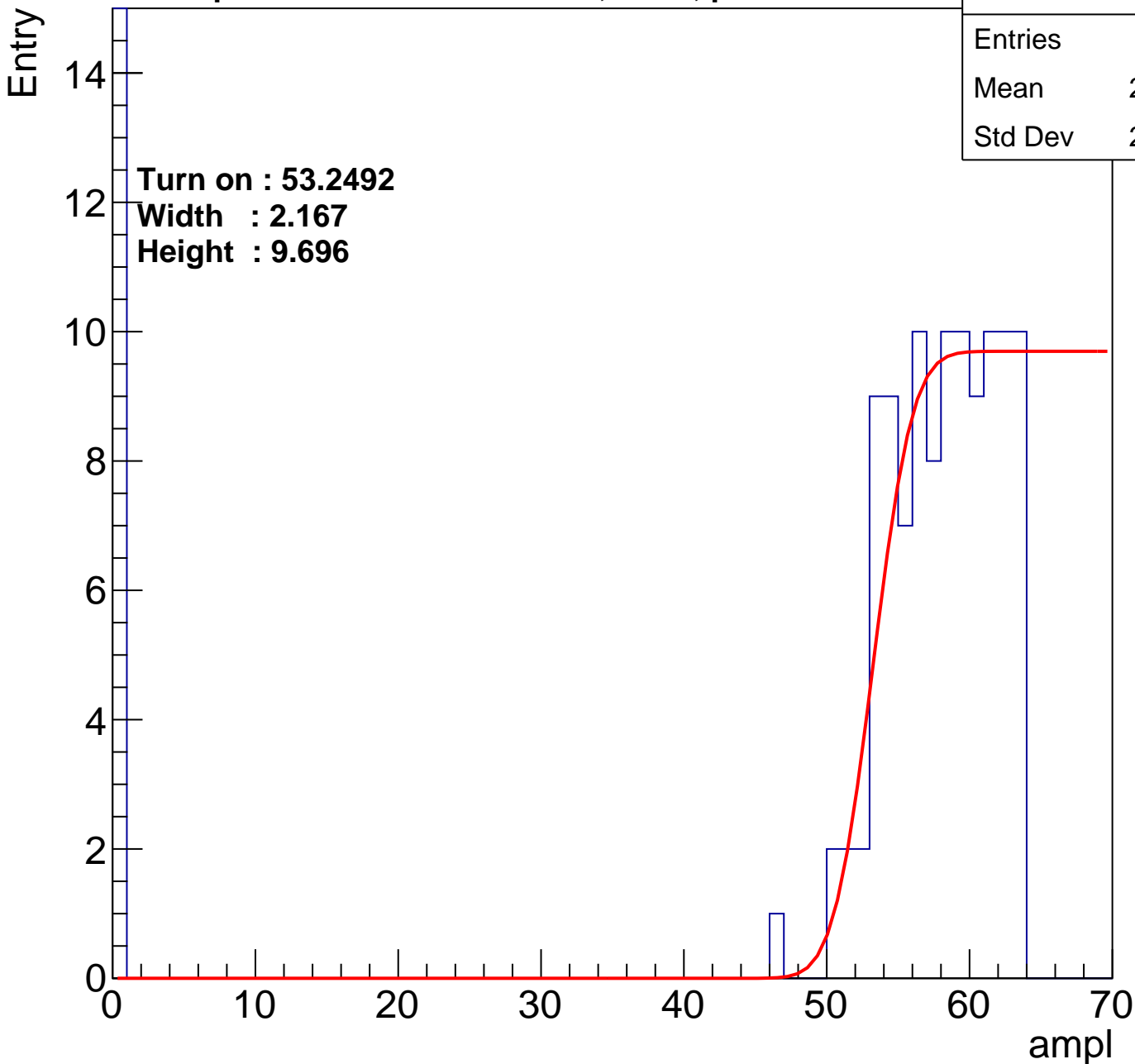
calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	29.37
Std Dev	28.95

Turn on : 53.2492

Width : 2.167

Height : 9.696



B1L104S, U23-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	36.43
Std Dev	28.13

Turn on : 54.1547

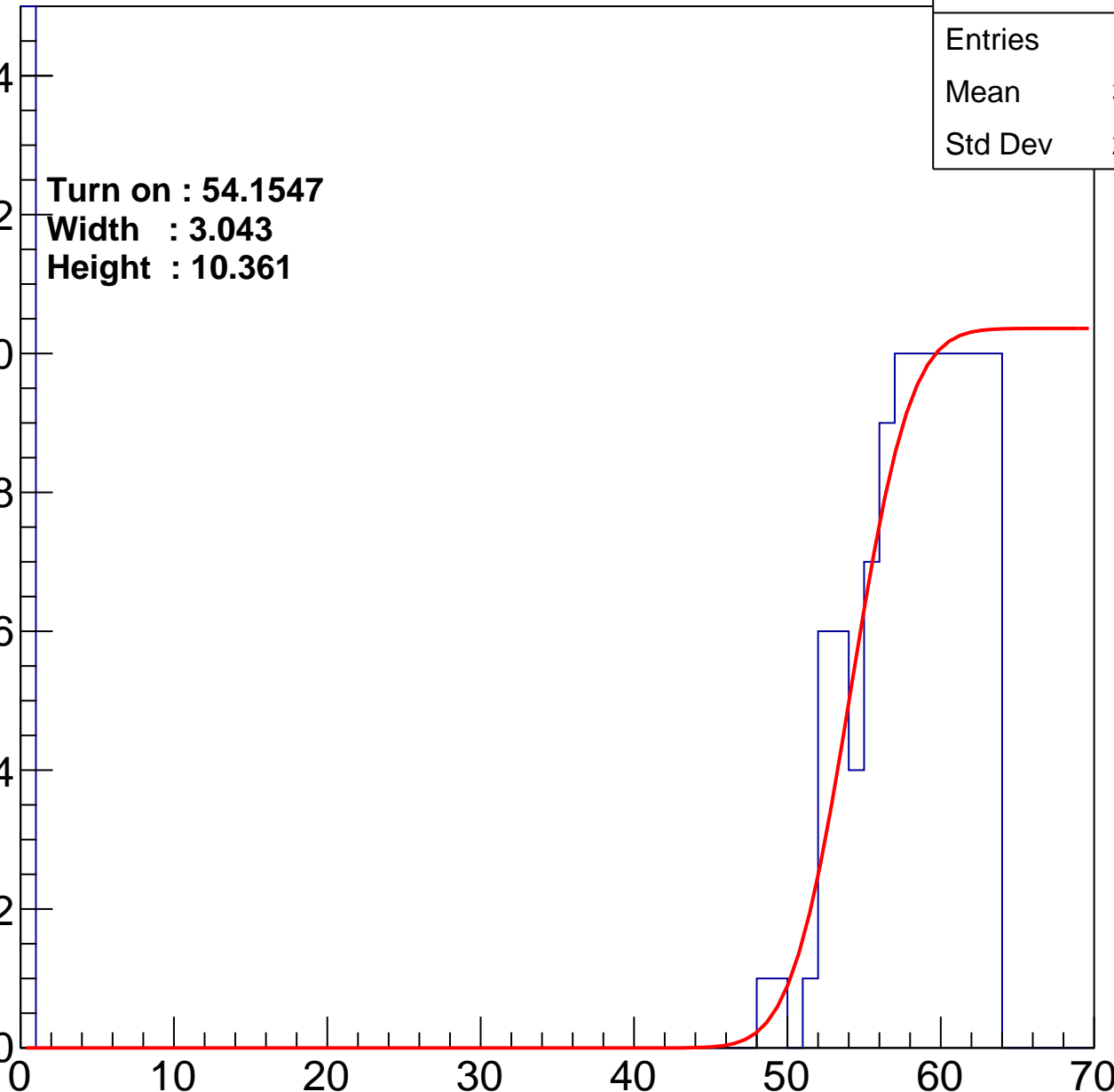
Width : 3.043

Height : 10.361

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	33.33
Std Dev	28.08

Turn on : 54.0549

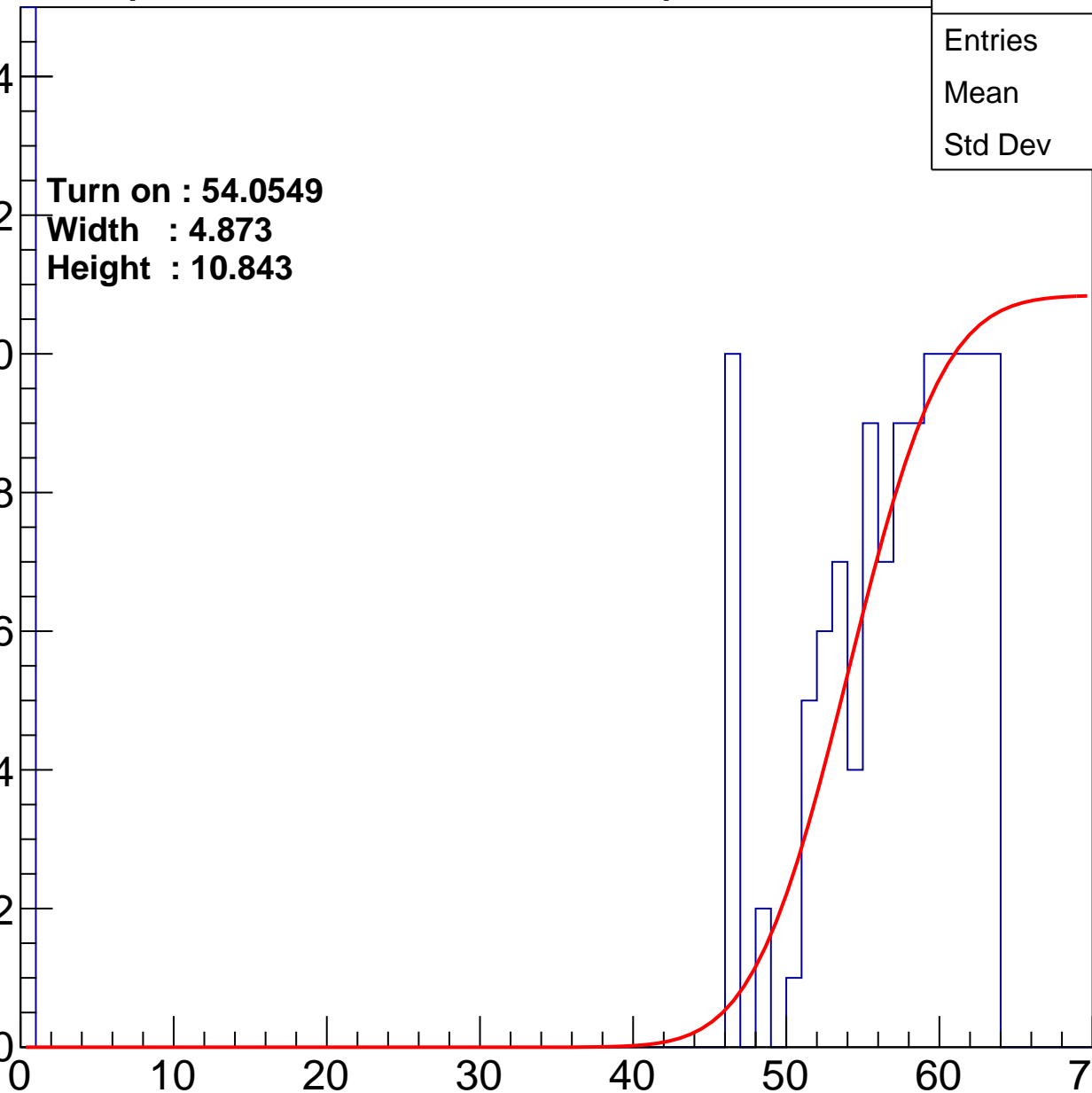
Width : 4.873

Height : 10.843

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch17

calib_packv5_033123_0516.root, FC#4, port A1

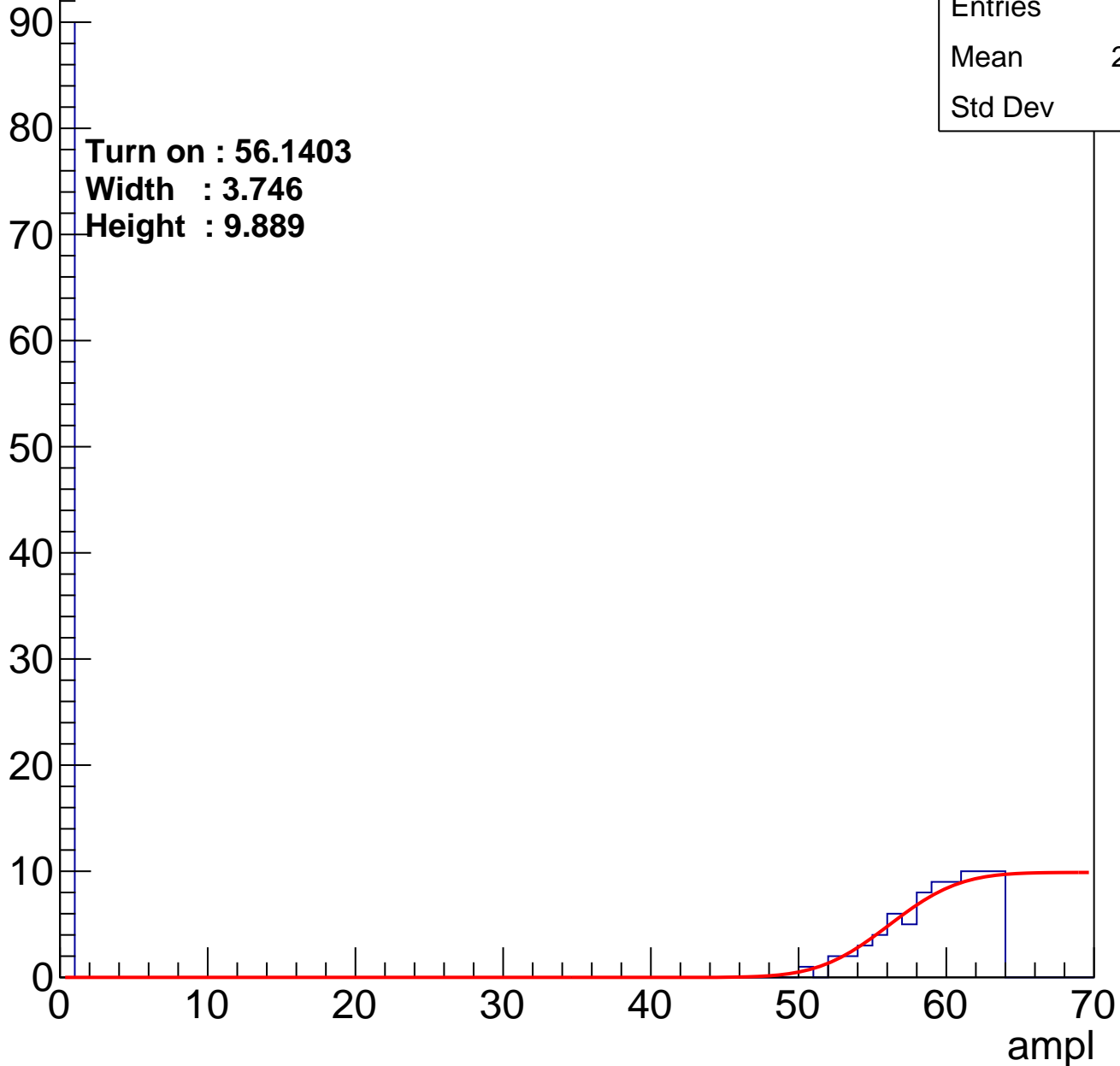
Entries	169
Mean	27.56
Std Dev	29.5

Turn on : 56.1403

Width : 3.746

Height : 9.889

Entry



B1L104S, U23-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	31.78
Std Dev	29.21

Turn on : 55.4596

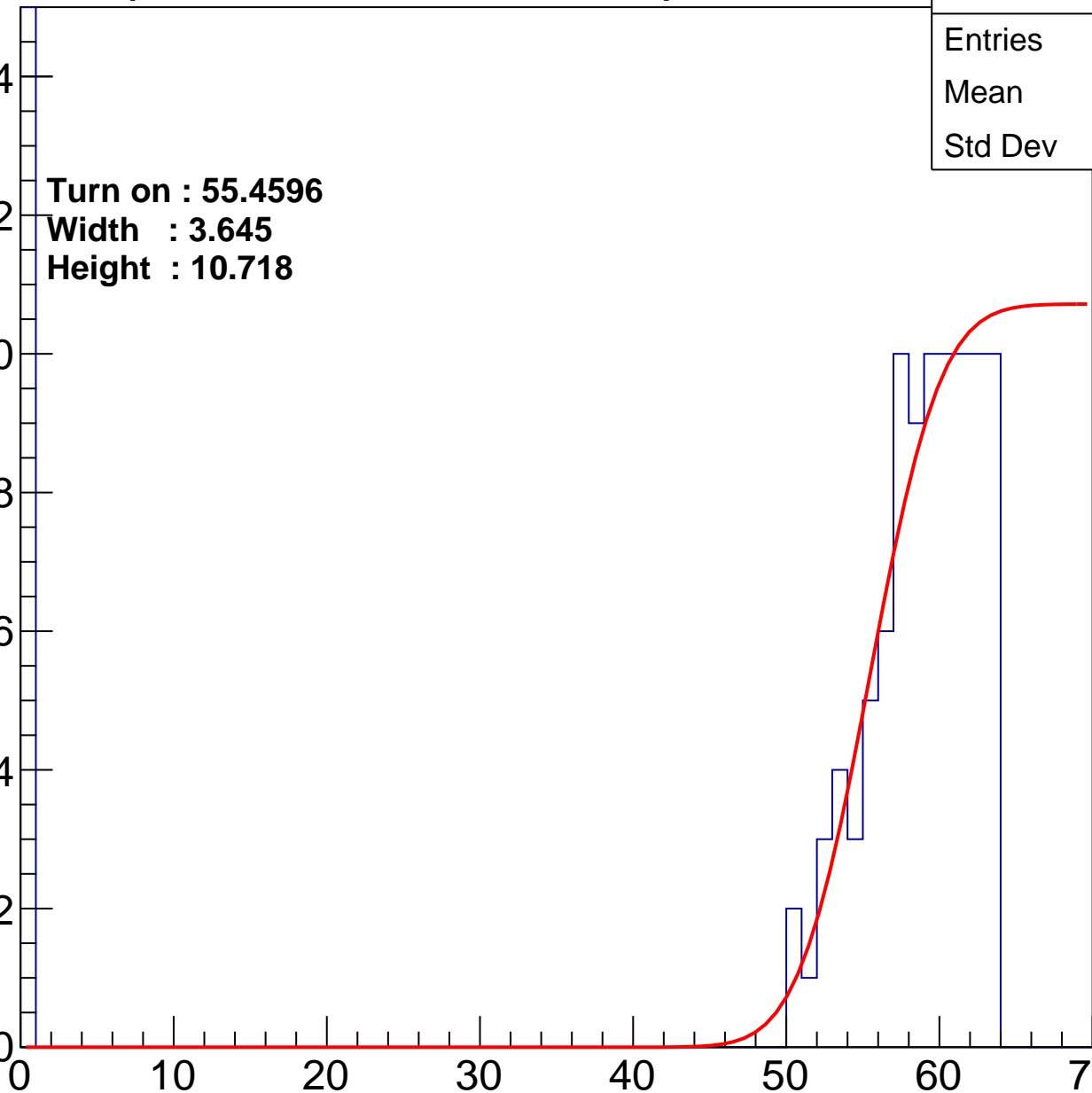
Width : 3.645

Height : 10.718

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	32.69
Std Dev	29.11

Turn on : 54.9300

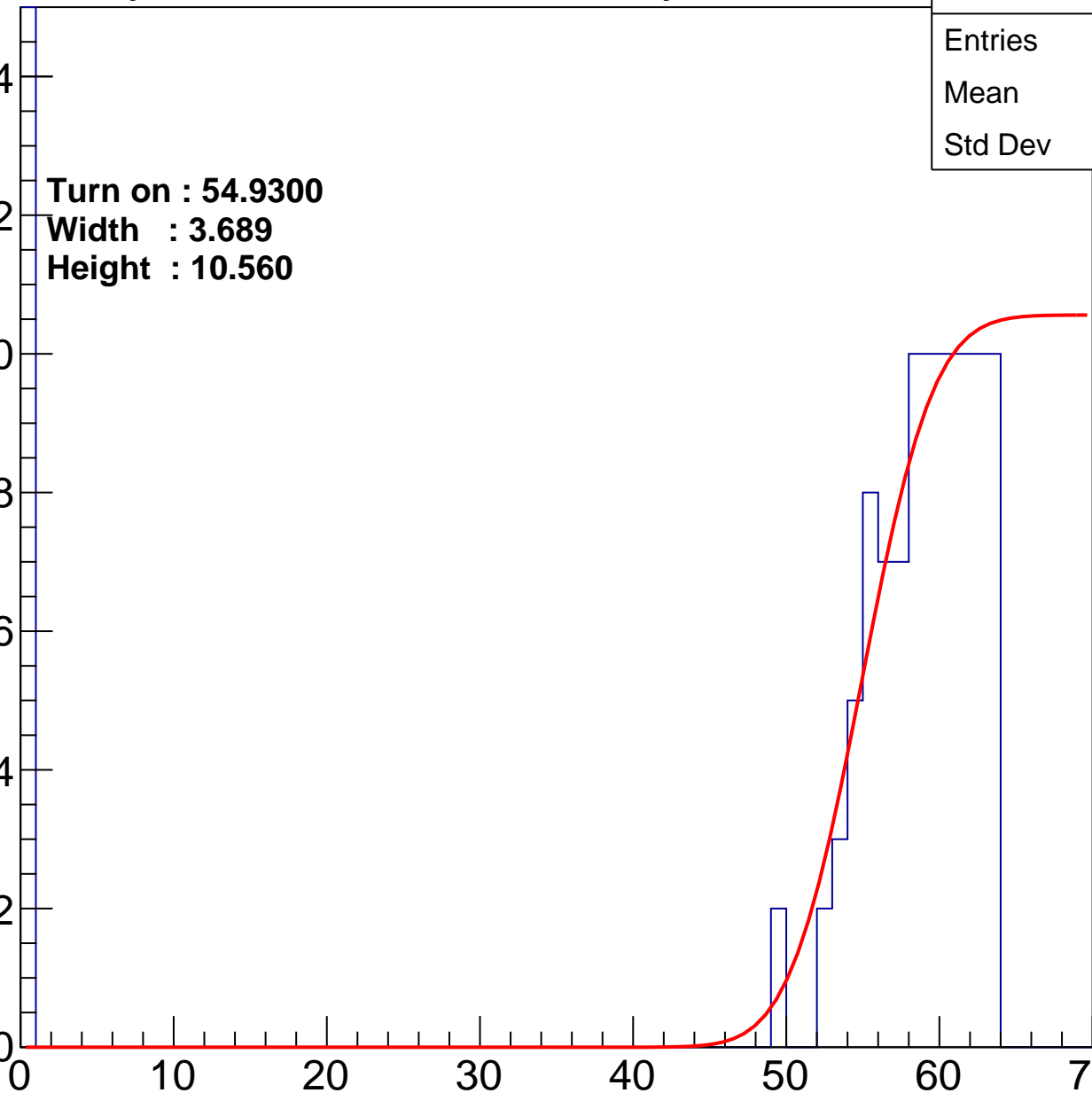
Width : 3.689

Height : 10.560

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	32.31
Std Dev	28.54

Turn on : 52.1965

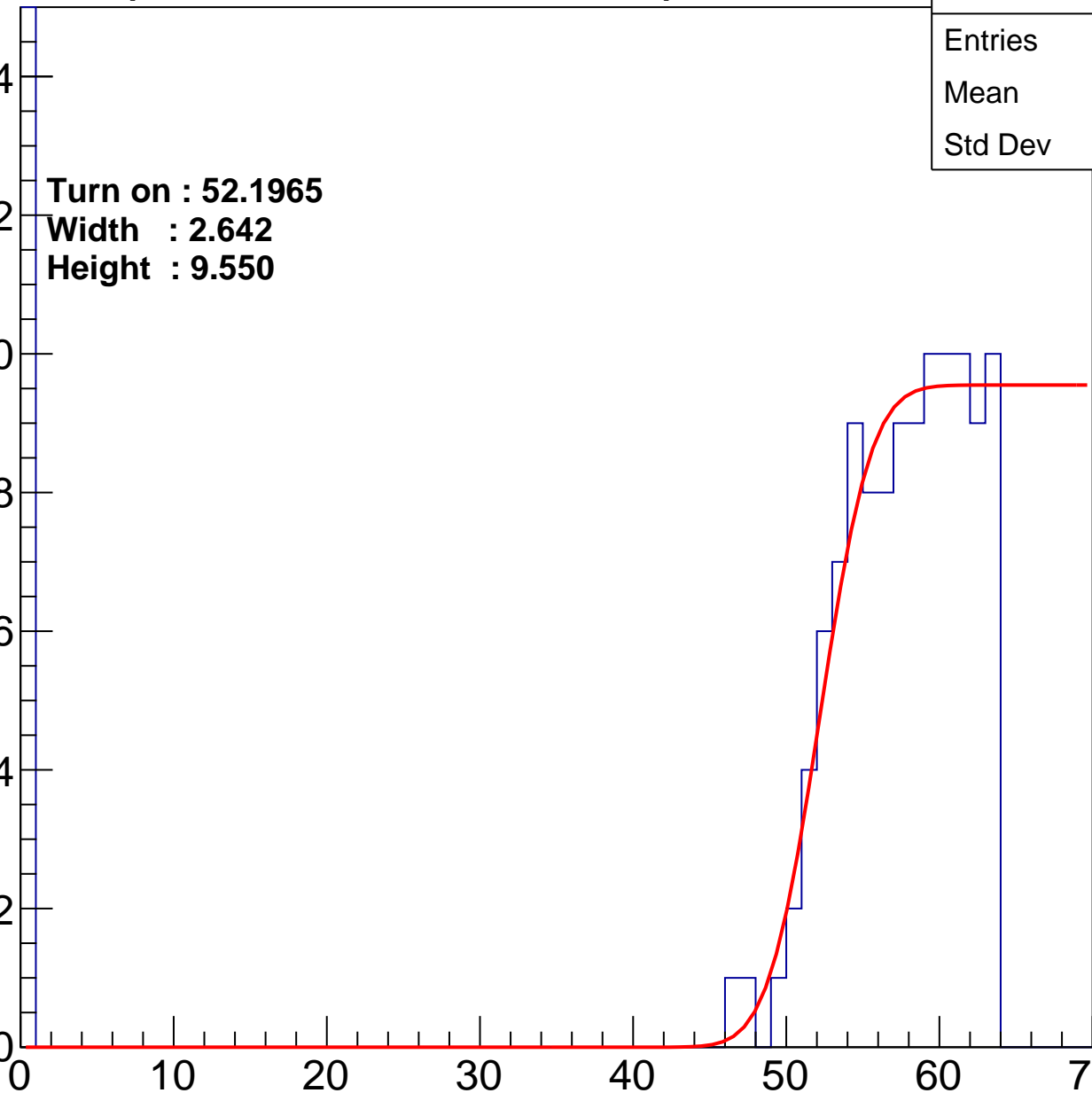
Width : 2.642

Height : 9.550

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.31
Std Dev	28.99

Turn on : 55.4926

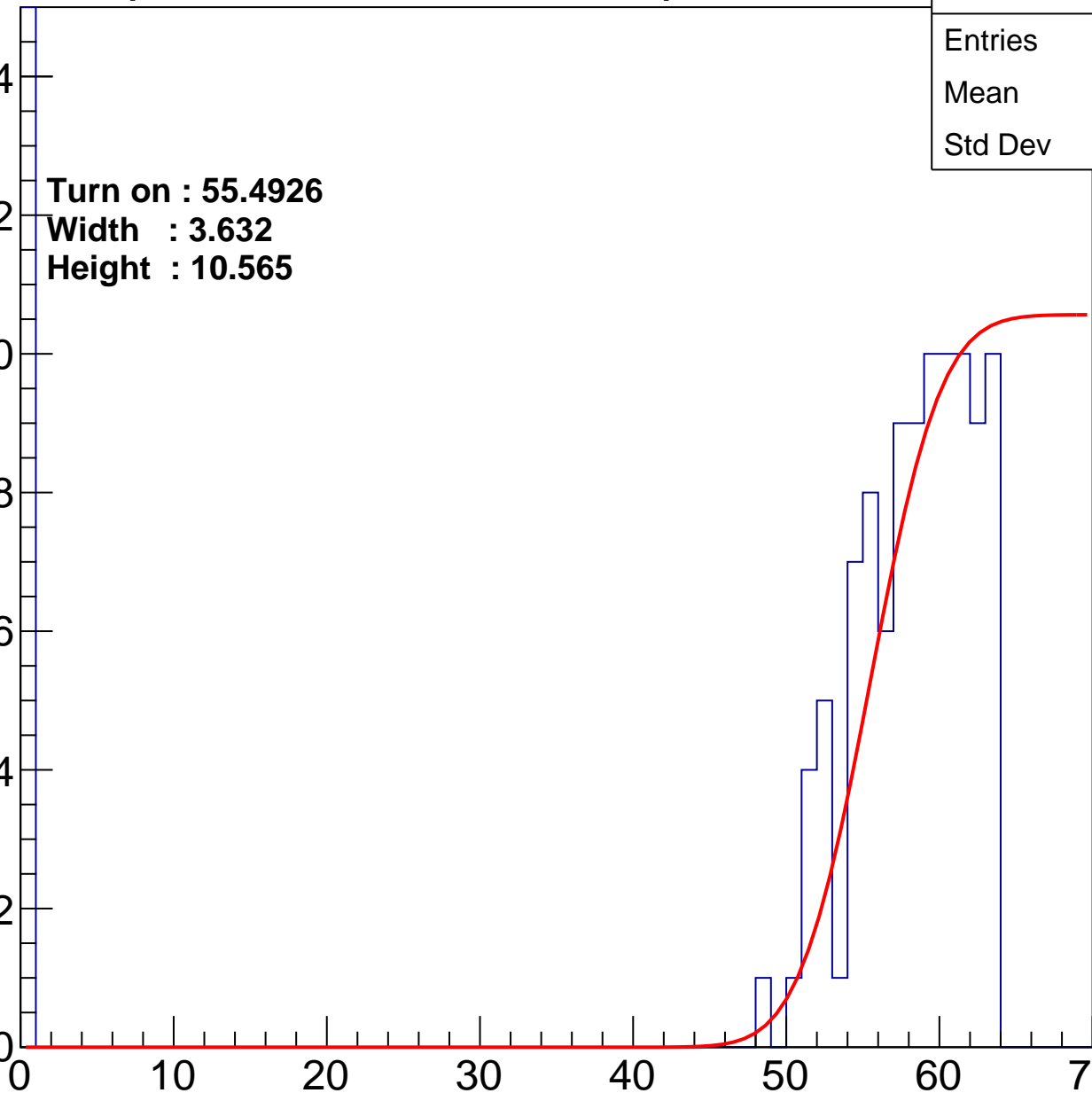
Width : 3.632

Height : 10.565

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	26
Std Dev	29.09

Turn on : 55.3728

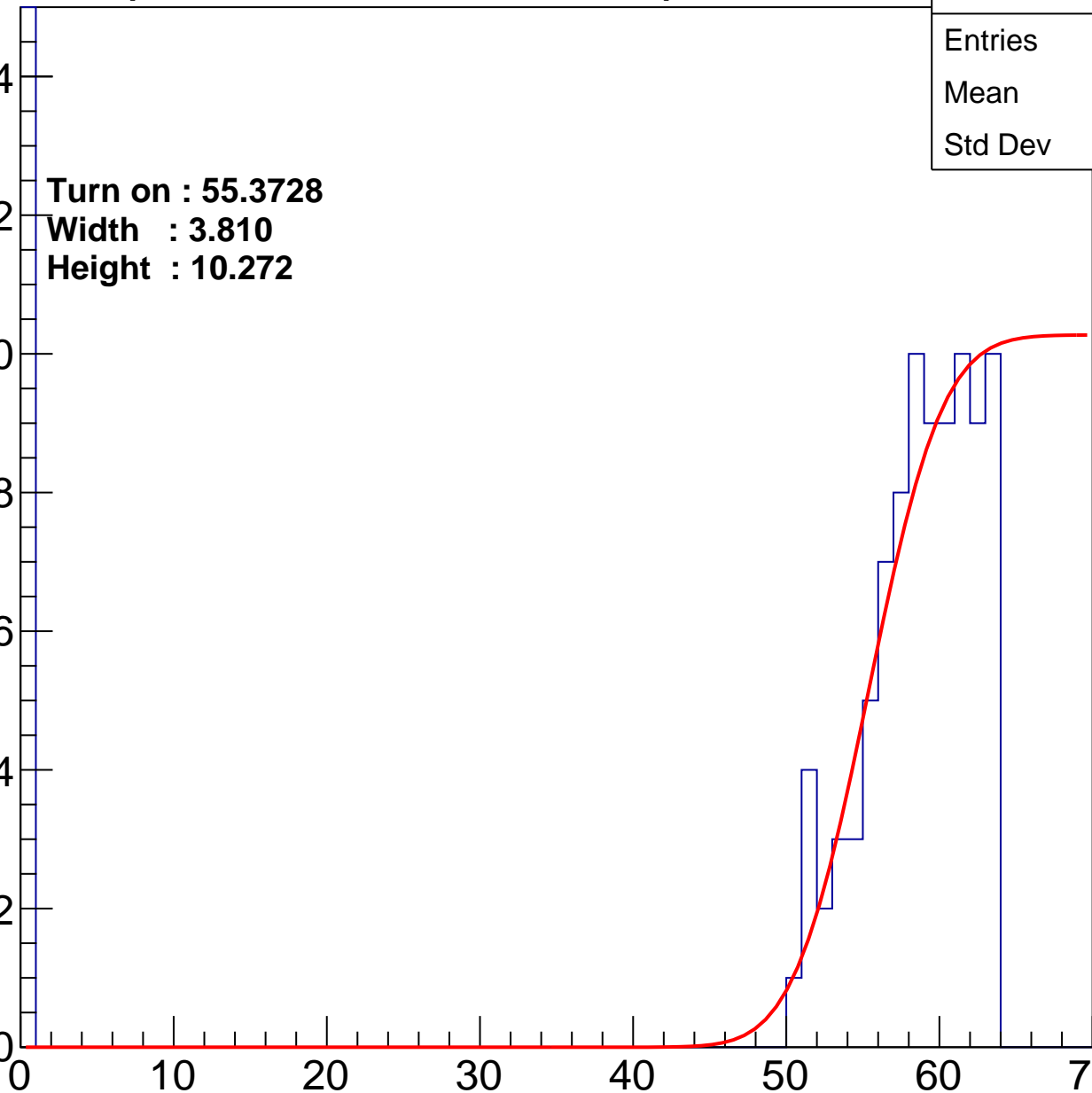
Width : 3.810

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch23

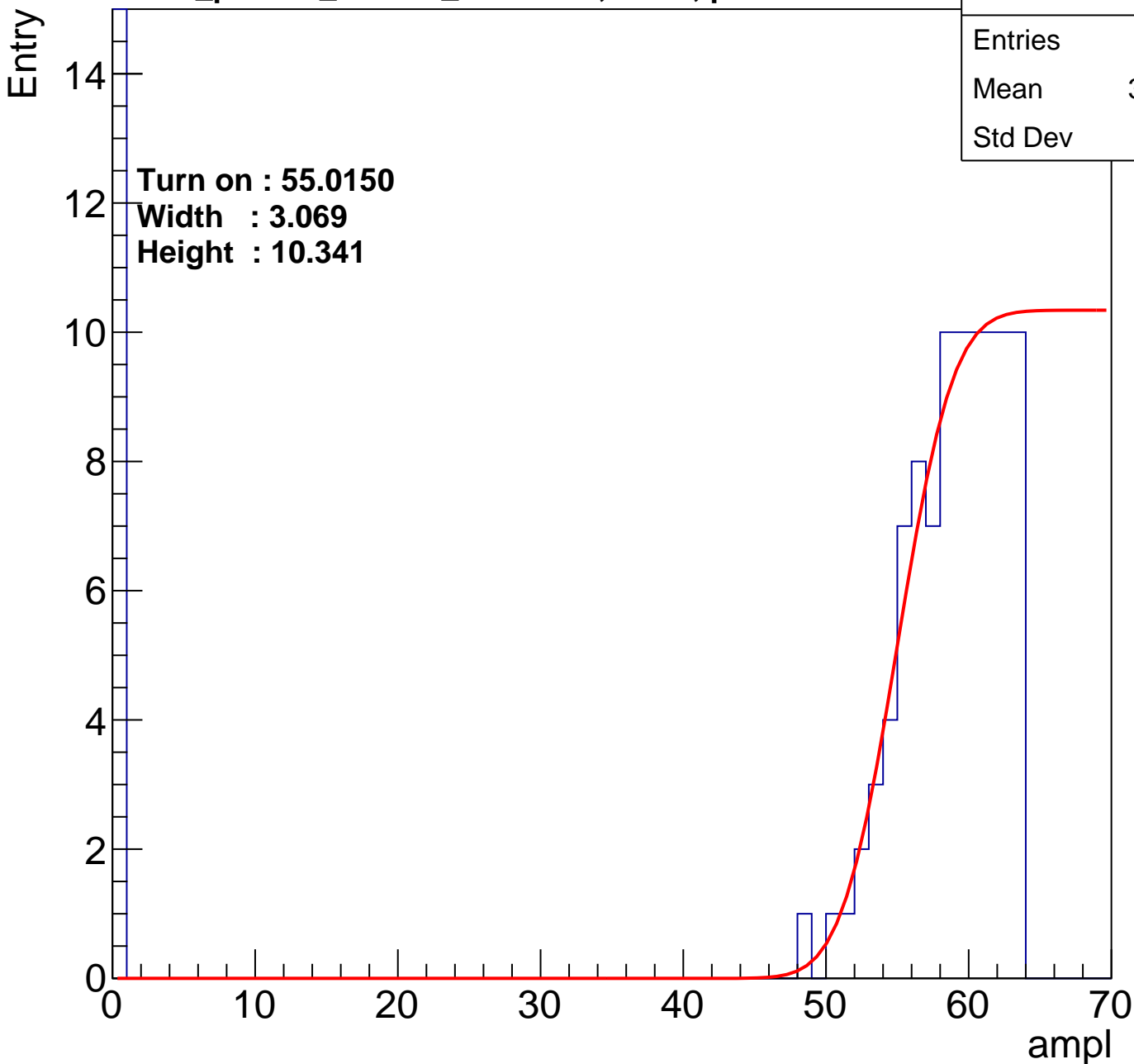
calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	33.48
Std Dev	29

Turn on : 55.0150

Width : 3.069

Height : 10.341

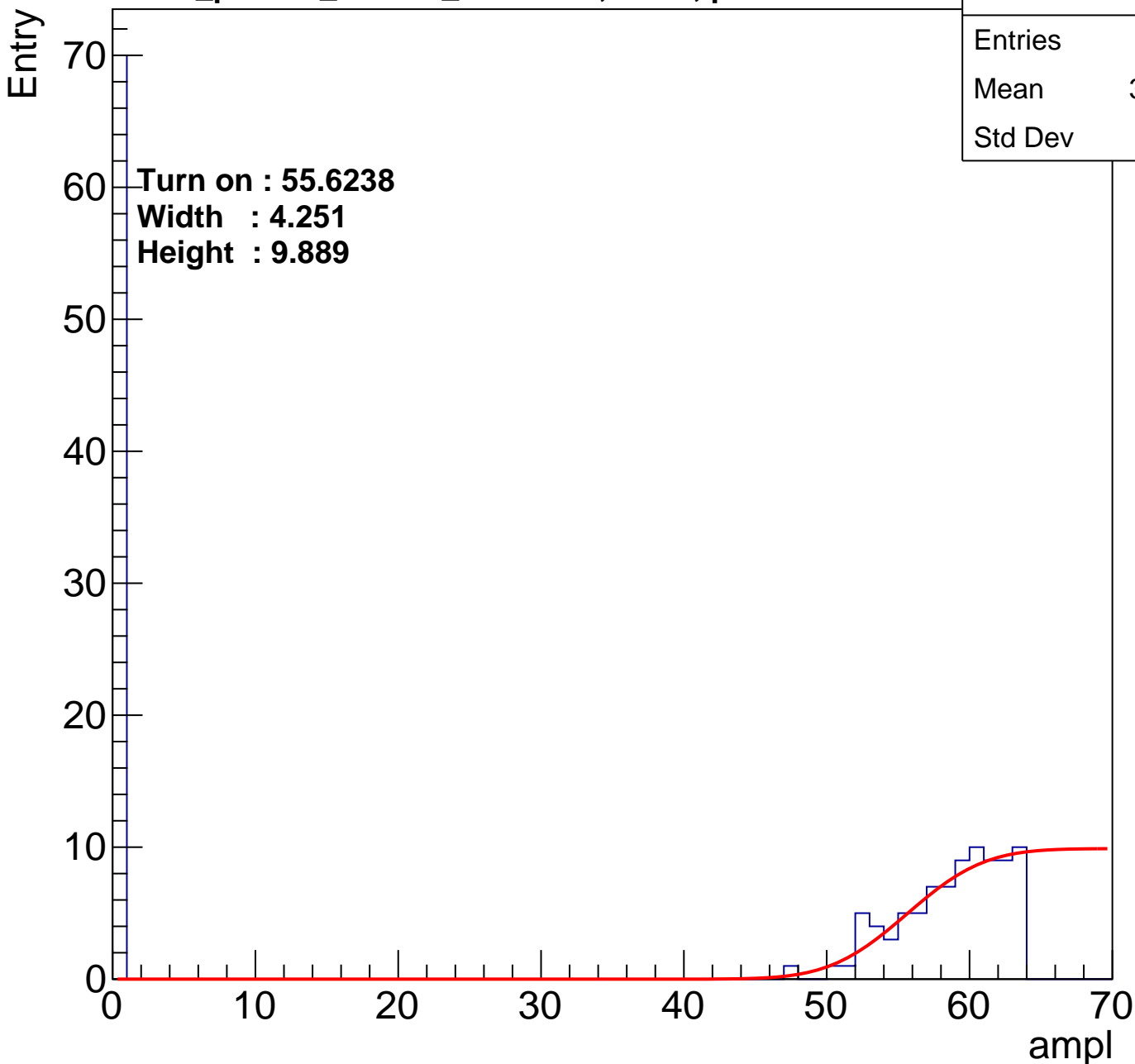


B1L104S, U23-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	32.12
Std Dev	29.1

Turn on : 55.6238
Width : 4.251
Height : 9.889



B1L104S, U23-ch25

calib_packv5_033123_0516.root, FC#4, port A1

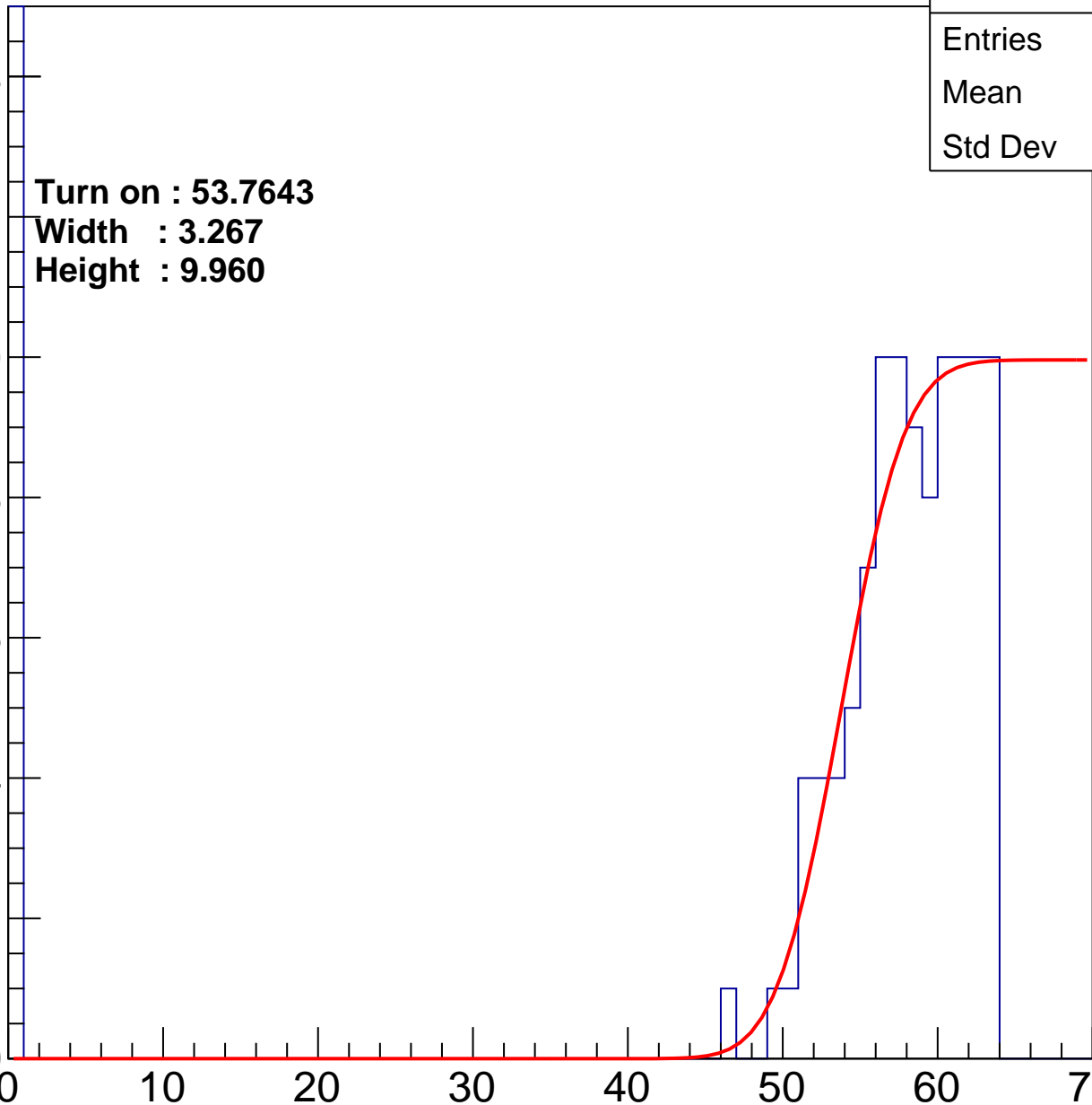
Entry

14
12
10
8
6
4
2
0

Turn on : 53.7643
Width : 3.267
Height : 9.960

Entries	181
Mean	33.19
Std Dev	28.7

ampl



B1L104S, U23-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	38.2
Std Dev	27.21

Turn on : 53.4025

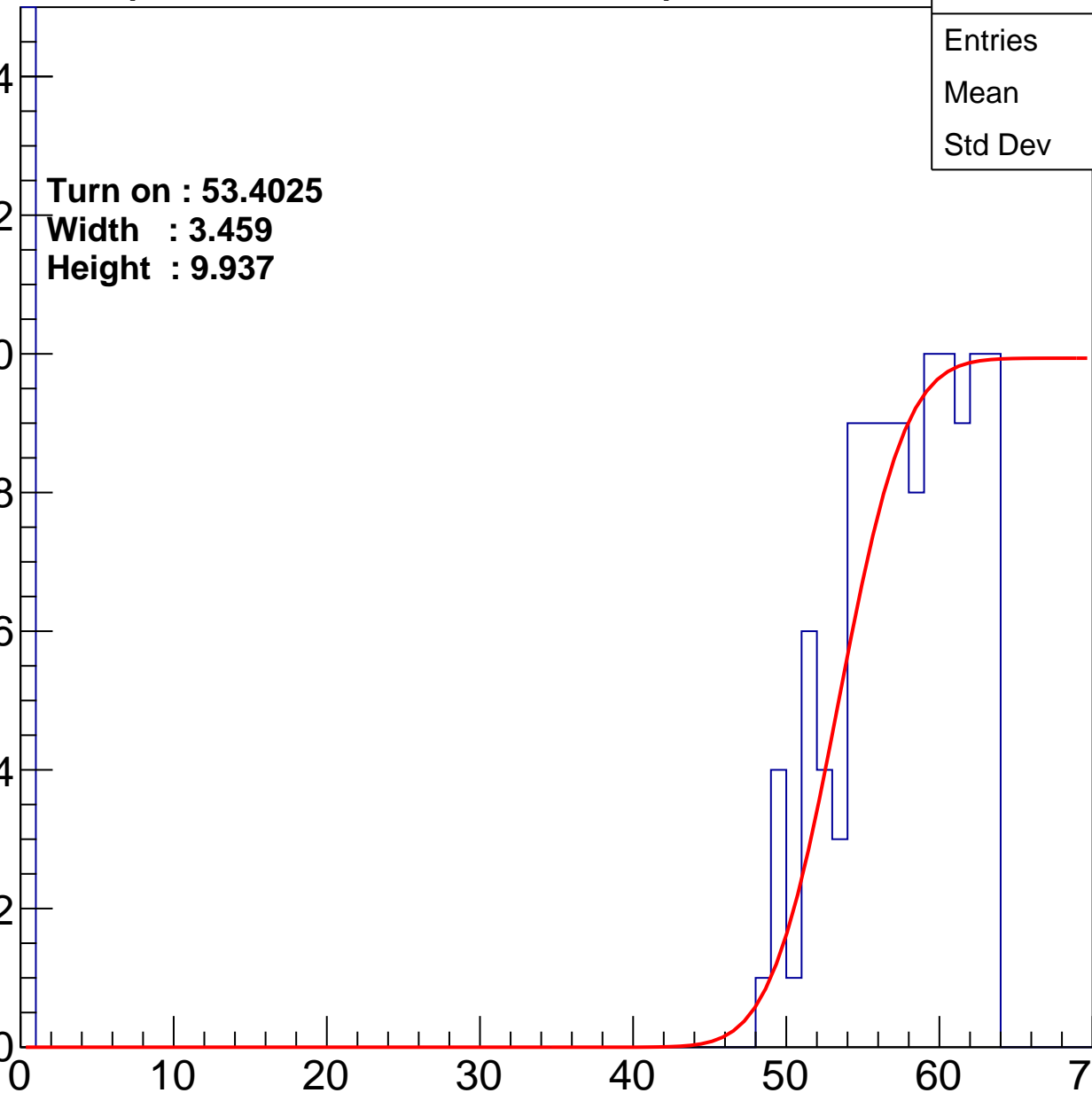
Width : 3.459

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	35.04
Std Dev	28.78

Turn on : 54.2947

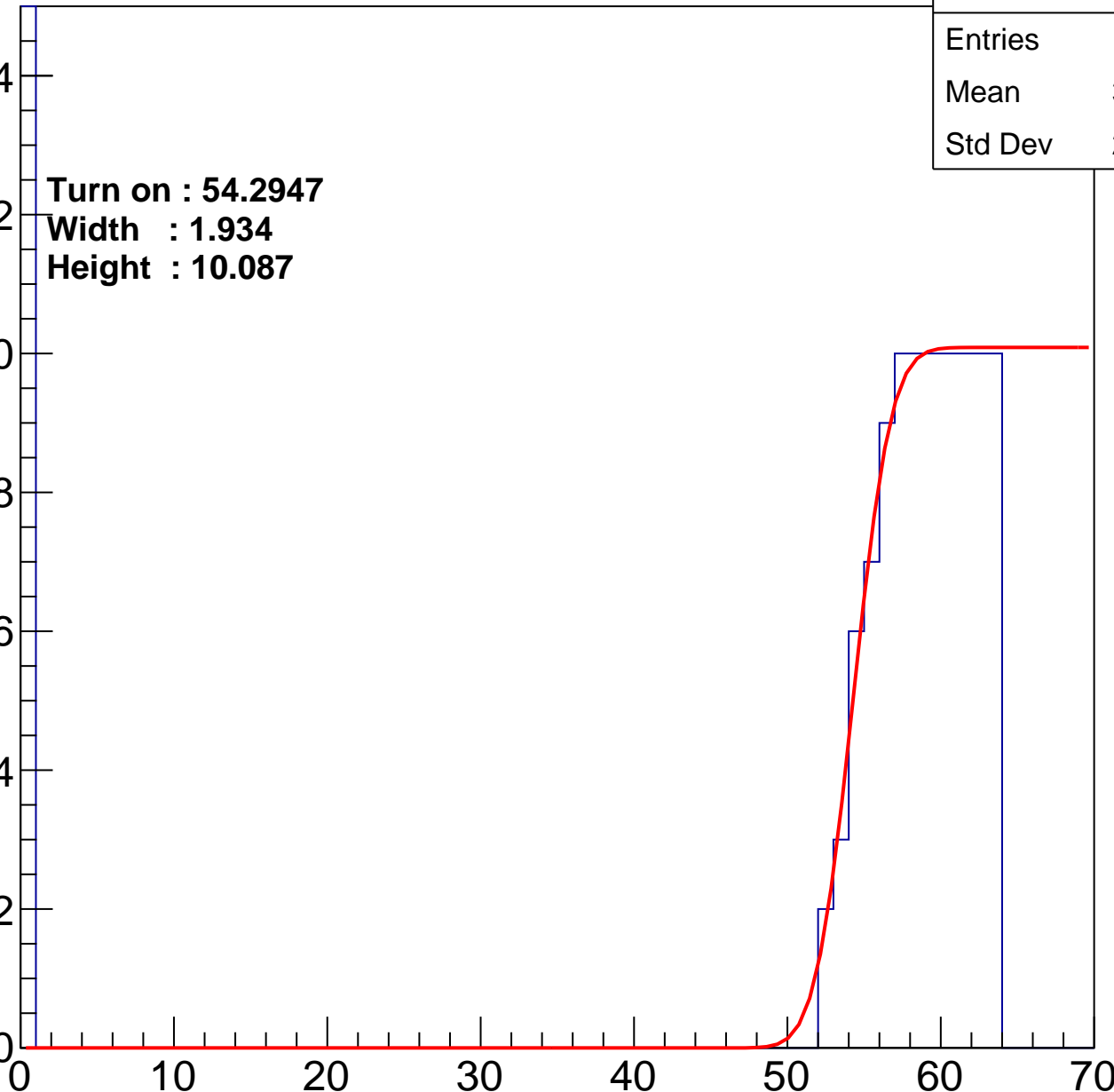
Width : 1.934

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	159
Mean	33.13
Std Dev	29.12

Turn on : 55.2040

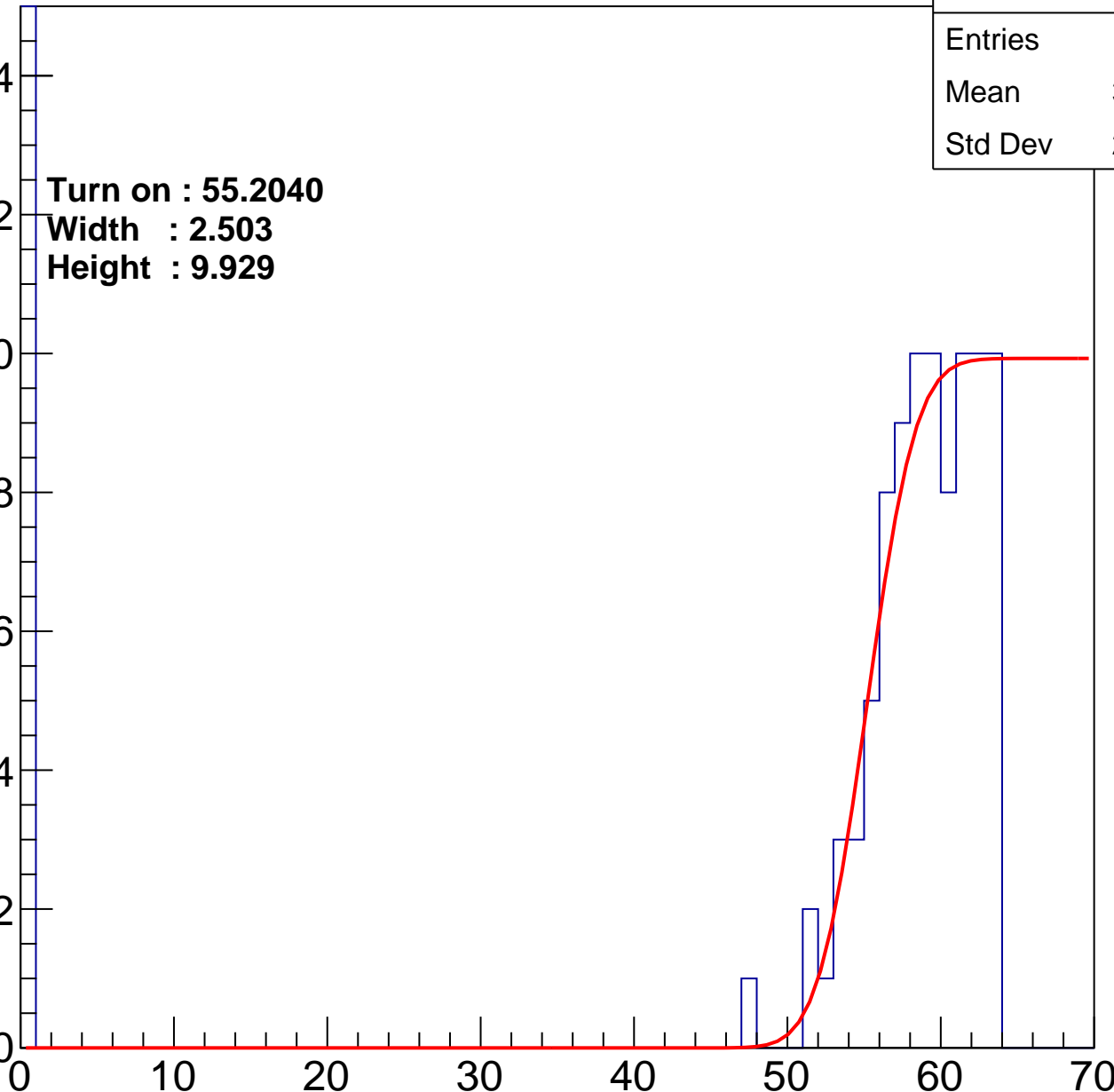
Width : 2.503

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	157
Mean	36.32
Std Dev	28.31

Turn on : 54.6750

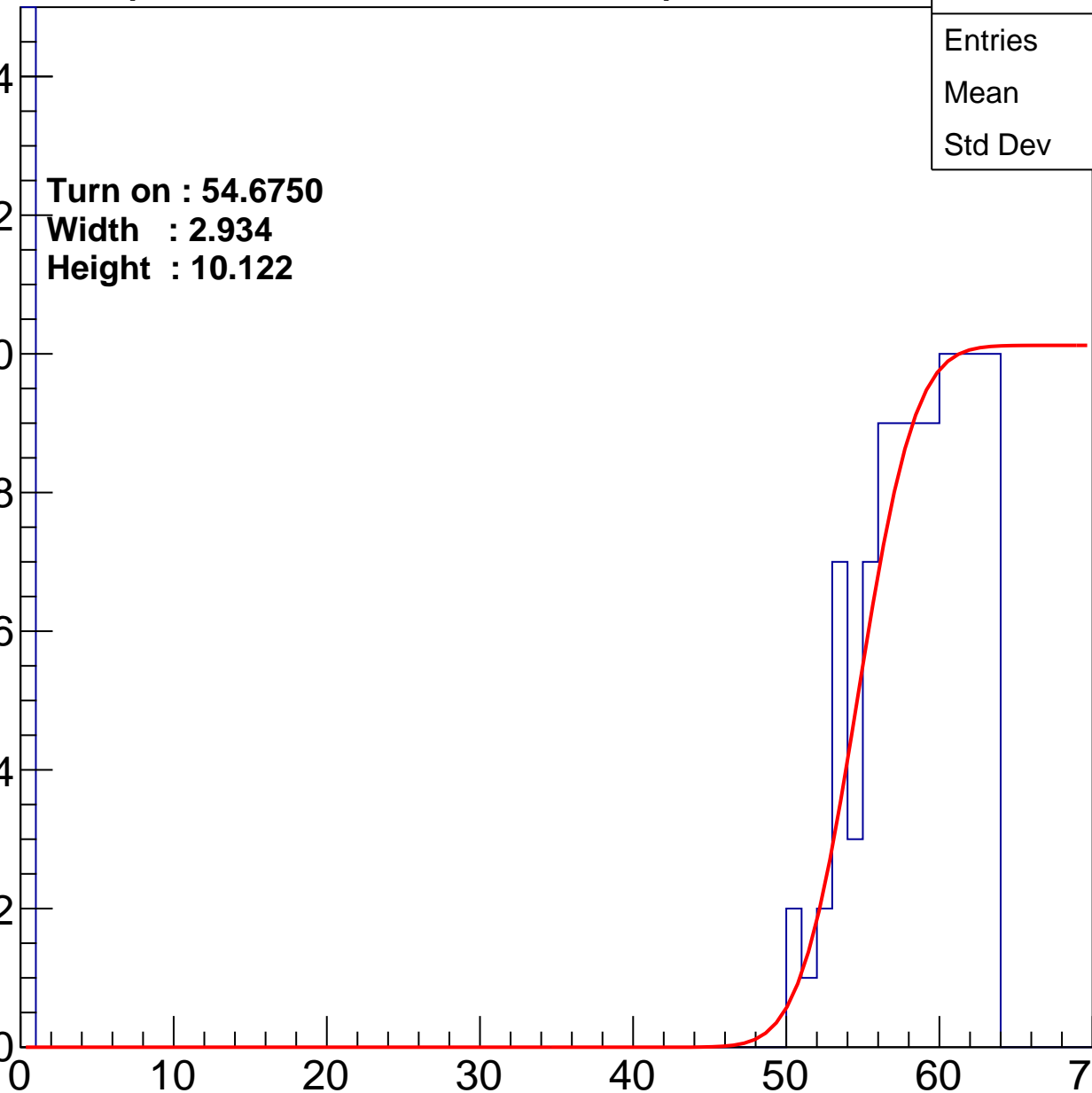
Width : 2.934

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	32.86
Std Dev	28.78

Turn on : 54.4908

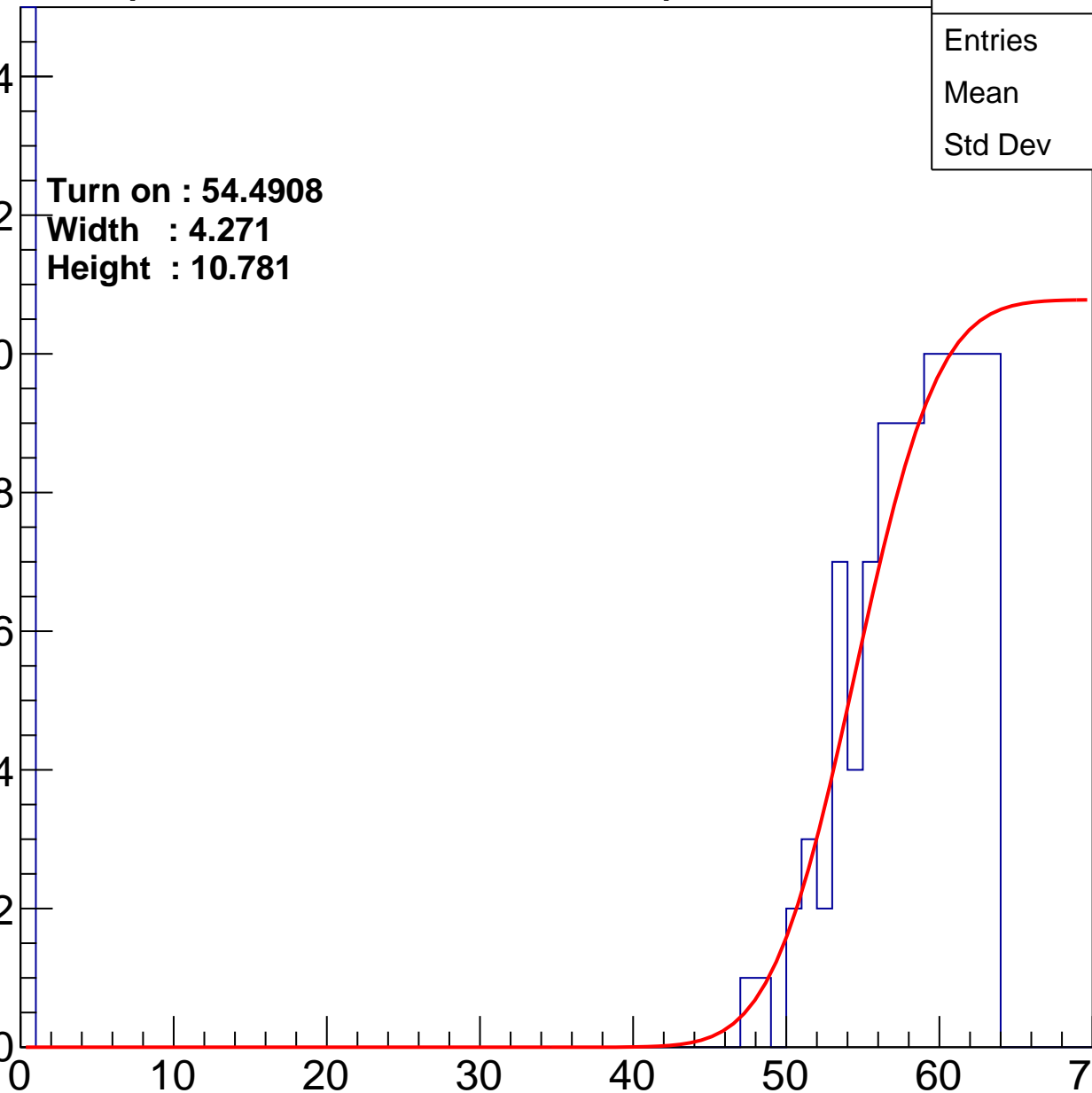
Width : 4.271

Height : 10.781

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	30.39
Std Dev	29.3

Turn on : 54.8075

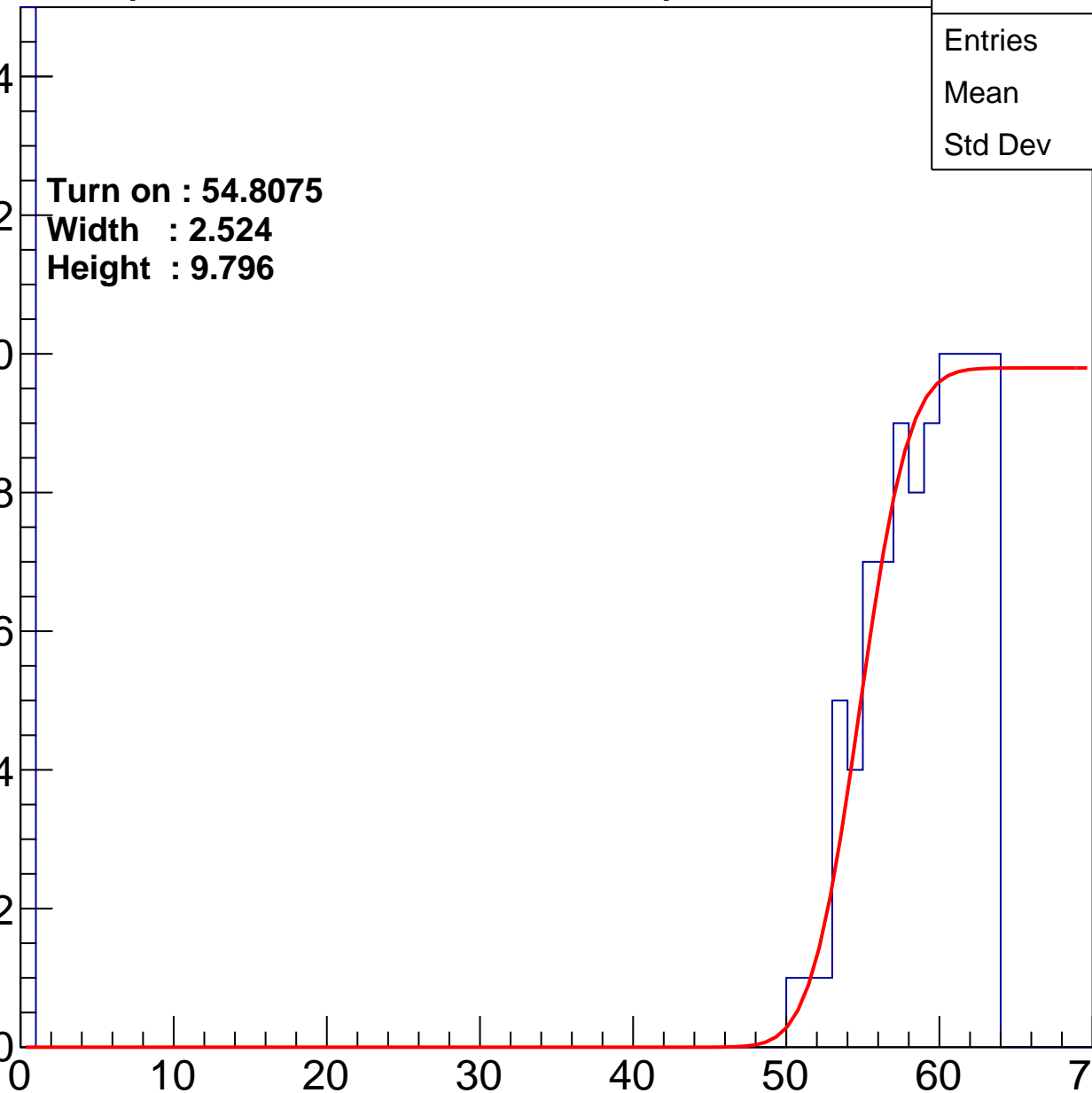
Width : 2.524

Height : 9.796

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	34.8
Std Dev	27.58

Turn on : 50.6307

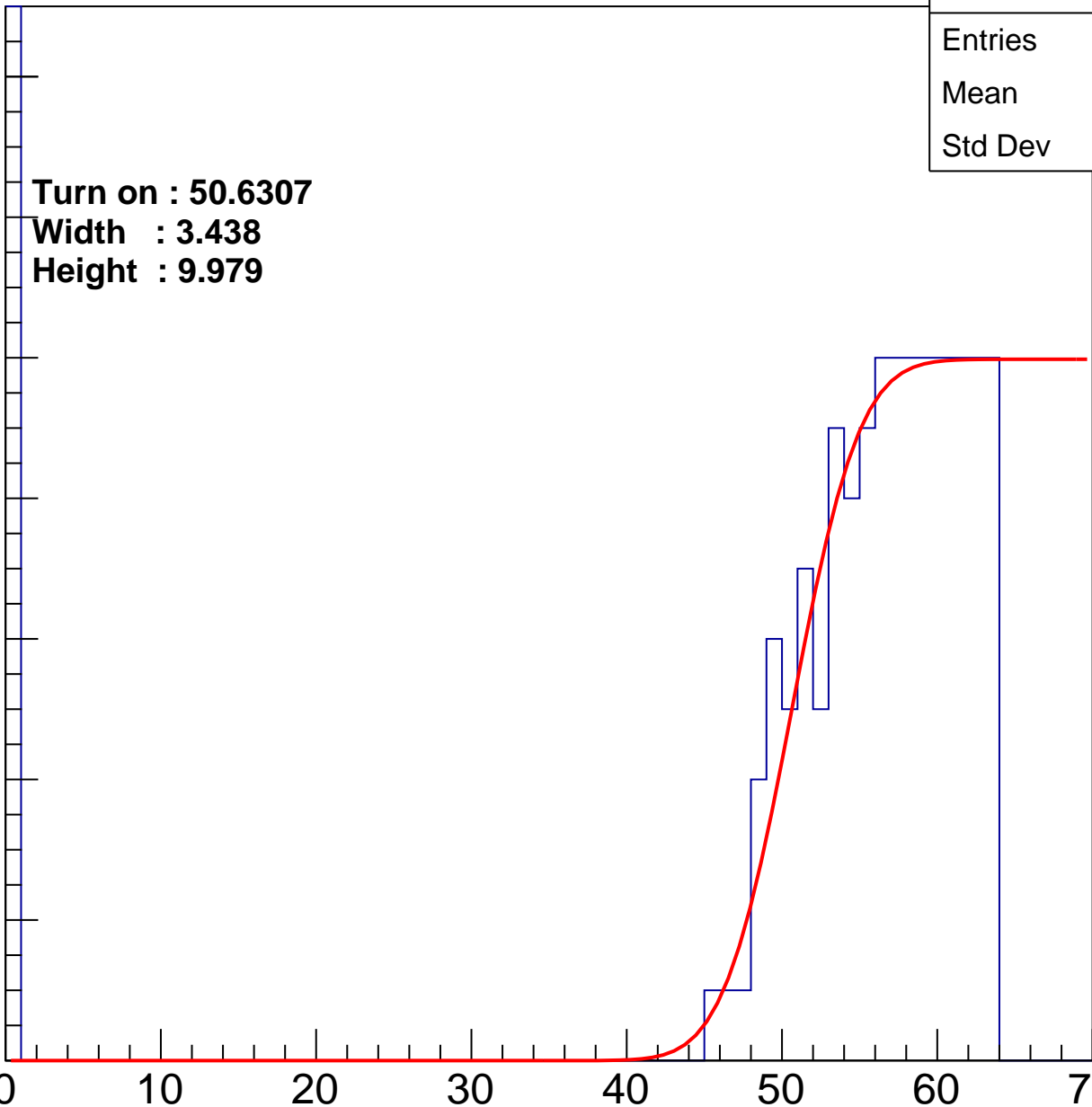
Width : 3.438

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	31.37
Std Dev	29.38

Turn on : 54.4430

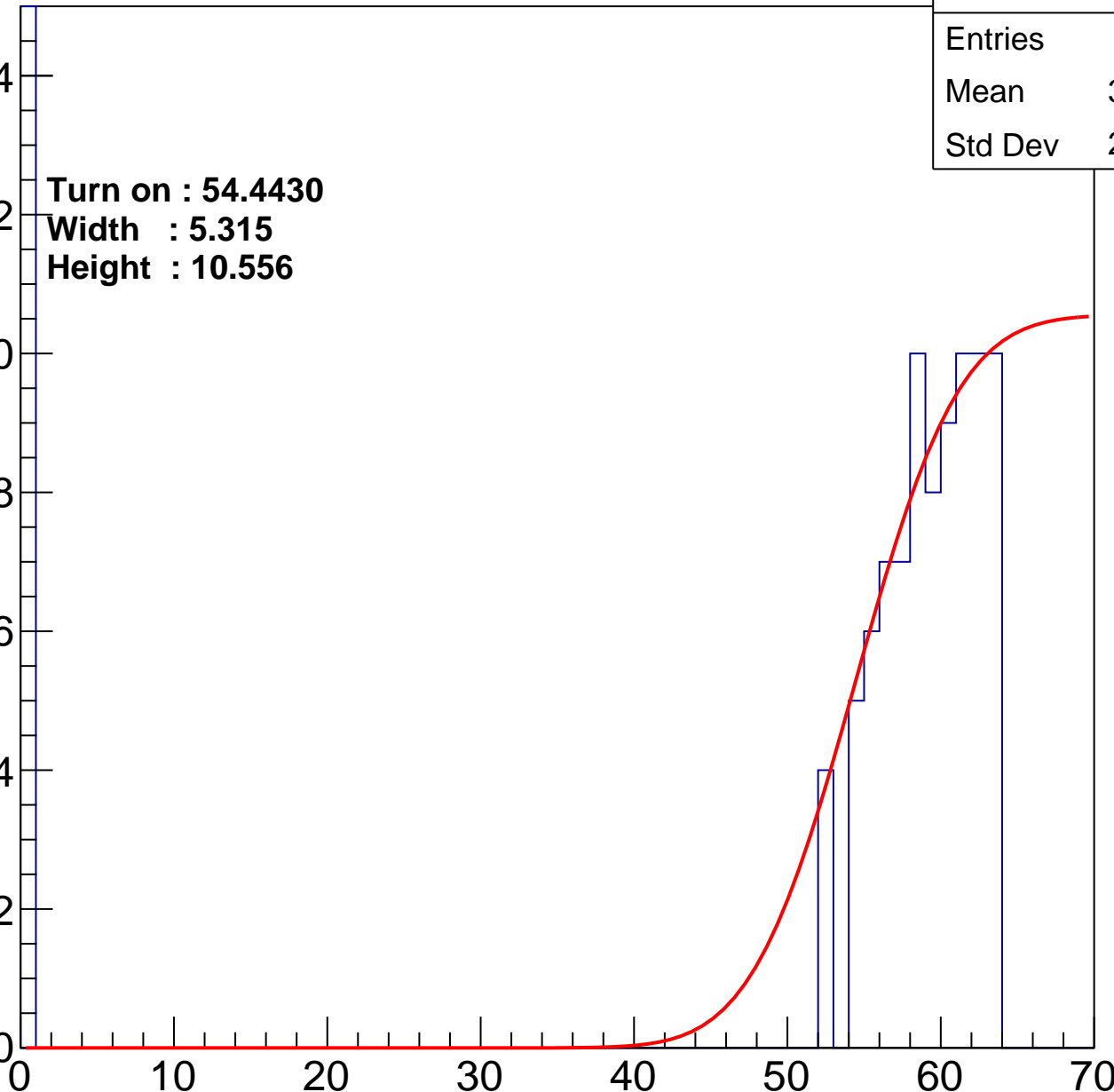
Width : 5.315

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	32.55
Std Dev	29.07

Turn on : 55.7608

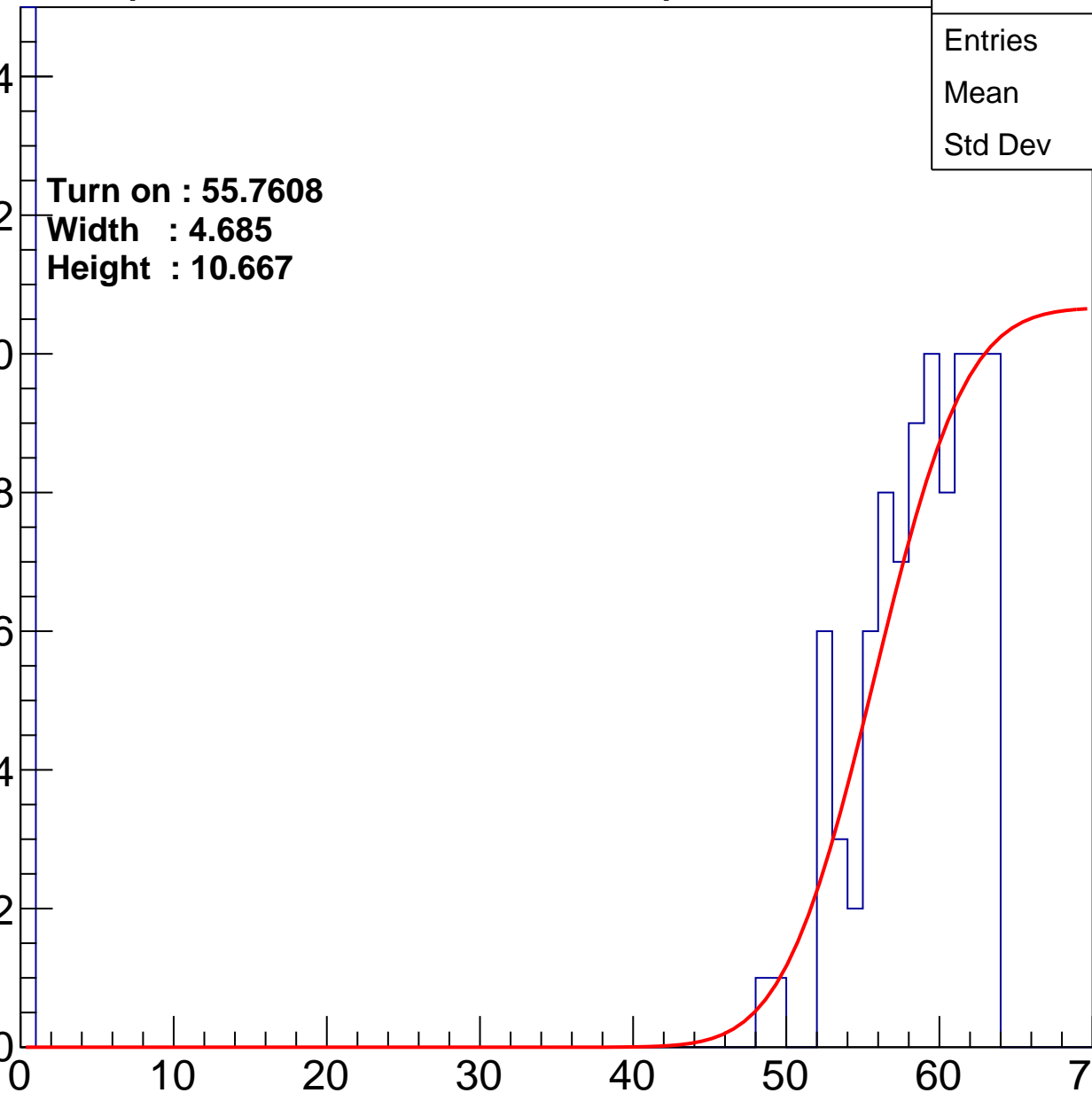
Width : 4.685

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch35

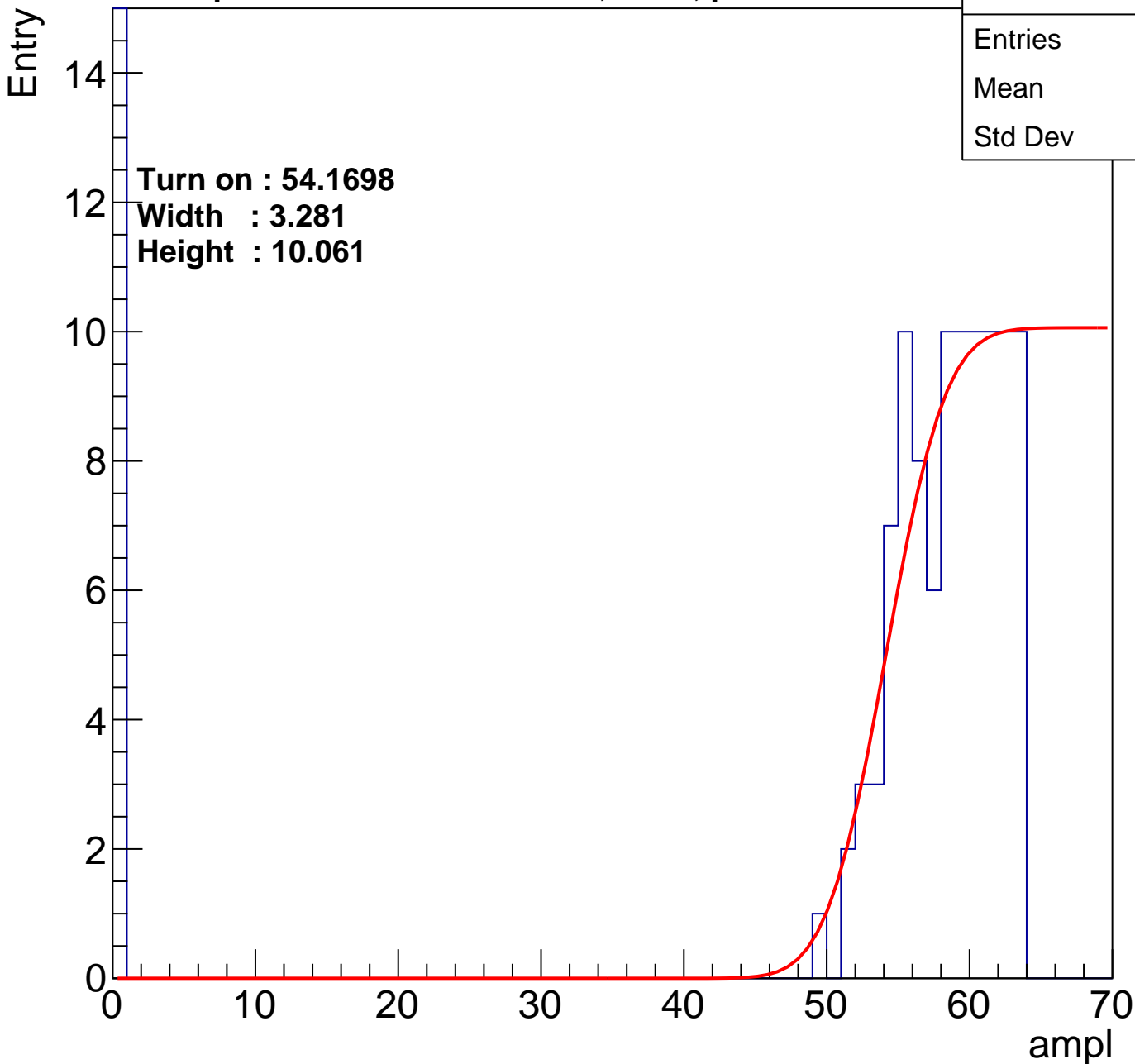
calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	33.8
Std Dev	28.8

Turn on : 54.1698

Width : 3.281

Height : 10.061



B1L104S, U23-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	30.1
Std Dev	29.22

Turn on : 56.0767

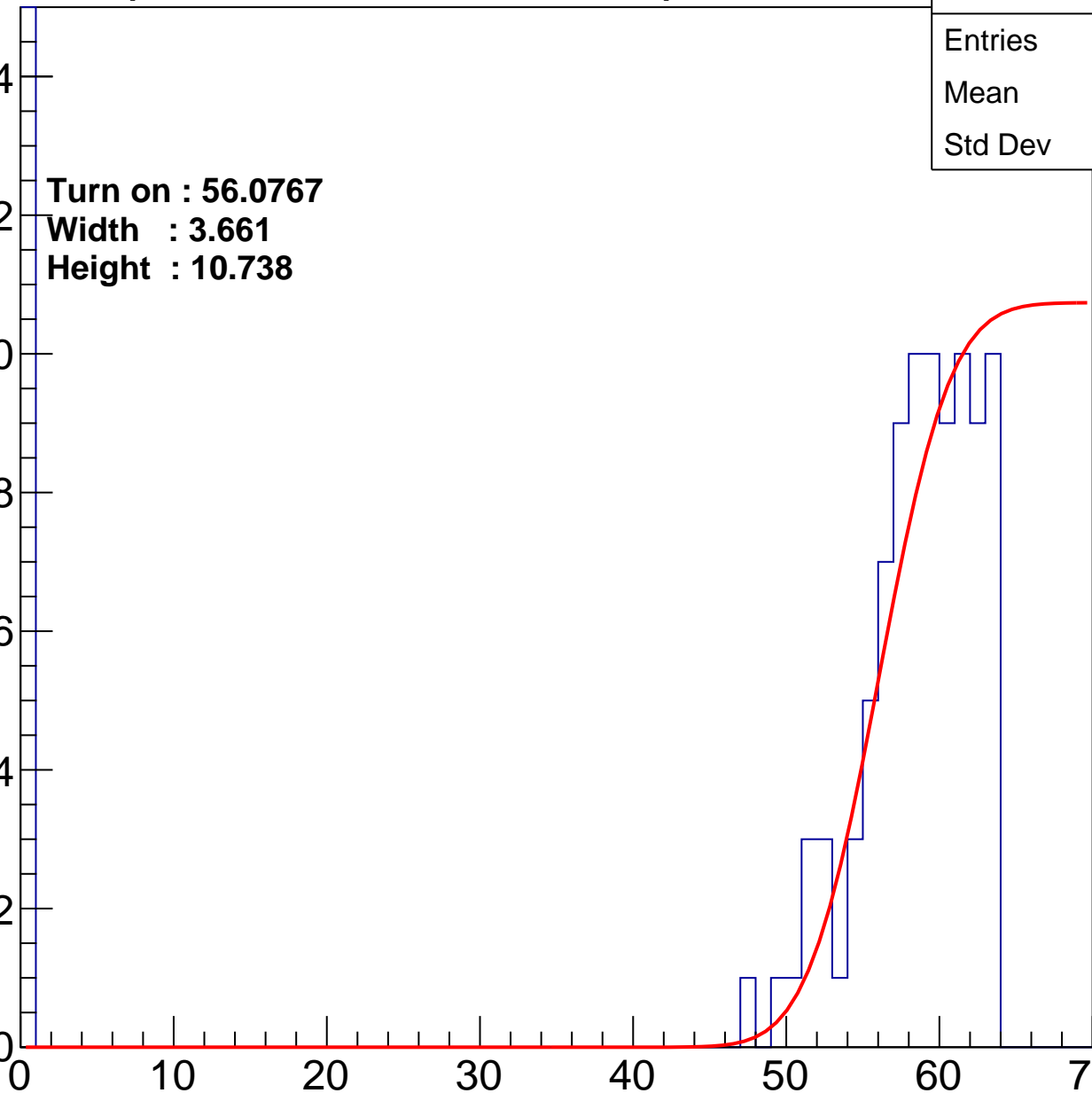
Width : 3.661

Height : 10.738

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.79
Std Dev	28.6

Turn on : 54.3840

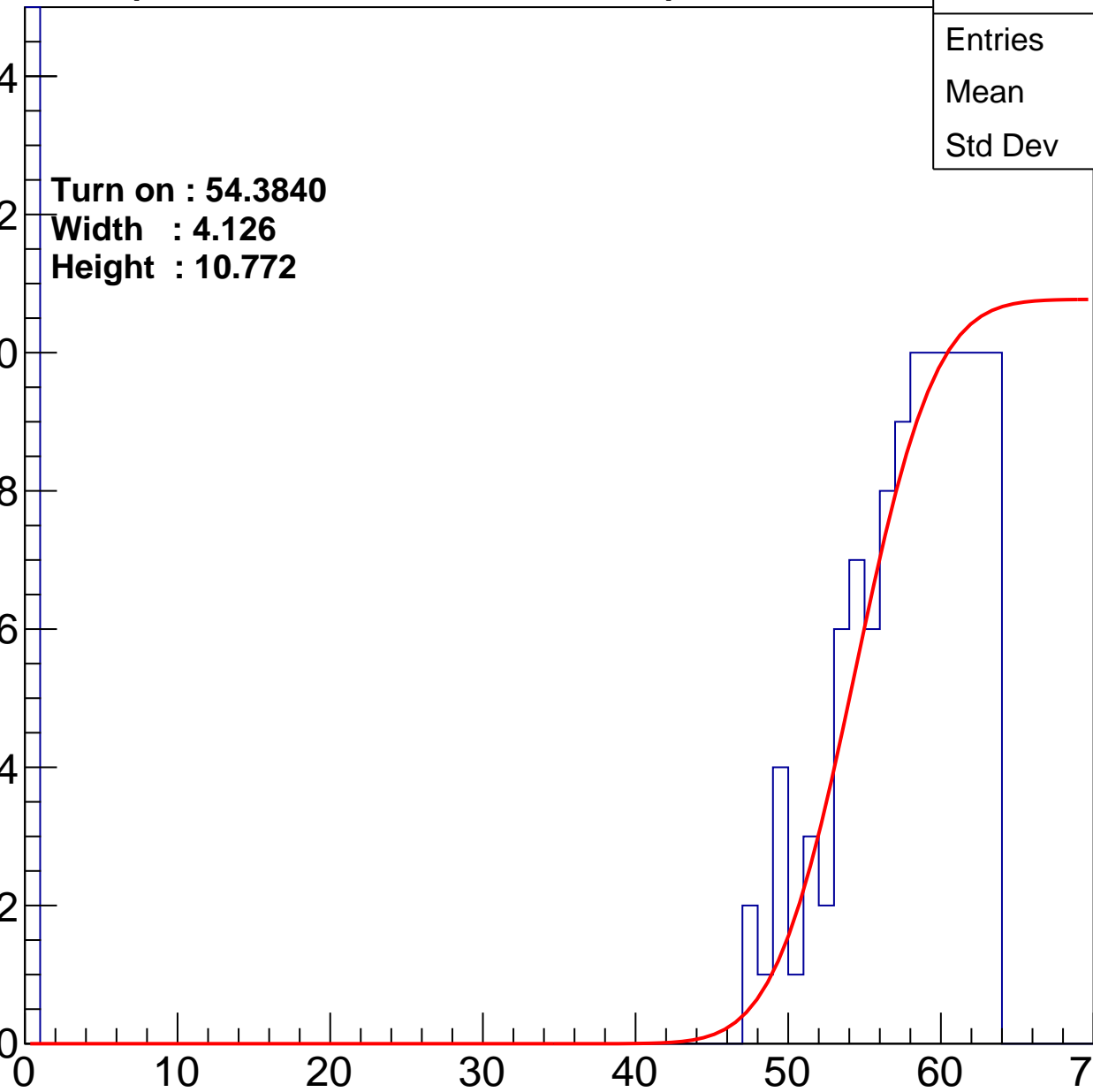
Width : 4.126

Height : 10.772

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	32.75
Std Dev	28.74

Turn on : 55.2179

Width : 4.335

Height : 11.163

Entry

14

12

10

8

6

4

2

0

0

10

20

30

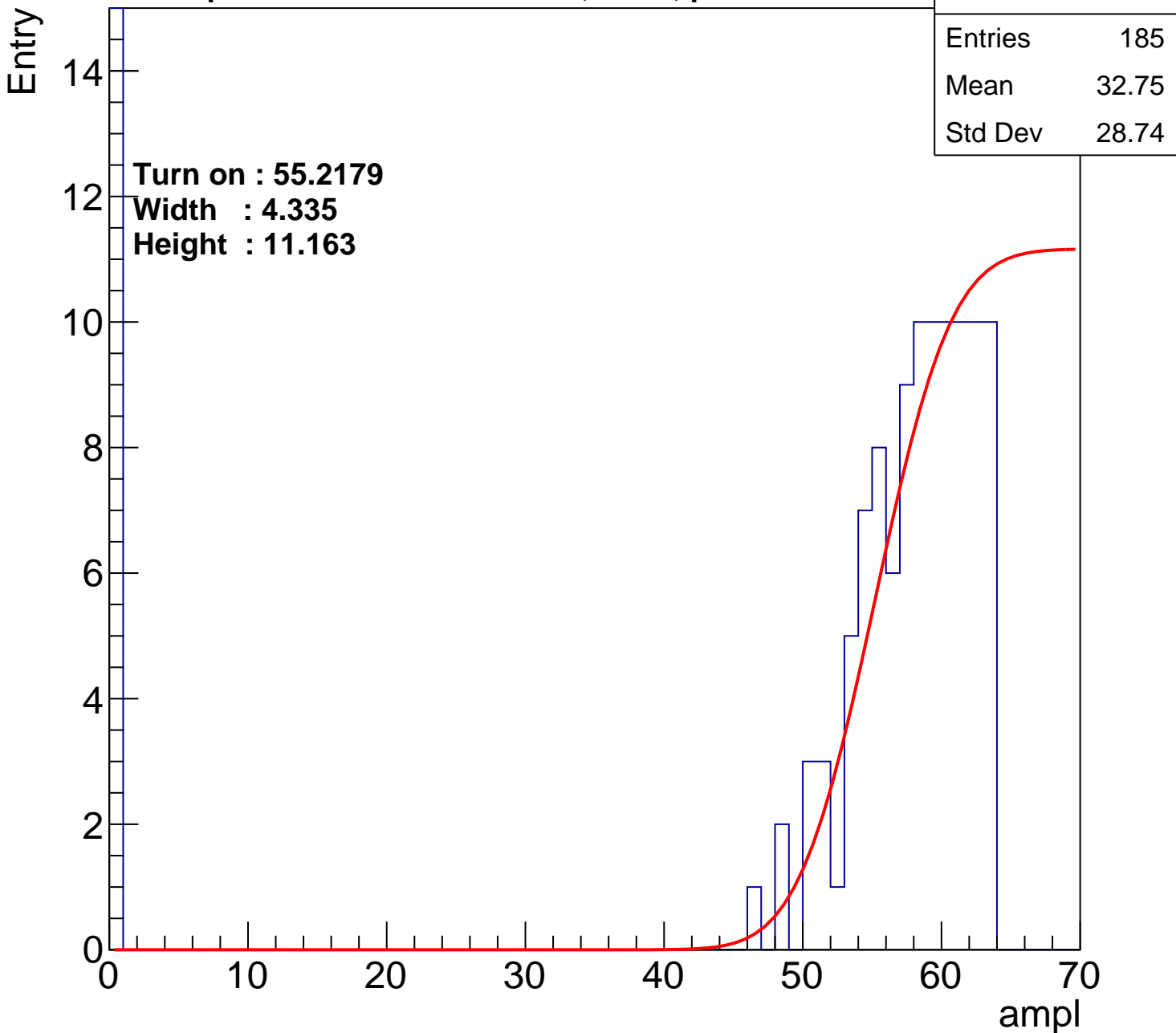
40

50

60

70

ampl



B1L104S, U23-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	32.37
Std Dev	29.14

Turn on : 54.8864

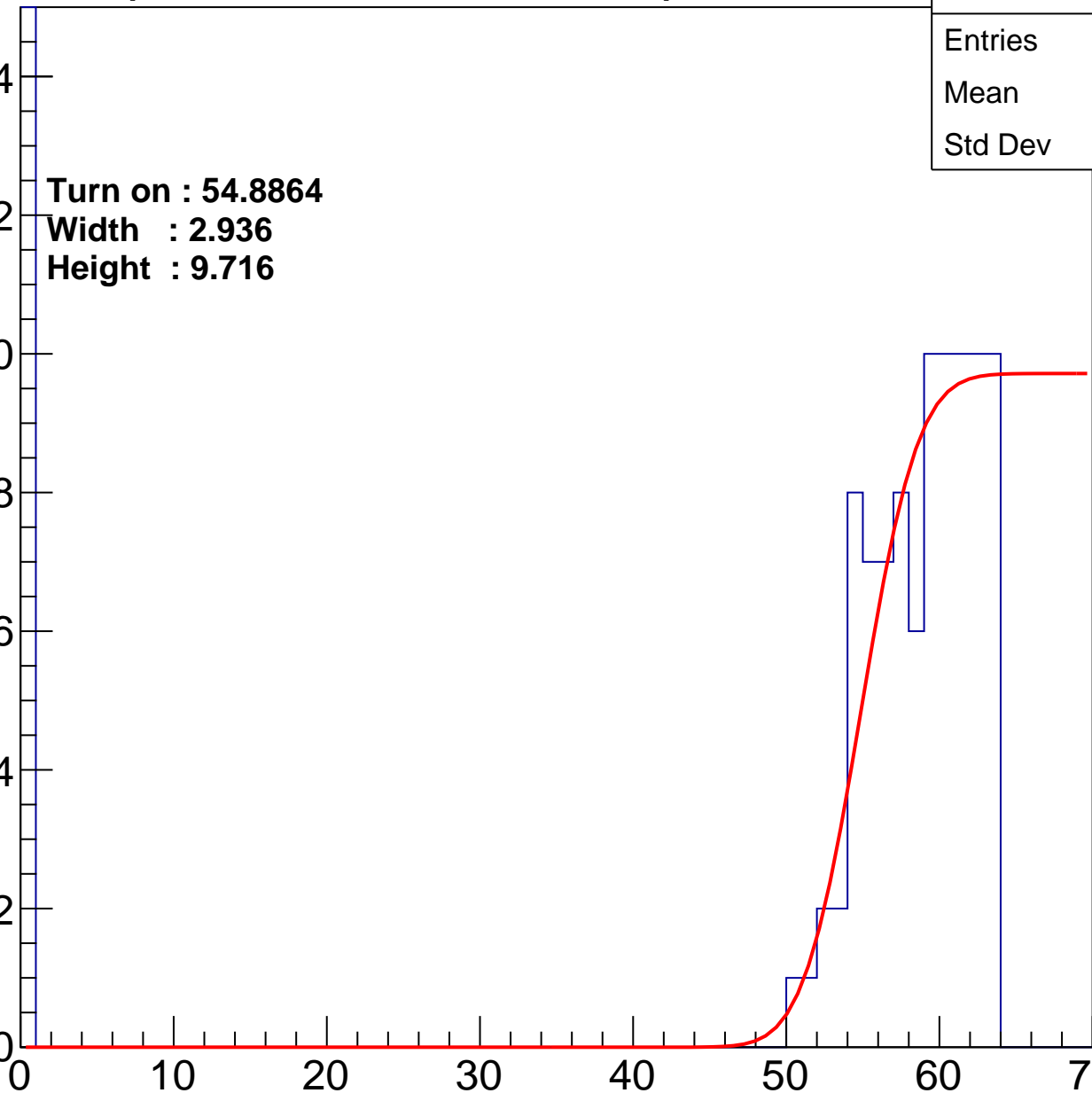
Width : 2.936

Height : 9.716

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch40

calib_packv5_033123_0516.root, FC#4, port A1

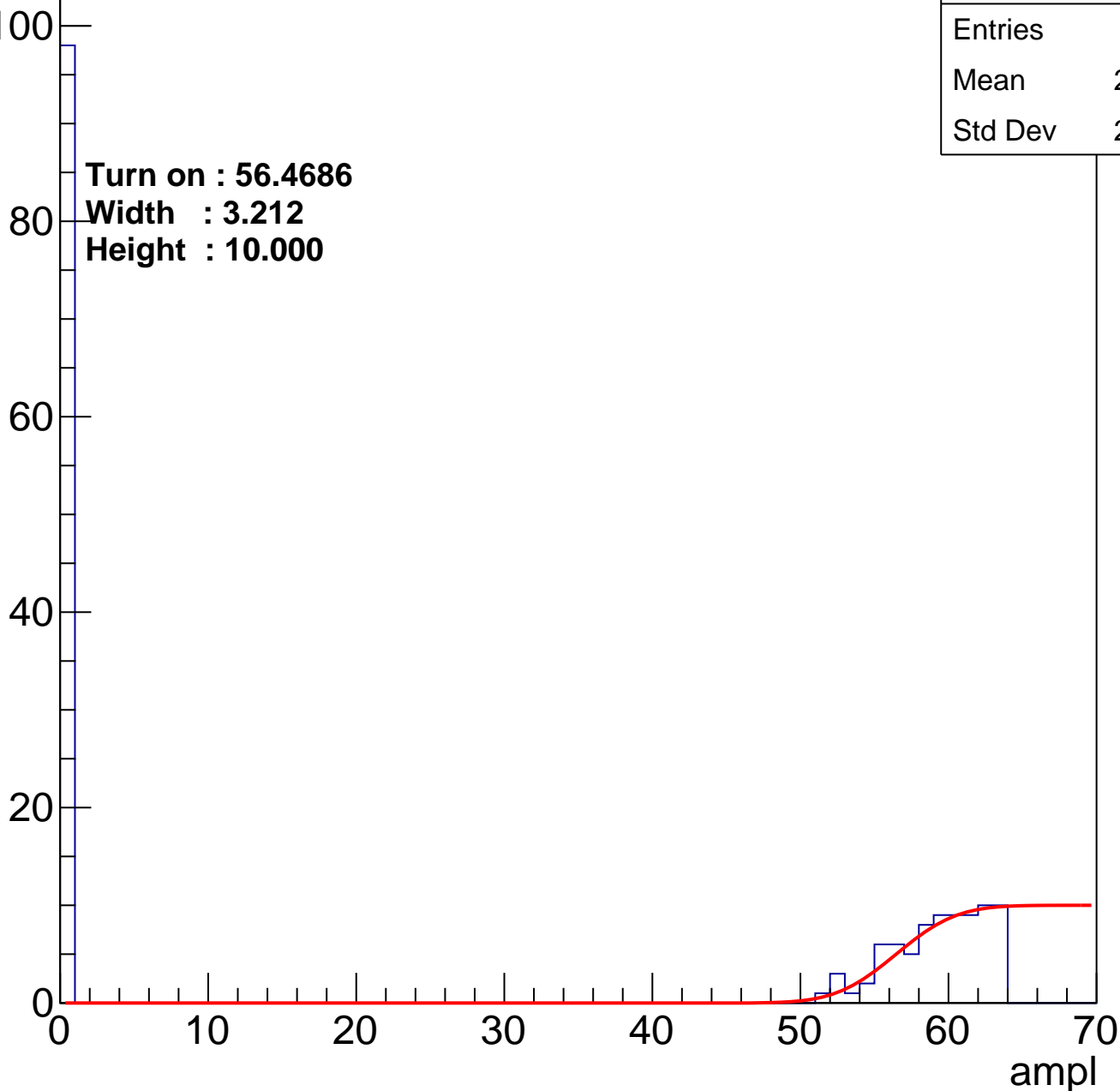
Entry

Entries	177
Mean	26.29
Std Dev	29.35

Turn on : 56.4686

Width : 3.212

Height : 10.000



B1L104S, U23-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	31.88
Std Dev	28.89

Turn on : 53.2308

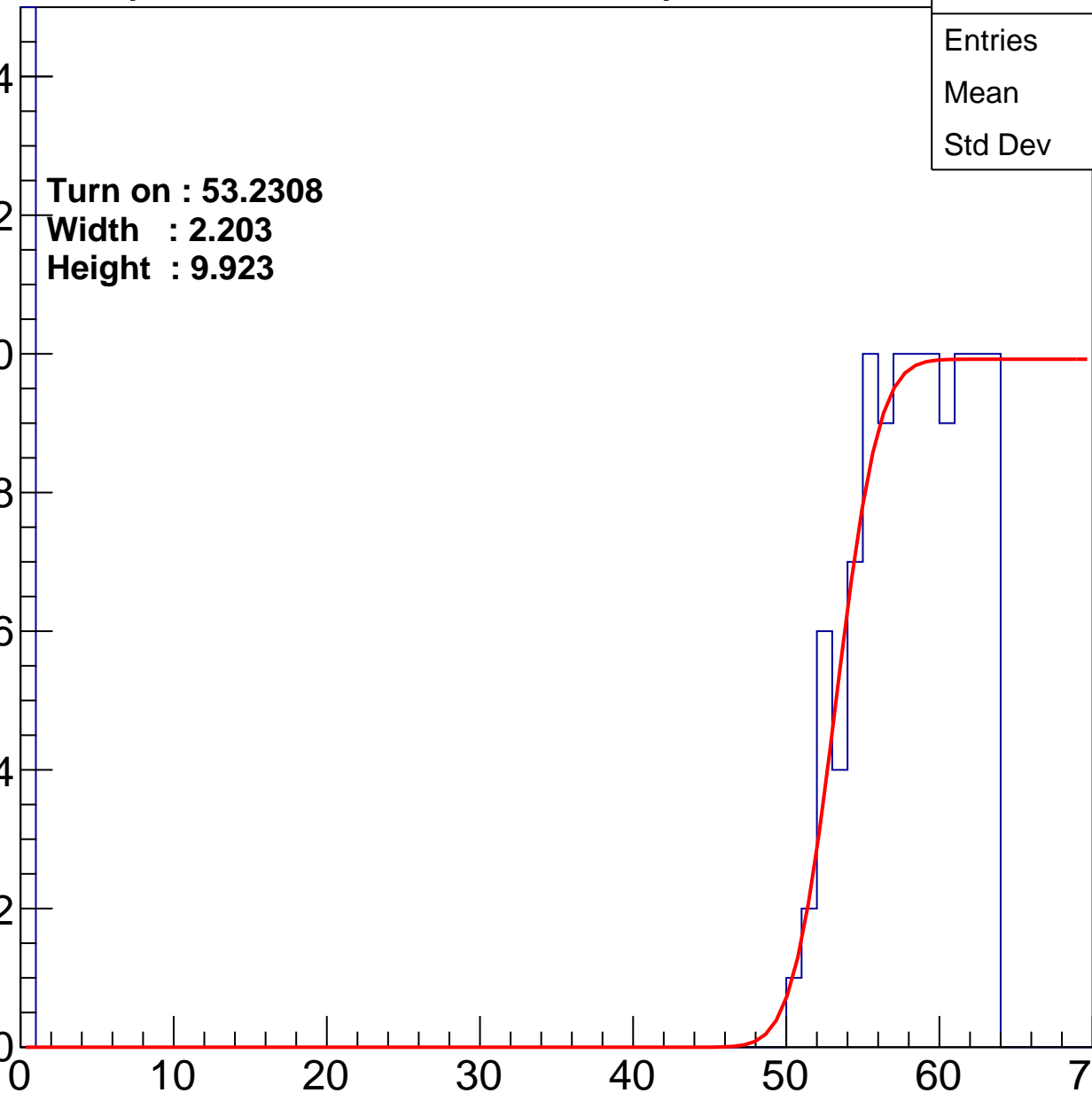
Width : 2.203

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	31.67
Std Dev	28.77

Turn on : 54.0882

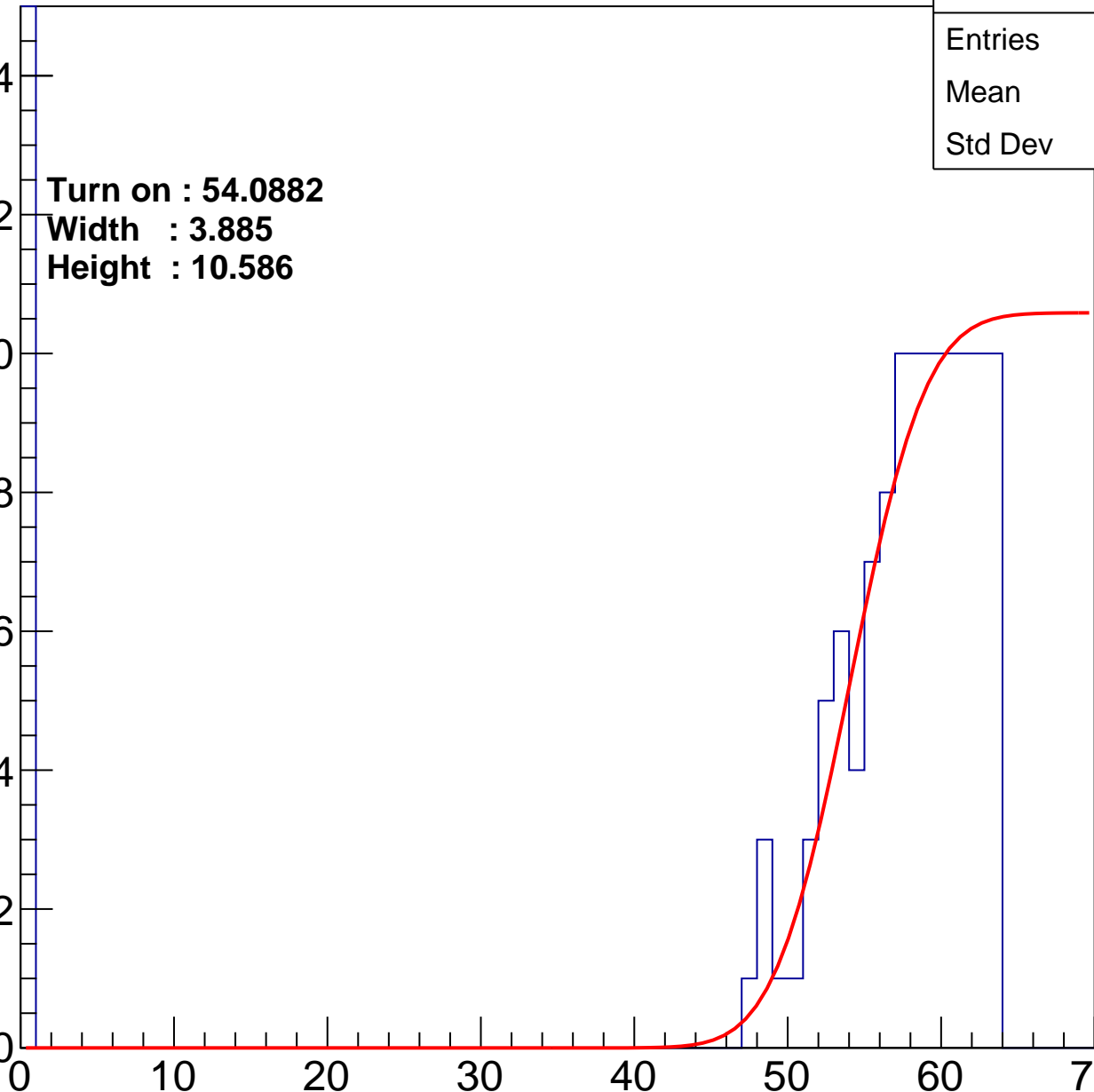
Width : 3.885

Height : 10.586

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	34.65
Std Dev	28.38

Turn on : 53.8565

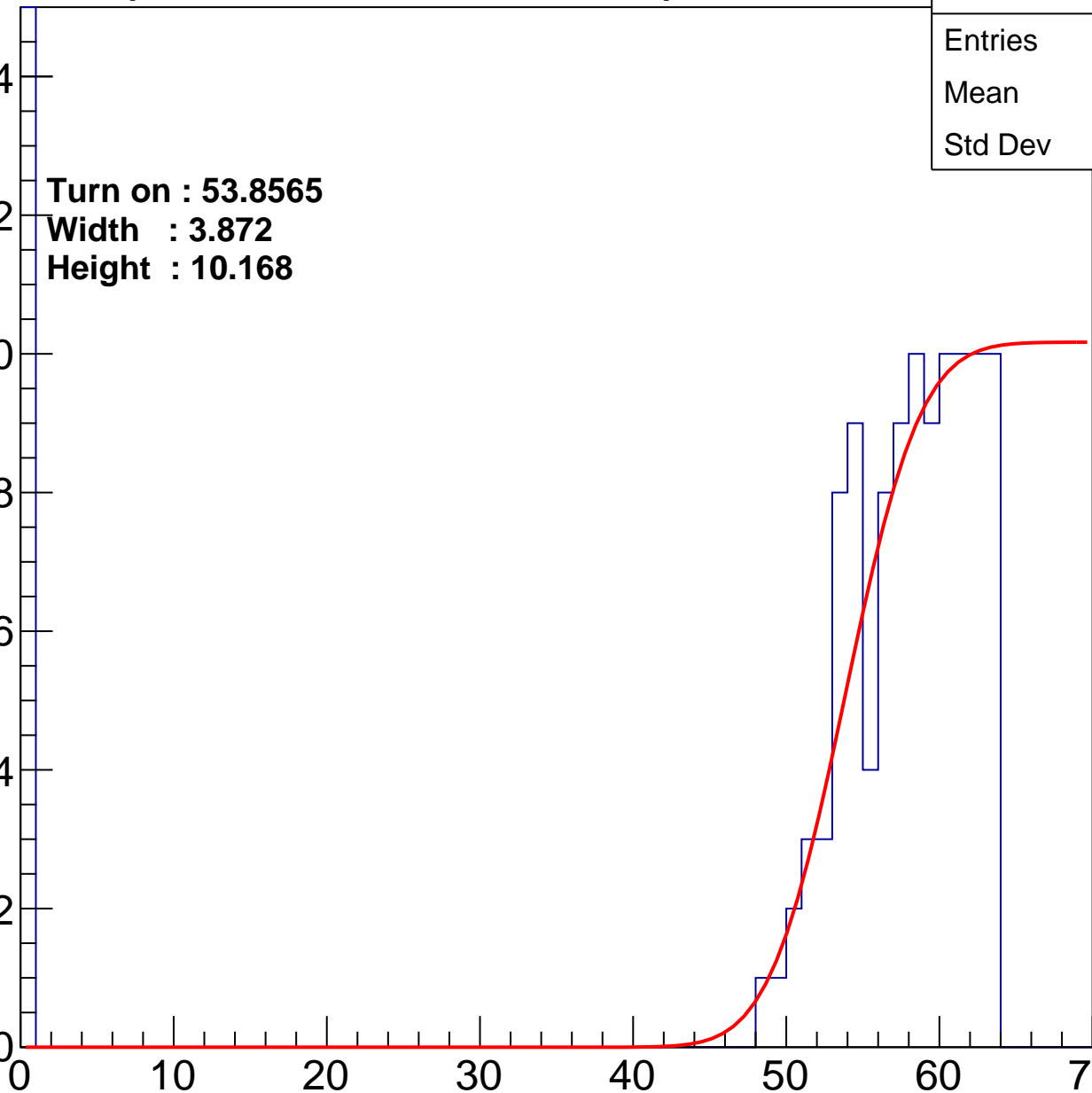
Width : 3.872

Height : 10.168

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch44

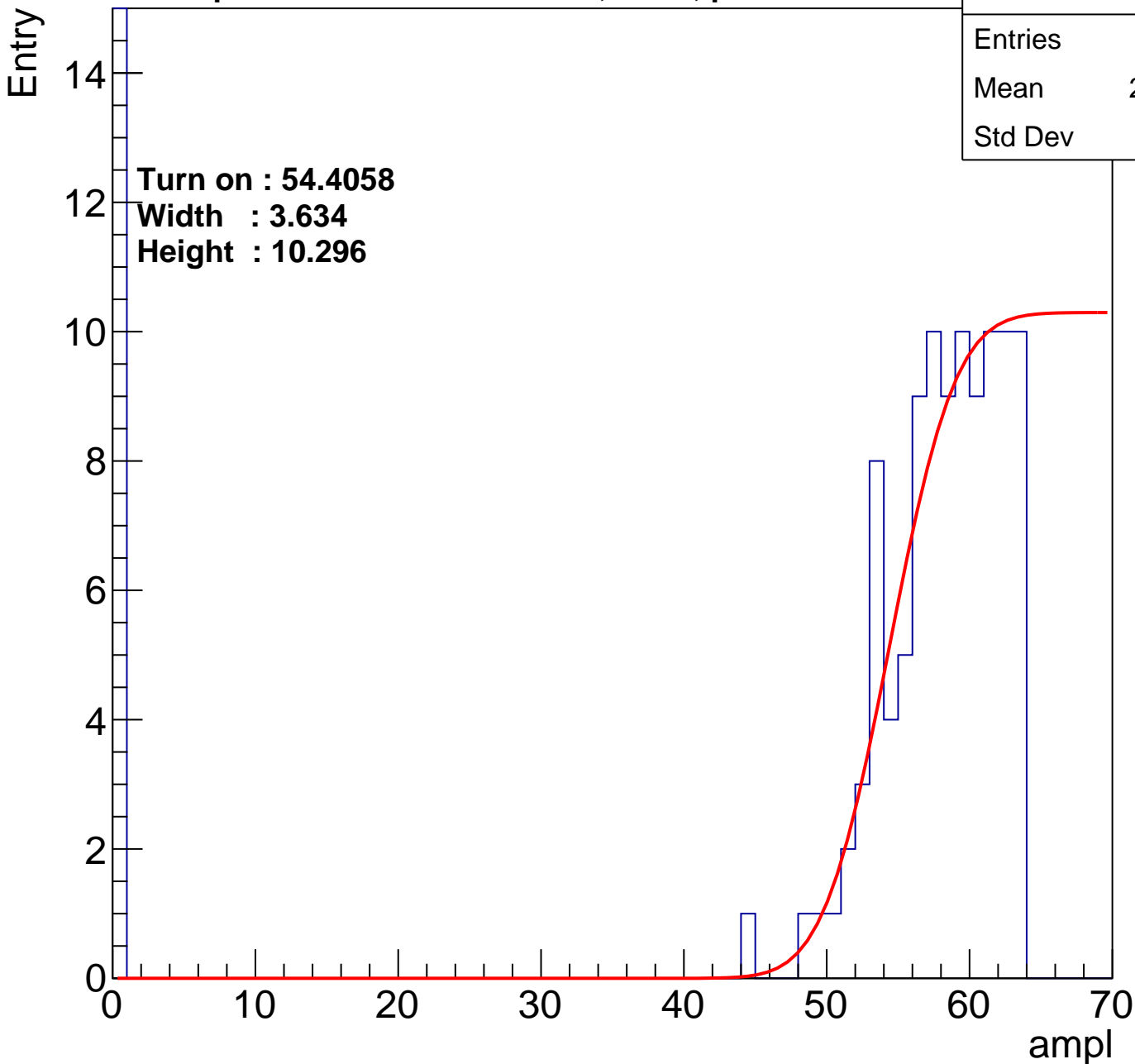
calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	29.75
Std Dev	29

Turn on : 54.4058

Width : 3.634

Height : 10.296



B1L104S, U23-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	31.82
Std Dev	29.35

Turn on : 55.6145

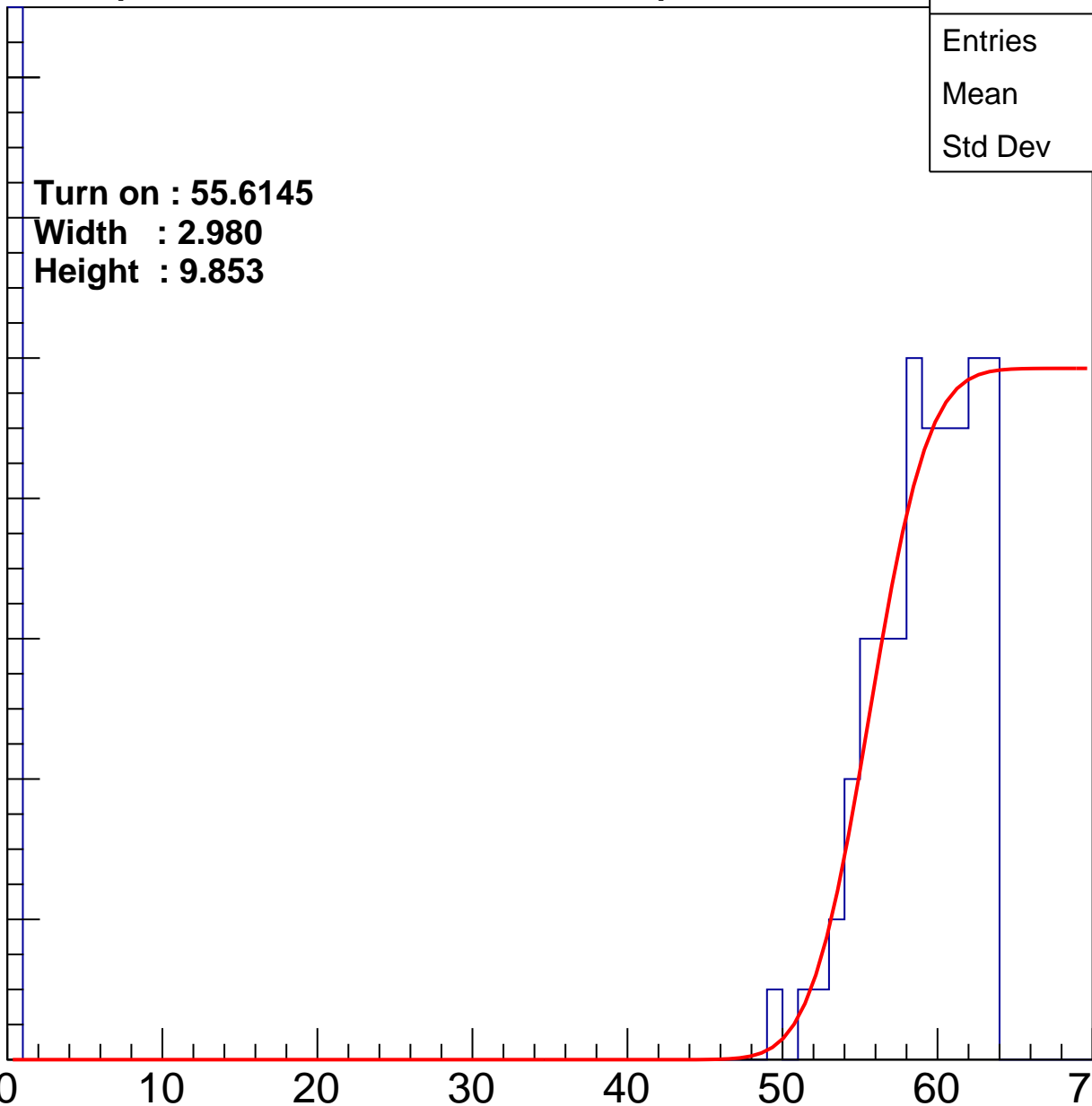
Width : 2.980

Height : 9.853

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	163
Mean	33.28
Std Dev	28.99

Turn on : 54.7712

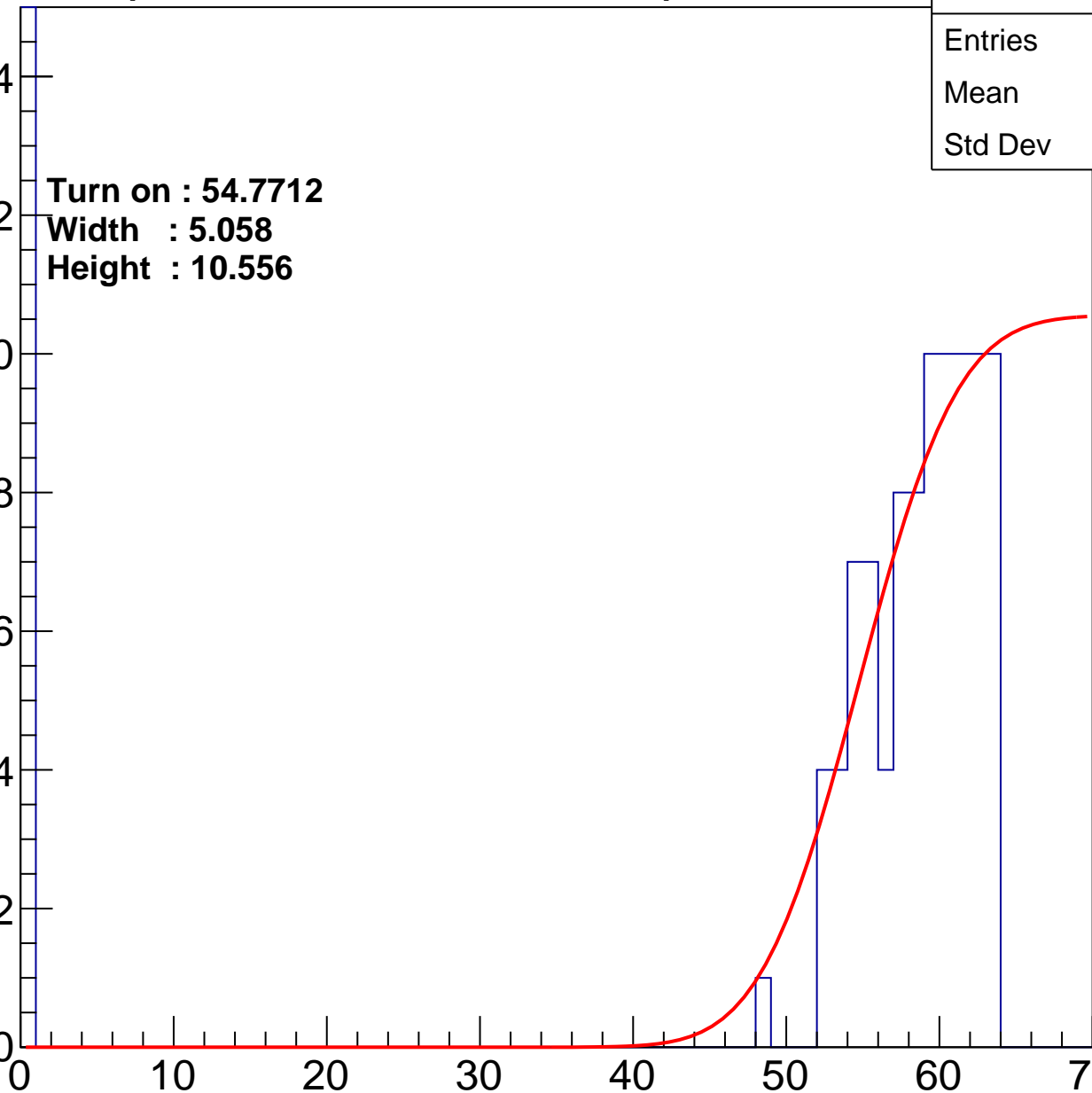
Width : 5.058

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	34.09
Std Dev	28.98

Turn on : 55.2550

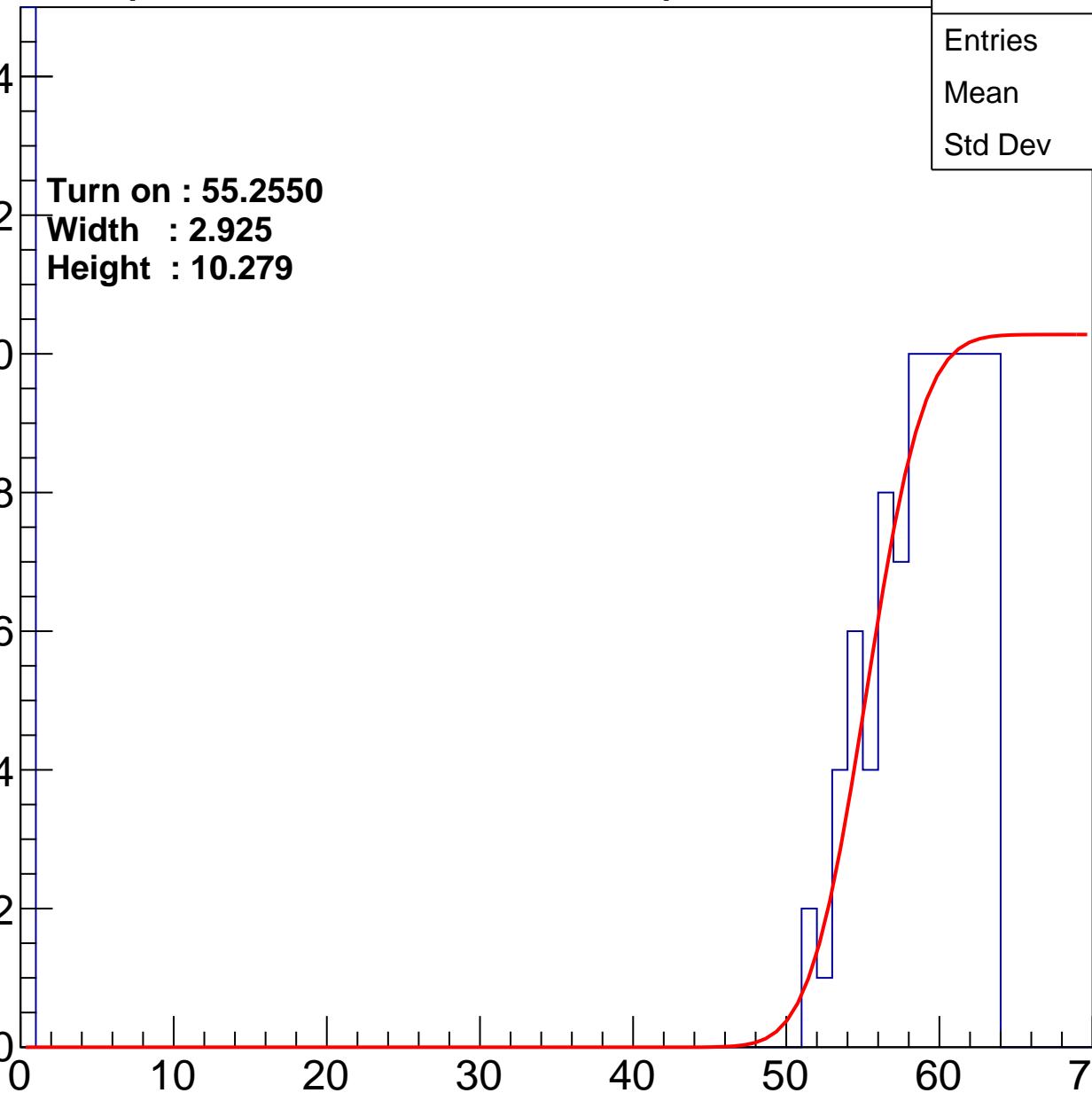
Width : 2.925

Height : 10.279

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	144
Mean	36.93
Std Dev	28.32

Turn on : 55.6884

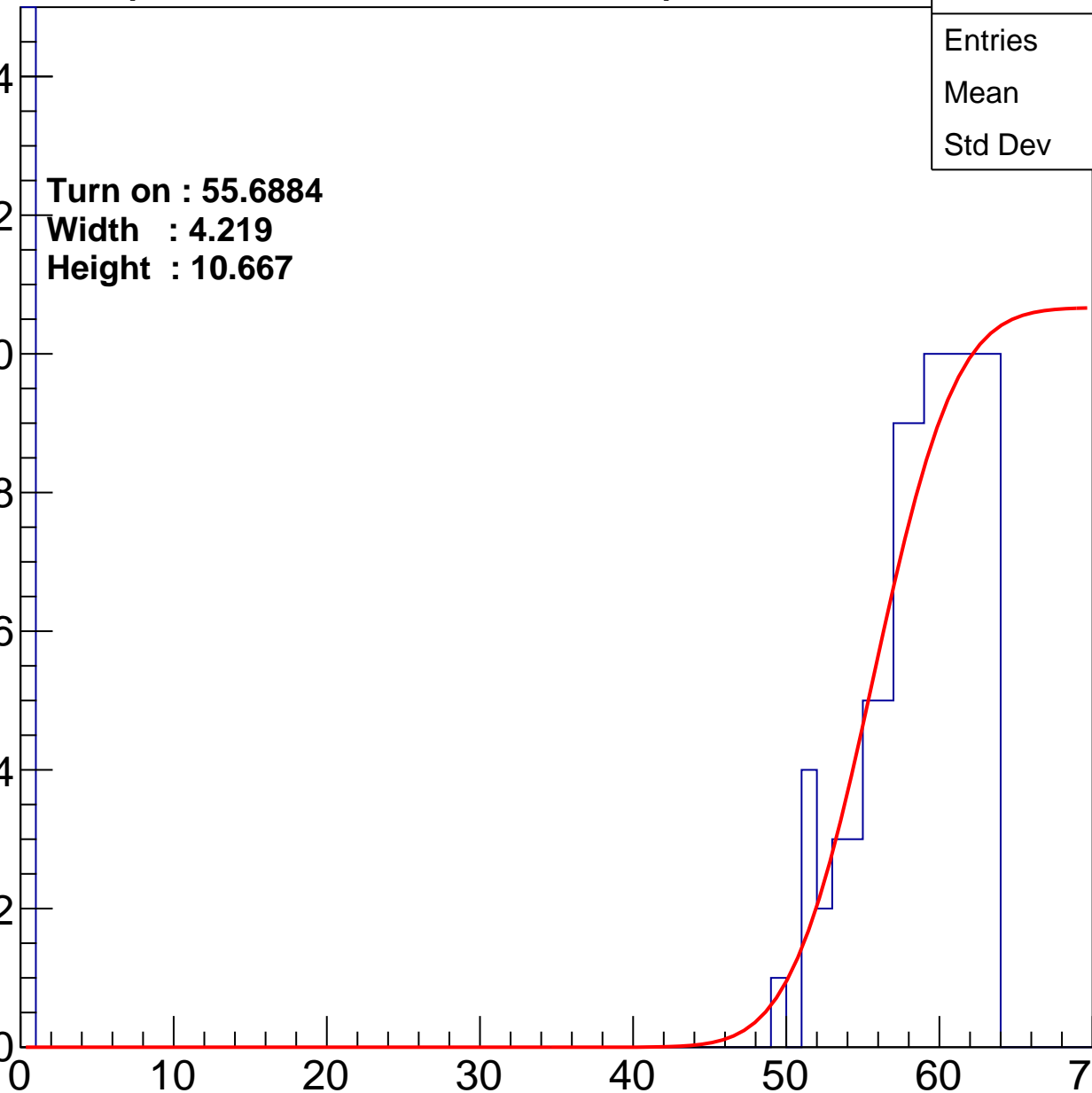
Width : 4.219

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	31.53
Std Dev	29.17

Turn on : 54.4700

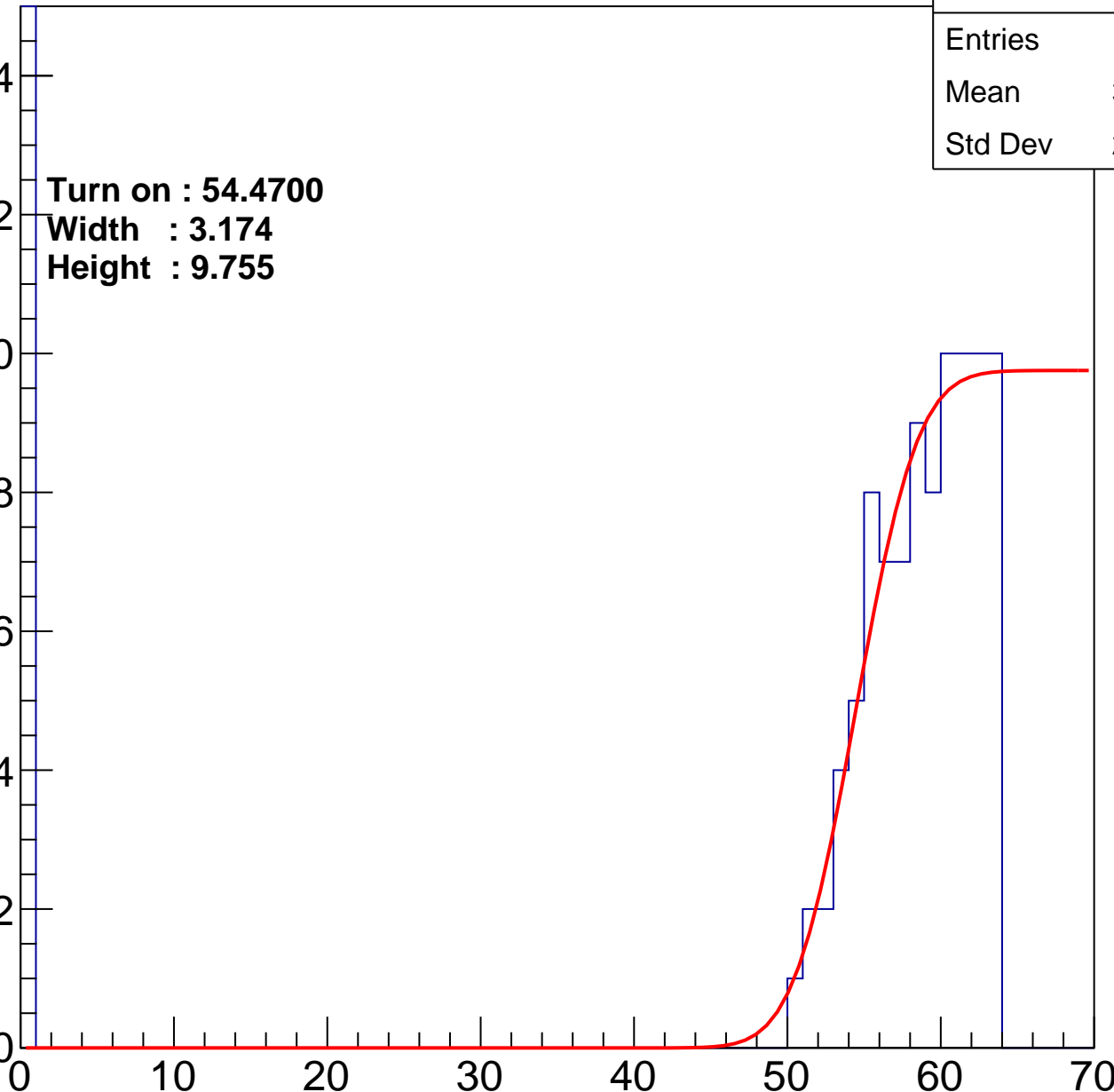
Width : 3.174

Height : 9.755

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	33.69
Std Dev	28.5

Turn on : 53.8365

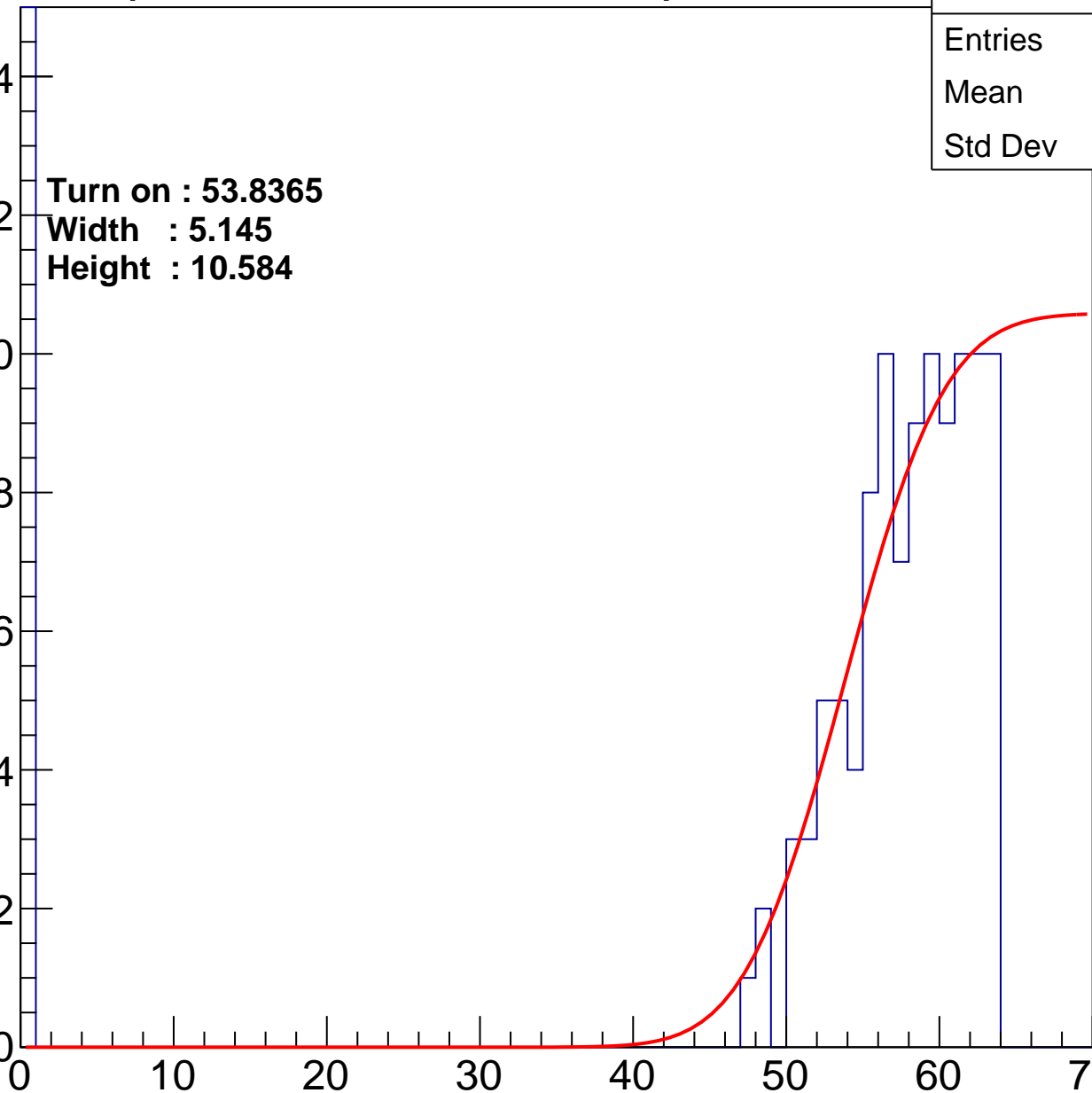
Width : 5.145

Height : 10.584

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch51

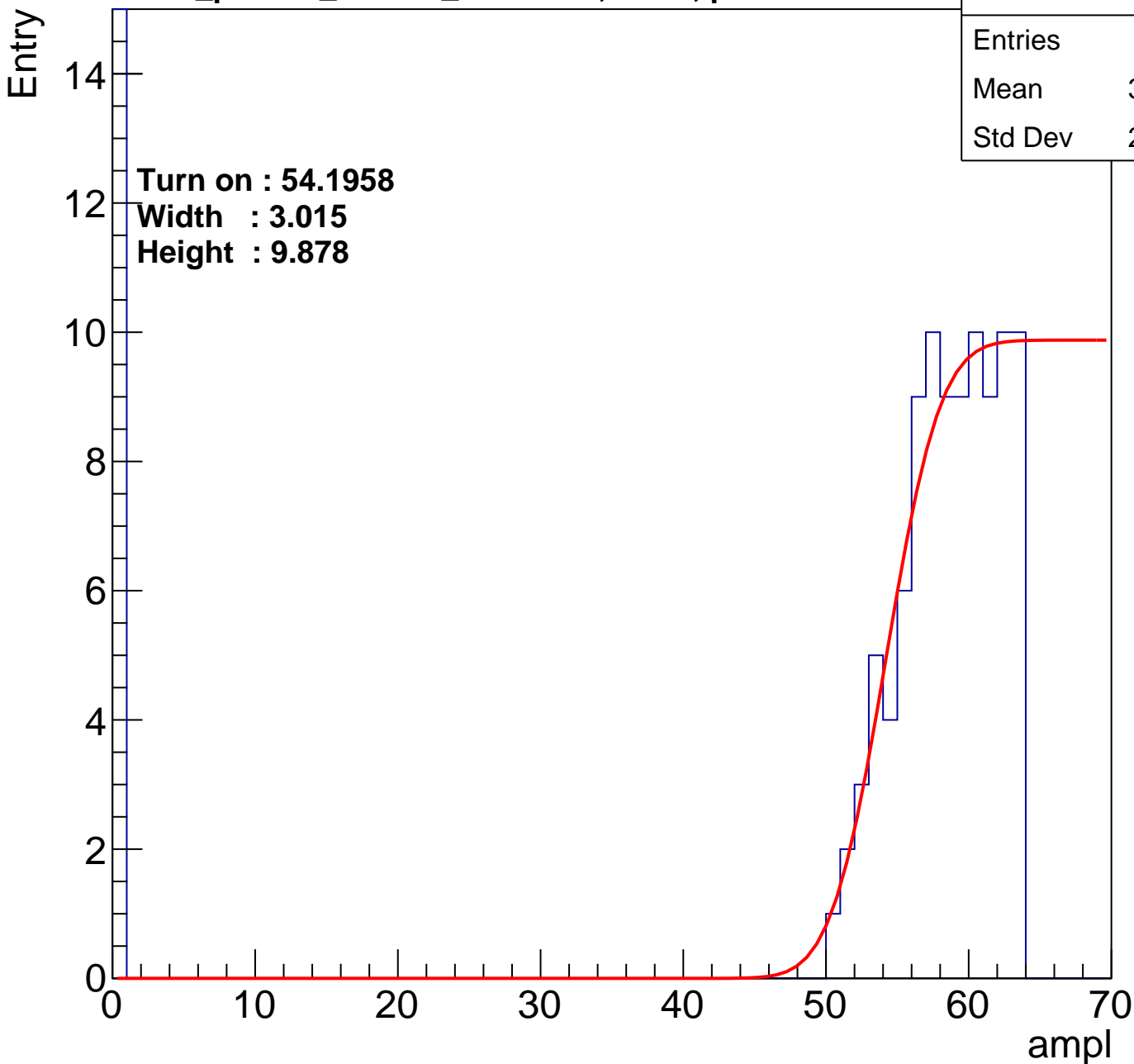
calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	31.02
Std Dev	29.14

Turn on : 54.1958

Width : 3.015

Height : 9.878



B1L104S, U23-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	34.87
Std Dev	28.25

Turn on : 53.4456

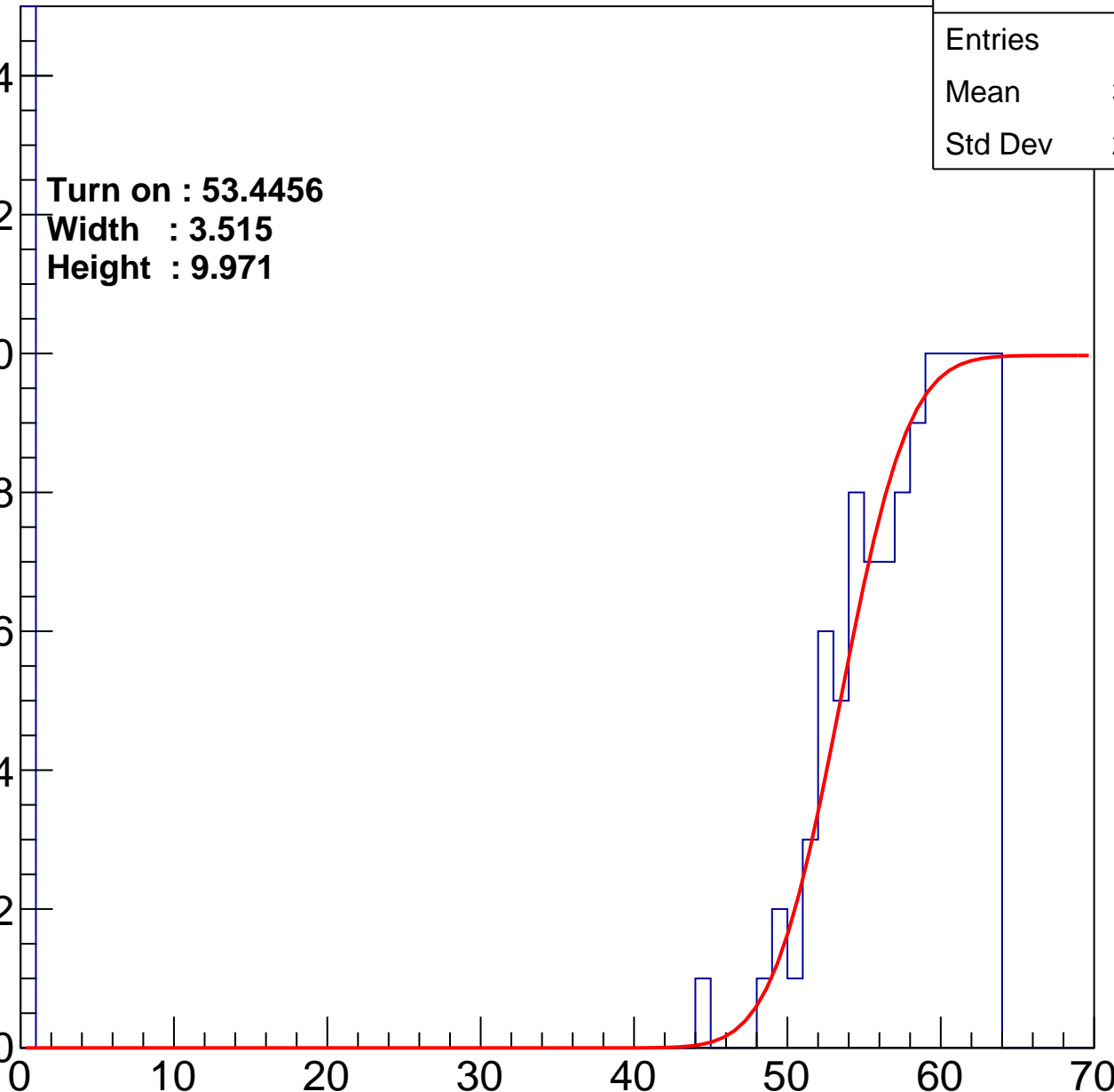
Width : 3.515

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	32.99
Std Dev	28.86

Turn on : 54.8522

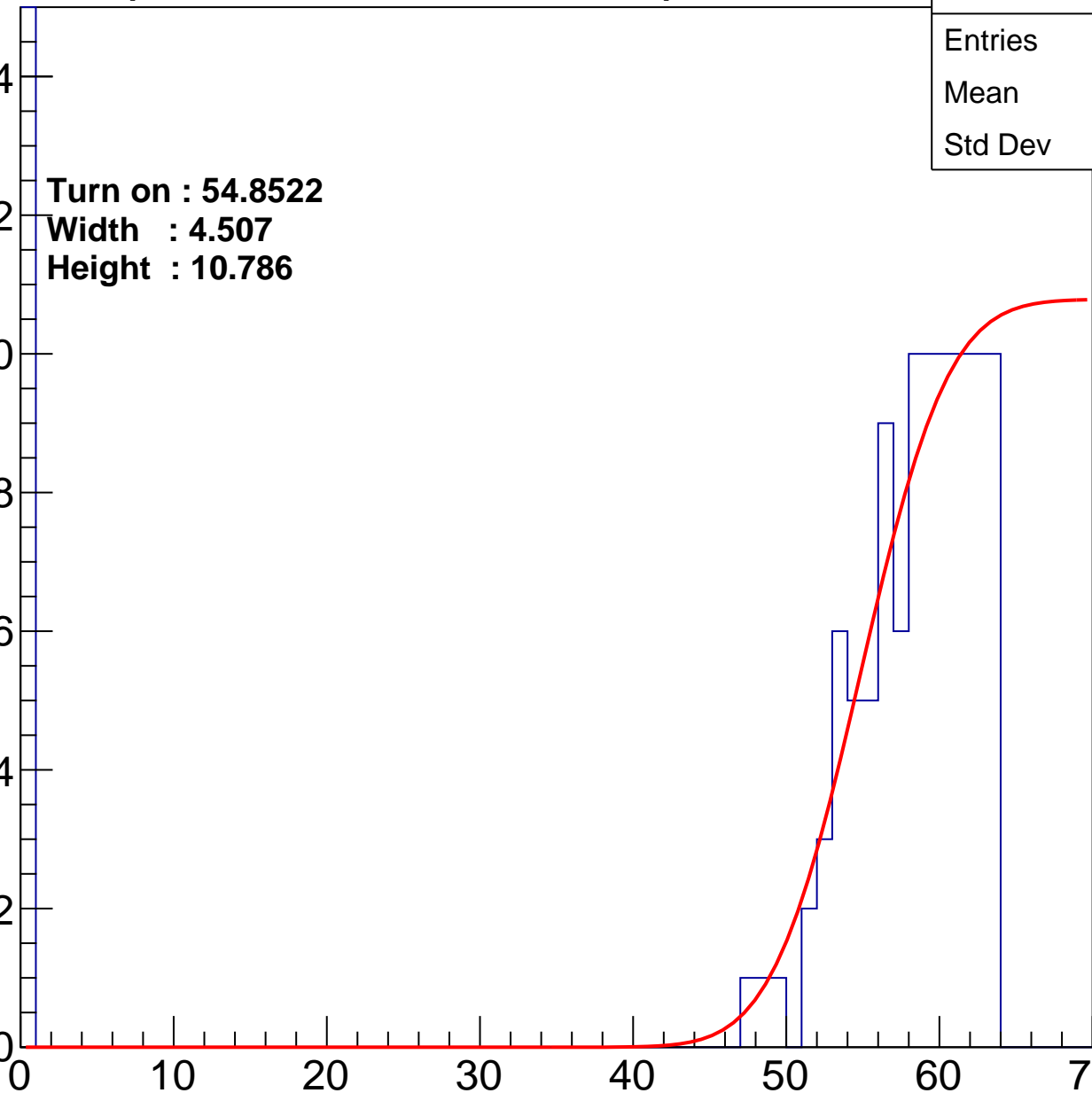
Width : 4.507

Height : 10.786

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	36.97
Std Dev	27.38

Turn on : 52.2706

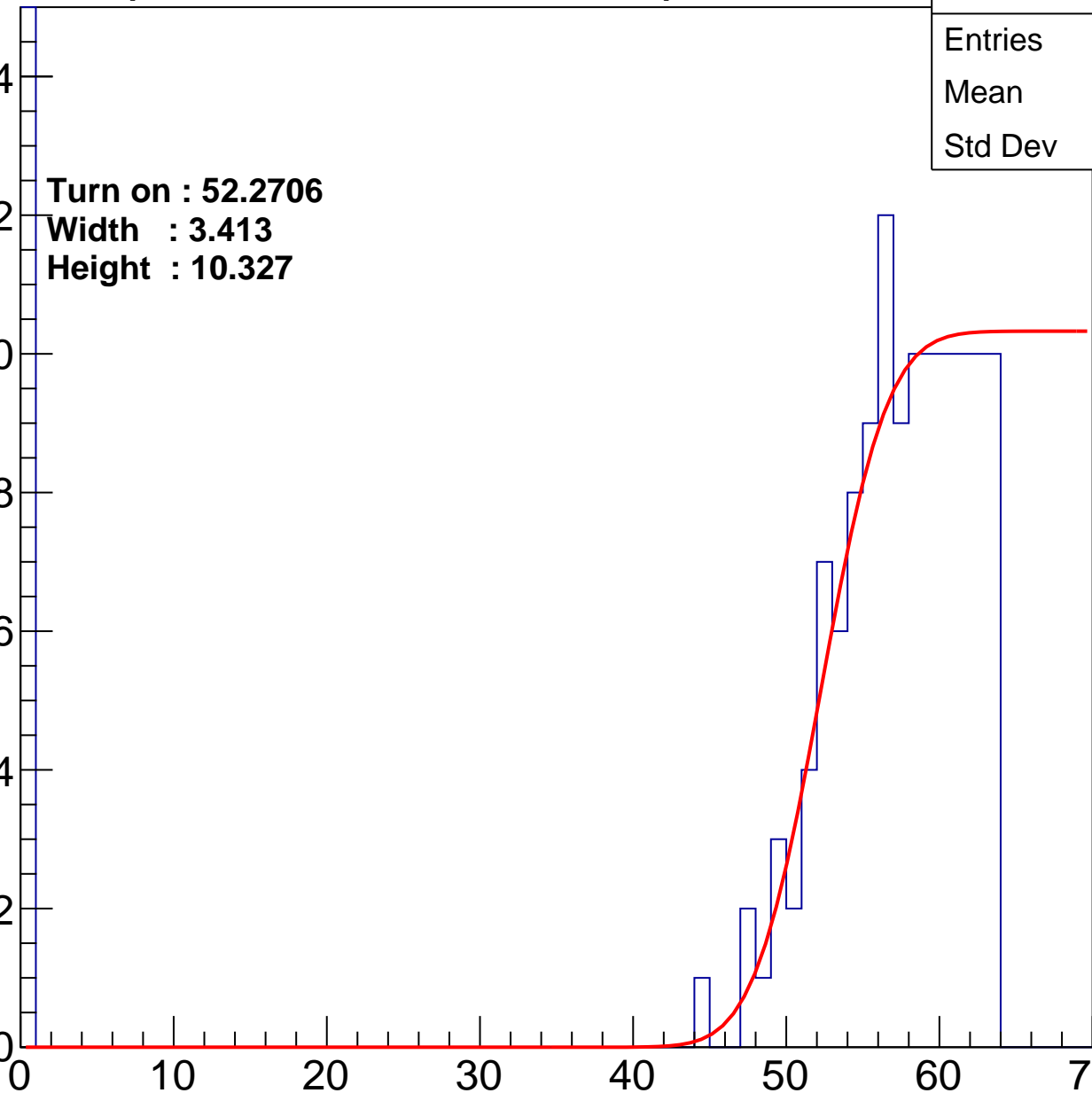
Width : 3.413

Height : 10.327

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	30.48
Std Dev	29.25

Turn on : 55.0159

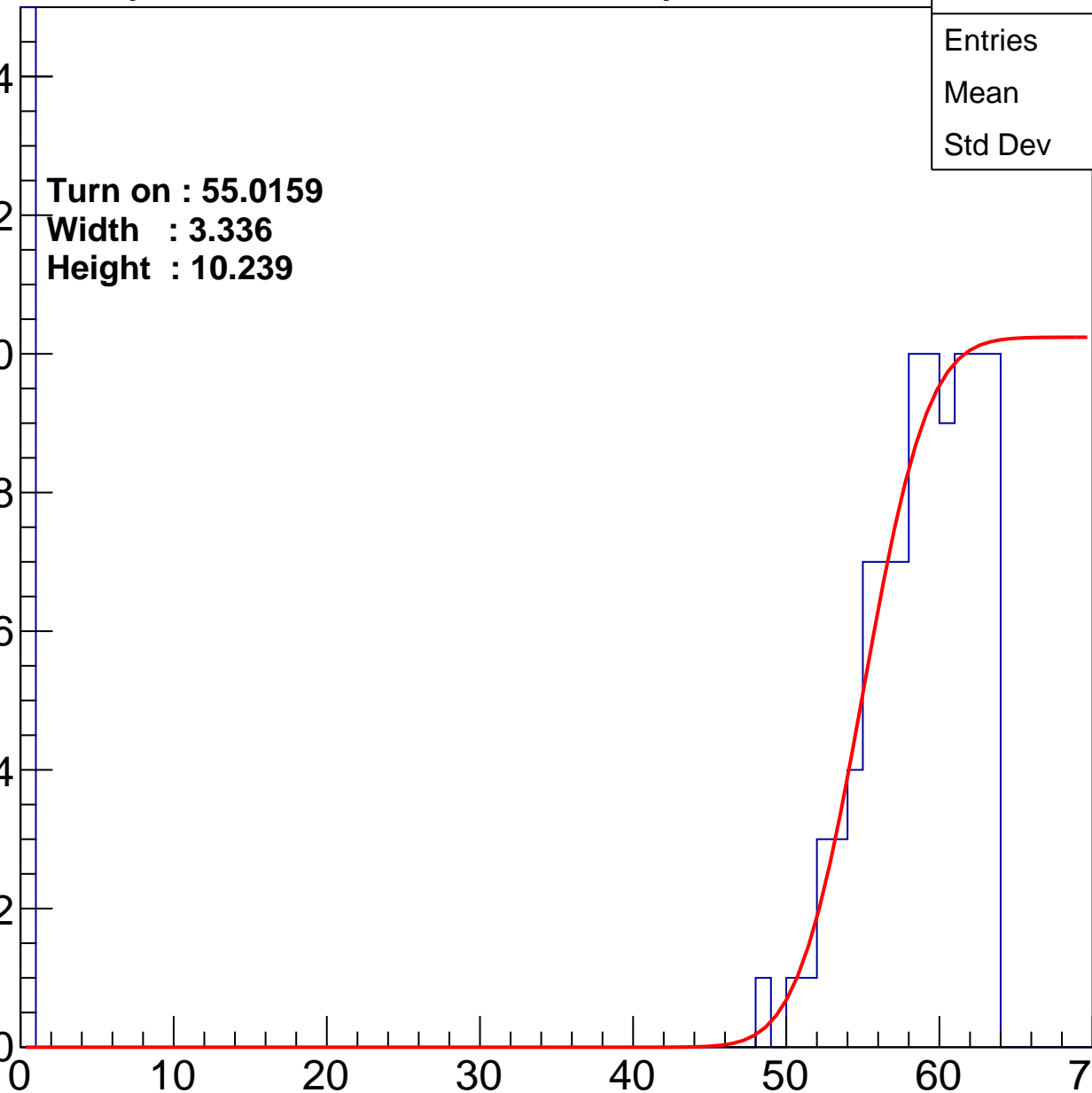
Width : 3.336

Height : 10.239

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	30.94
Std Dev	28.73

Turn on : 52.8782

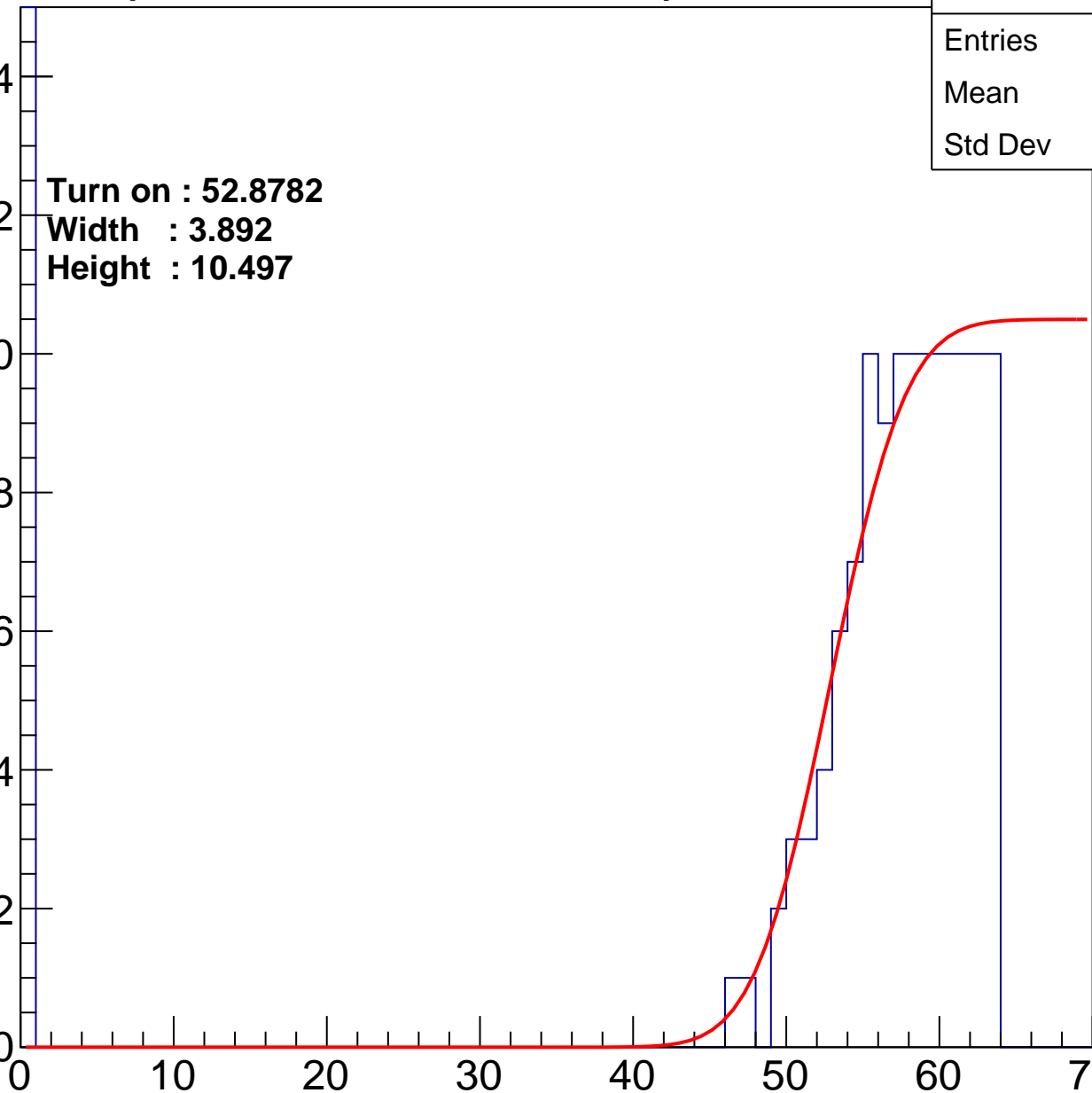
Width : 3.892

Height : 10.497

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	29.33
Std Dev	29.58

Turn on : 55.7304

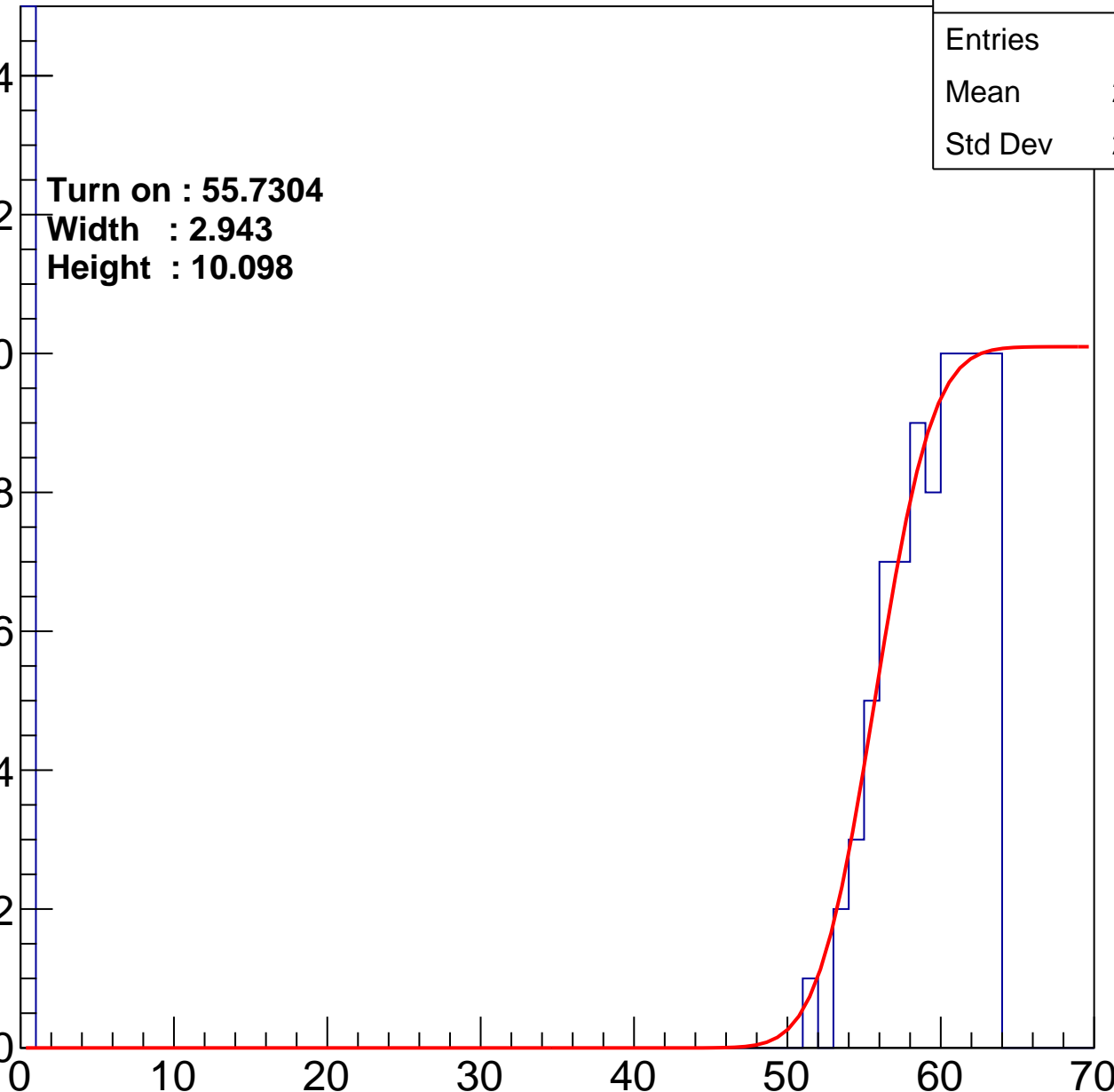
Width : 2.943

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch58

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	29.92
Std Dev	28.99

Turn on : 55.6562

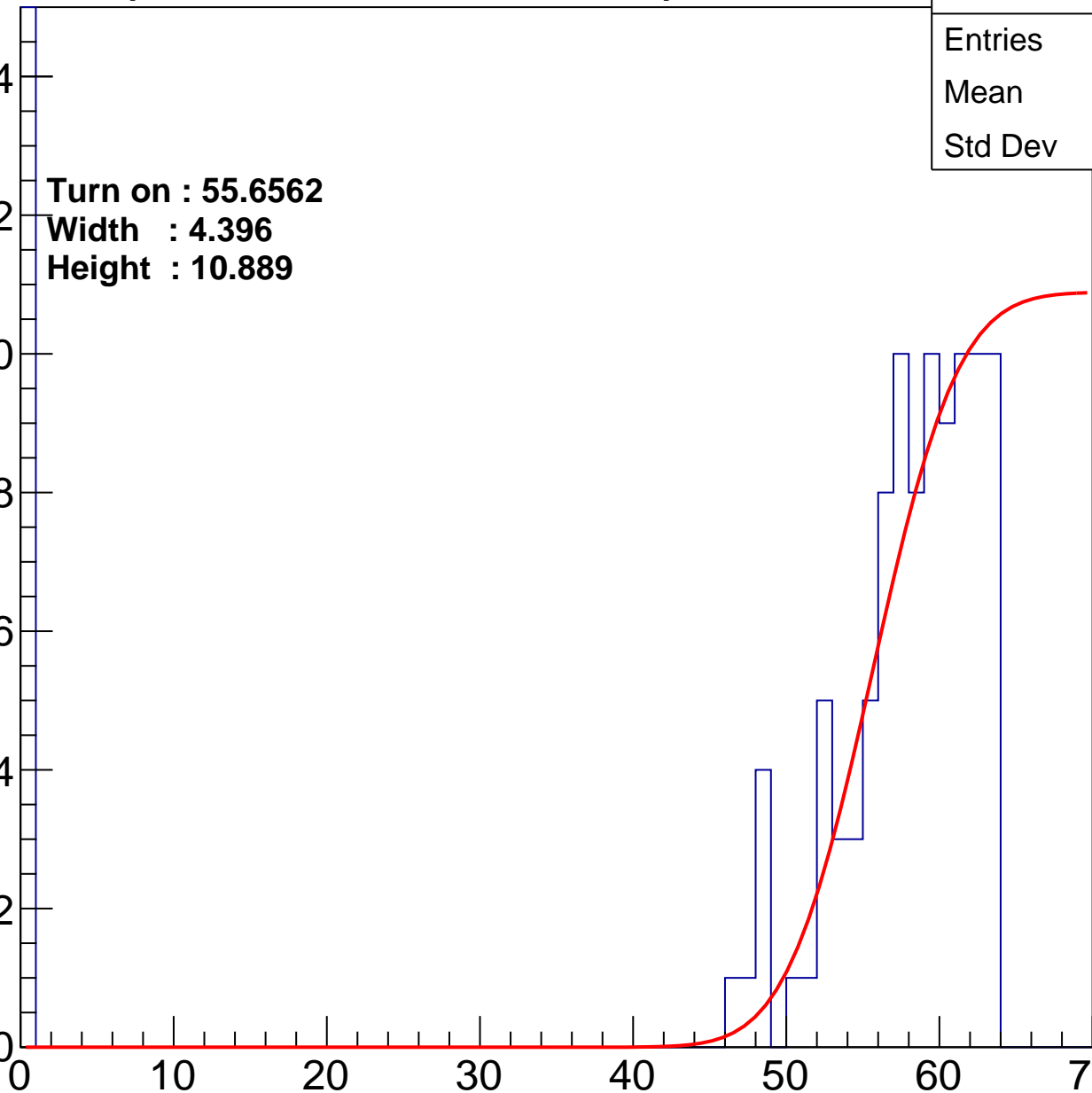
Width : 4.396

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	30.8
Std Dev	29.2

Turn on : 55.2845

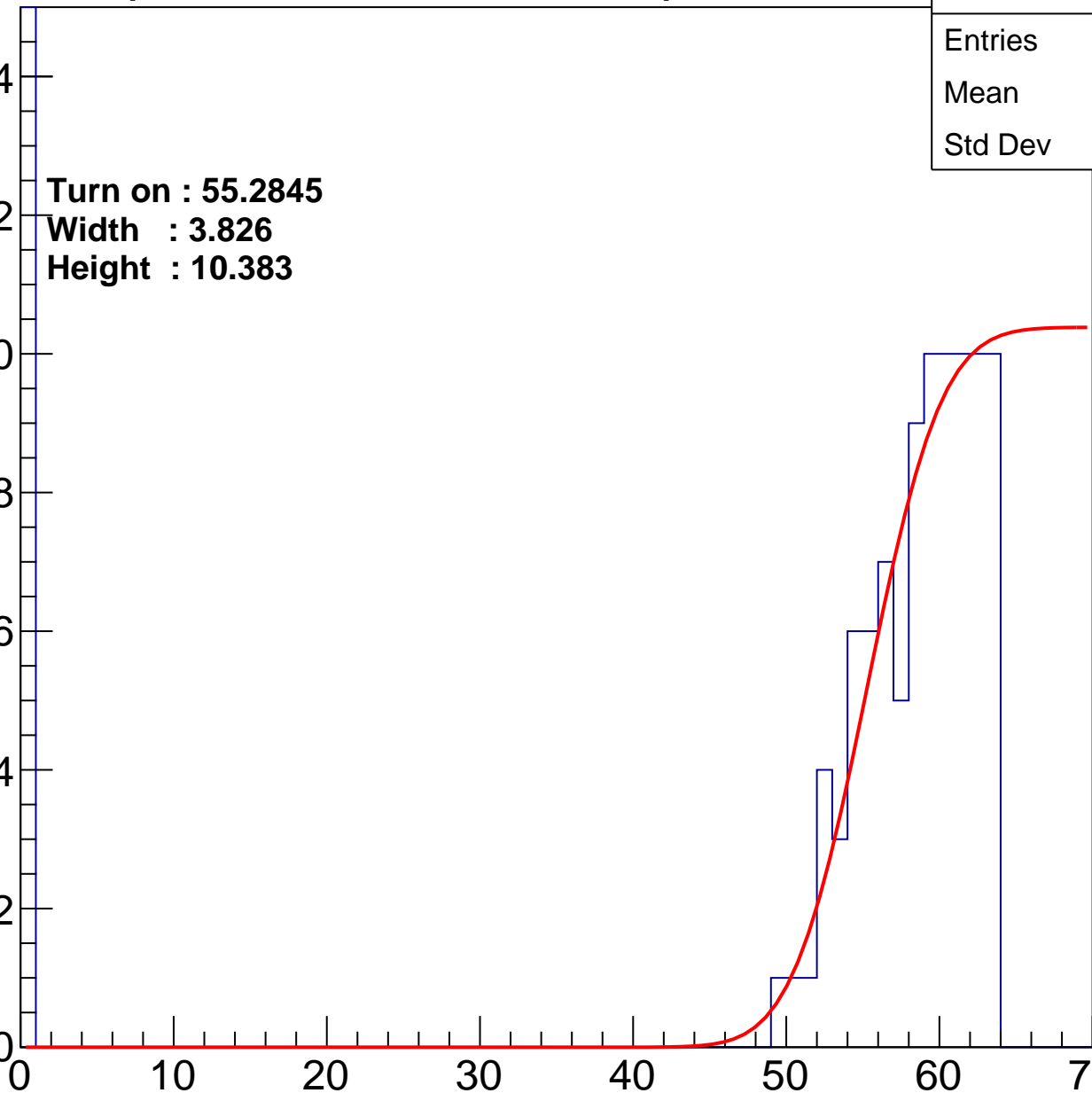
Width : 3.826

Height : 10.383

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	35.62
Std Dev	27.93

Turn on : 52.8218

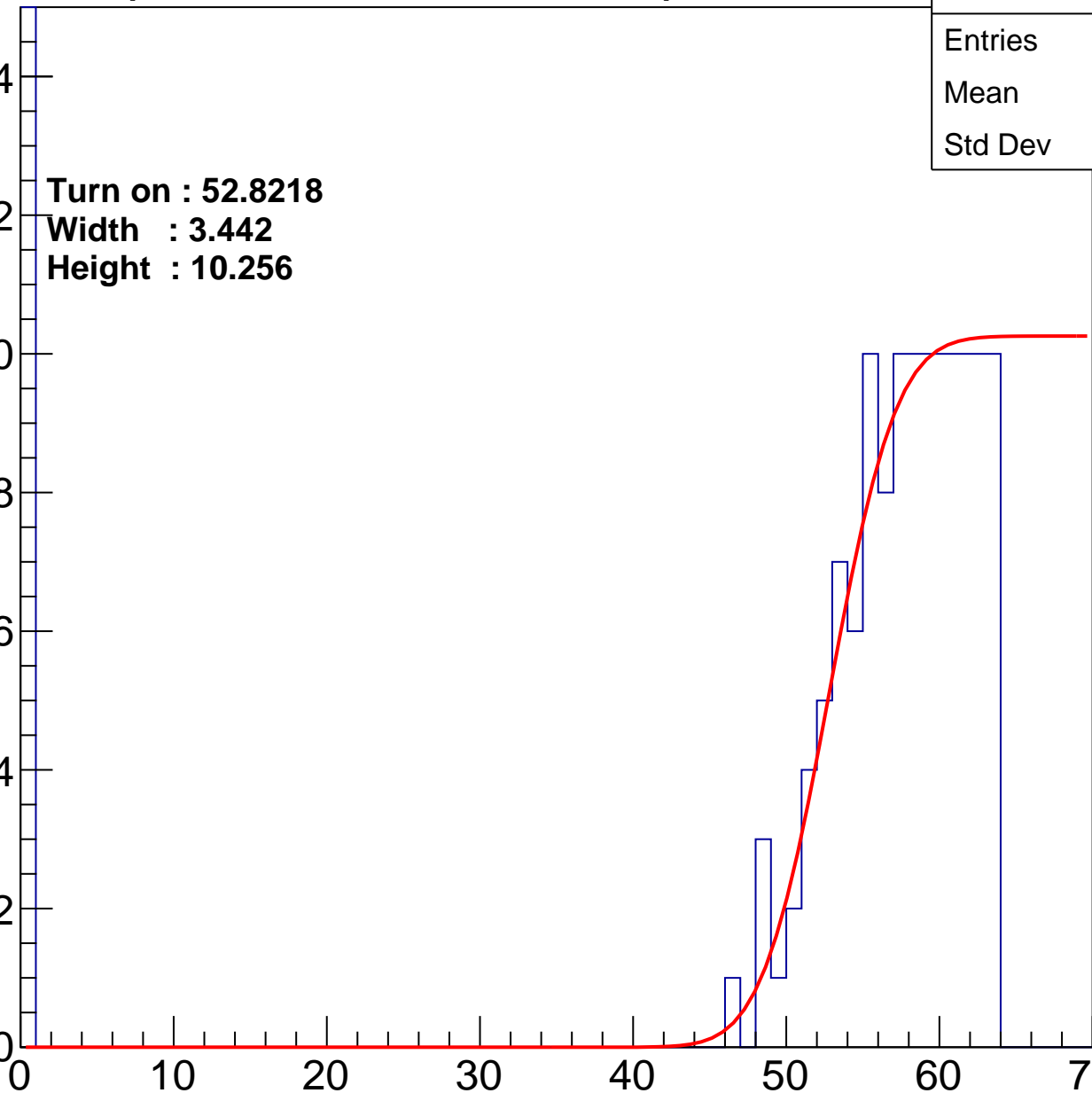
Width : 3.442

Height : 10.256

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	31.97
Std Dev	29.34

Turn on : 55.9497

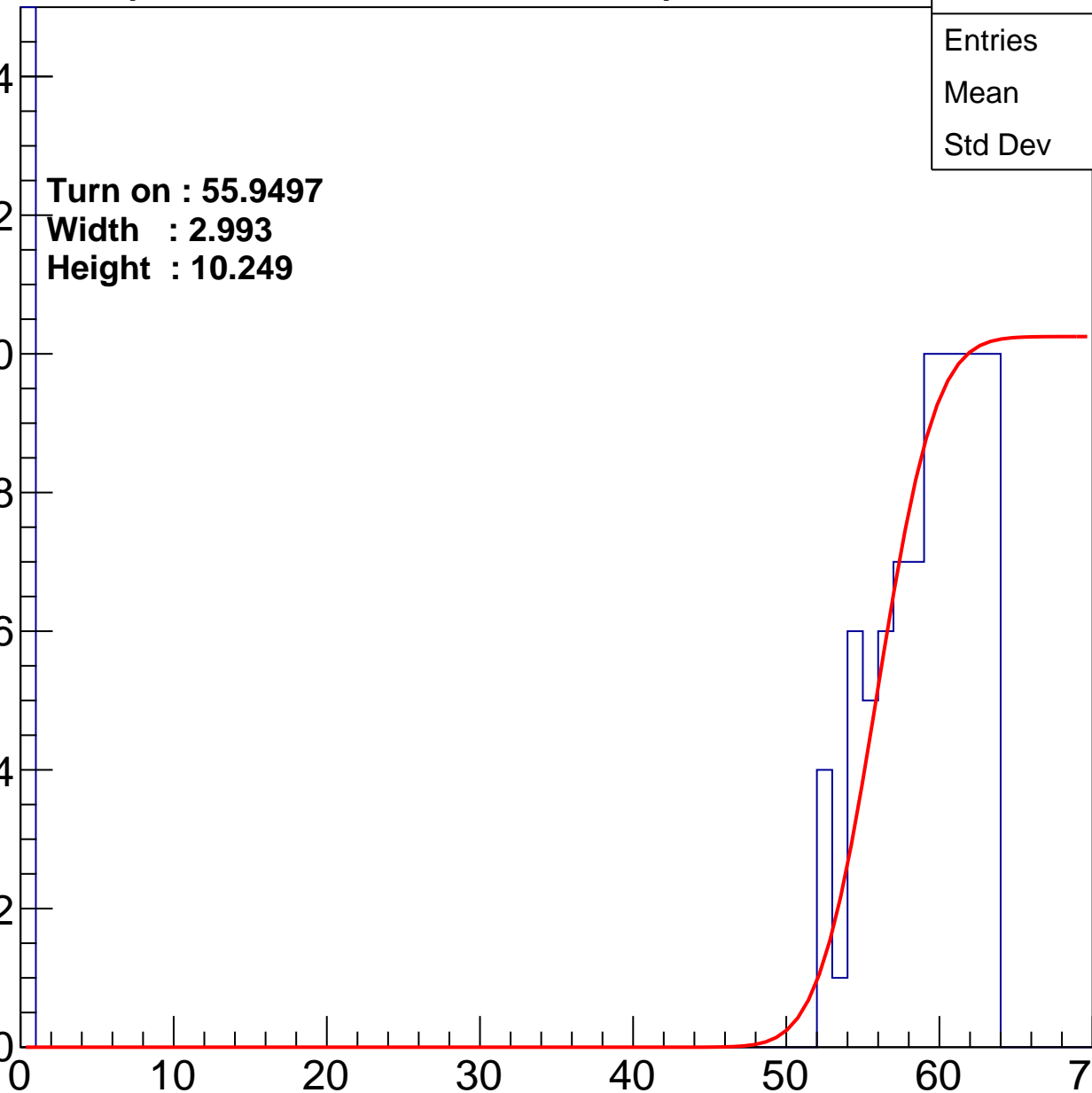
Width : 2.993

Height : 10.249

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	31.78
Std Dev	29.22

Turn on : 55.9817

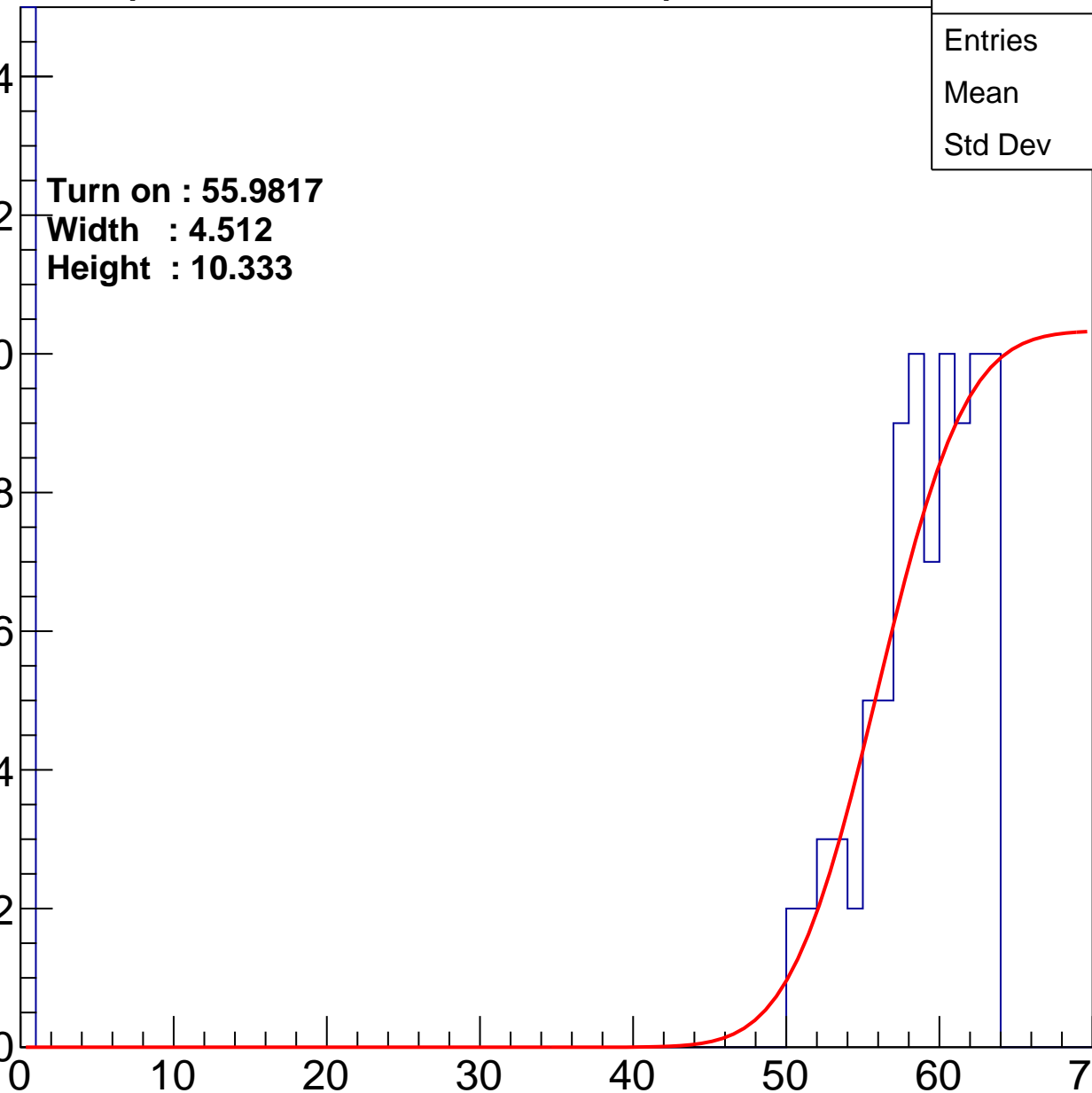
Width : 4.512

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	33.56
Std Dev	28.76

Turn on : 55.5070

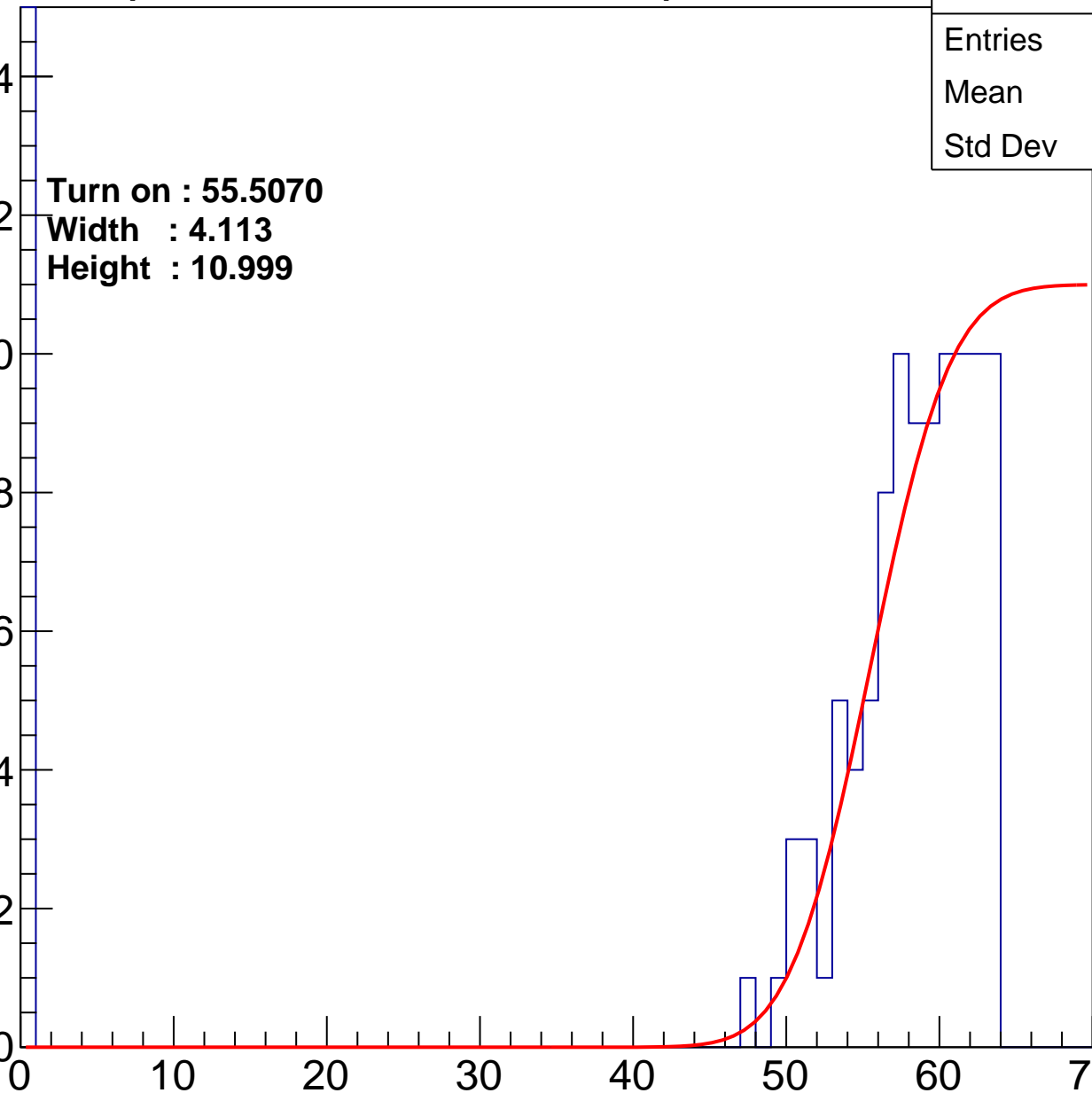
Width : 4.113

Height : 10.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch64

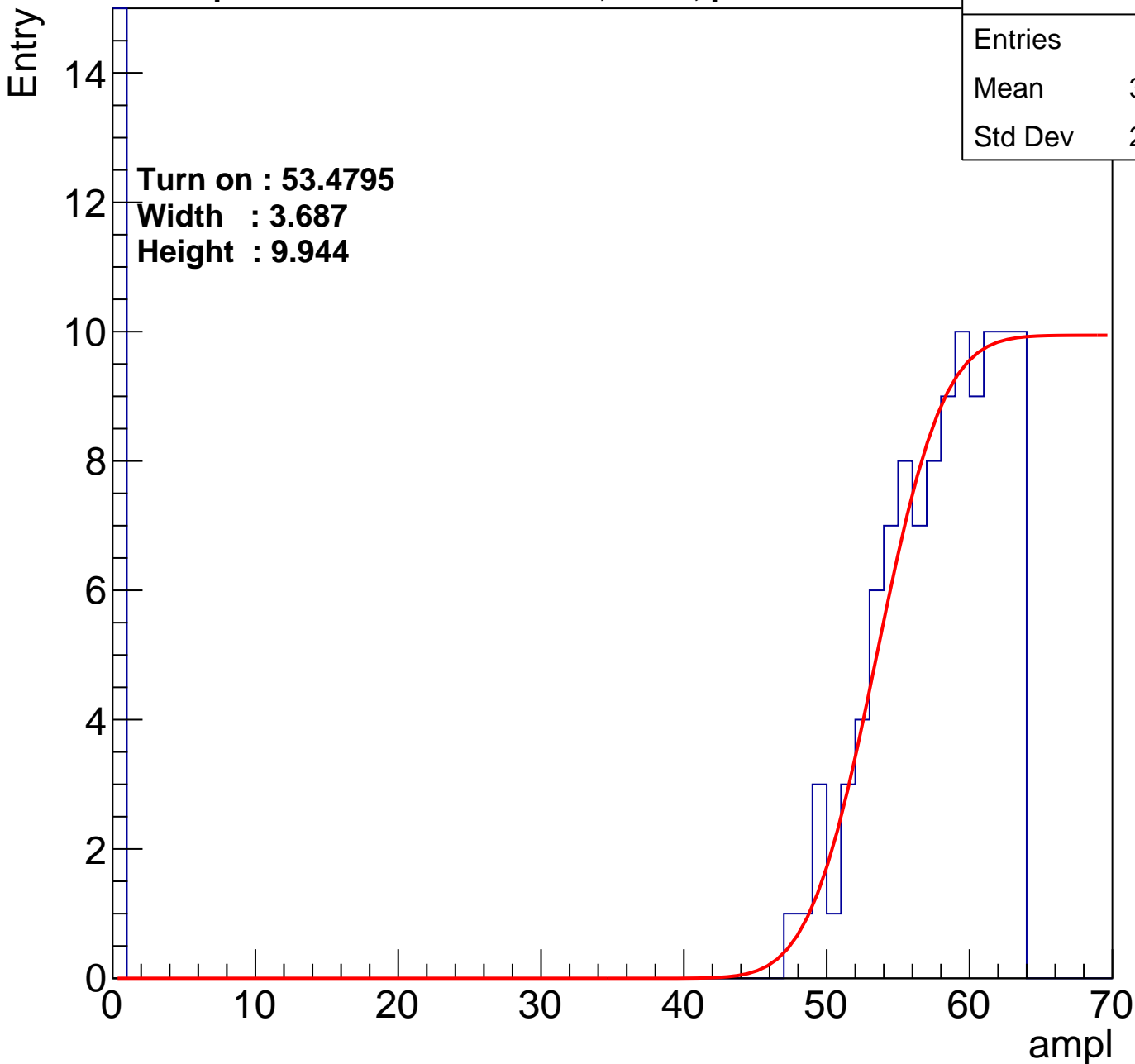
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	34.16
Std Dev	28.38

Turn on : 53.4795

Width : 3.687

Height : 9.944



B1L104S, U23-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	32.85
Std Dev	28.96

Turn on : 55.4453

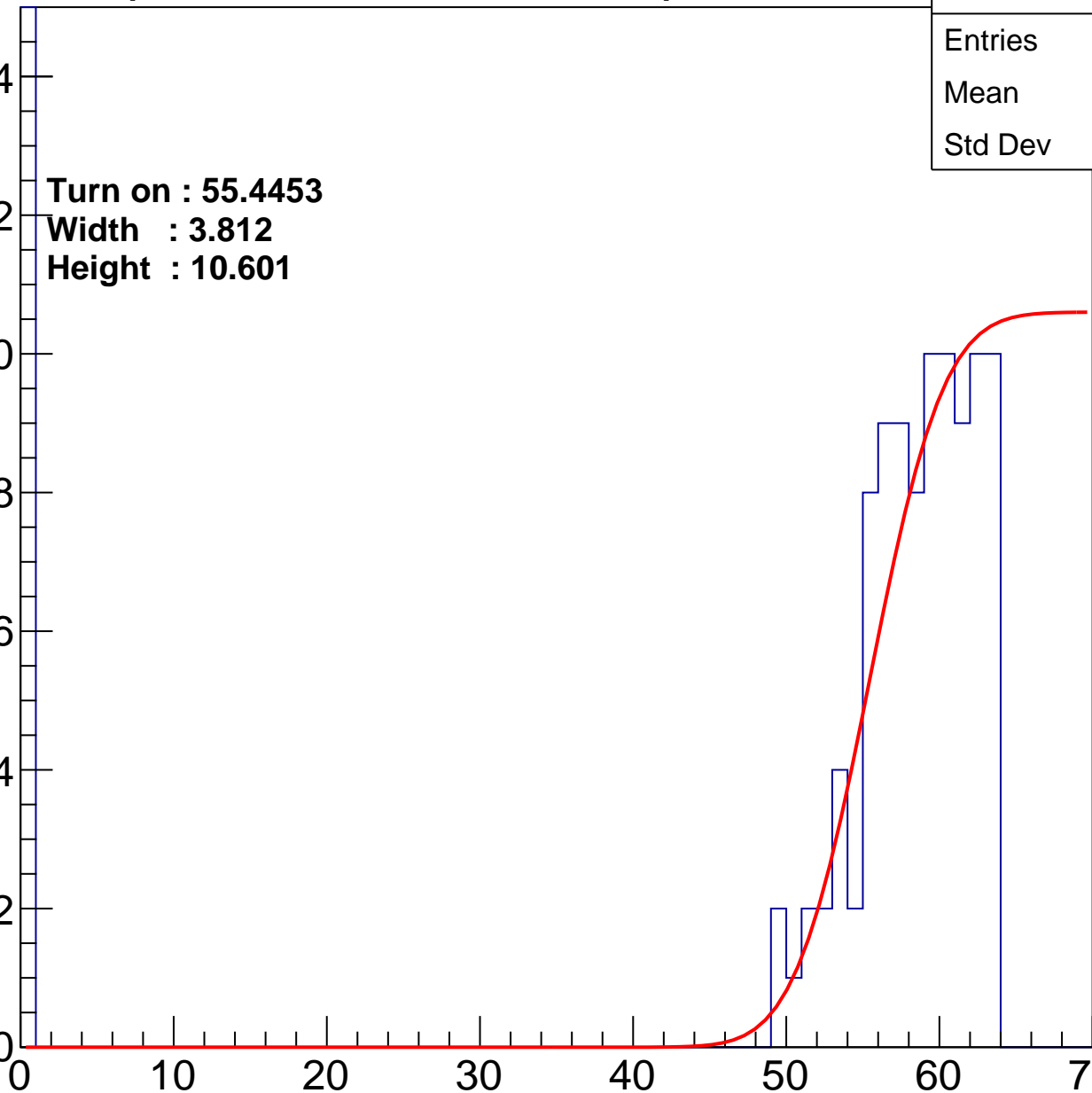
Width : 3.812

Height : 10.601

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	32.63
Std Dev	28.5

Turn on : 52.7753

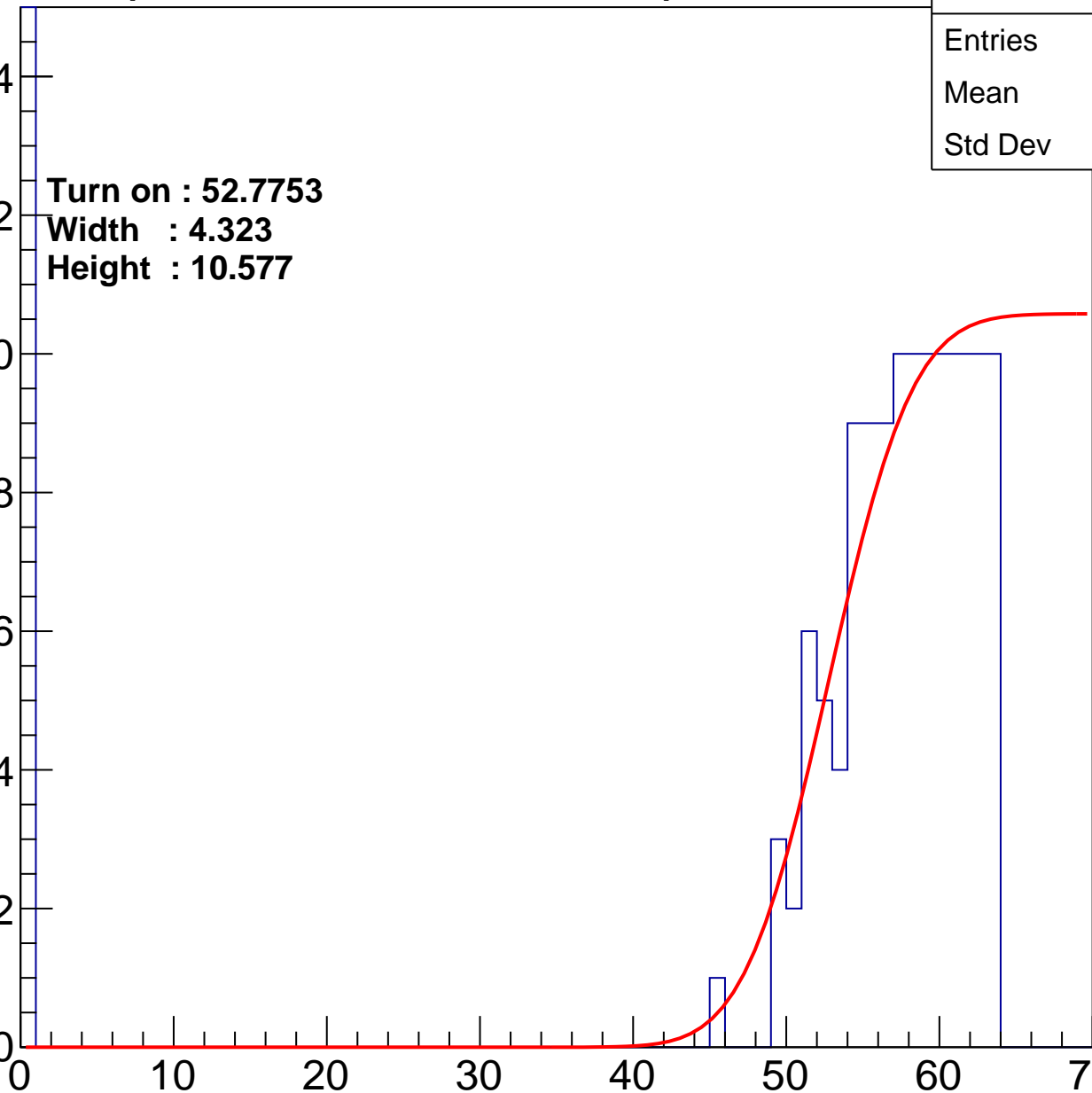
Width : 4.323

Height : 10.577

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	31.94
Std Dev	28.81

Turn on : 53.8446

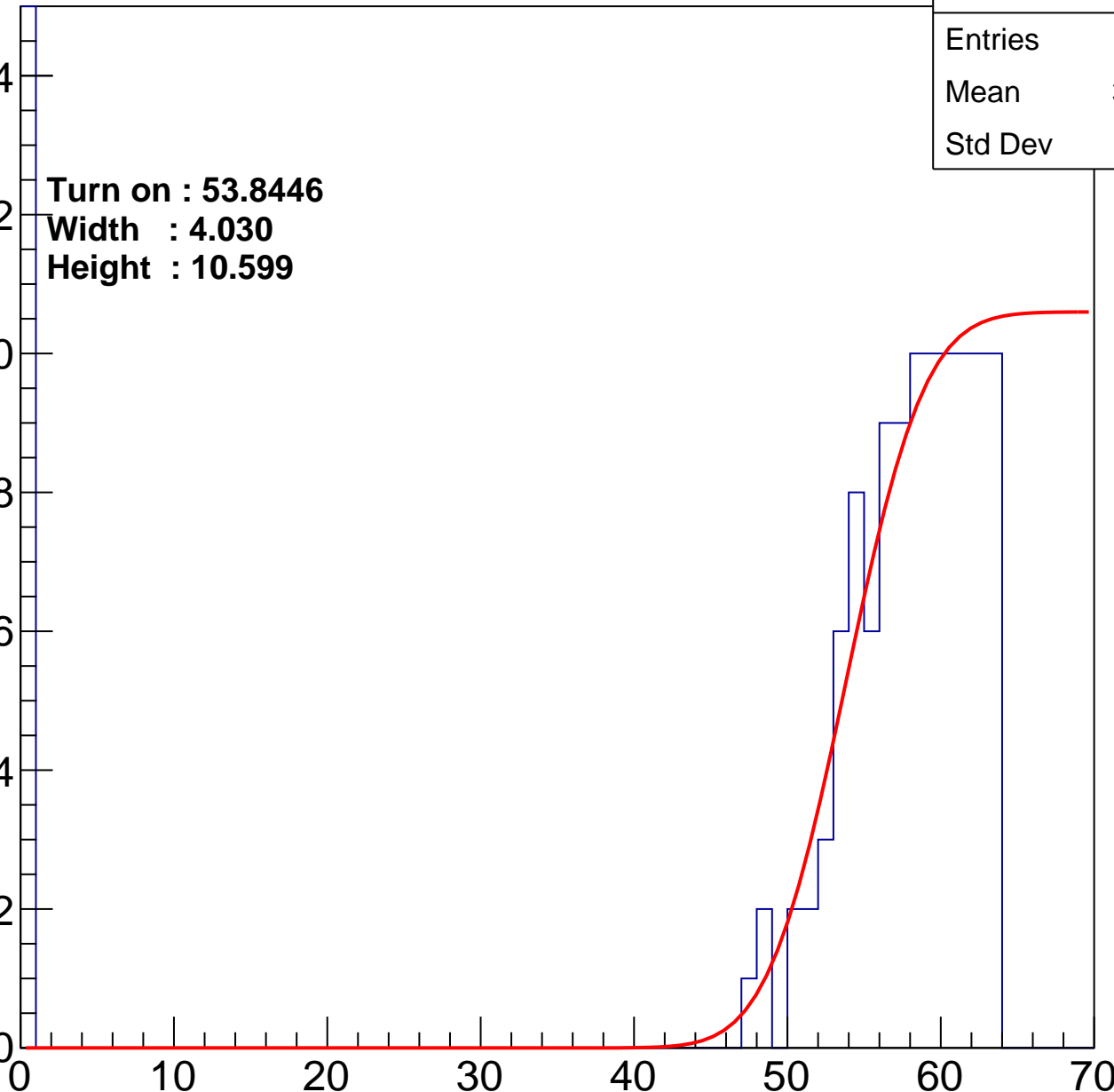
Width : 4.030

Height : 10.599

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	32.21
Std Dev	28.98

Turn on : 54.9157

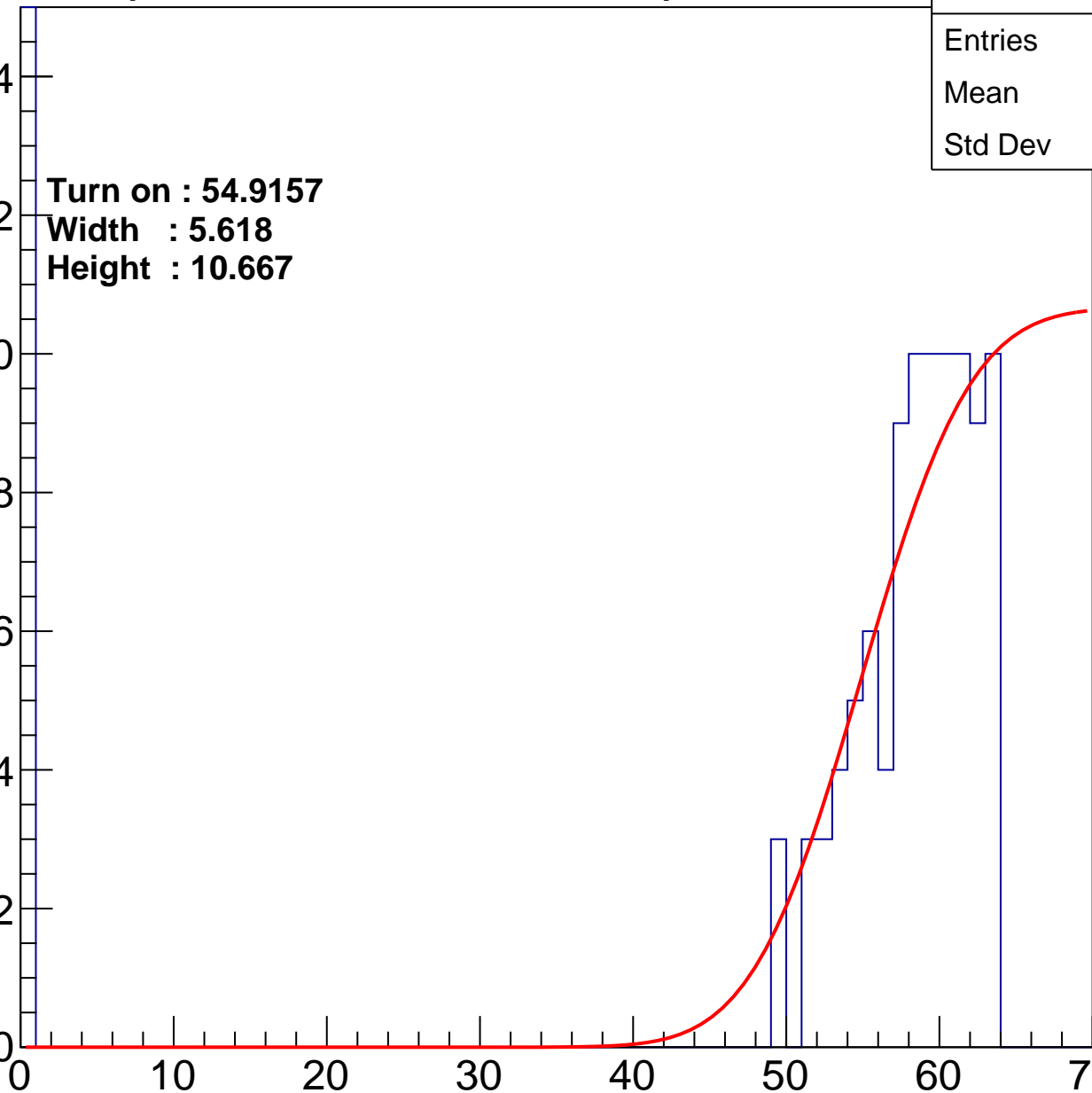
Width : 5.618

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch69

calib_packv5_033123_0516.root, FC#4, port A1

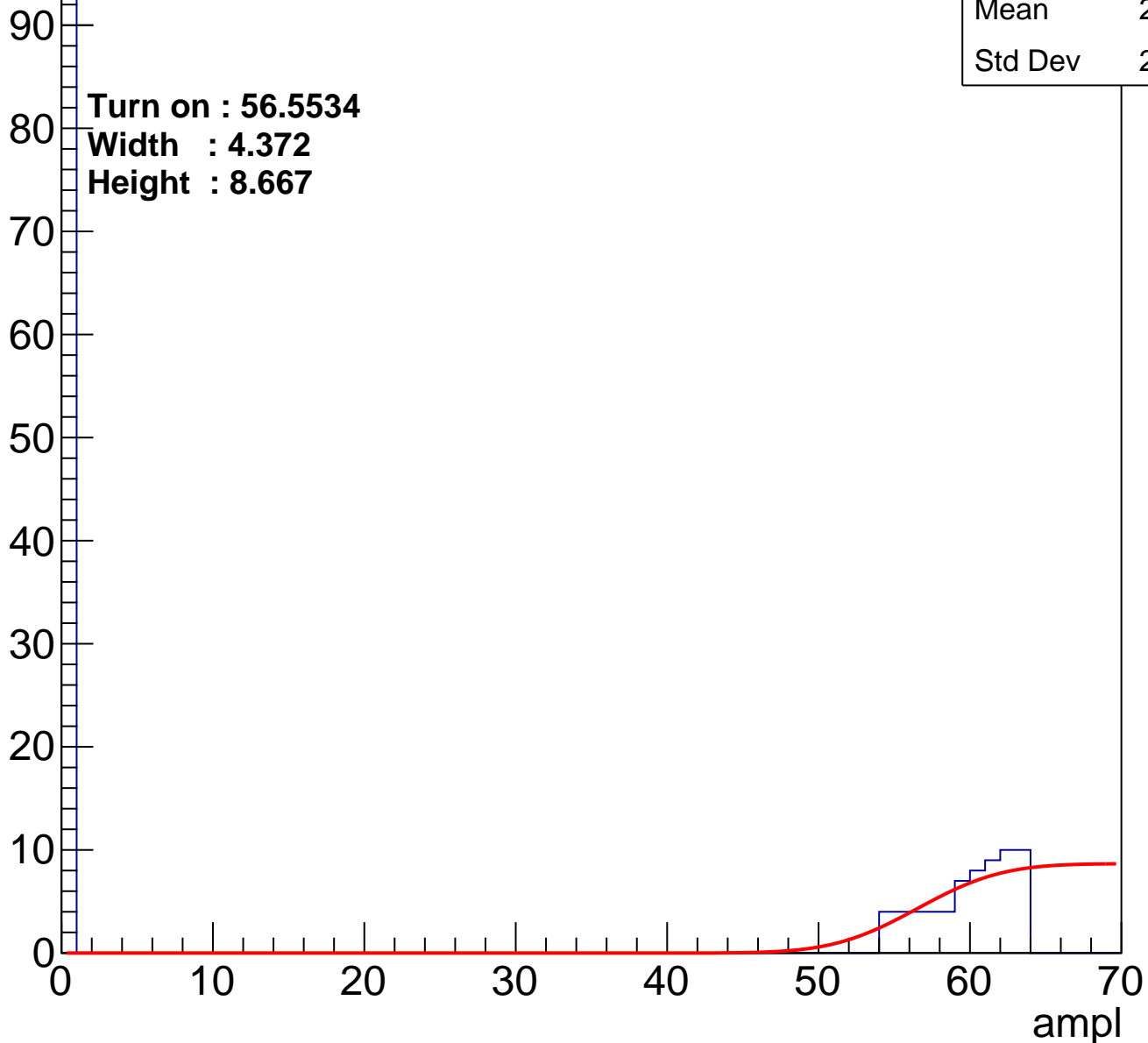
Entries	159
Mean	23.97
Std Dev	29.26

Turn on : 56.5534

Width : 4.372

Height : 8.667

Entry



B1L104S, U23-ch70

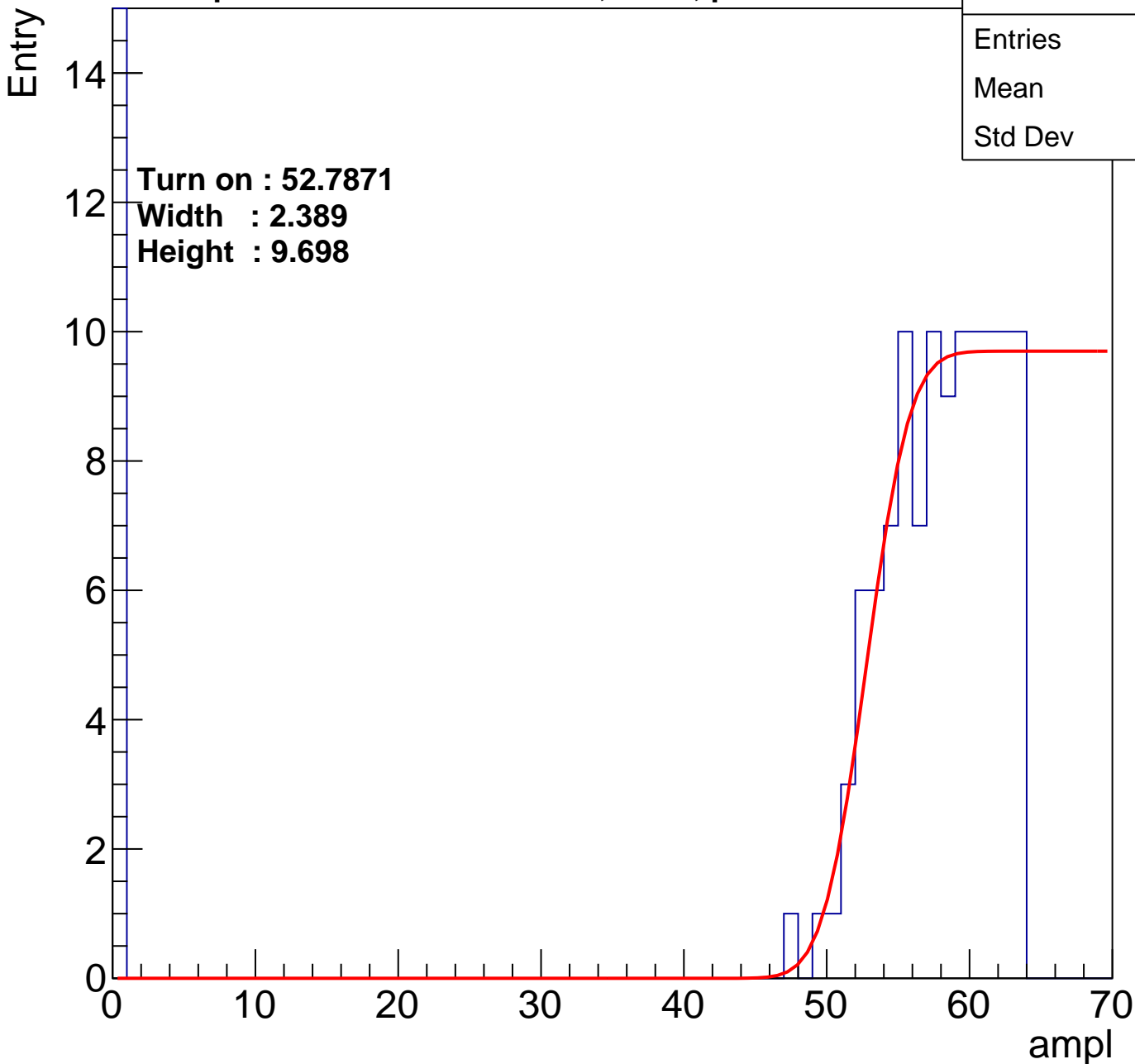
calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	35.7
Std Dev	28.1

Turn on : 52.7871

Width : 2.389

Height : 9.698



B1L104S, U23-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	34.54
Std Dev	28.62

Turn on : 55.1612

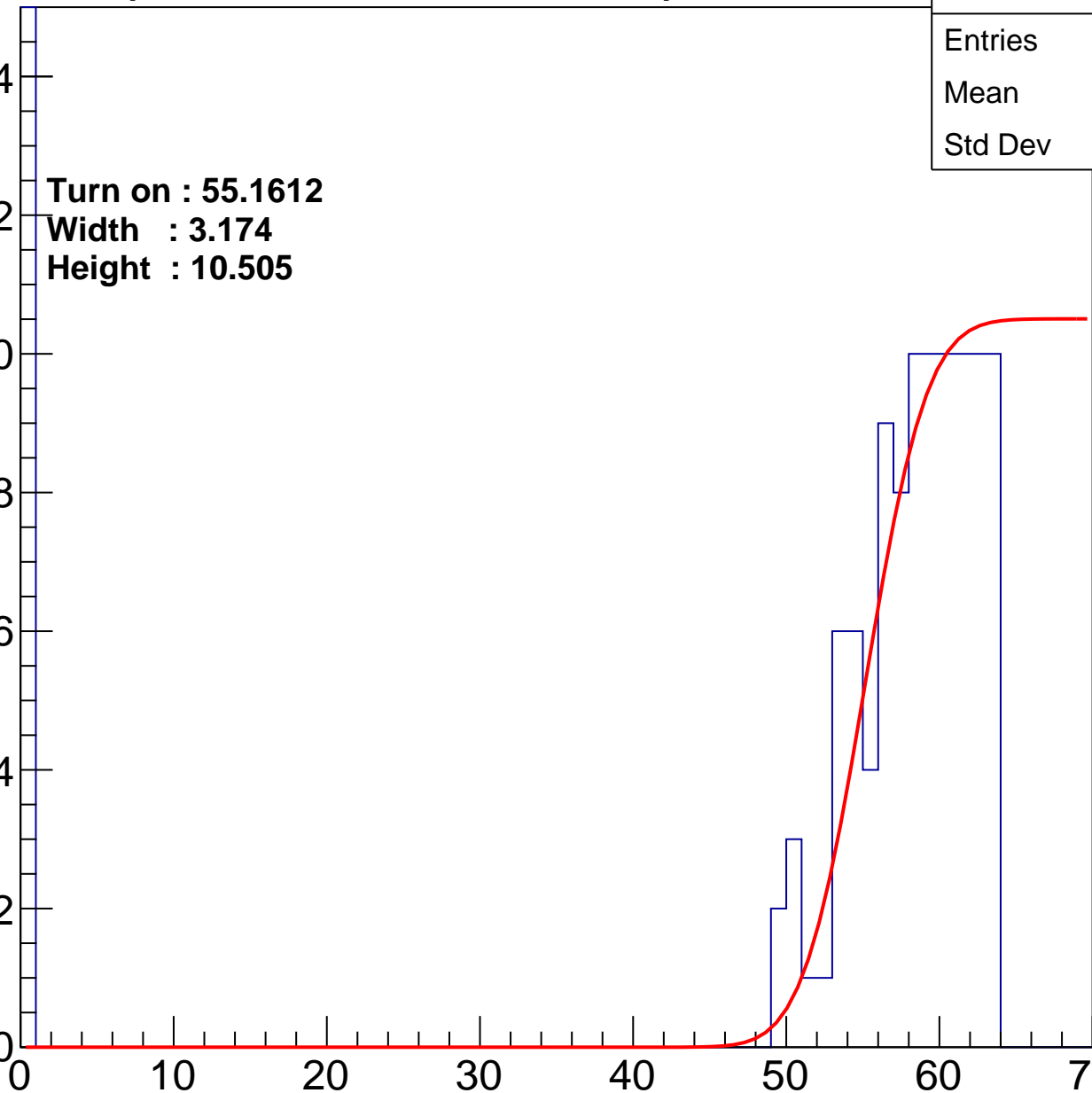
Width : 3.174

Height : 10.505

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	30.63
Std Dev	29.01

Turn on : 55.3008

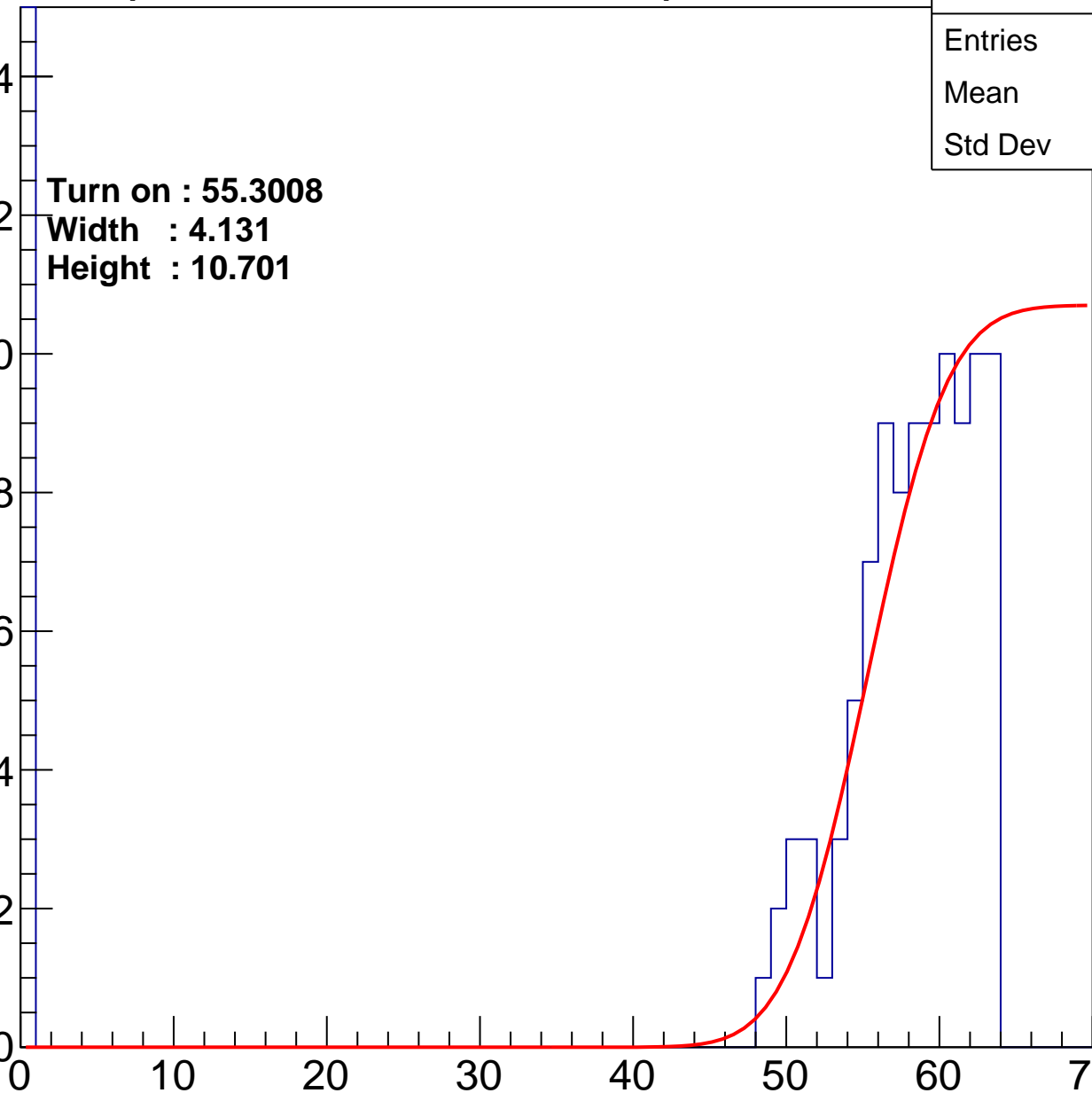
Width : 4.131

Height : 10.701

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	160
Mean	35.46
Std Dev	28.35

Turn on : 52.8886

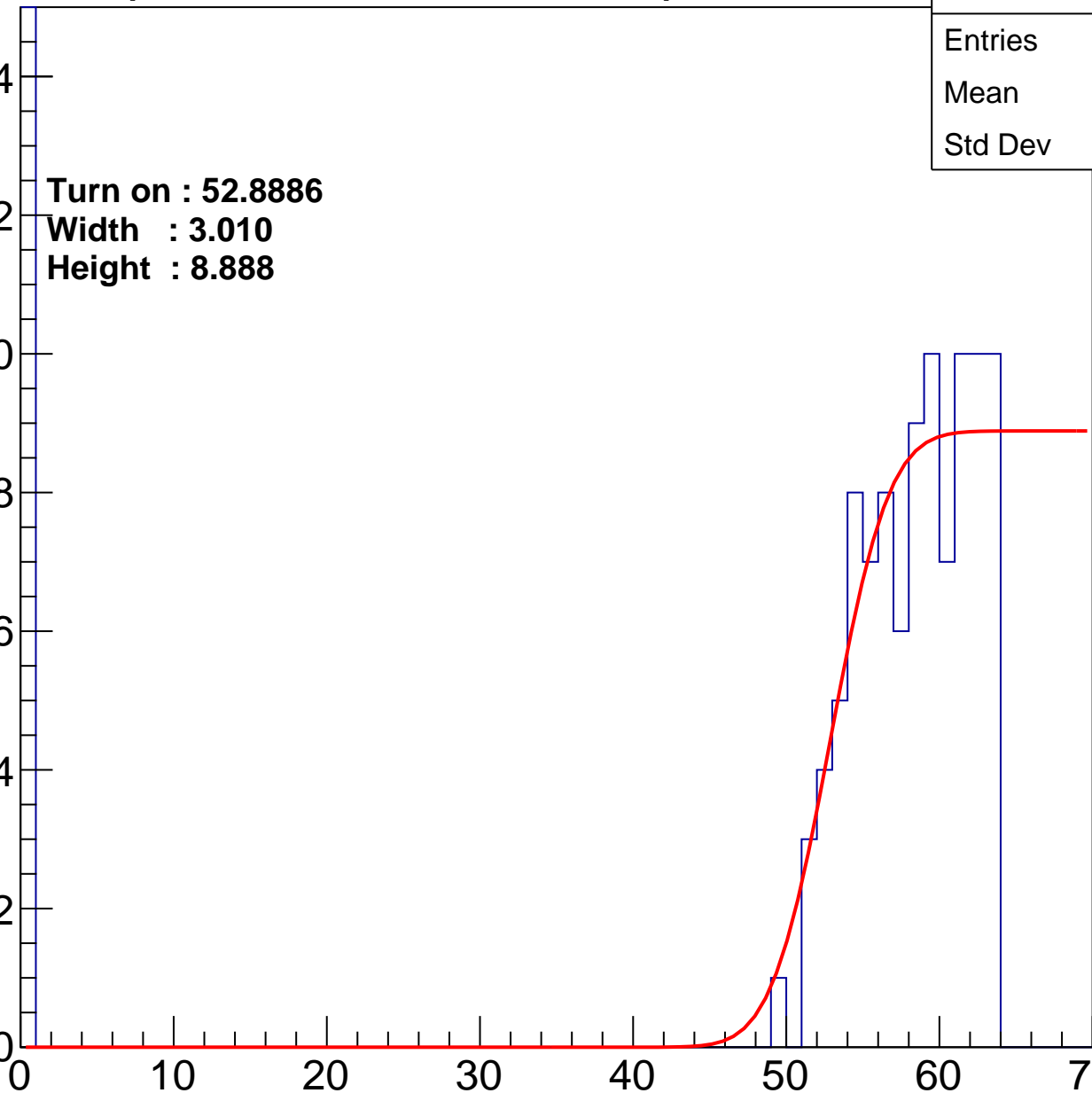
Width : 3.010

Height : 8.888

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	29.84
Std Dev	29.05

Turn on : 55.3344

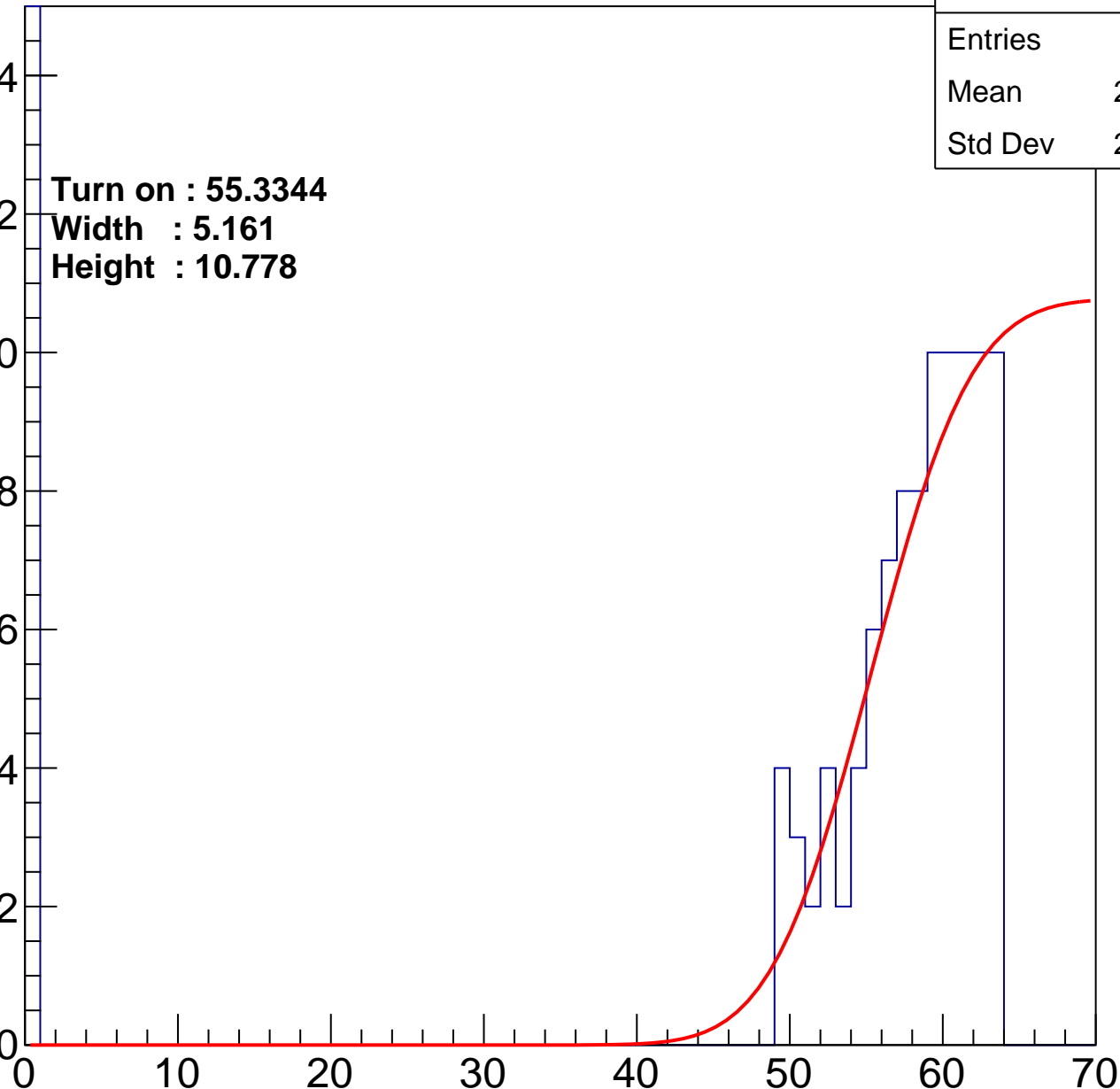
Width : 5.161

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.04
Std Dev	28.83

Turn on : 53.9203

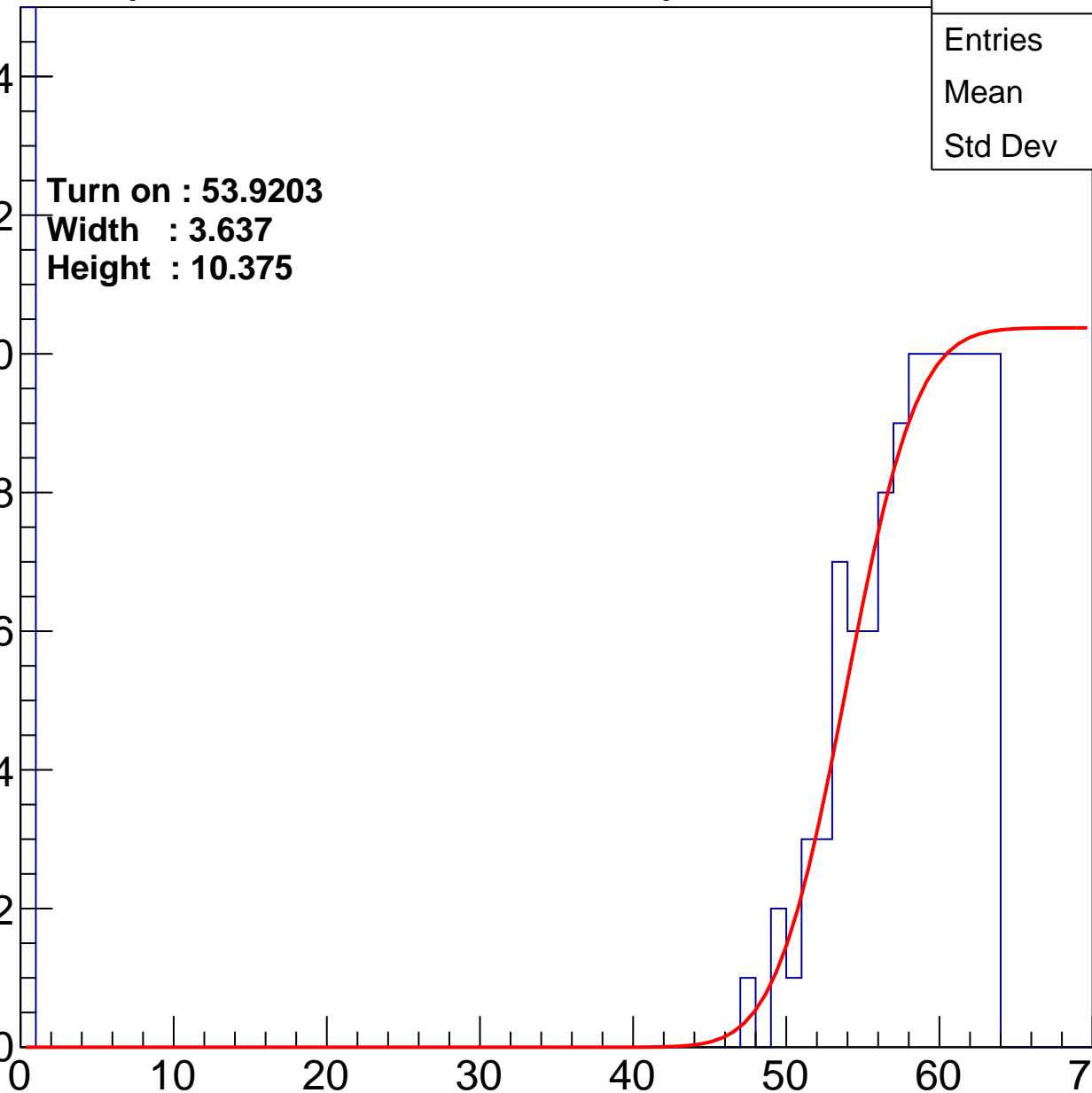
Width : 3.637

Height : 10.375

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch76

calib_packv5_033123_0516.root, FC#4, port A1

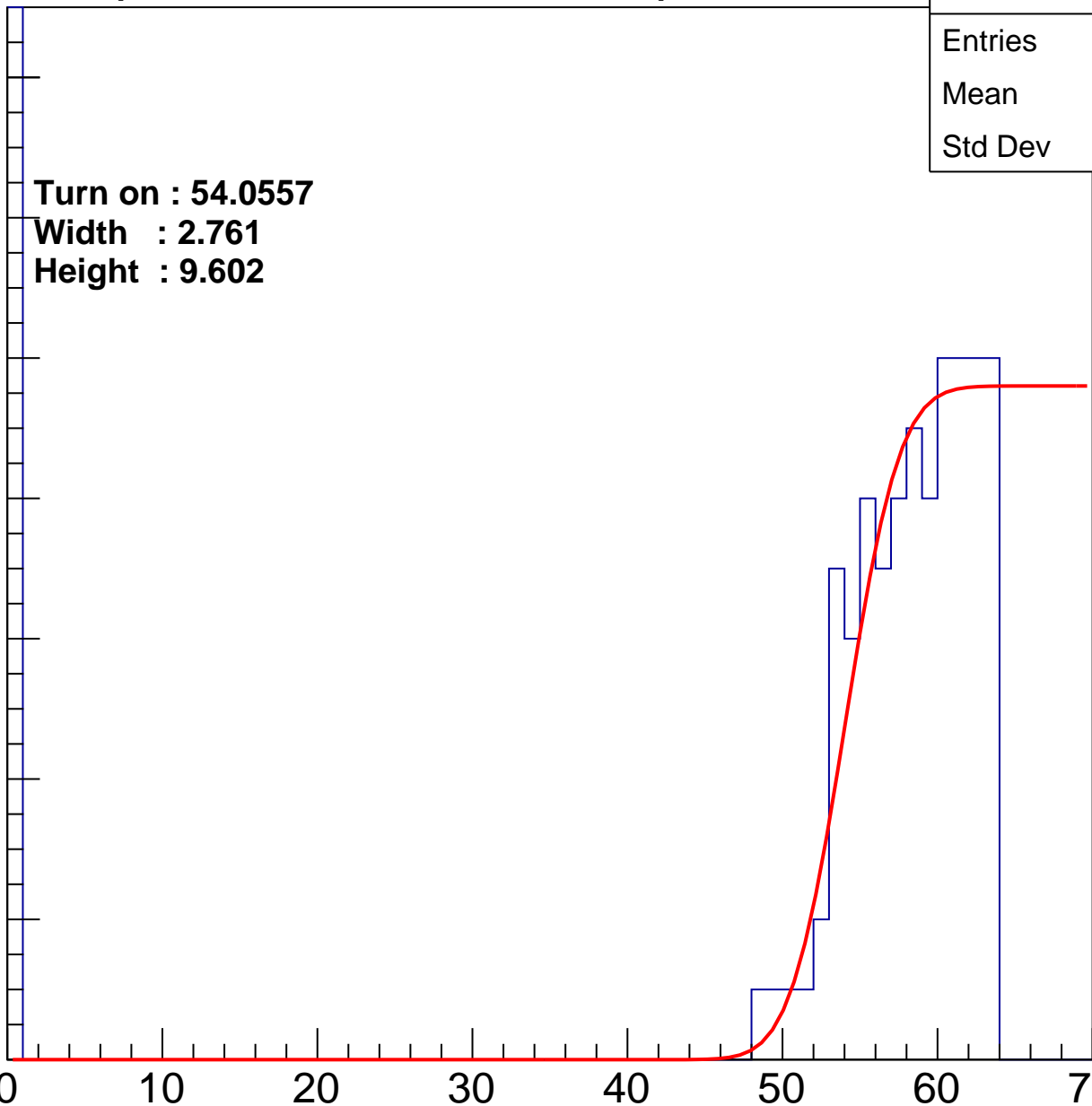
Entry

14
12
10
8
6
4
2
0

Turn on : 54.0557
Width : 2.761
Height : 9.602

Entries	168
Mean	34.16
Std Dev	28.65

ampl



B1L104S, U23-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	31.35
Std Dev	28.81

Turn on : 53.4272

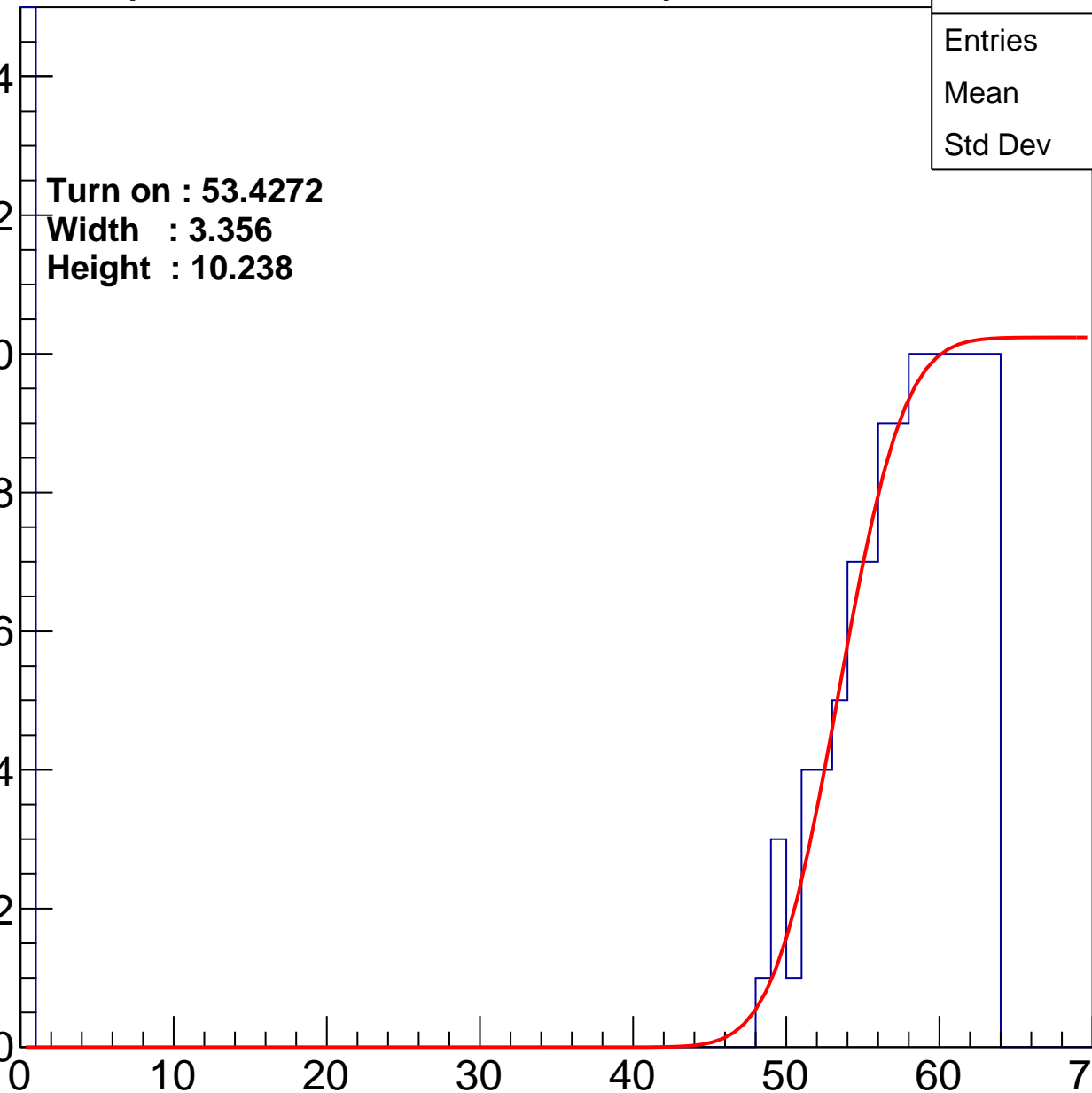
Width : 3.356

Height : 10.238

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch78

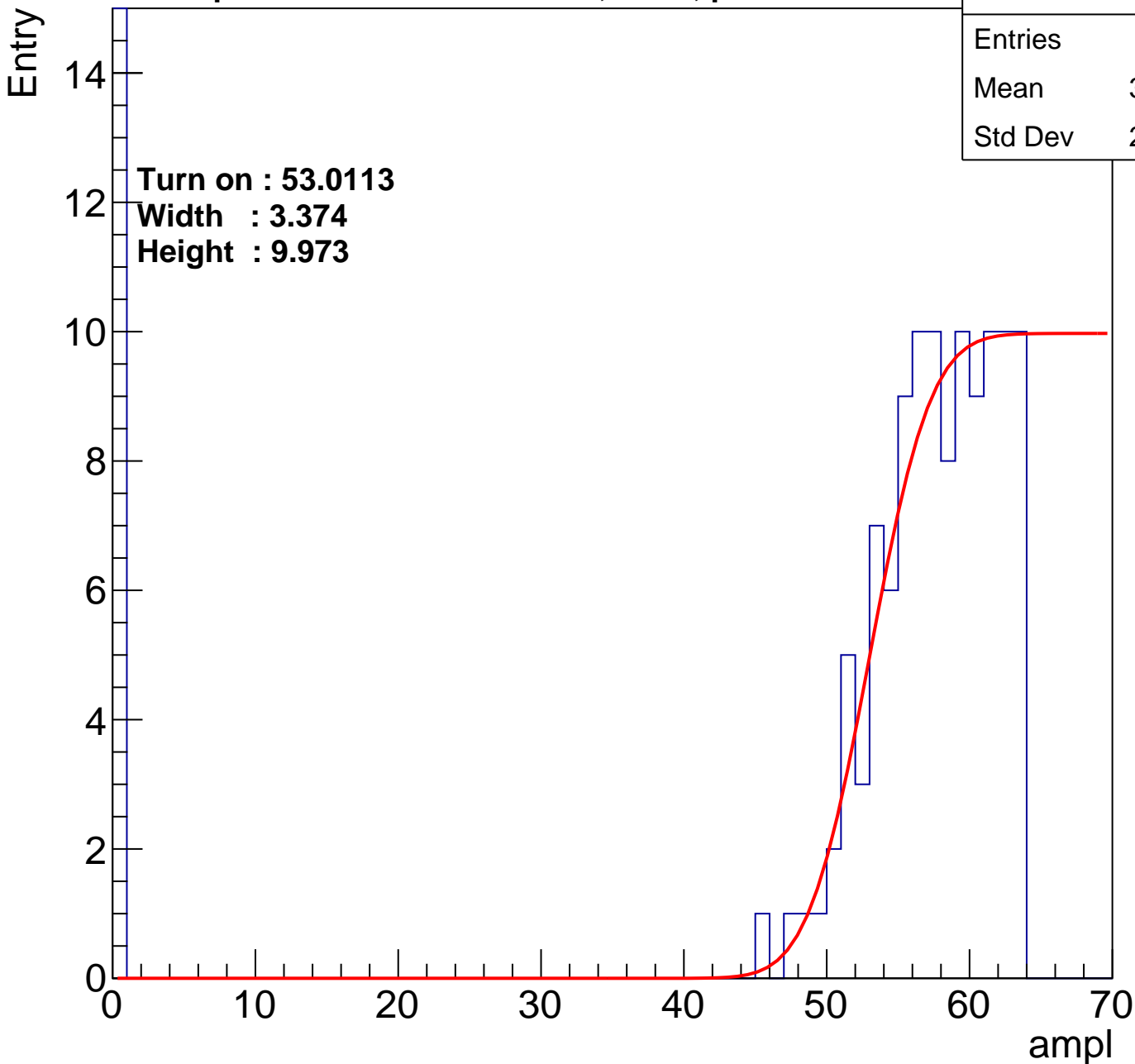
calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	31.12
Std Dev	28.69

Turn on : 53.0113

Width : 3.374

Height : 9.973



B1L104S, U23-ch79

calib_packv5_033123_0516.root, FC#4, port A1

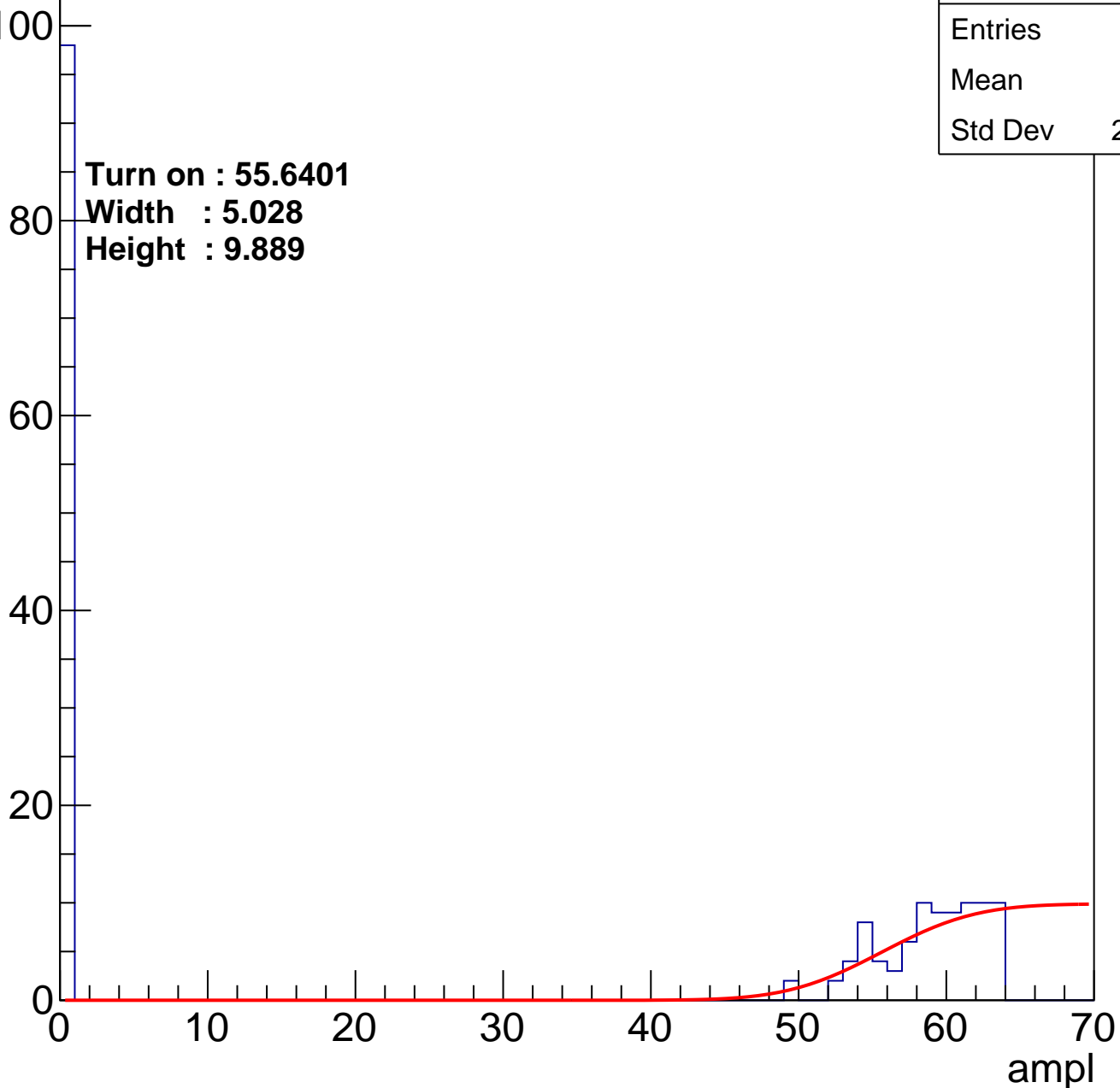
Entry

Entries	185
Mean	27.5
Std Dev	29.28

Turn on : 55.6401

Width : 5.028

Height : 9.889



B1L104S, U23-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	27.18
Std Dev	28.99

Turn on : 53.8994

Width : 3.828

Height : 10.056

Entry

14

12

10

8

6

4

2

0

0

10

20

30

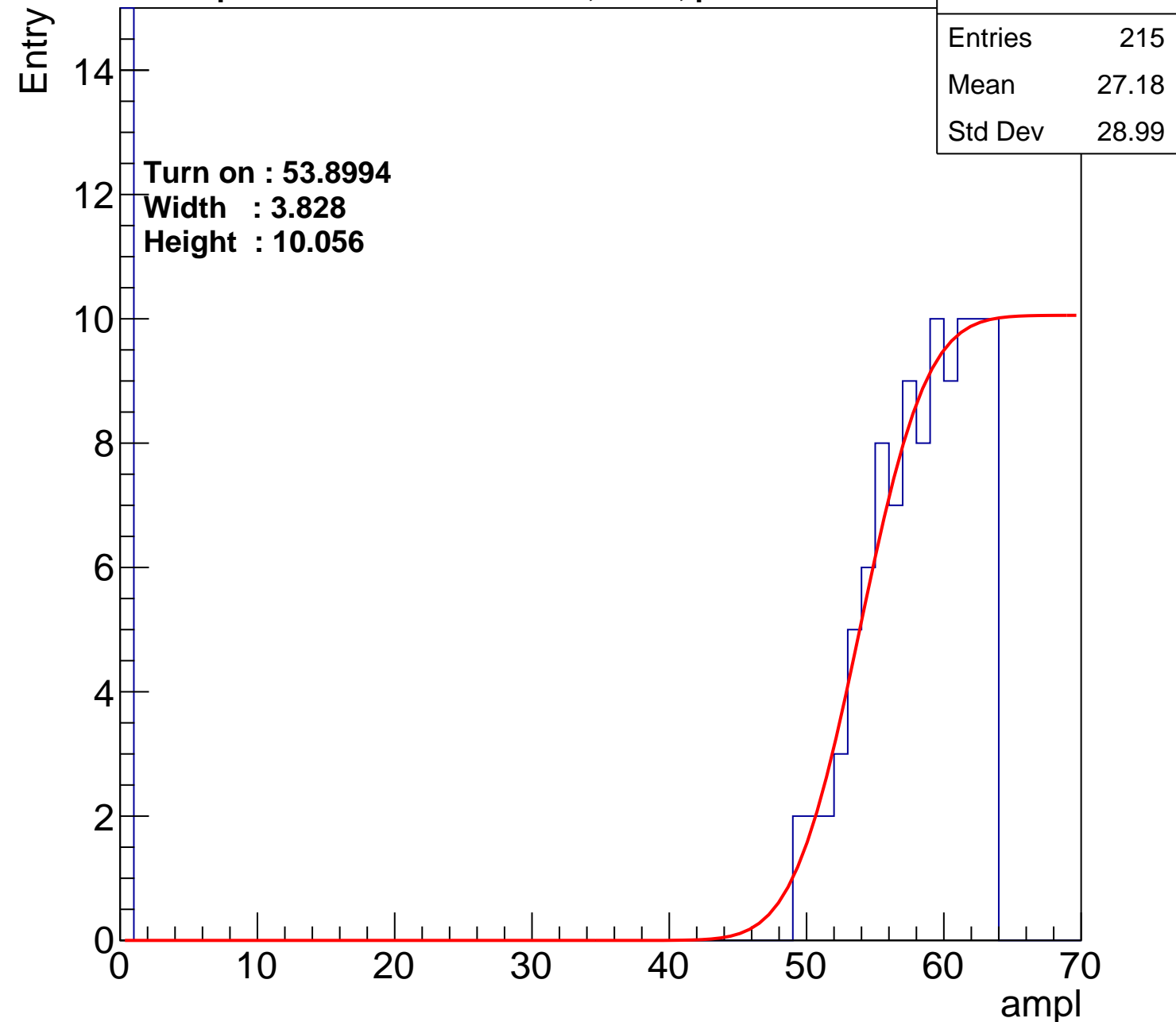
40

50

60

70

ampl



B1L104S, U23-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	33.22
Std Dev	28.6

Turn on : 52.9019

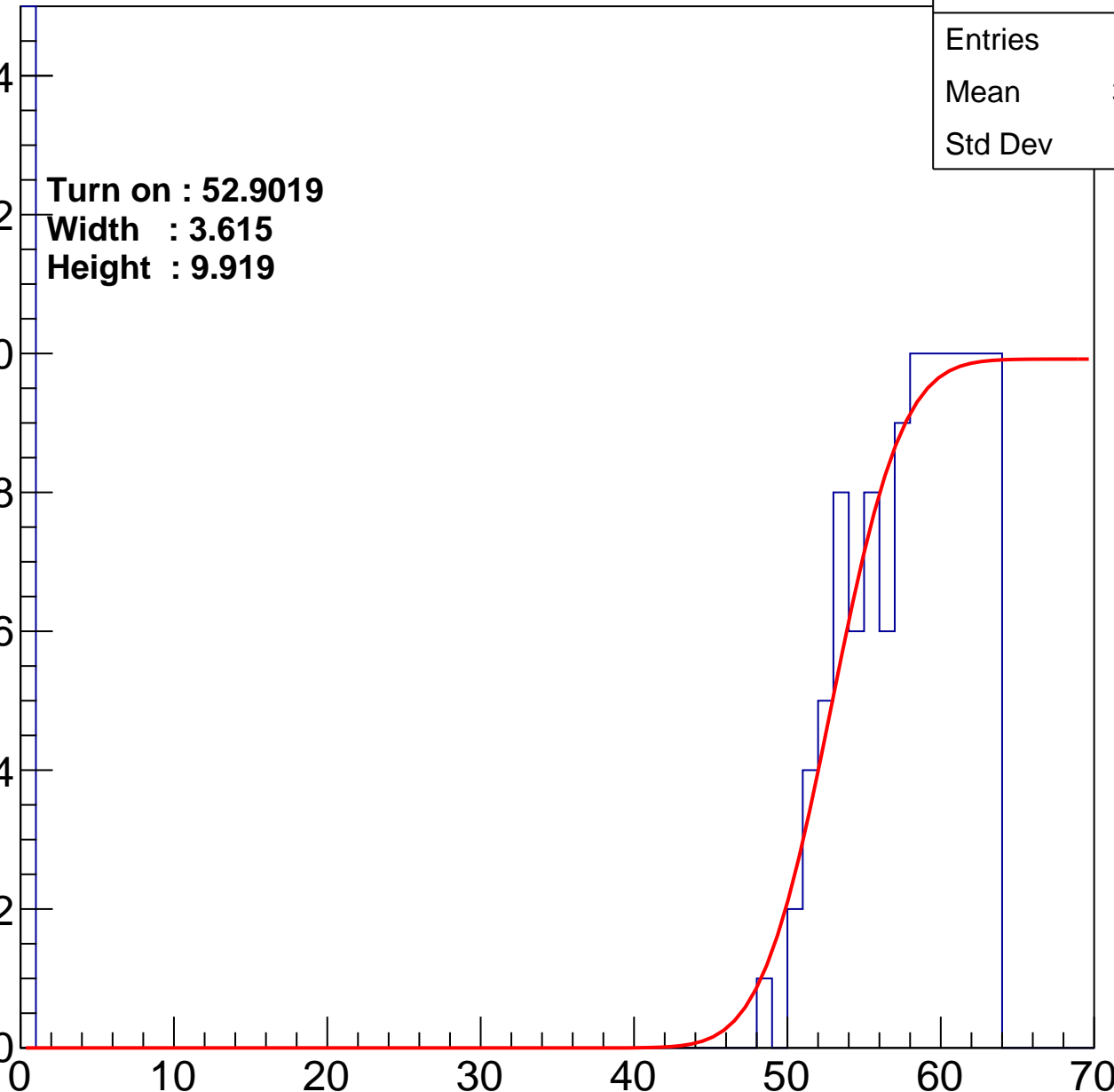
Width : 3.615

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	37.4
Std Dev	27.55

Turn on : 53.0727

Width : 3.568

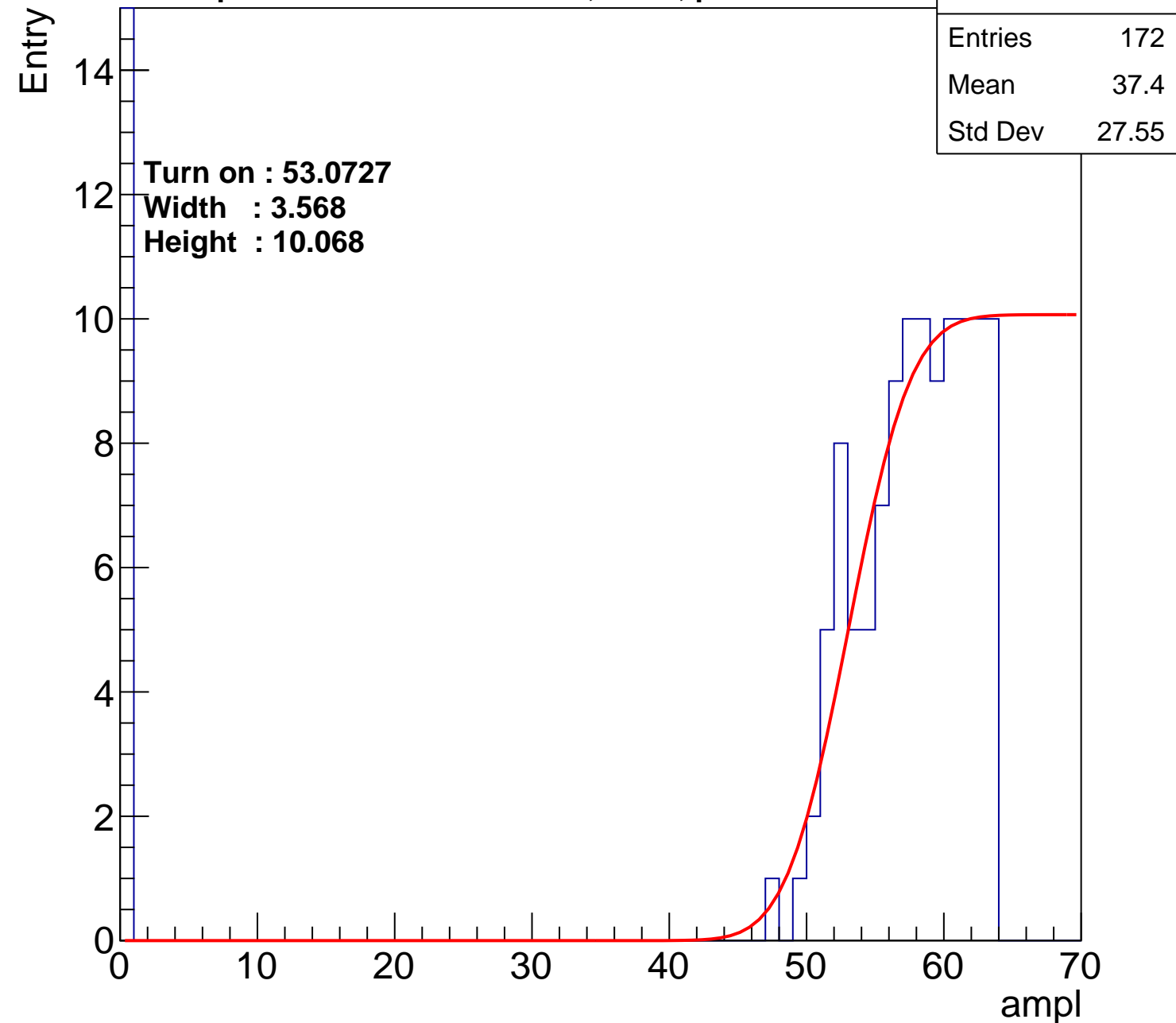
Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U23-ch83

calib_packv5_033123_0516.root, FC#4, port A1

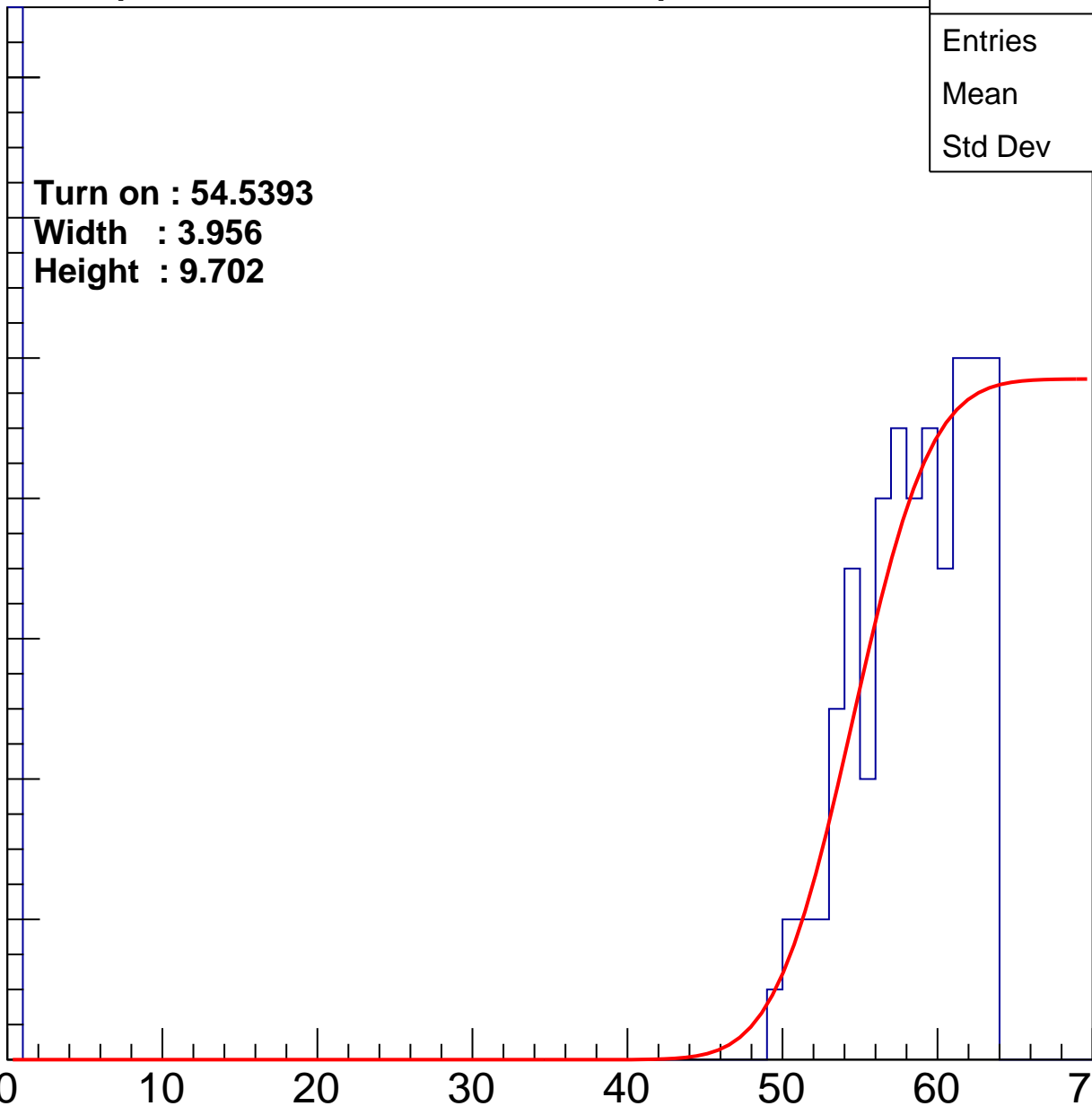
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5393
Width : 3.956
Height : 9.702

Entries	160
Mean	34.09
Std Dev	28.7

ampl



B1L104S, U23-ch84

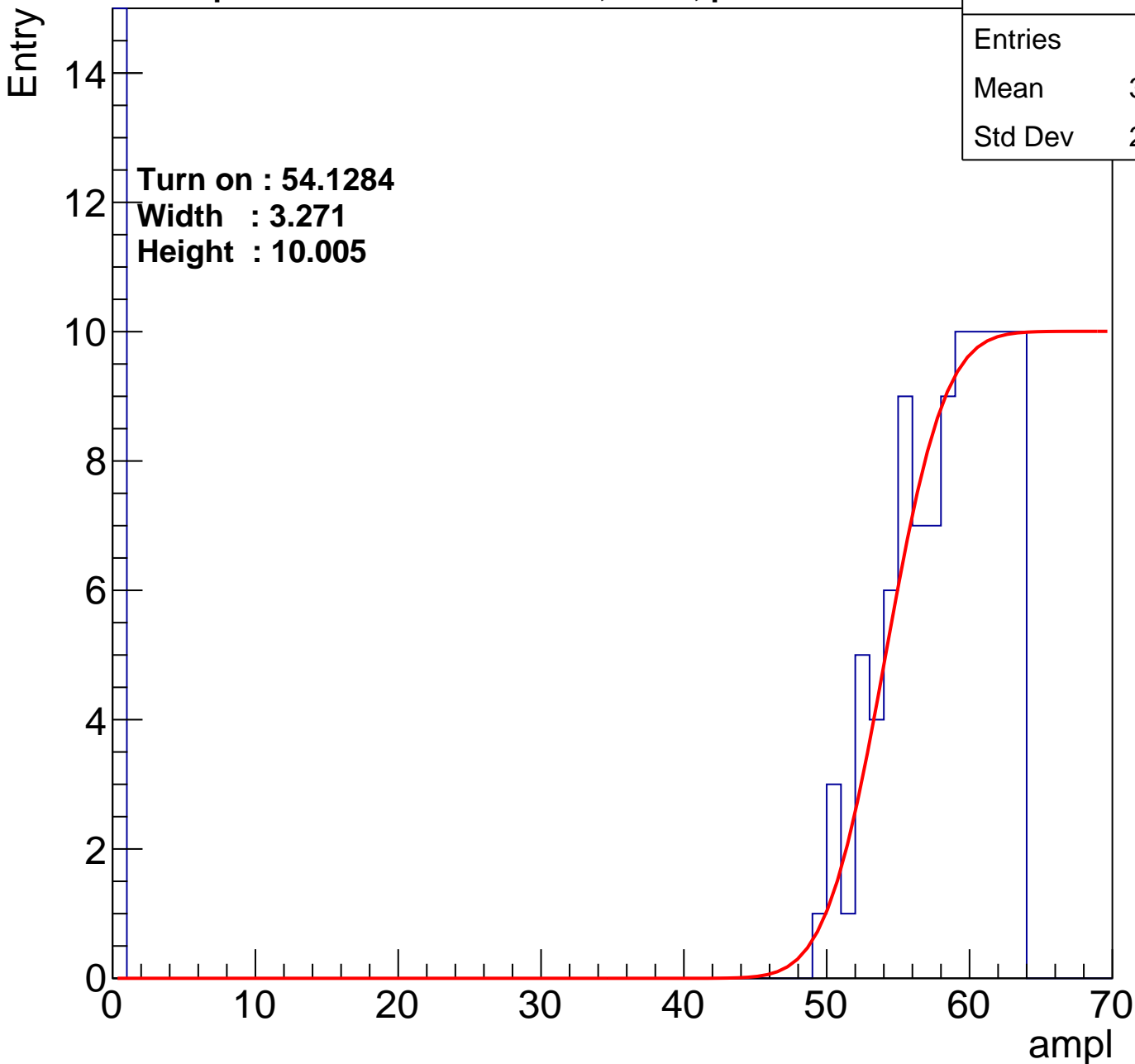
calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	32.26
Std Dev	28.88

Turn on : 54.1284

Width : 3.271

Height : 10.005



B1L104S, U23-ch85

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.28
Std Dev	28.92

Turn on : 53.2920

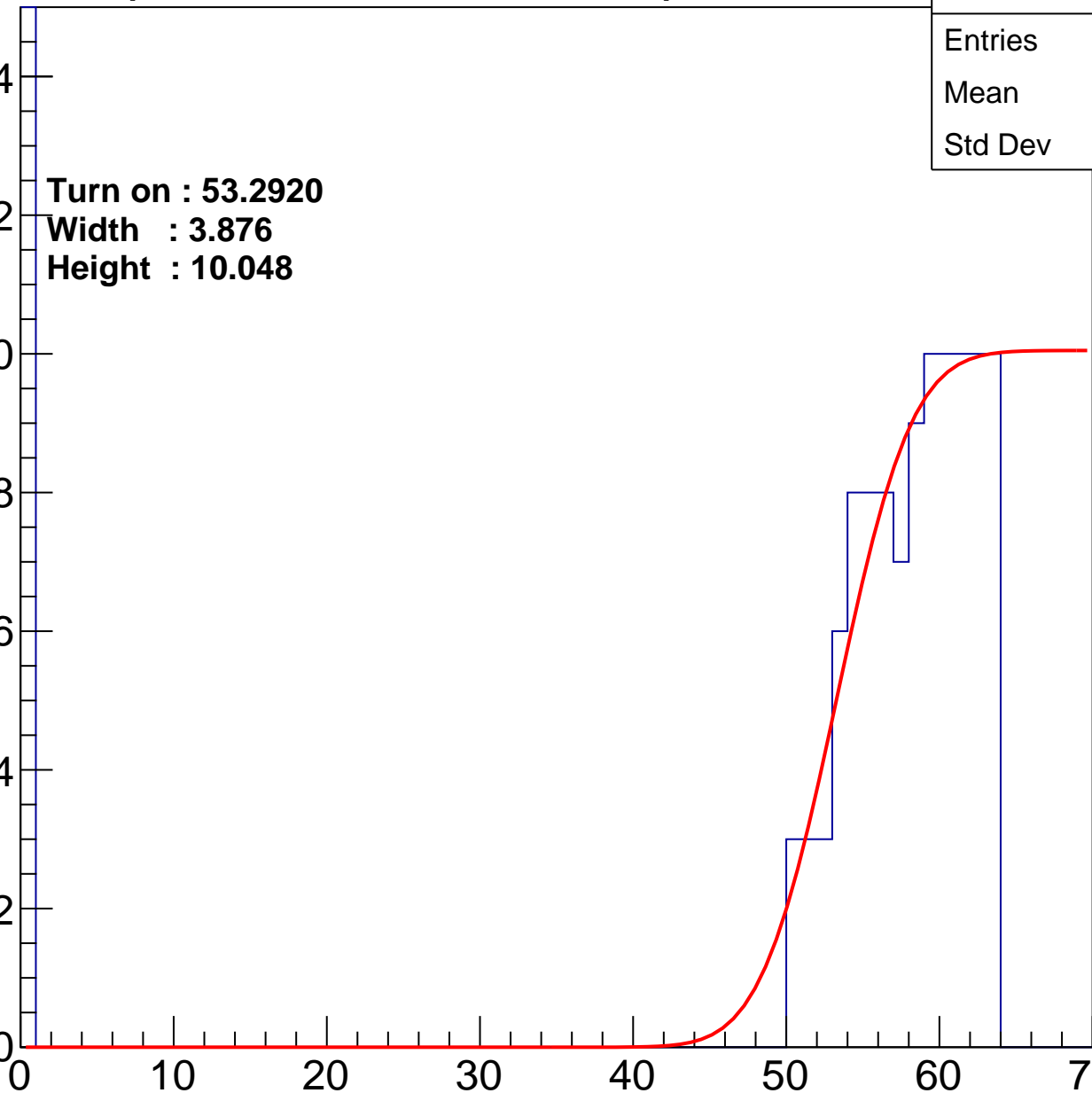
Width : 3.876

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	31.58
Std Dev	28.75

Turn on : 52.9761

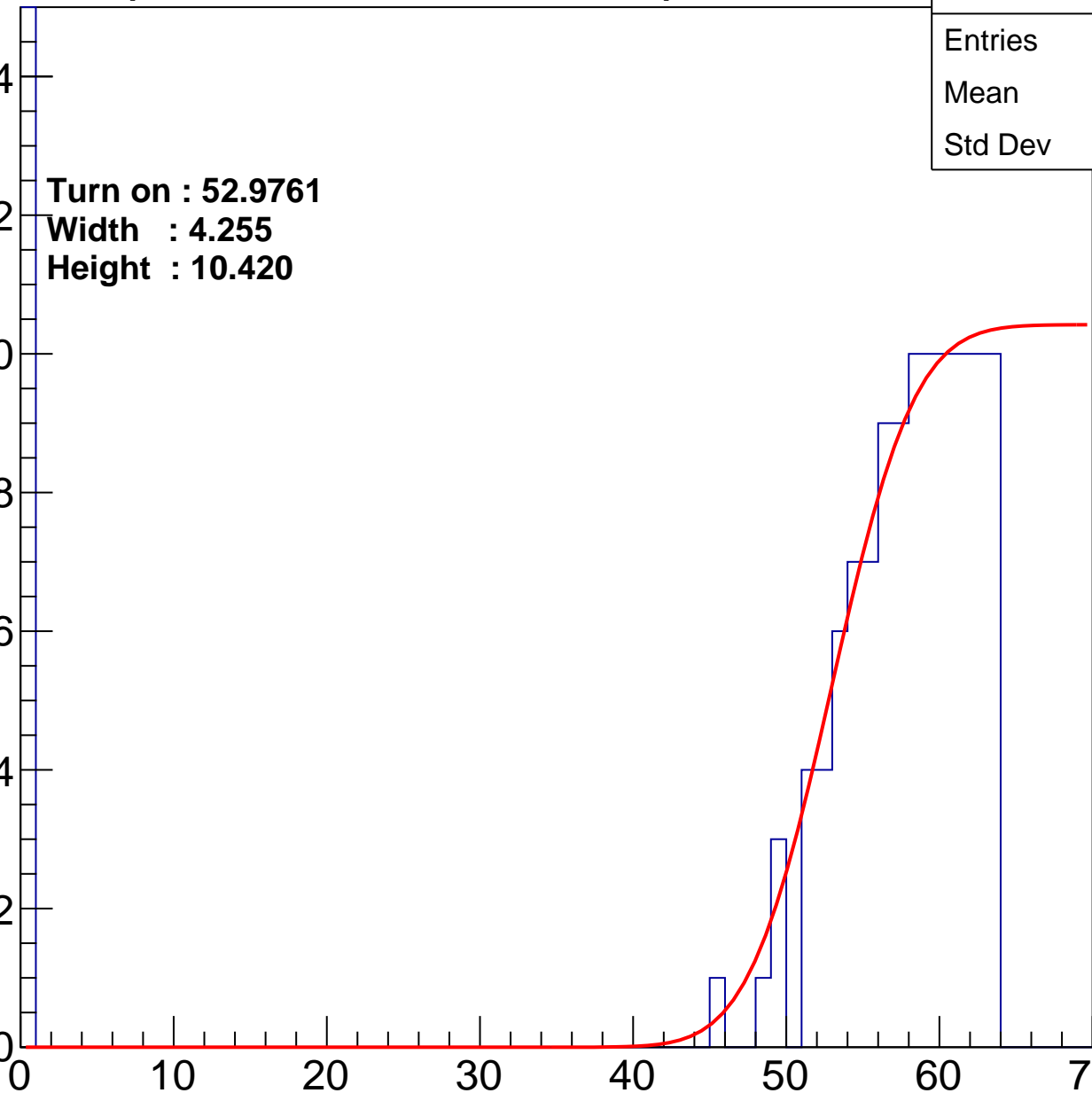
Width : 4.255

Height : 10.420

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	30.9
Std Dev	29.08

Turn on : 54.4177

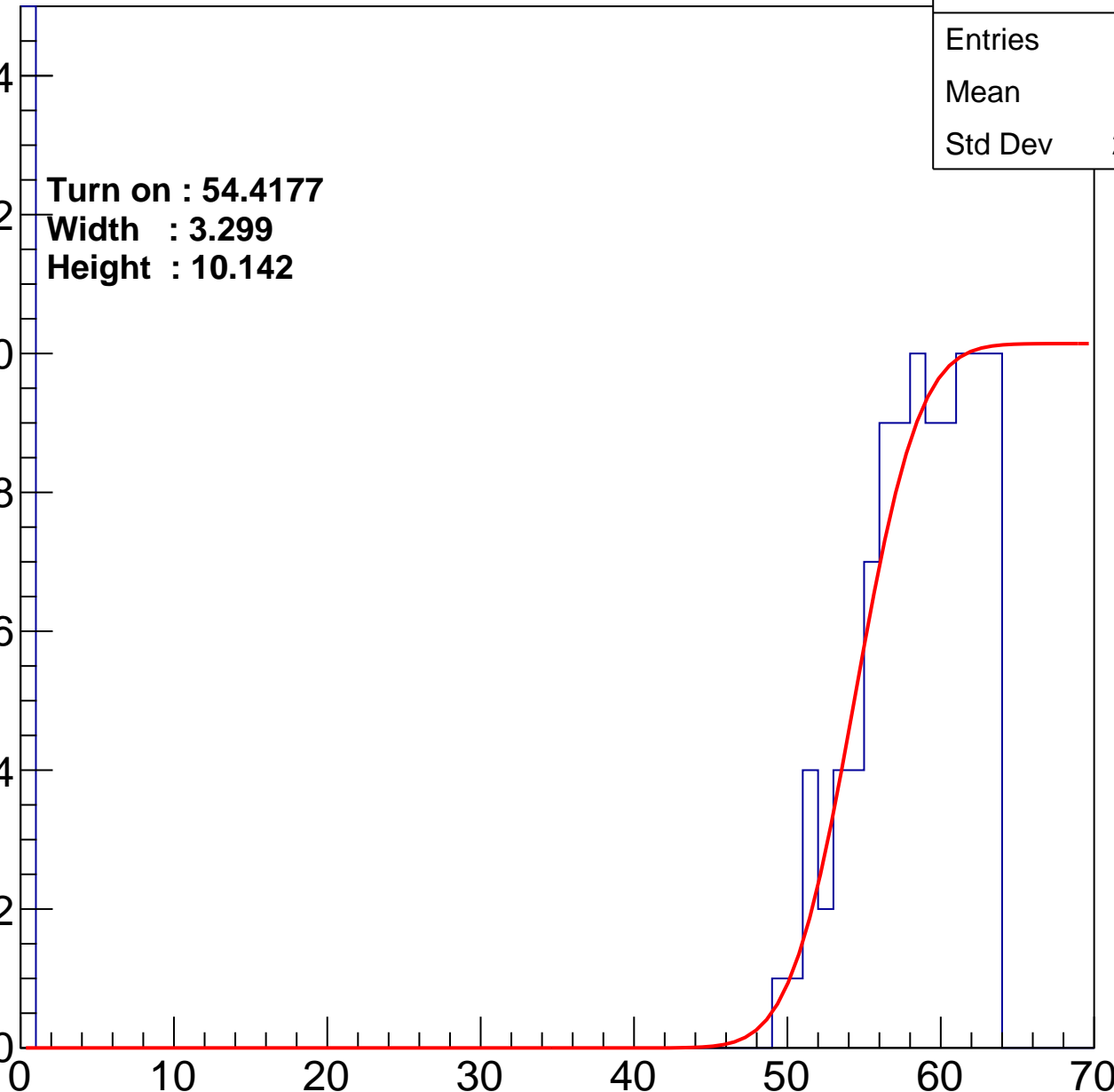
Width : 3.299

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	32.84
Std Dev	28.45

Turn on : 53.7022

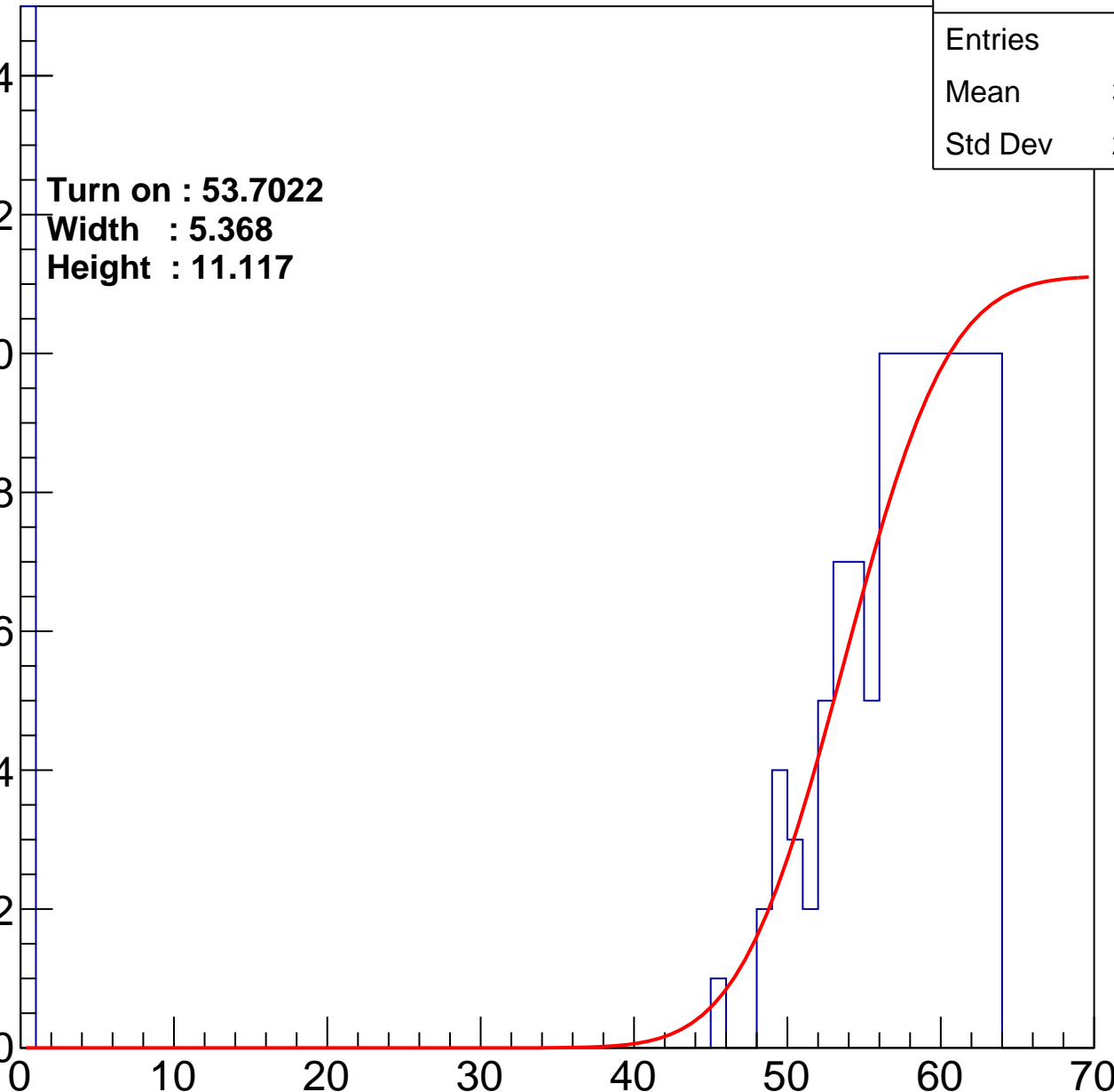
Width : 5.368

Height : 11.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	31.32
Std Dev	29.24

Turn on : 55.7677

Width : 4.603

Height : 10.111

Entry

70

50

30

10

0

0

10

20

30

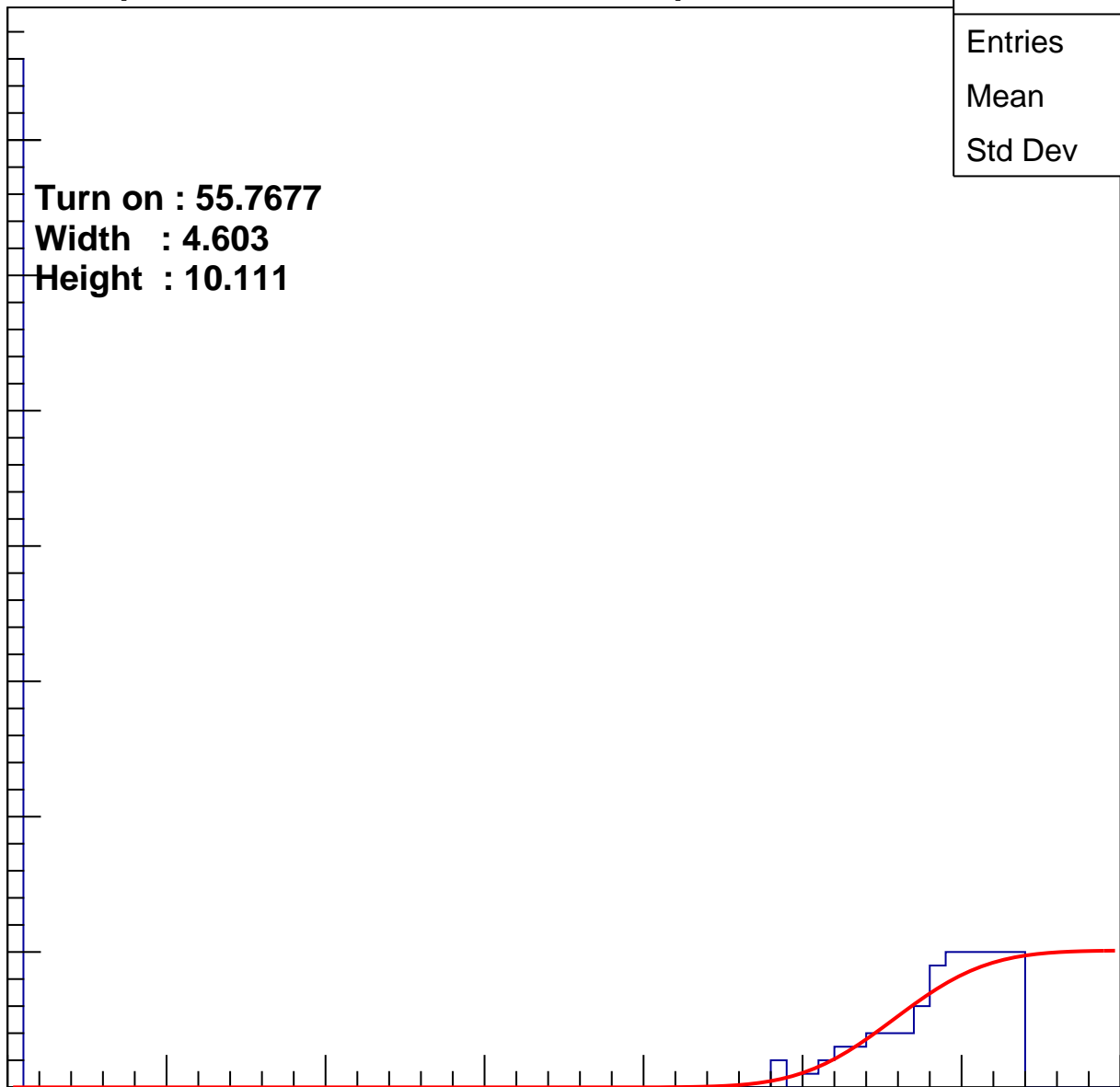
40

50

60

70

ampl



B1L104S, U23-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	27.59
Std Dev	29.14

Turn on : 54.8841

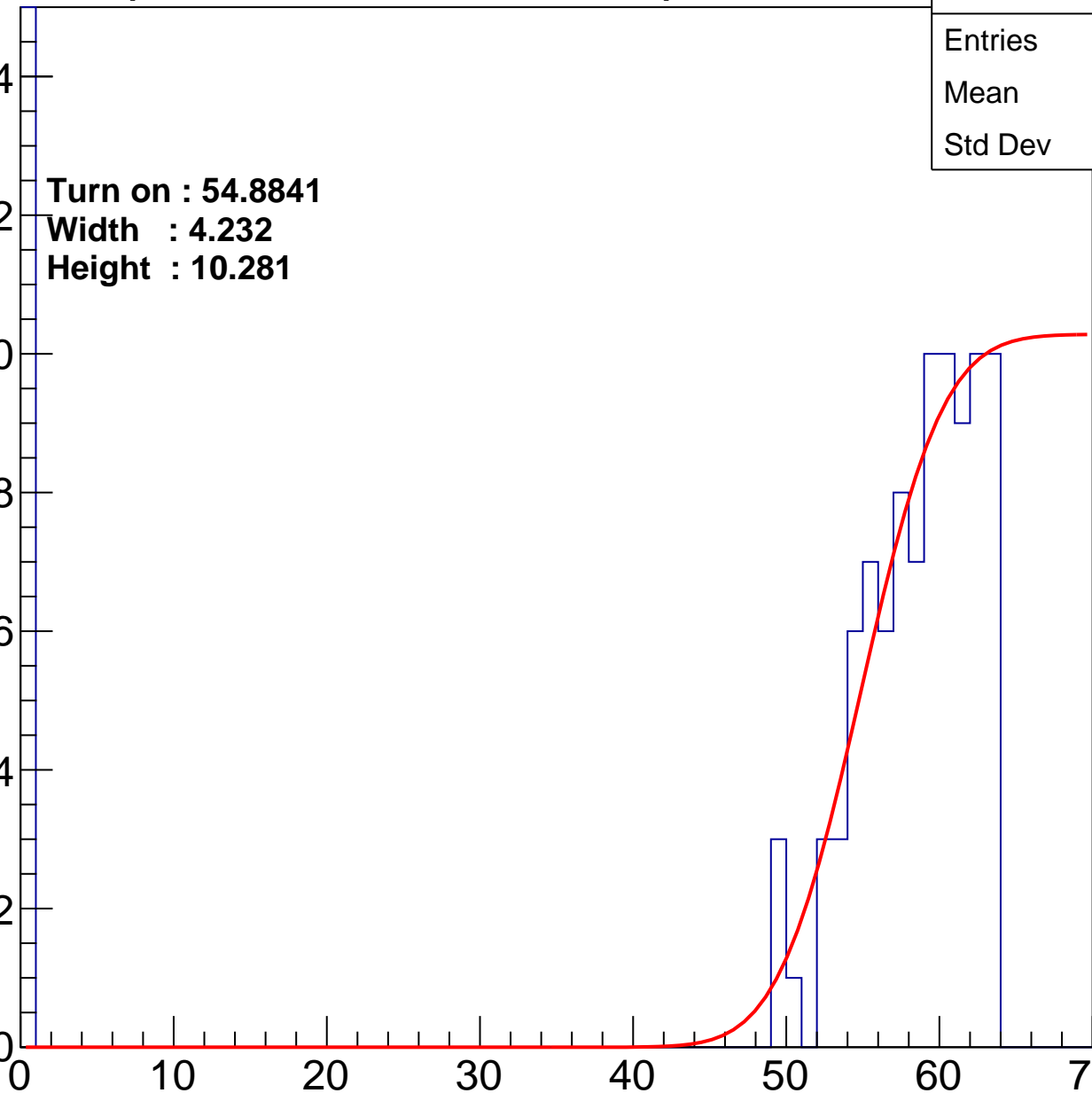
Width : 4.232

Height : 10.281

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	32.66
Std Dev	28.81

Turn on : 55.0541

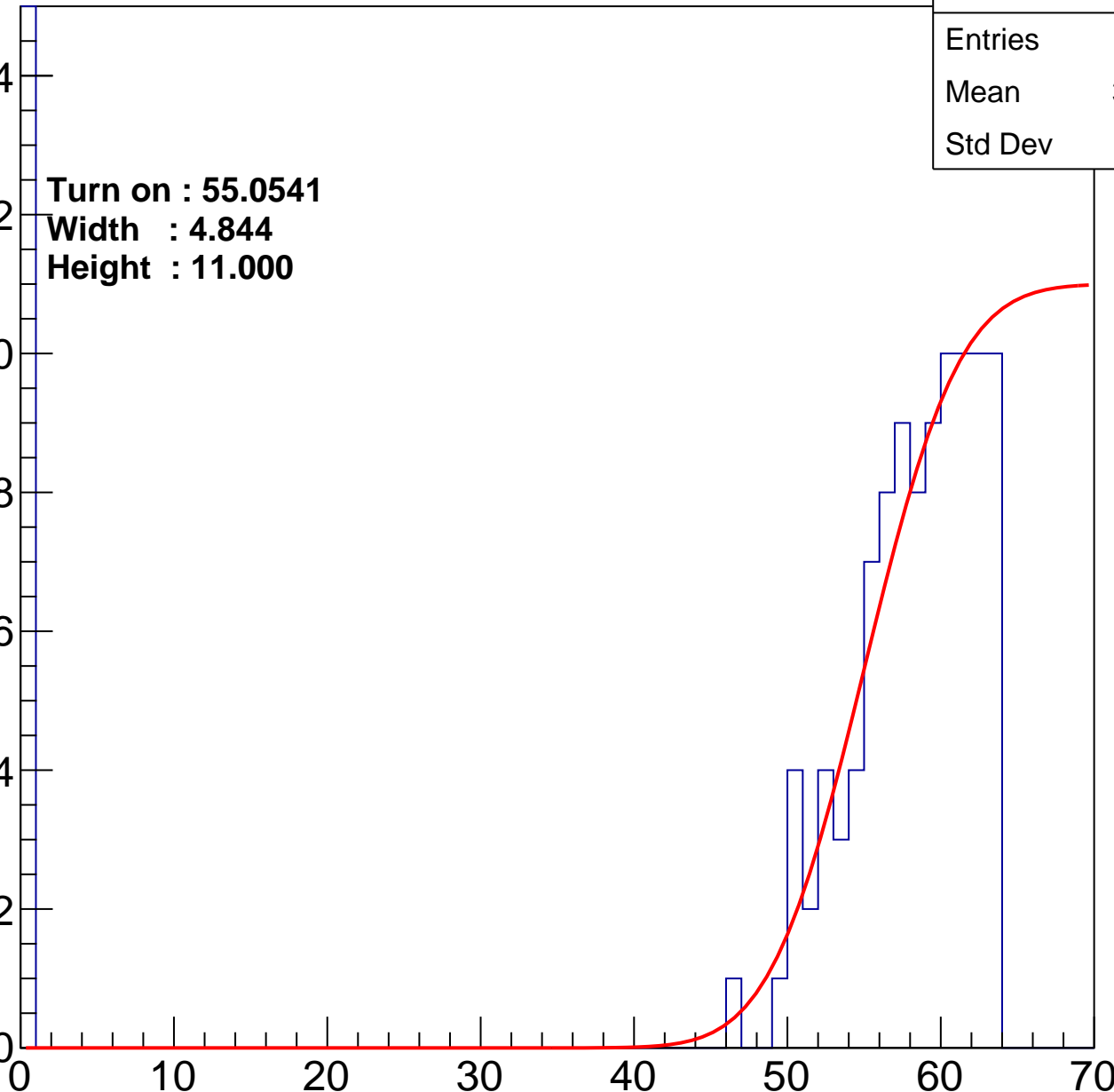
Width : 4.844

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch92

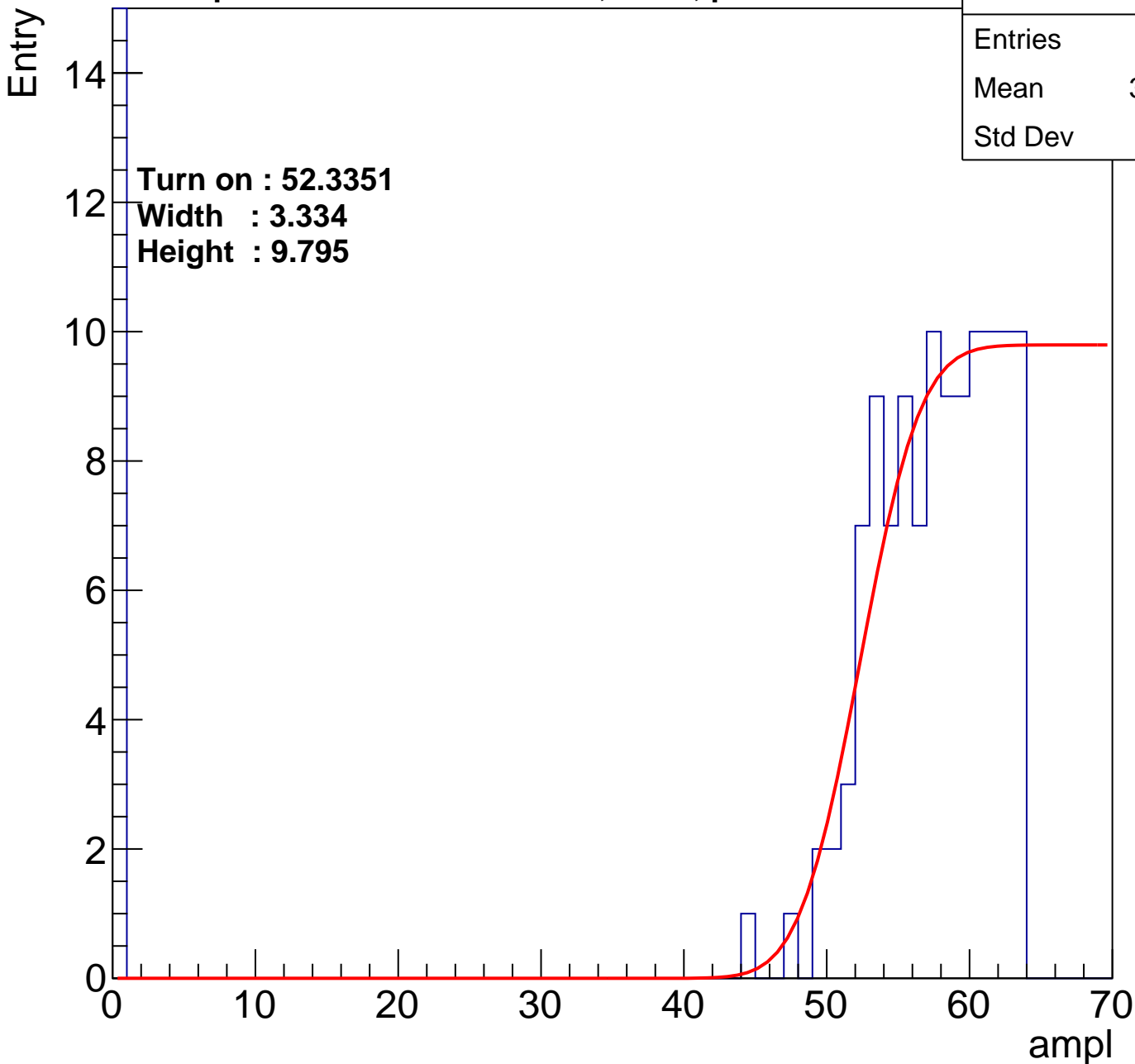
calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	33.66
Std Dev	28.3

Turn on : 52.3351

Width : 3.334

Height : 9.795



B1L104S, U23-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	34.45
Std Dev	28.61

Turn on : 54.2071

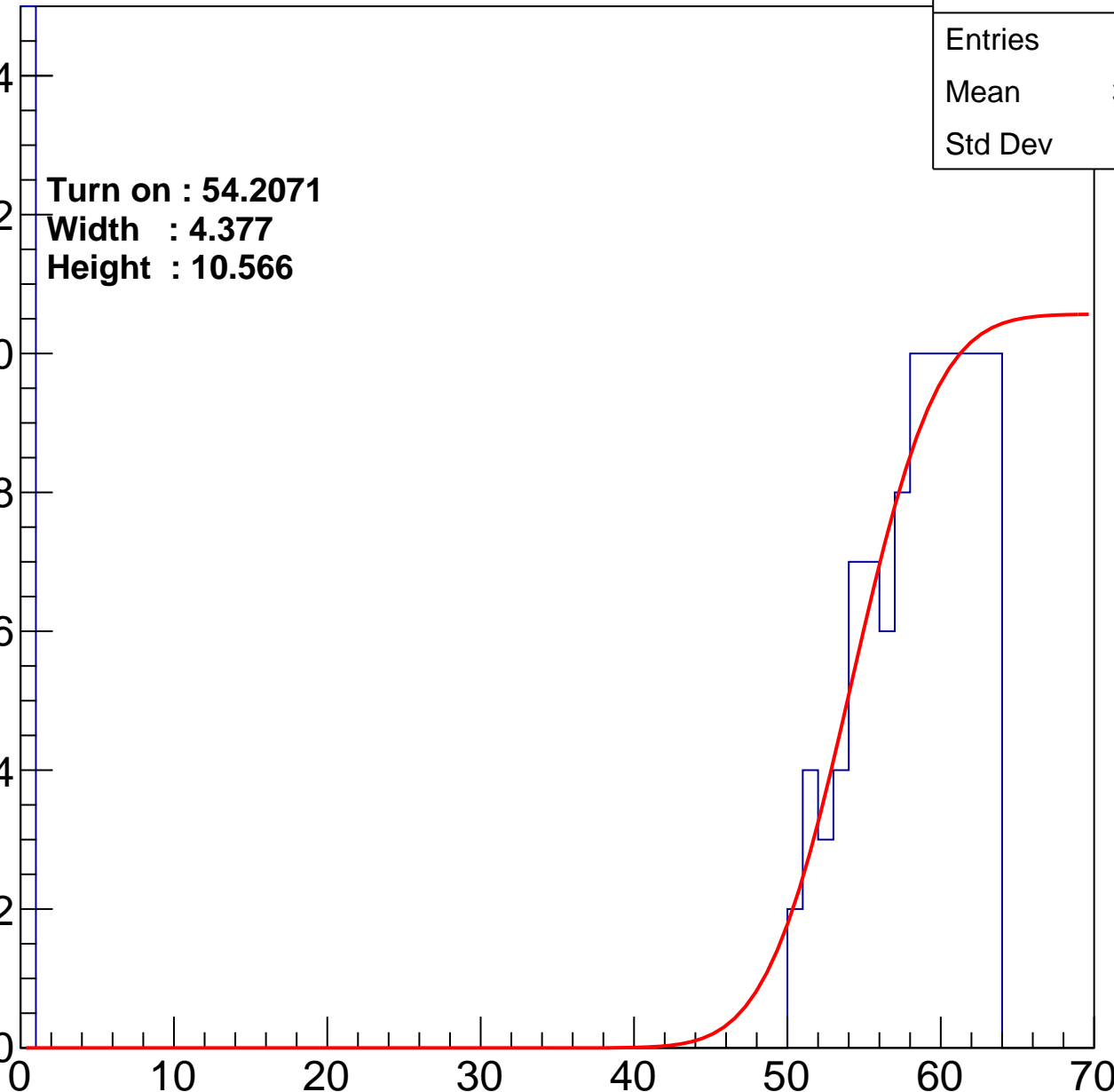
Width : 4.377

Height : 10.566

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	29.03
Std Dev	29.14

Turn on : 54.2863

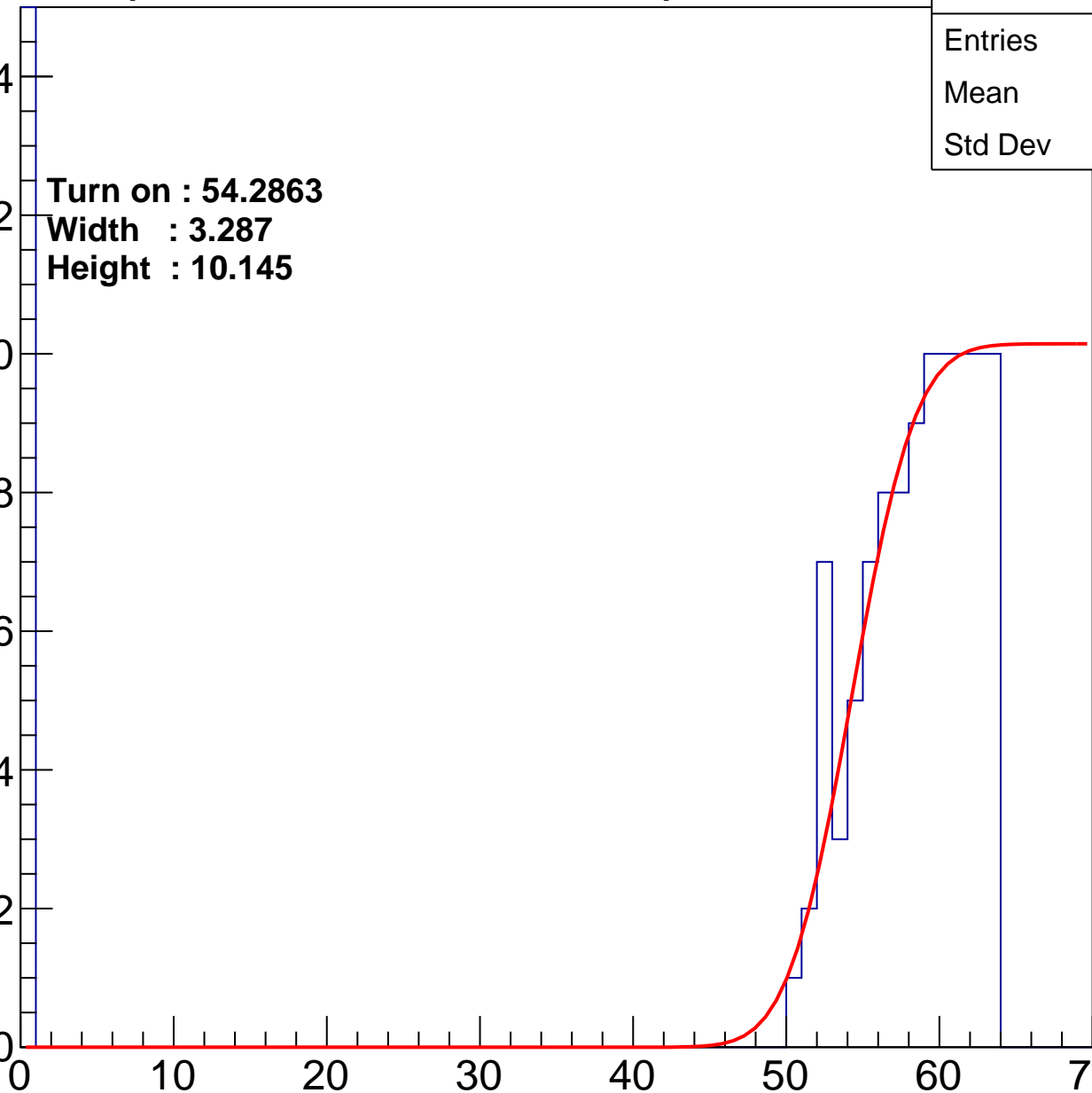
Width : 3.287

Height : 10.145

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	33.31
Std Dev	28.91

Turn on : 54.2878

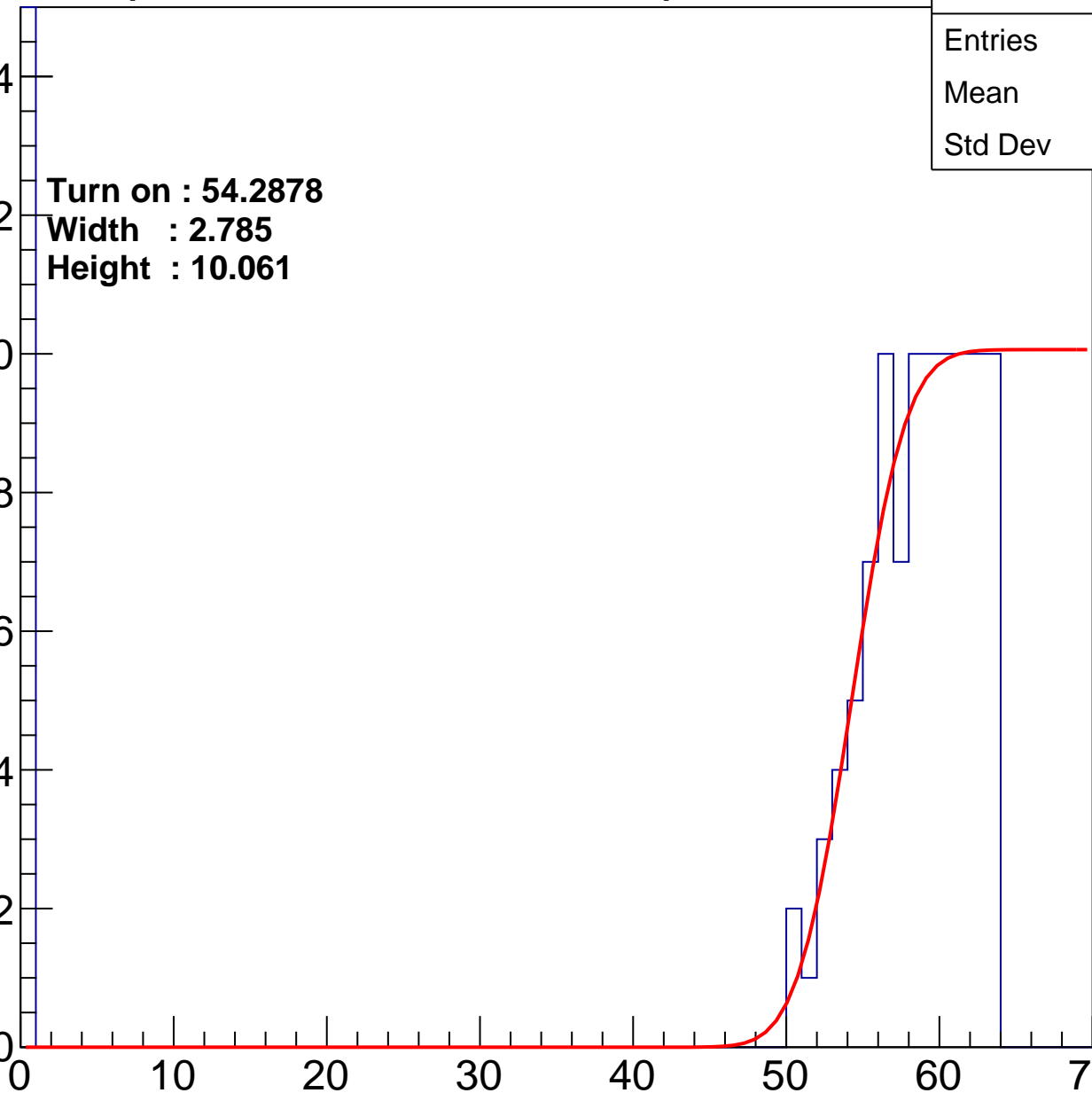
Width : 2.785

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch96

calib_packv5_033123_0516.root, FC#4, port A1

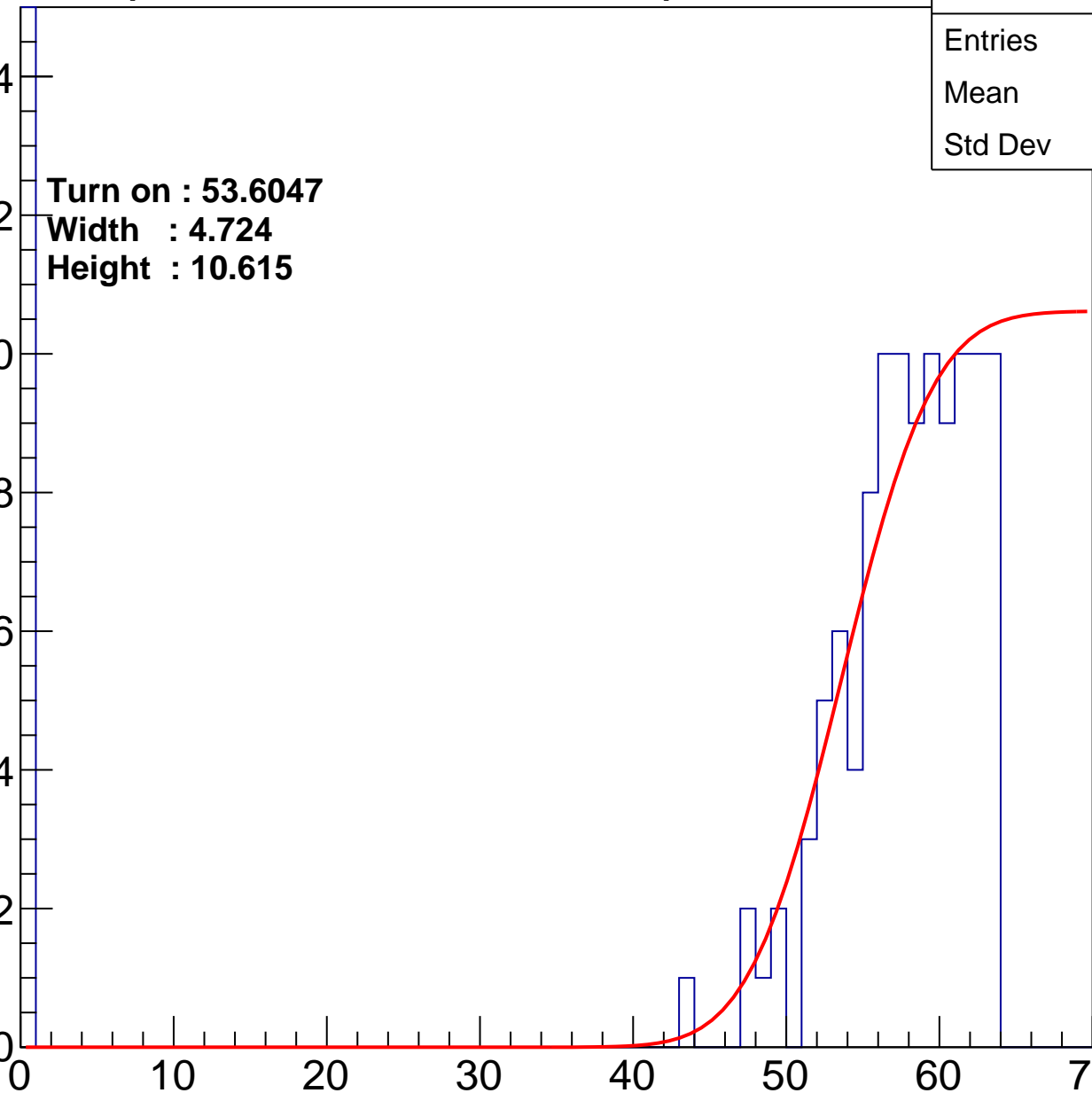
Entry

14
12
10
8
6
4
2
0

Turn on : 53.6047
Width : 4.724
Height : 10.615

Entries	198
Mean	31.88
Std Dev	28.68

ampl



B1L104S, U23-ch97

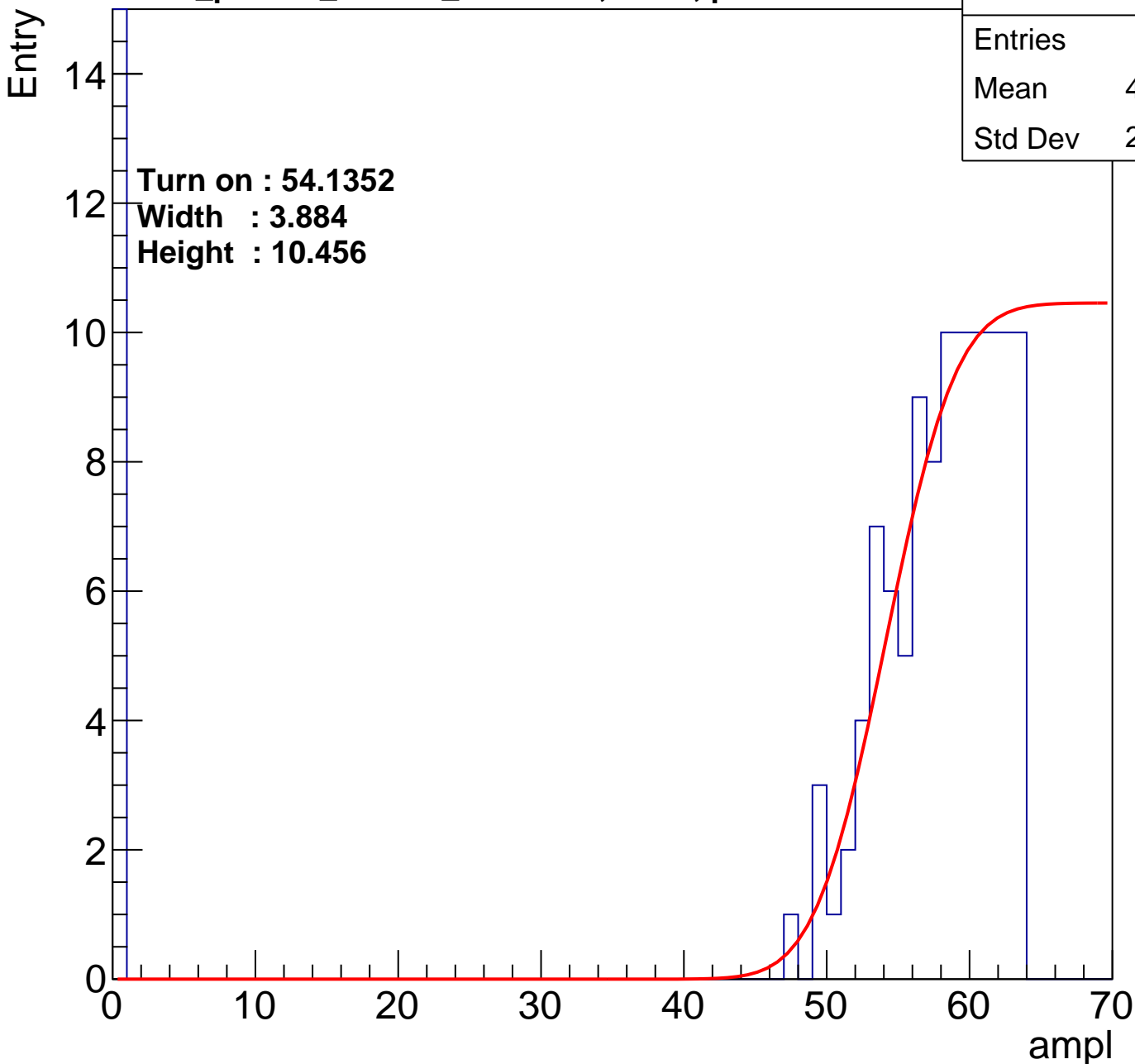
calib_packv5_033123_0516.root, FC#4, port A1

Entries	151
Mean	40.49
Std Dev	26.58

Turn on : 54.1352

Width : 3.884

Height : 10.456



B1L104S, U23-ch98

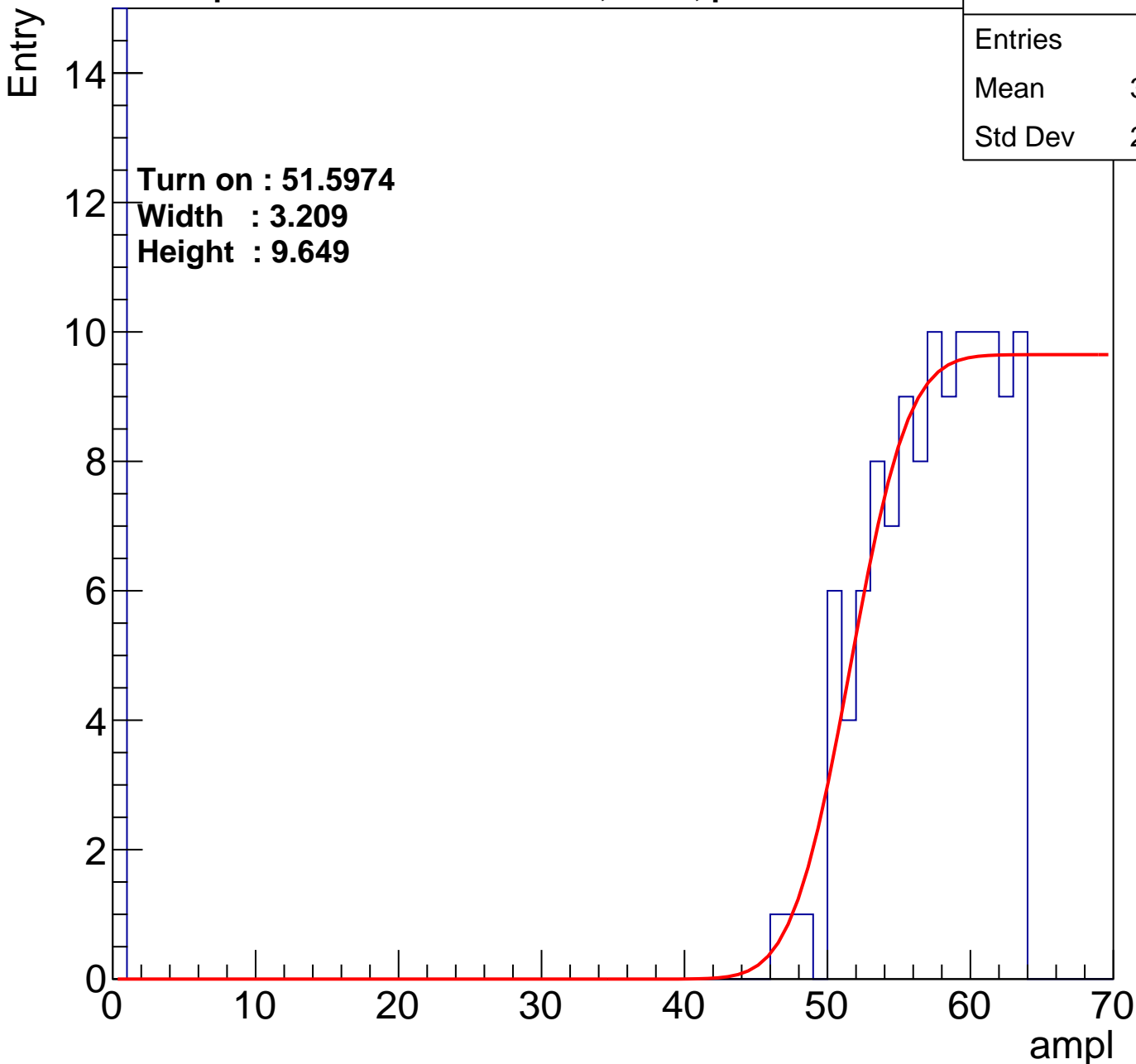
calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	34.96
Std Dev	27.94

Turn on : 51.5974

Width : 3.209

Height : 9.649



B1L104S, U23-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entries	152
Mean	34.12
Std Dev	28.84

Turn on : 55.1263

Width : 4.725

Height : 10.222

Entry

60

50

40

30

20

10

0

0

10

20

30

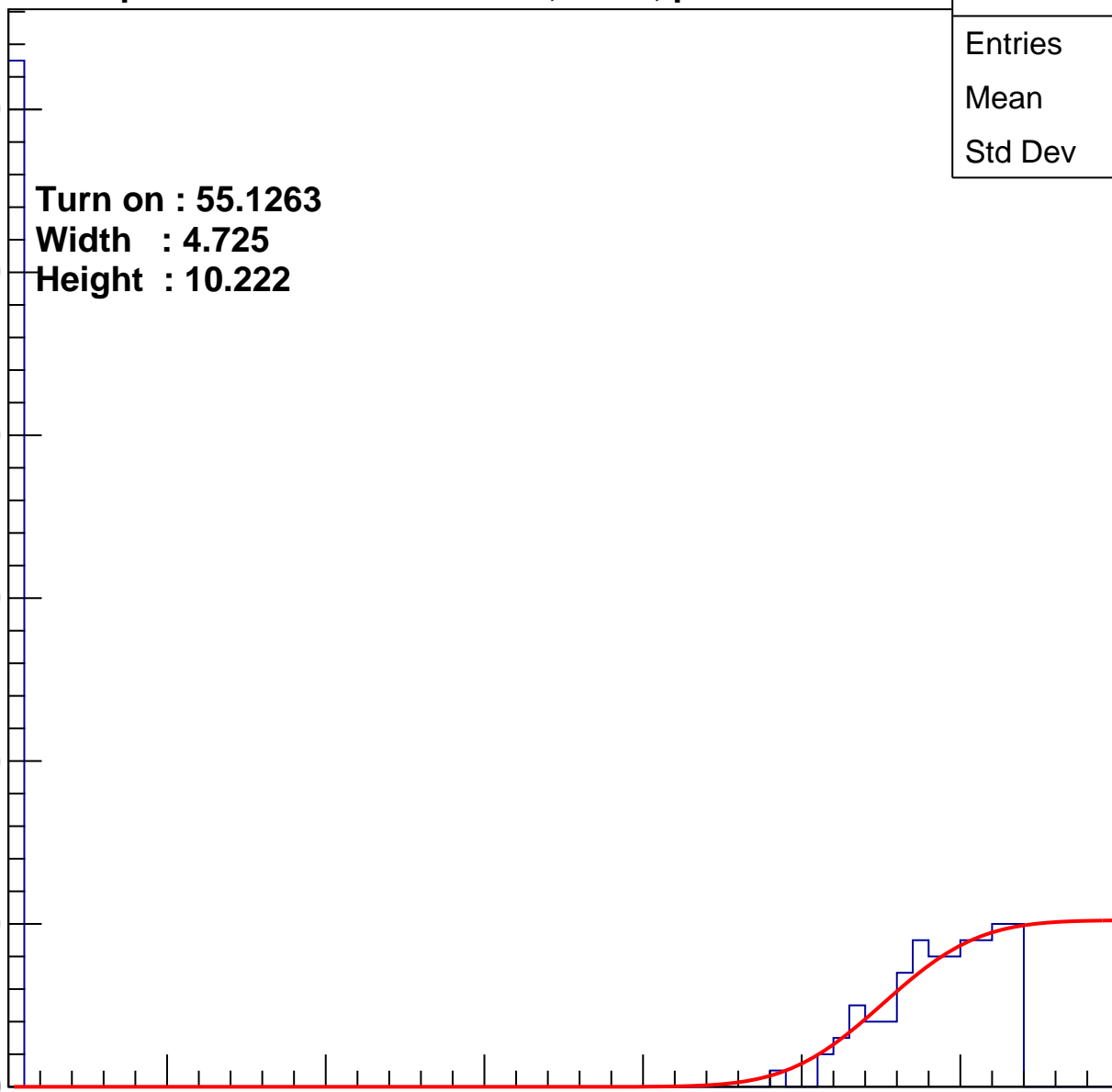
40

50

60

70

ampl



B1L104S, U23-ch100

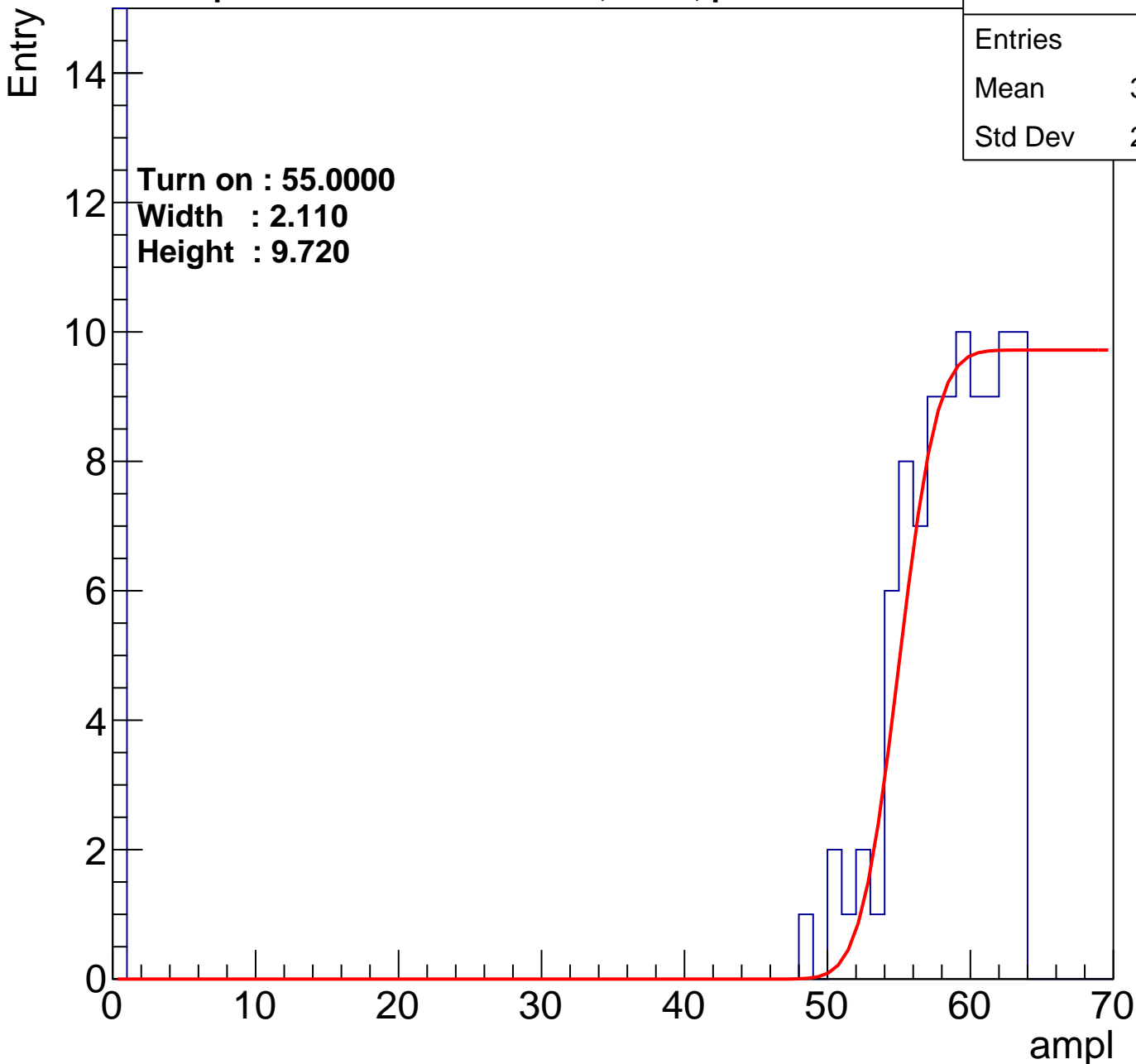
calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	30.76
Std Dev	29.19

Turn on : 55.0000

Width : 2.110

Height : 9.720



B1L104S, U23-ch101

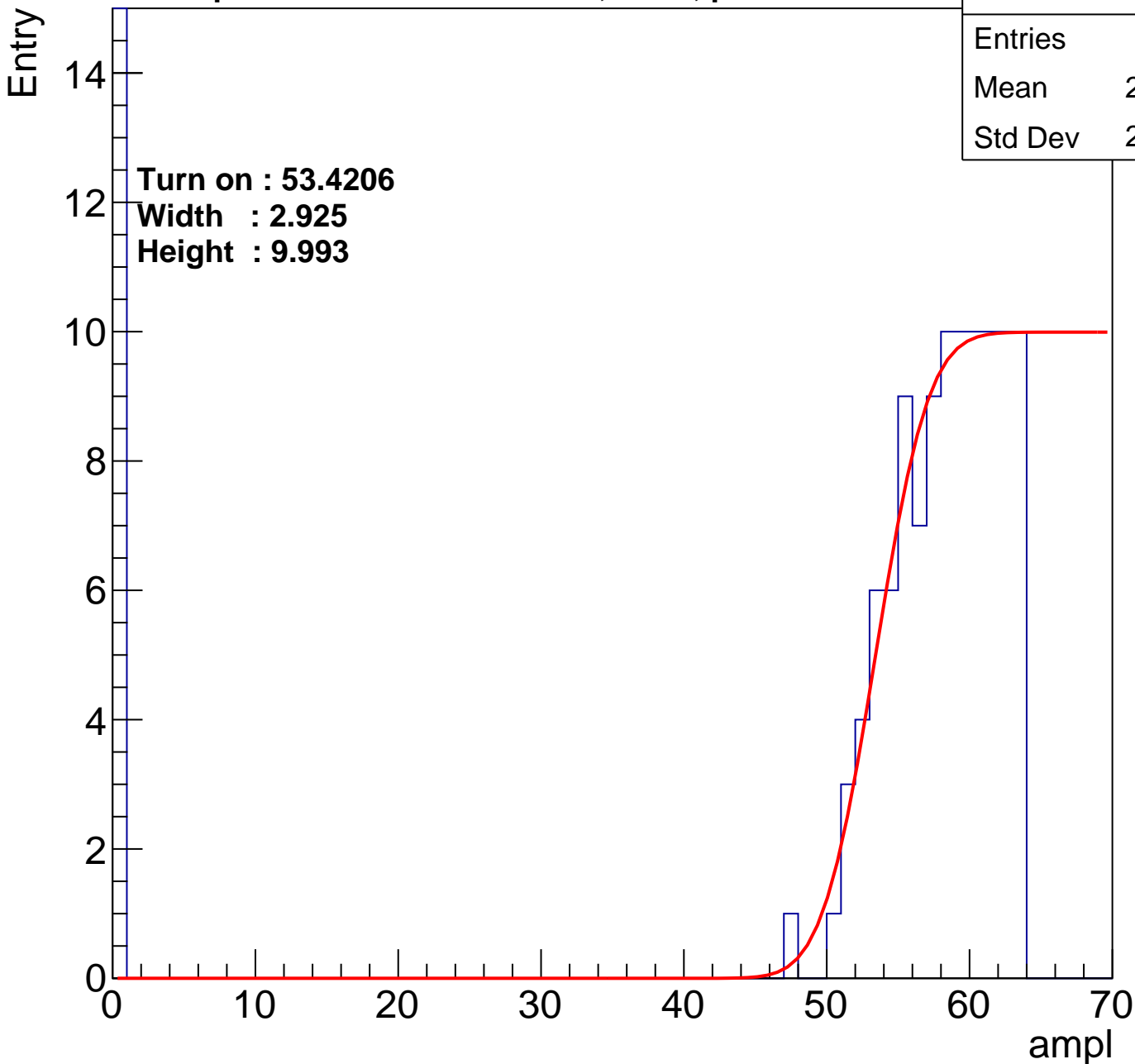
calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	29.05
Std Dev	29.03

Turn on : 53.4206

Width : 2.925

Height : 9.993



B1L104S, U23-ch102

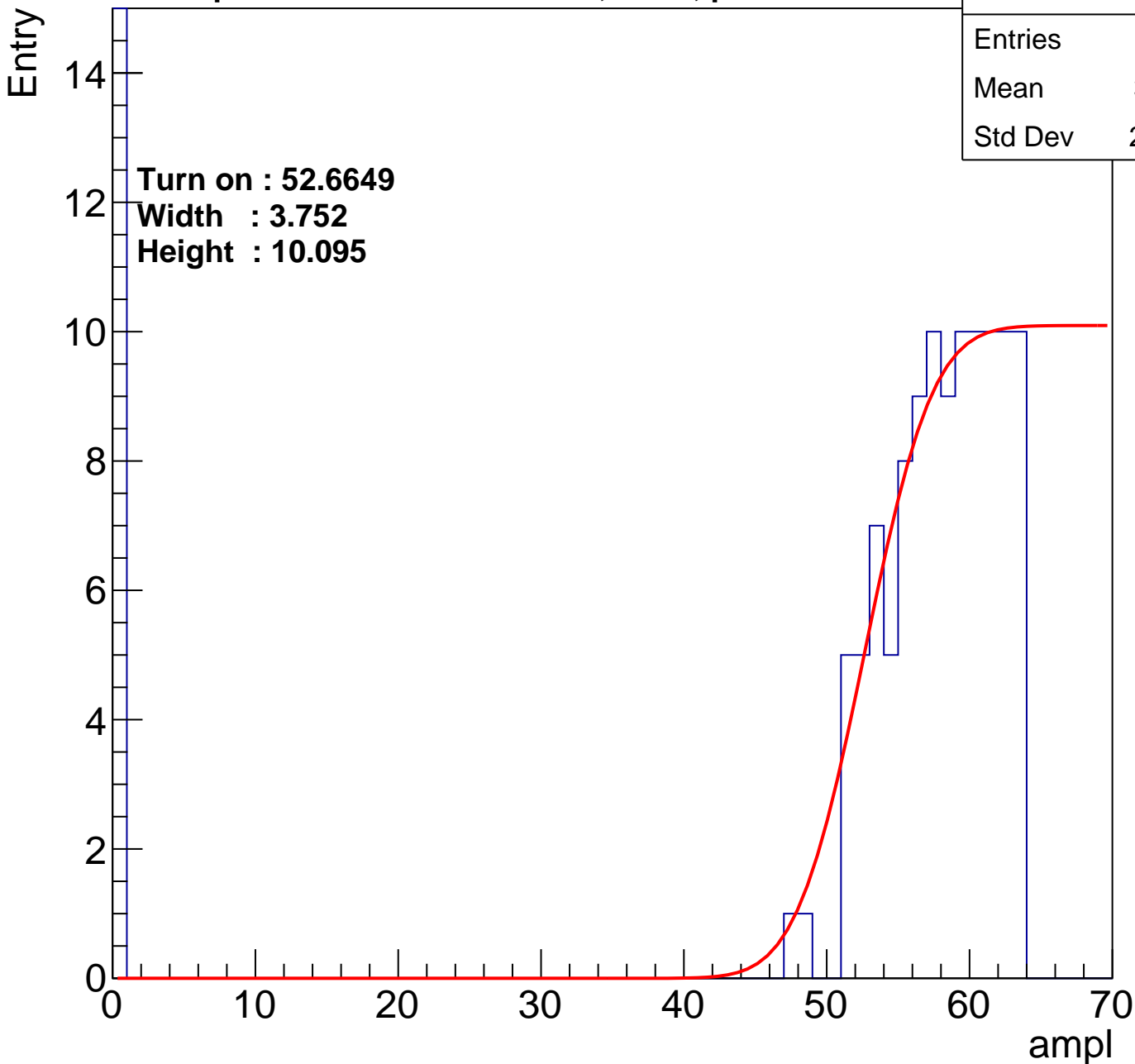
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	33.71
Std Dev	28.53

Turn on : 52.6649

Width : 3.752

Height : 10.095



B1L104S, U23-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	30.97
Std Dev	29.01

Turn on : 54.1155

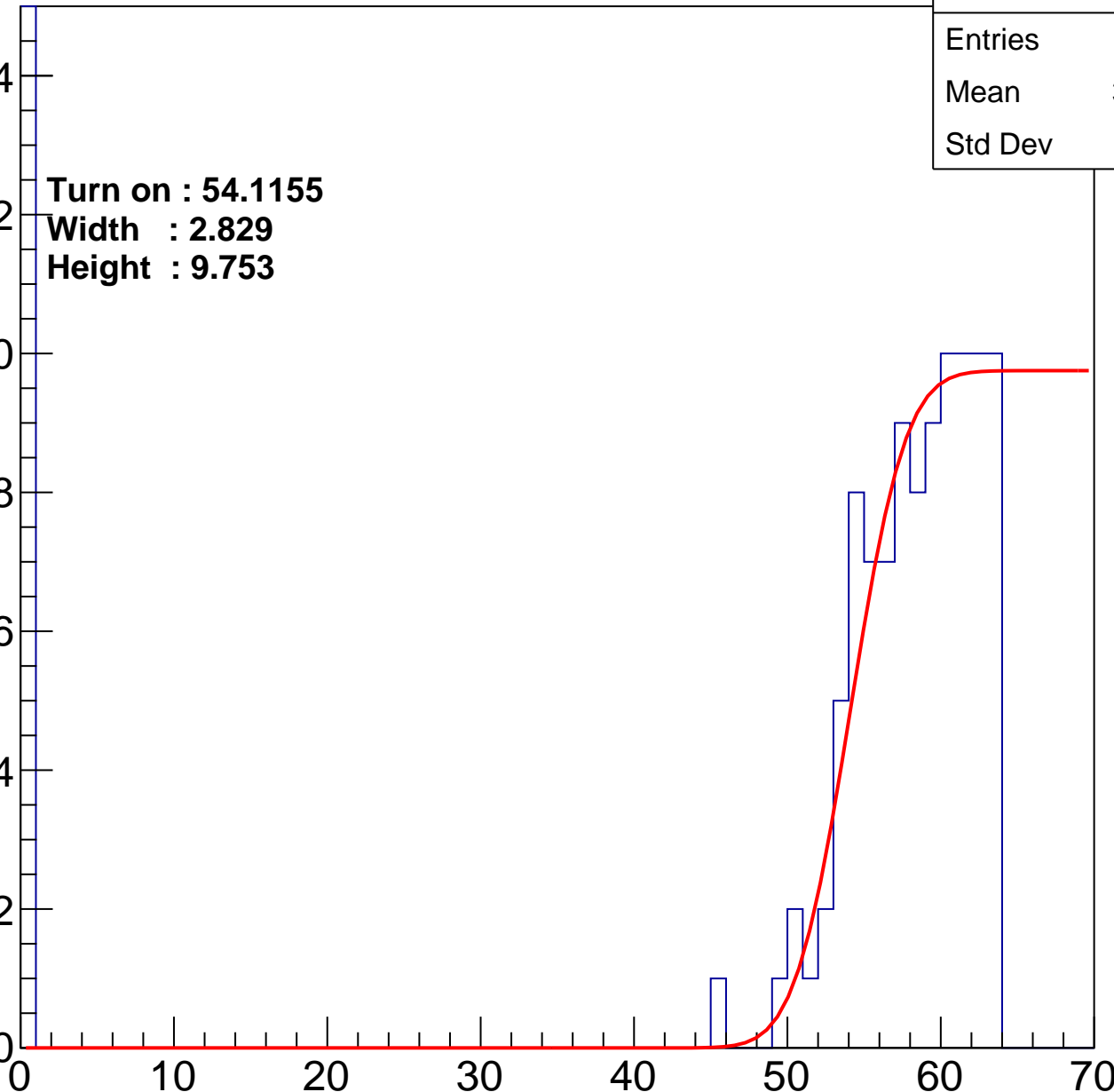
Width : 2.829

Height : 9.753

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.07
Std Dev	28.82

Turn on : 54.3001

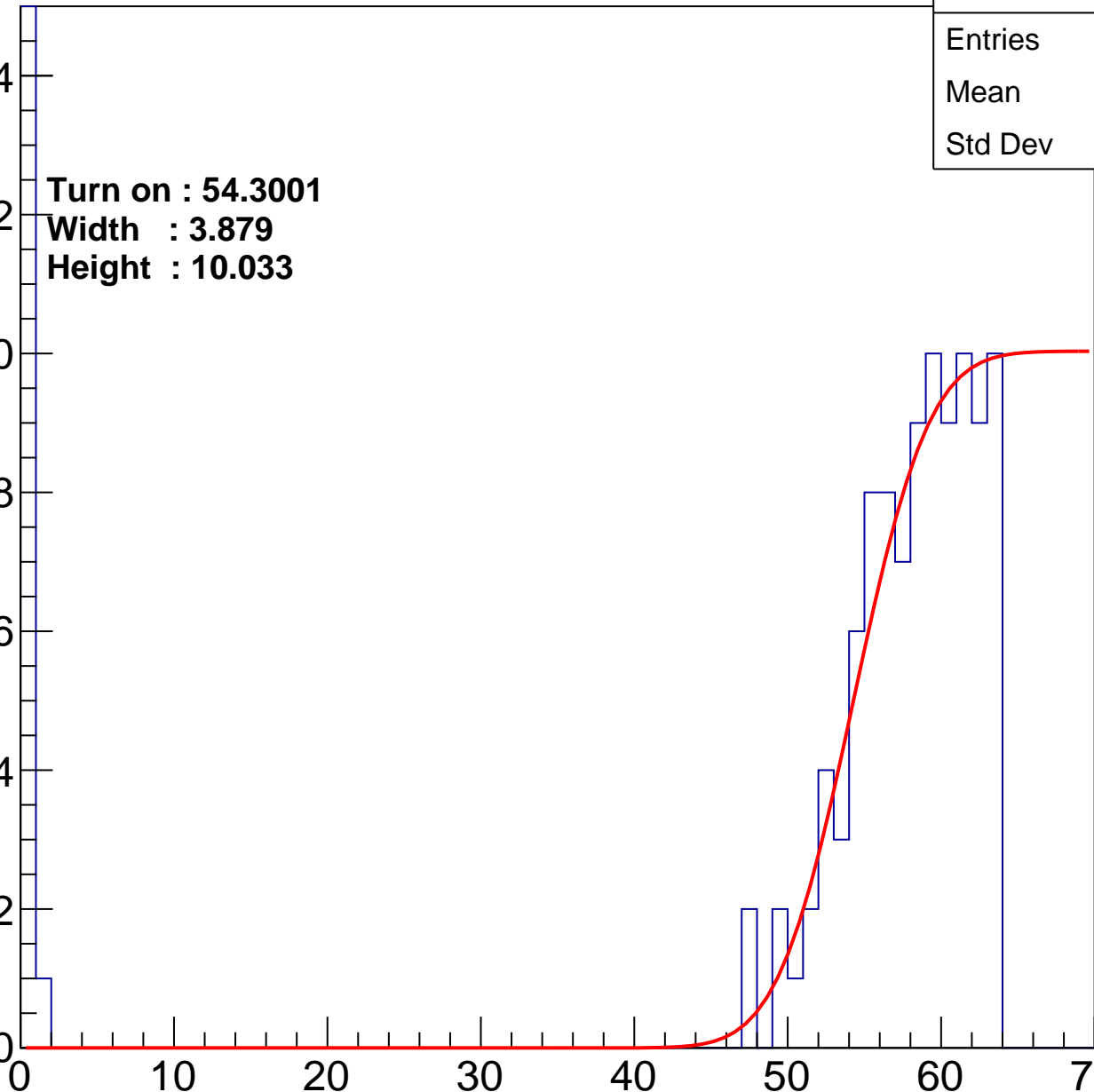
Width : 3.879

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	31.65
Std Dev	29.17

Turn on : 54.4856

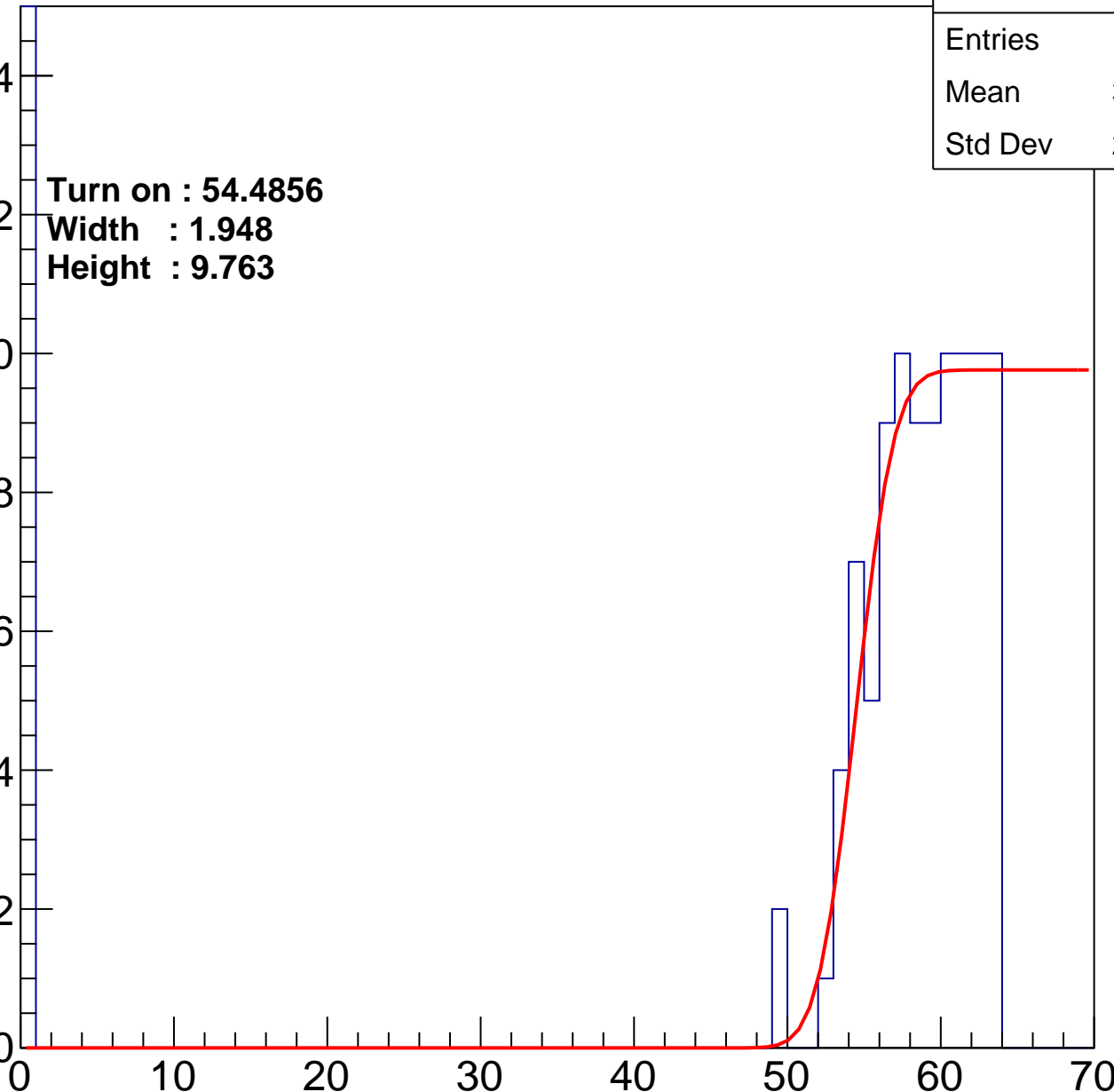
Width : 1.948

Height : 9.763

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	26.63
Std Dev	29.07

Turn on : 55.3532

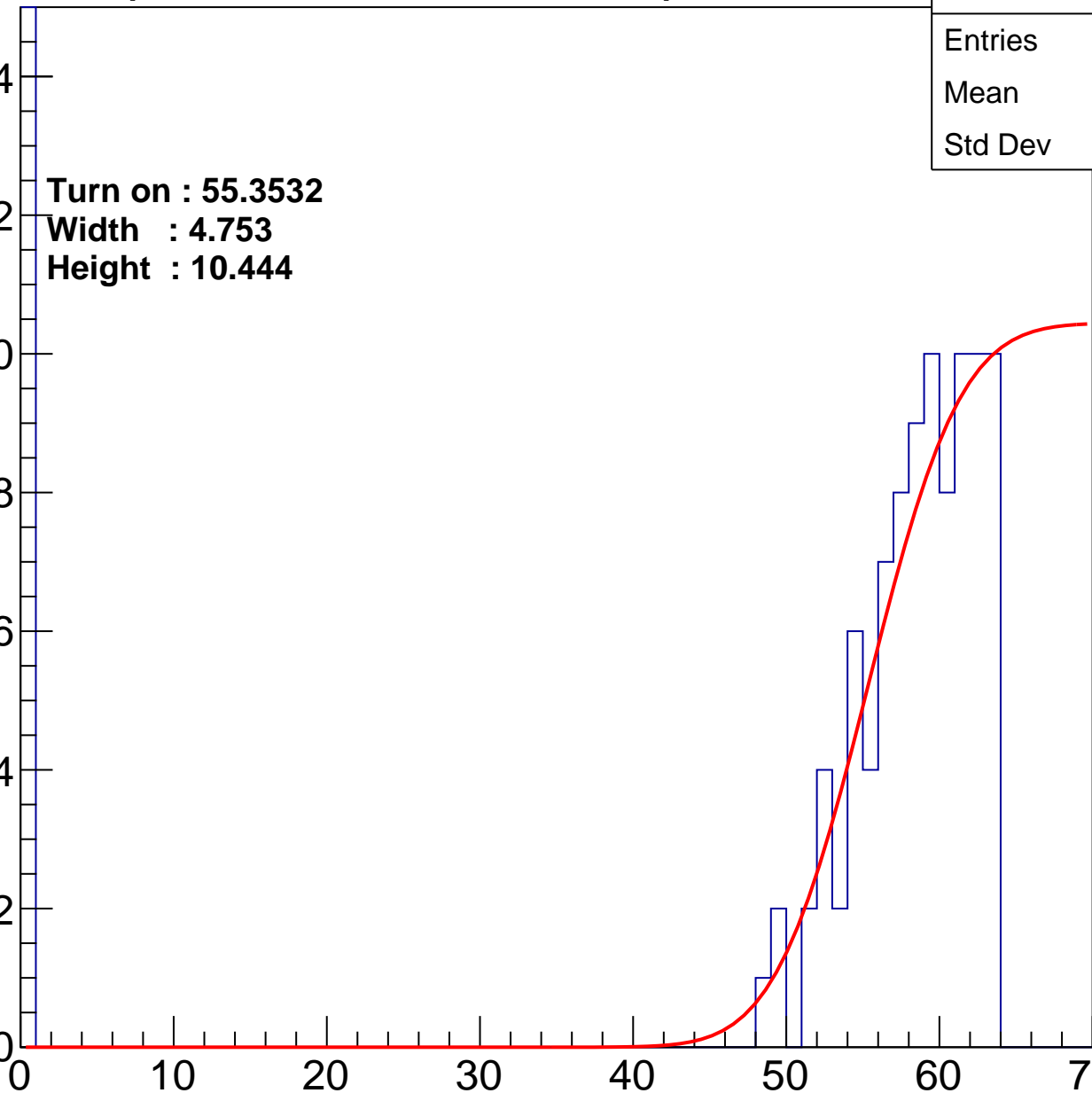
Width : 4.753

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	32.22
Std Dev	28.76

Turn on : 53.9052

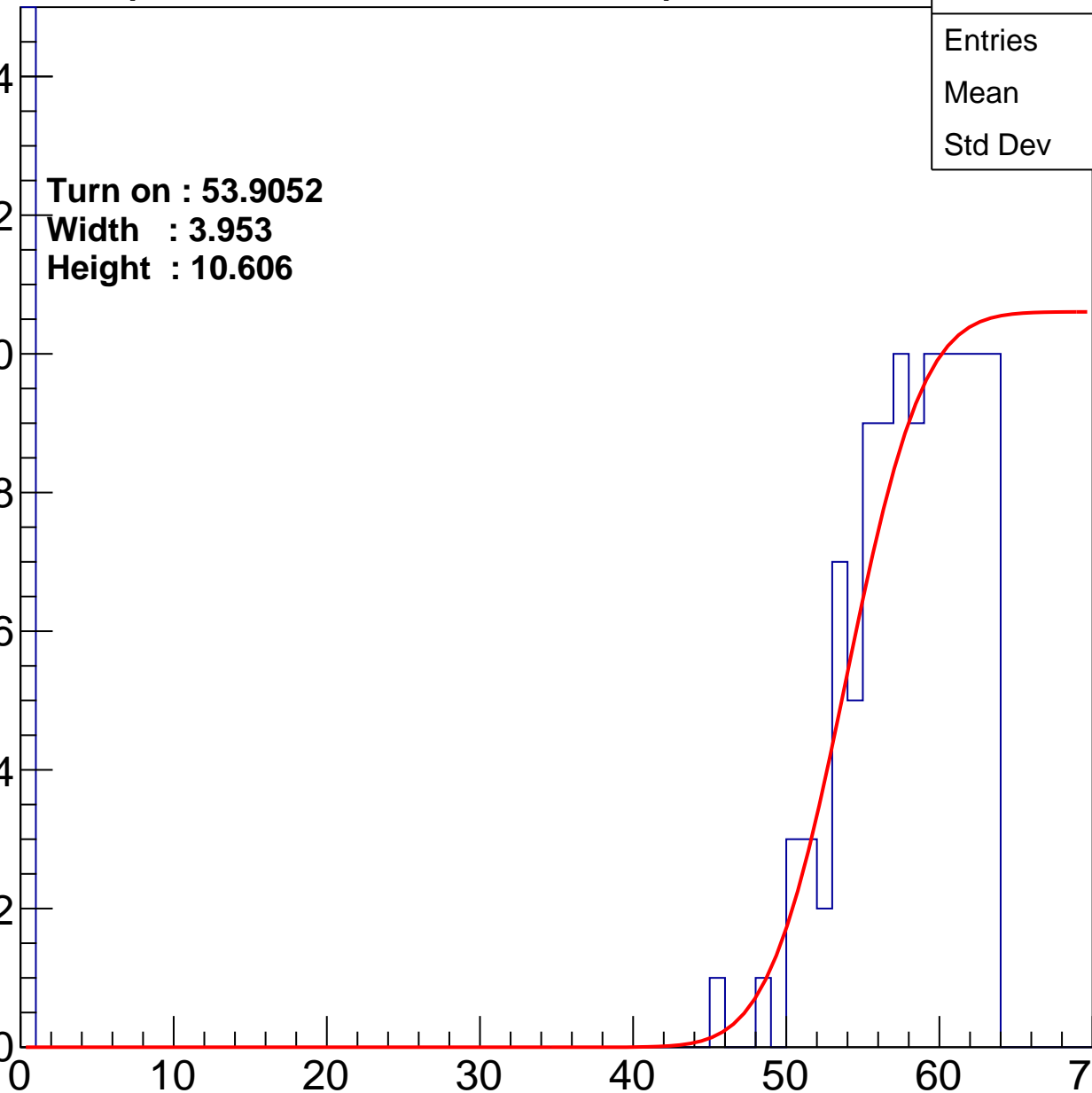
Width : 3.953

Height : 10.606

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	29.43
Std Dev	29.09

Turn on : 53.4679

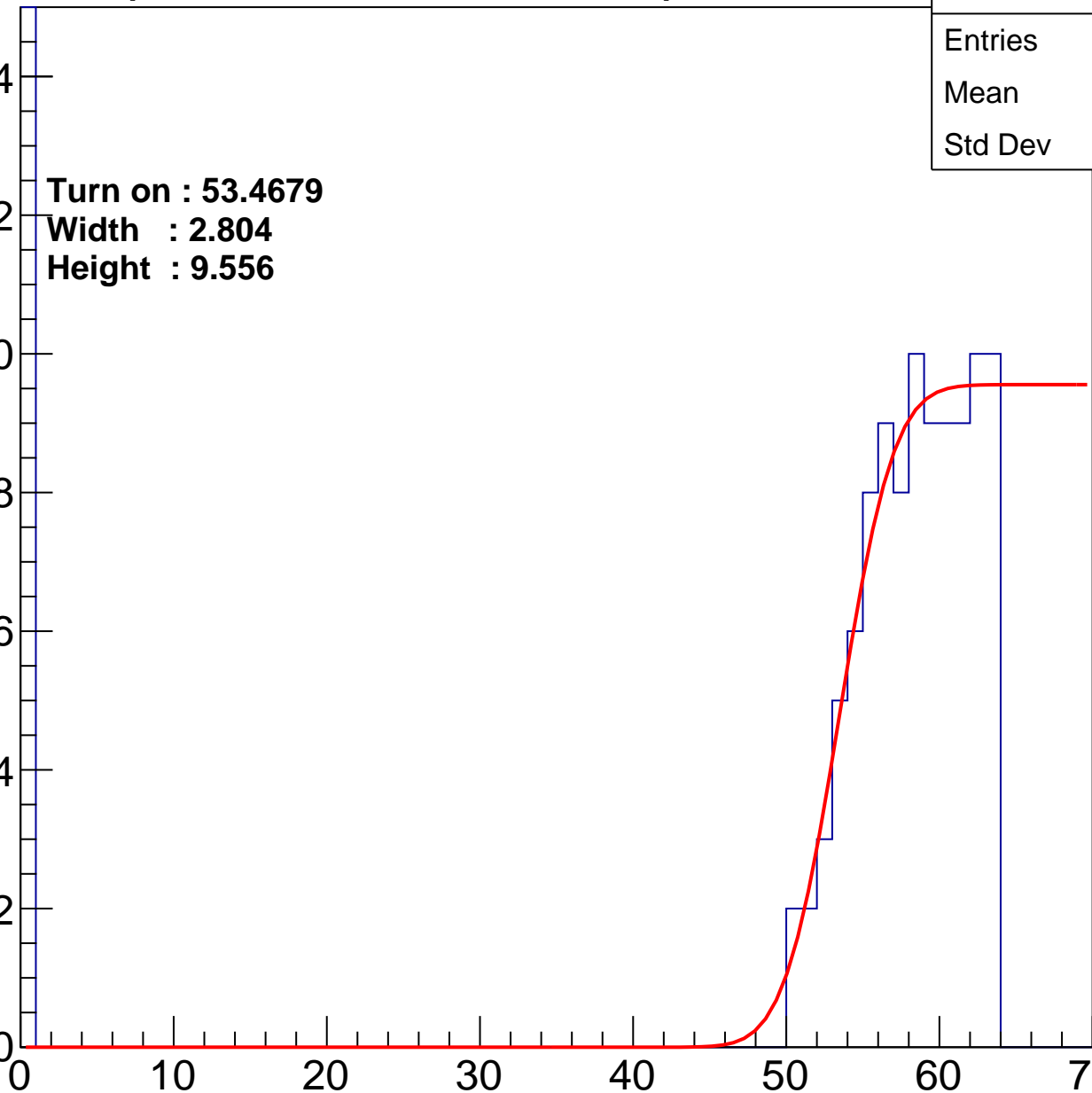
Width : 2.804

Height : 9.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entries	253
Mean	23.81
Std Dev	28.59

Turn on : 54.1018

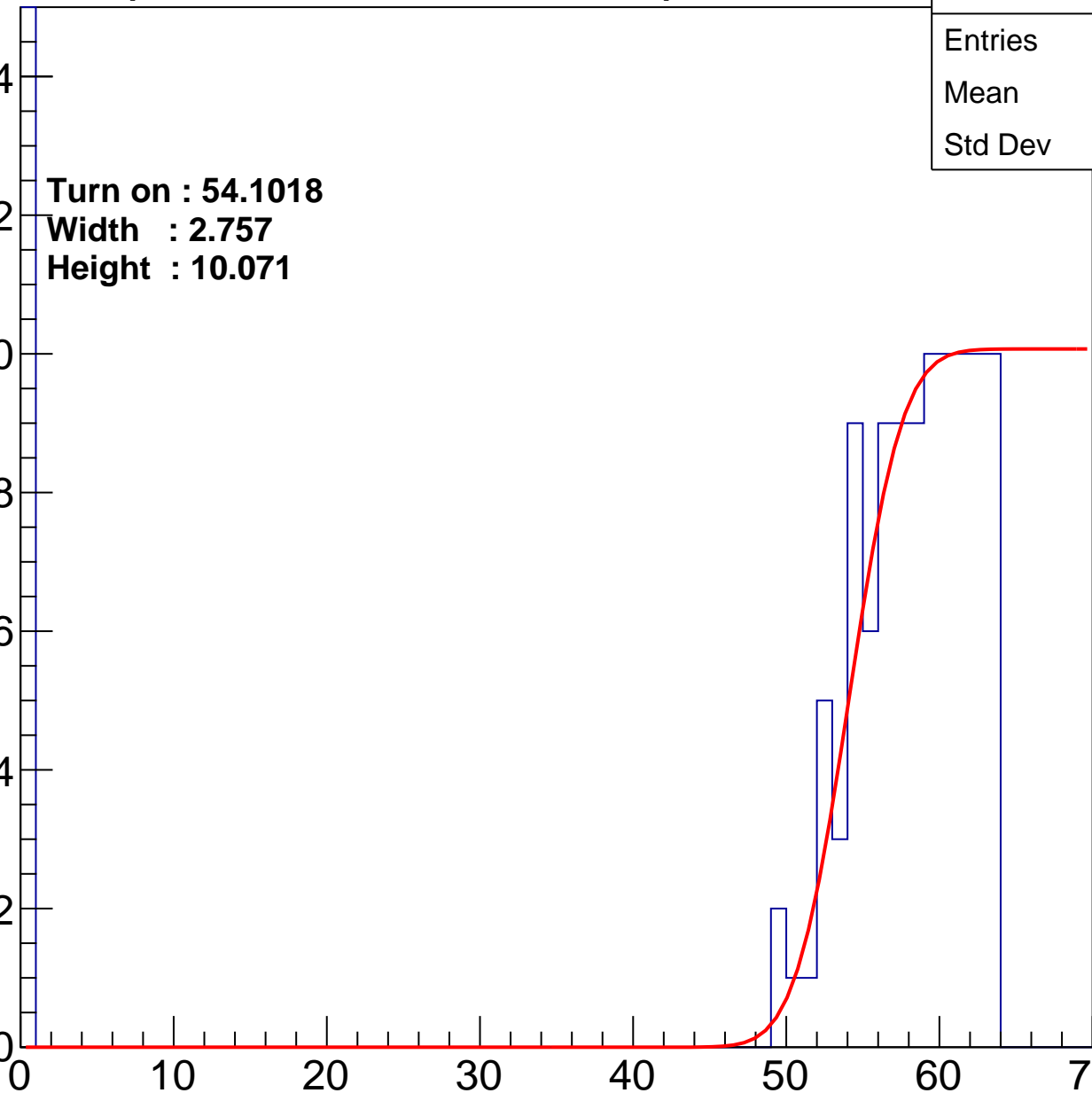
Width : 2.757

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.48
Std Dev	28.67

Turn on : 53.8543

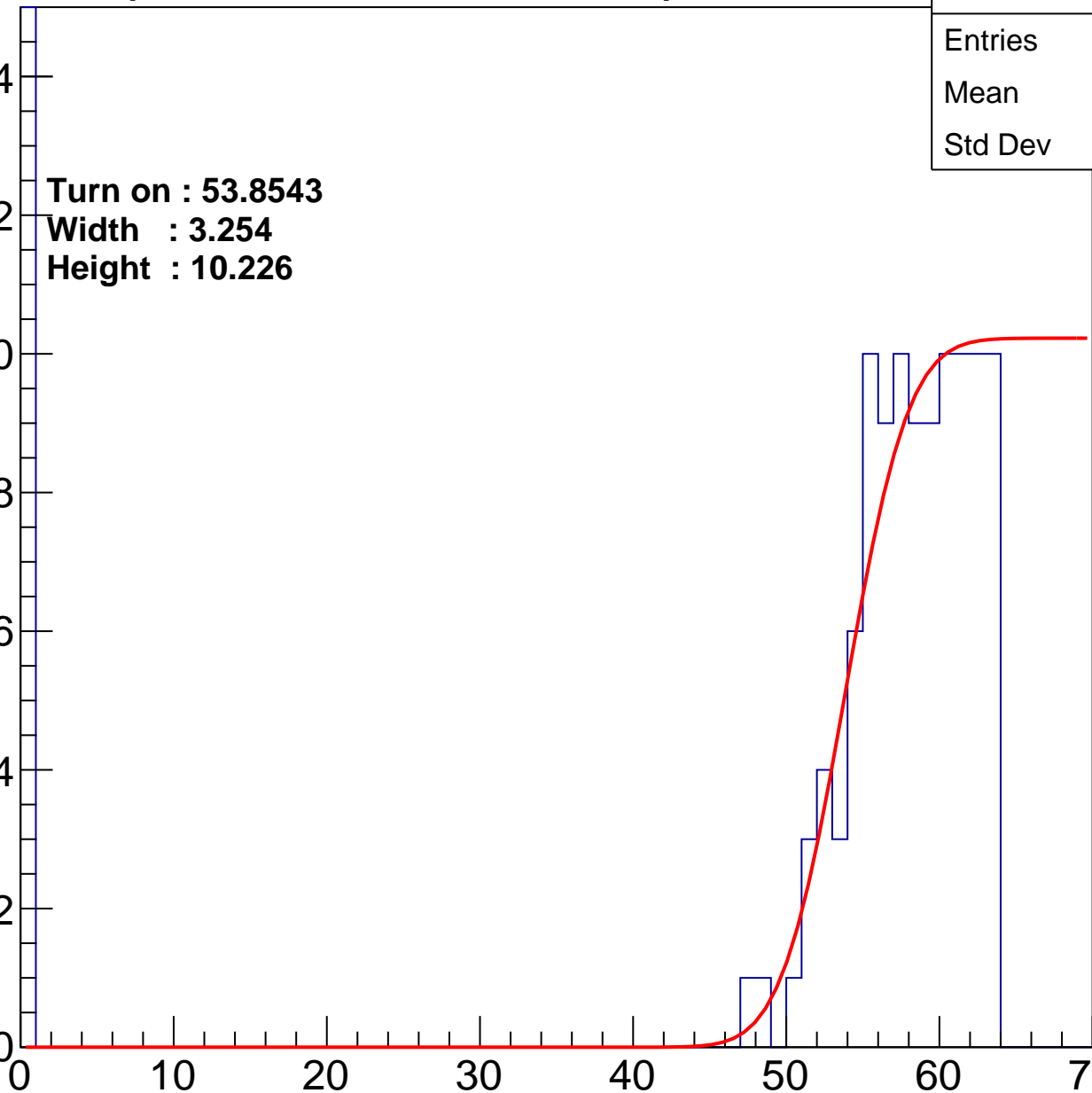
Width : 3.254

Height : 10.226

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	31.3
Std Dev	29.12

Turn on : 55.2857

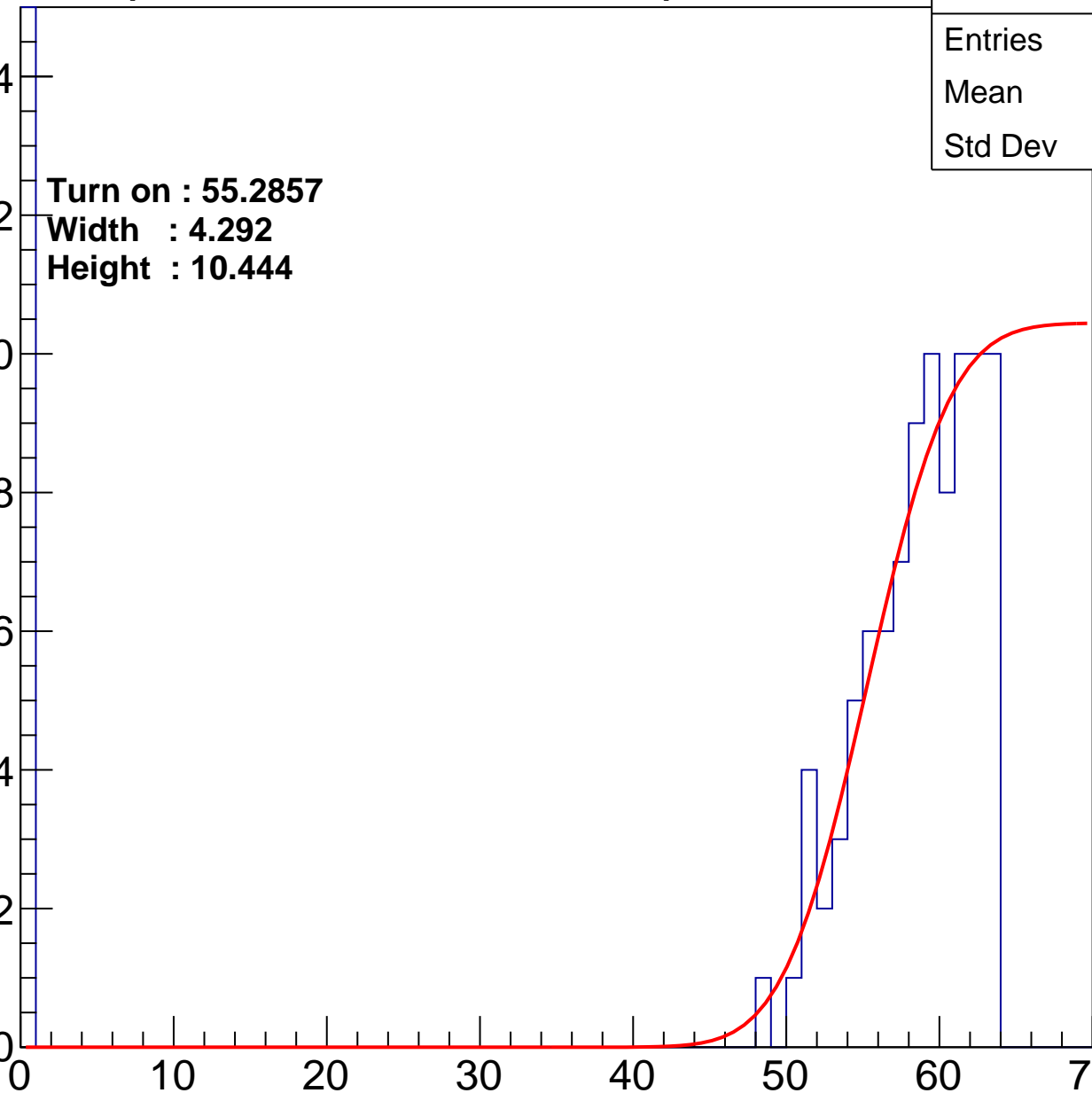
Width : 4.292

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	33.75
Std Dev	28.24

Turn on : 52.0111

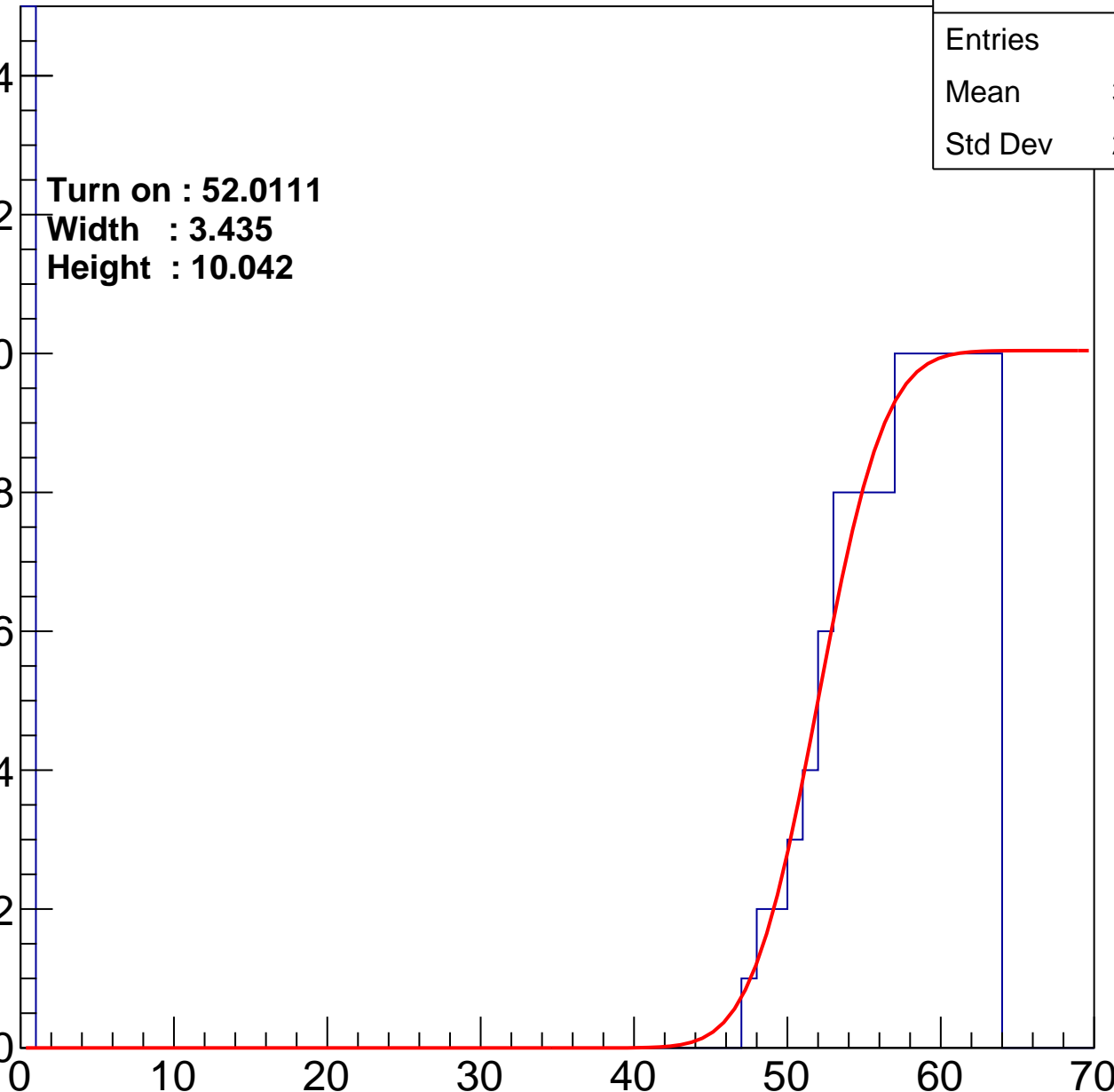
Width : 3.435

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	186
Mean	39.05
Std Dev	26.26

Turn on : 52.5072

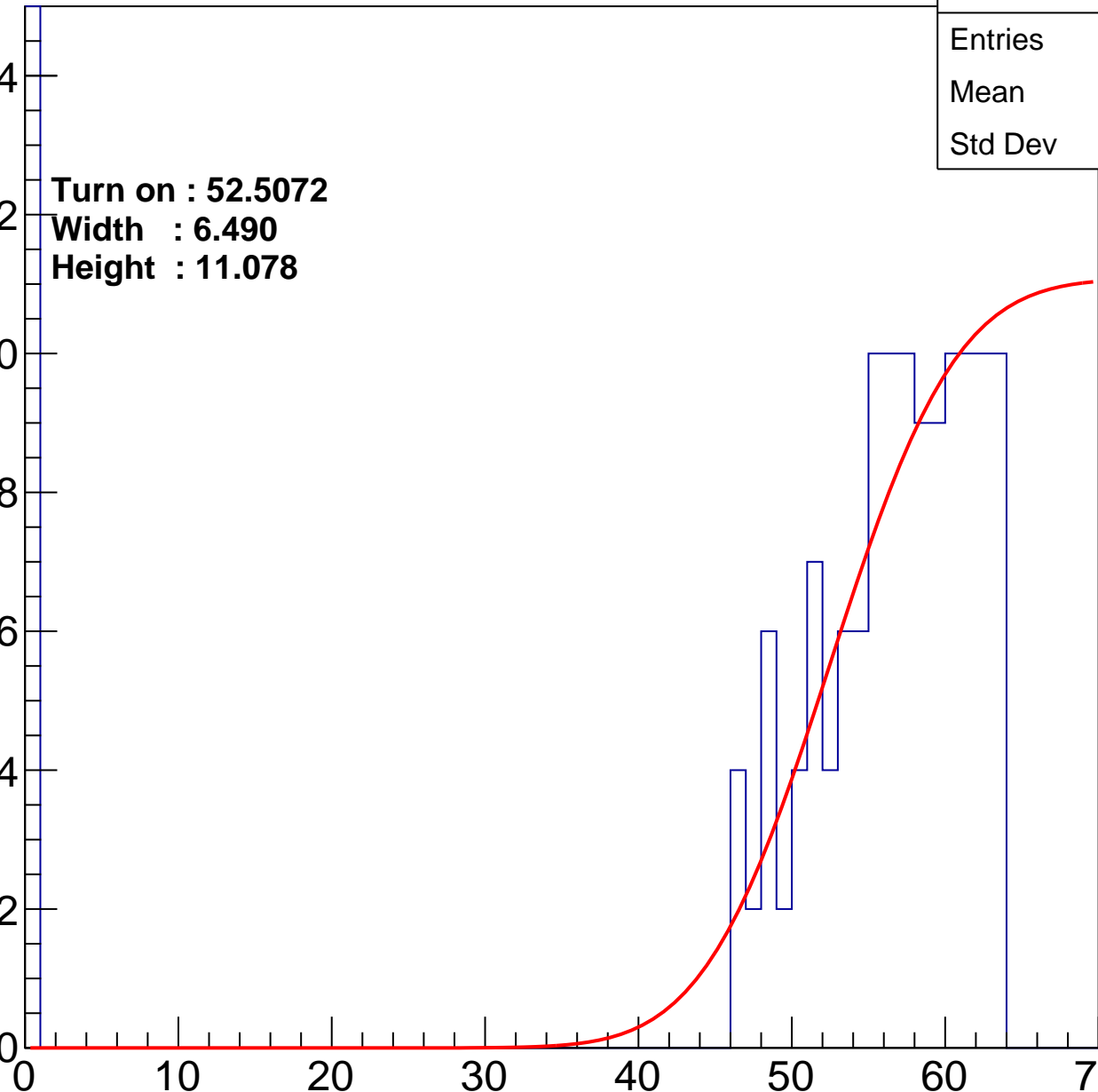
Width : 6.490

Height : 11.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	25.84
Std Dev	28.97

Turn on : 55.7011

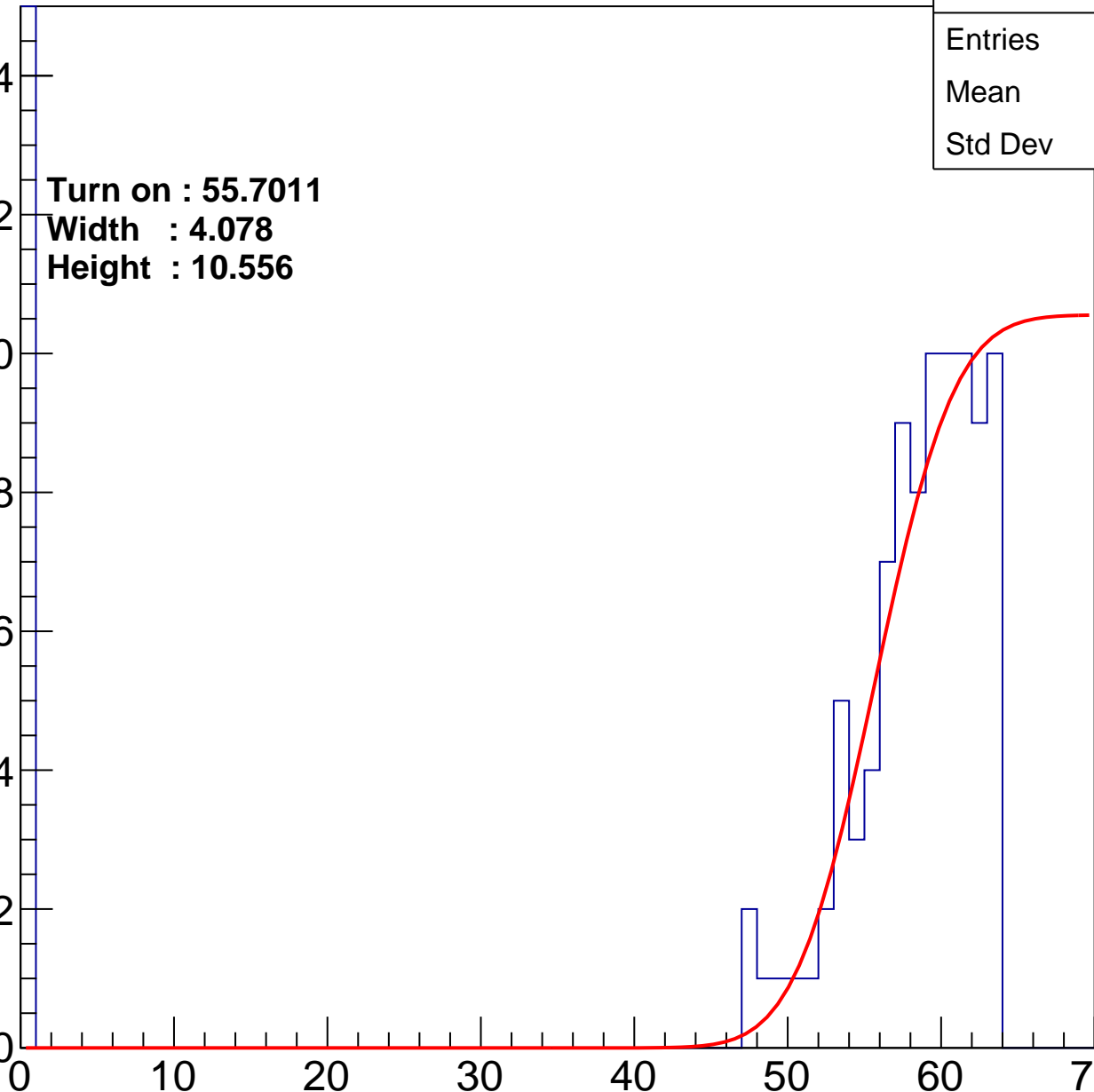
Width : 4.078

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch115

calib_packv5_033123_0516.root, FC#4, port A1

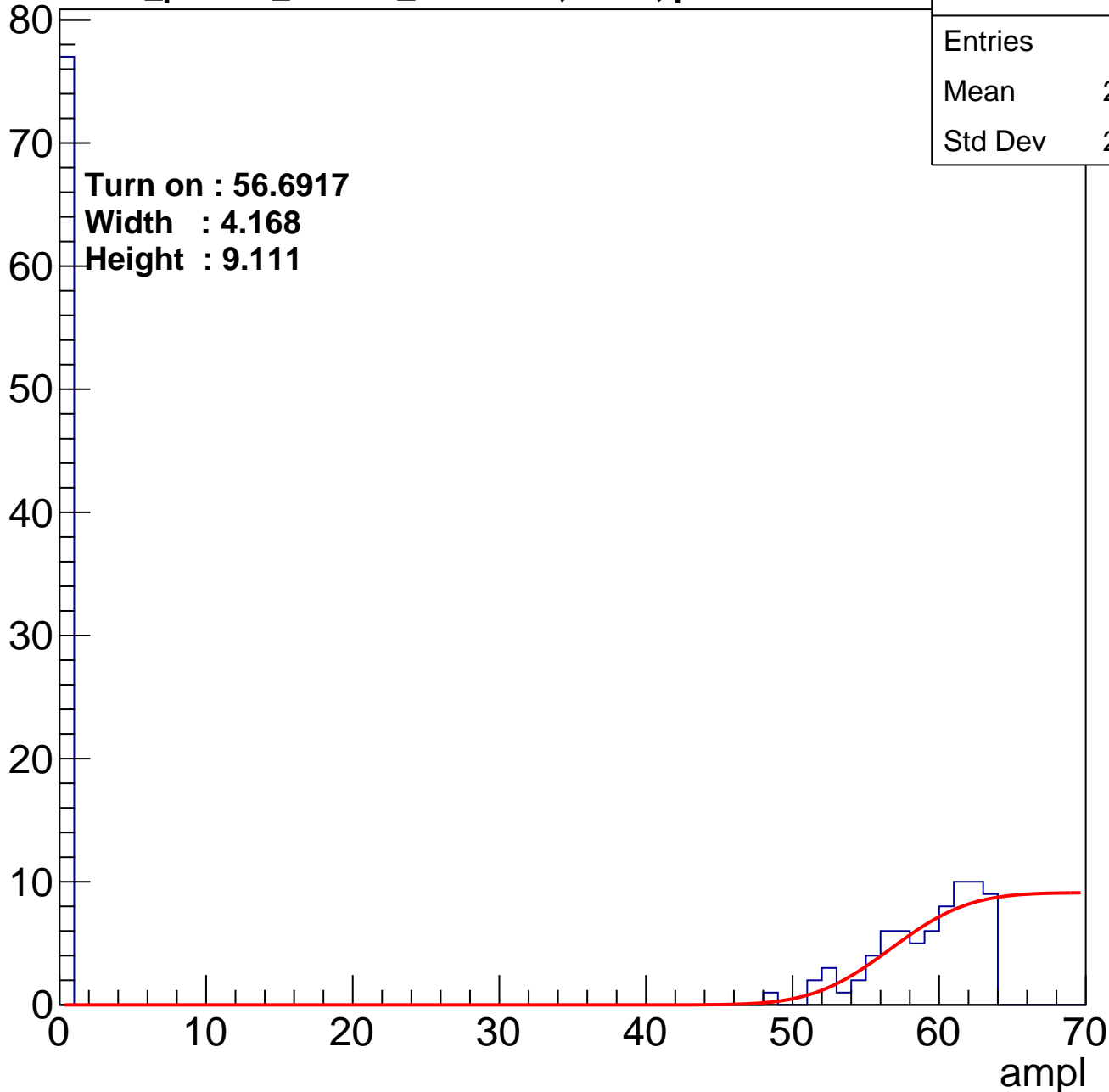
Entries	150
Mean	28.57
Std Dev	29.45

Turn on : 56.6917

Width : 4.168

Height : 9.111

Entry



B1L104S, U23-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	32.24
Std Dev	28.46

Turn on : 52.8015

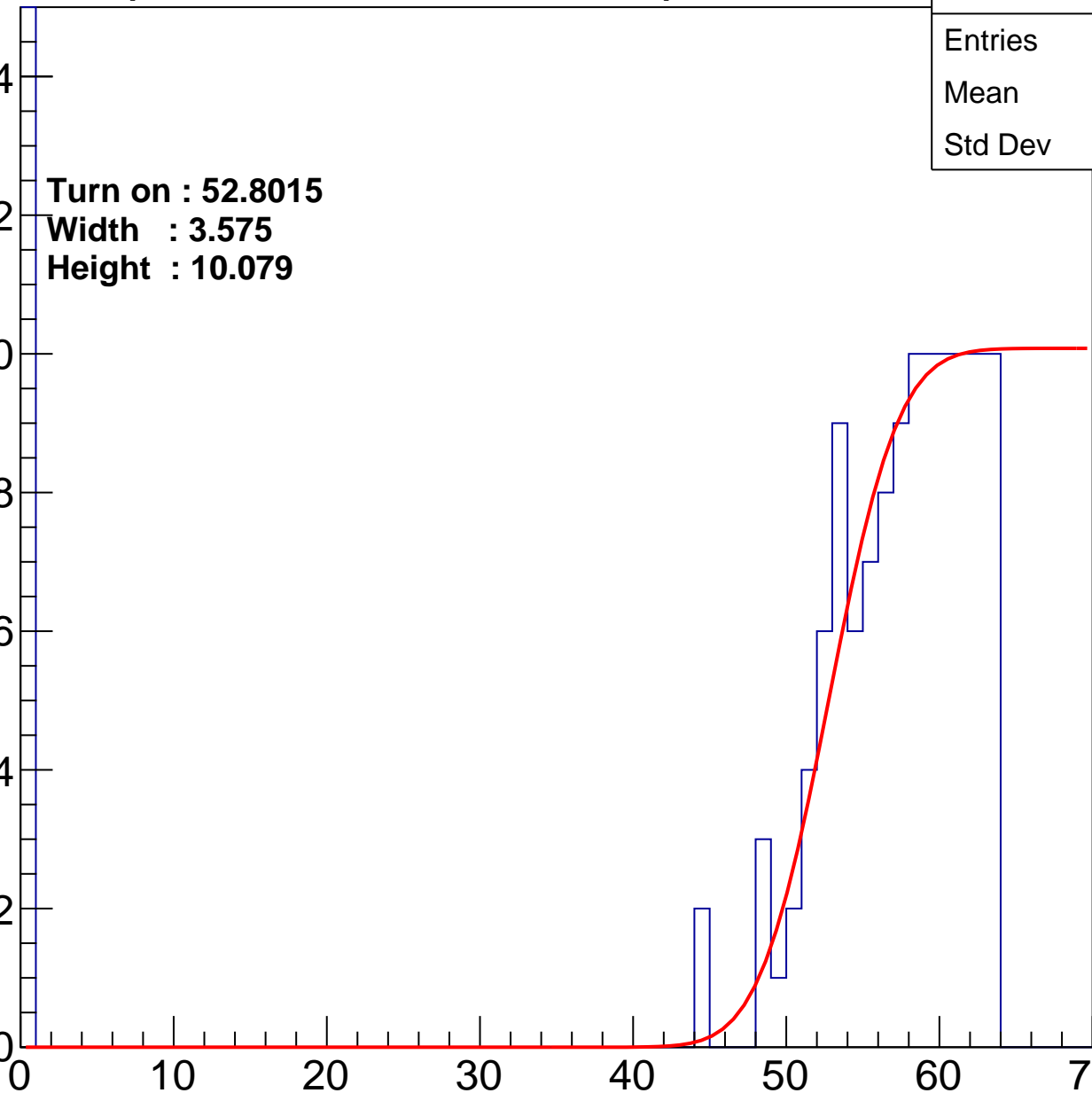
Width : 3.575

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch117

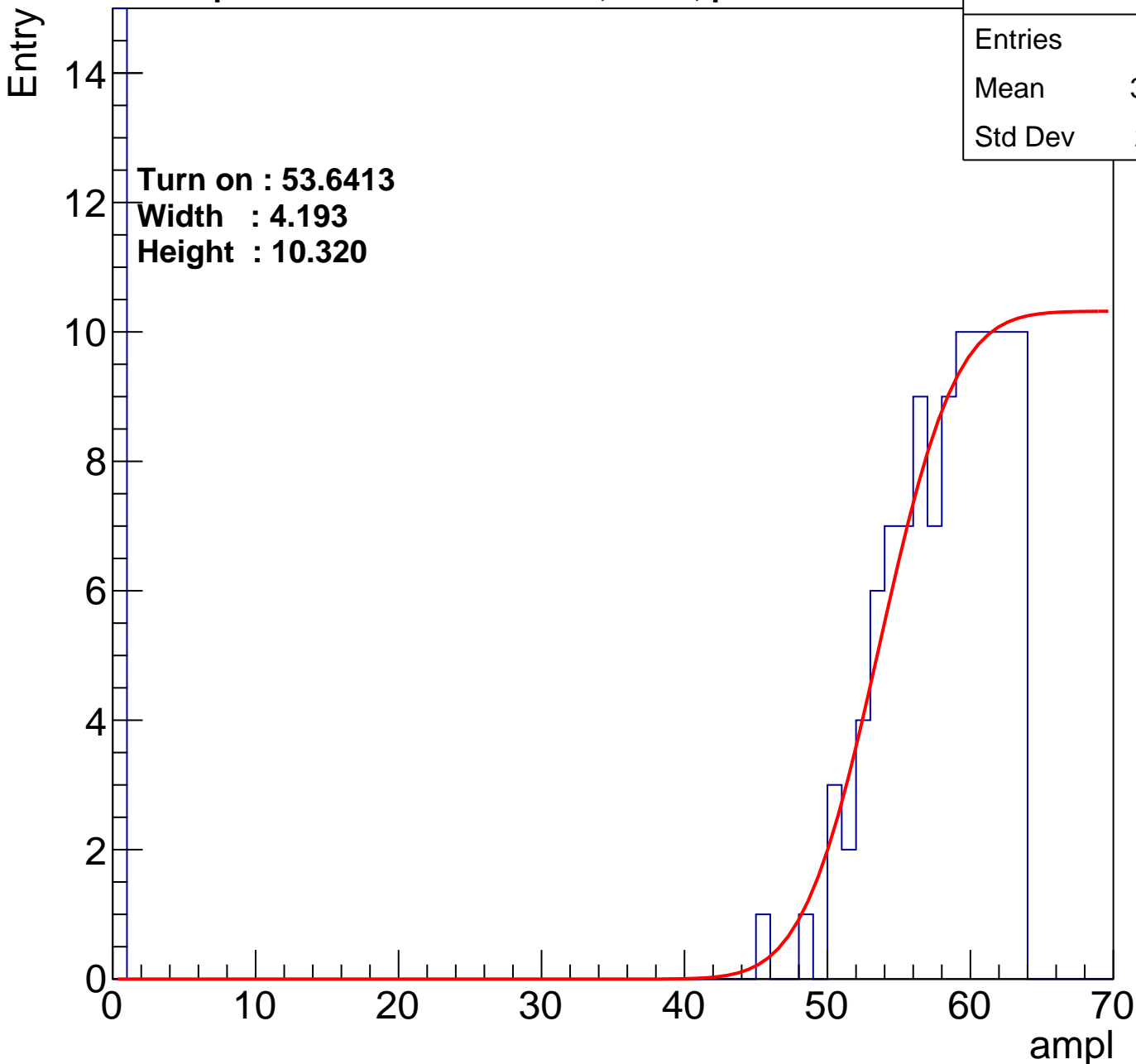
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.94
Std Dev	28.51

Turn on : 53.6413

Width : 4.193

Height : 10.320



B1L104S, U23-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	33.58
Std Dev	28.81

Turn on : 54.4159

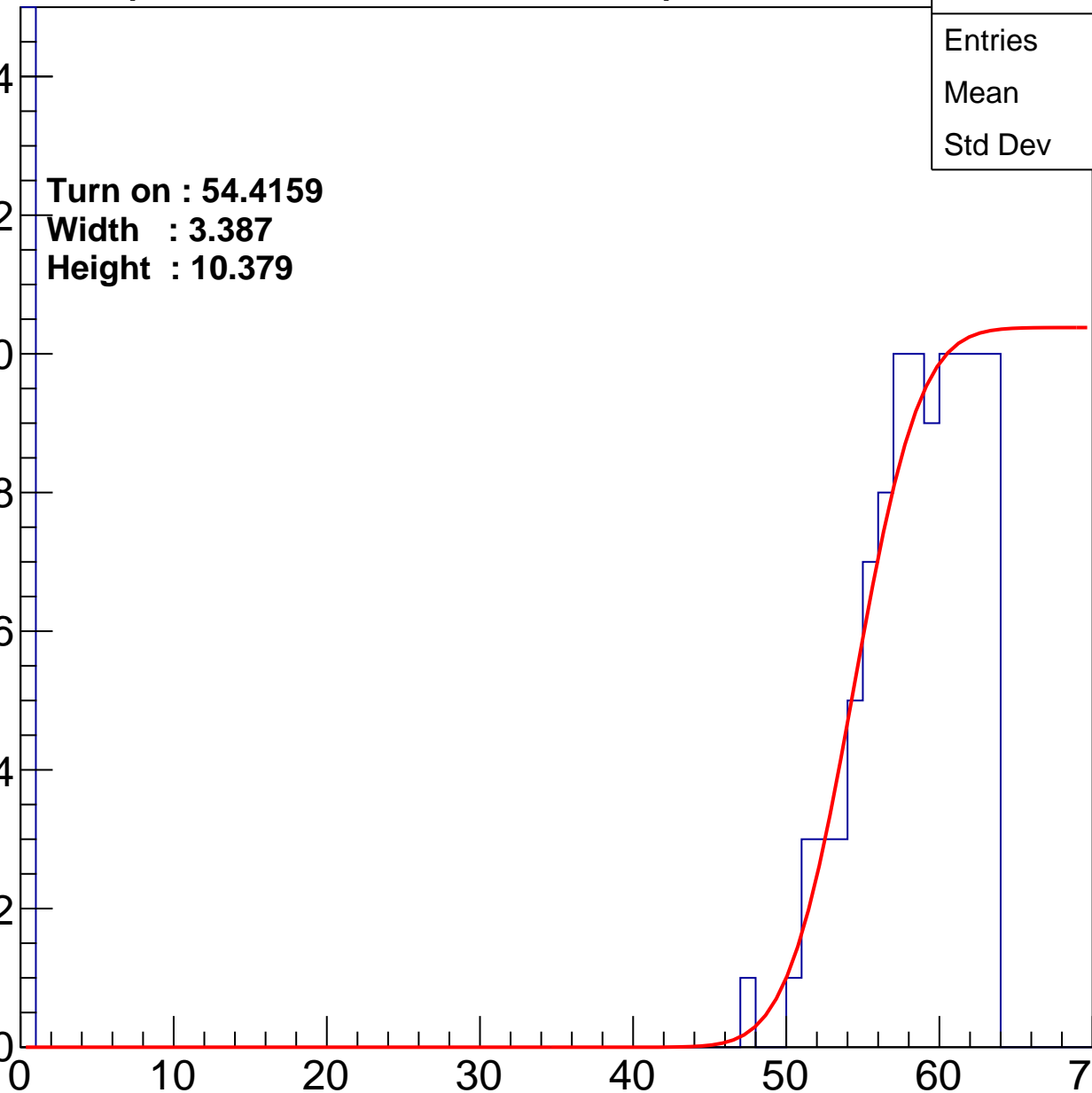
Width : 3.387

Height : 10.379

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	27.93
Std Dev	29.21

Turn on : 55.0019

Width : 3.821

Height : 10.320

Entry

14

12

10

8

6

4

2

0

0

10

20

30

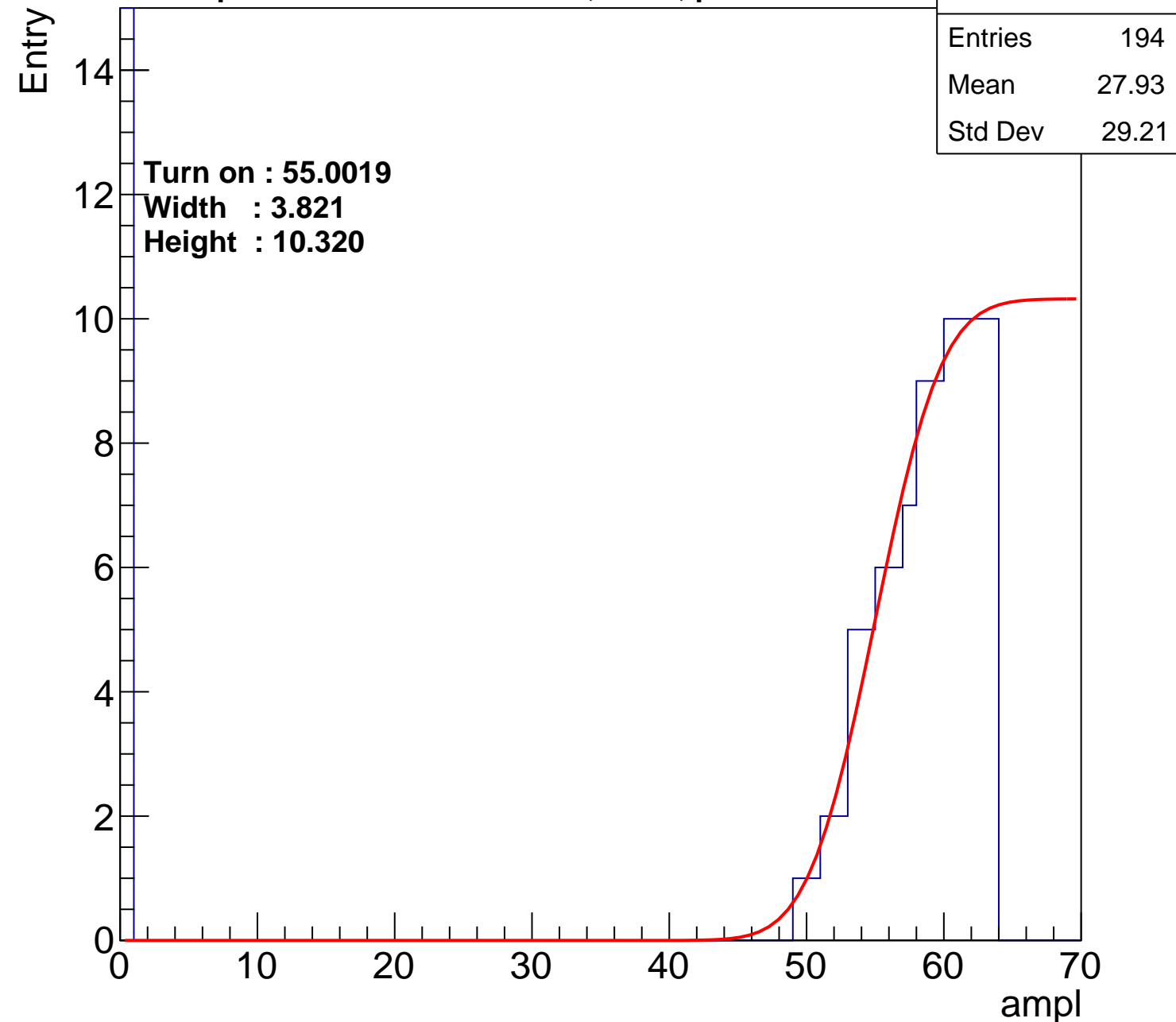
40

50

60

70

ampl



B1L104S, U23-ch120

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	29.87
Std Dev	28.51

Turn on : 54.0005

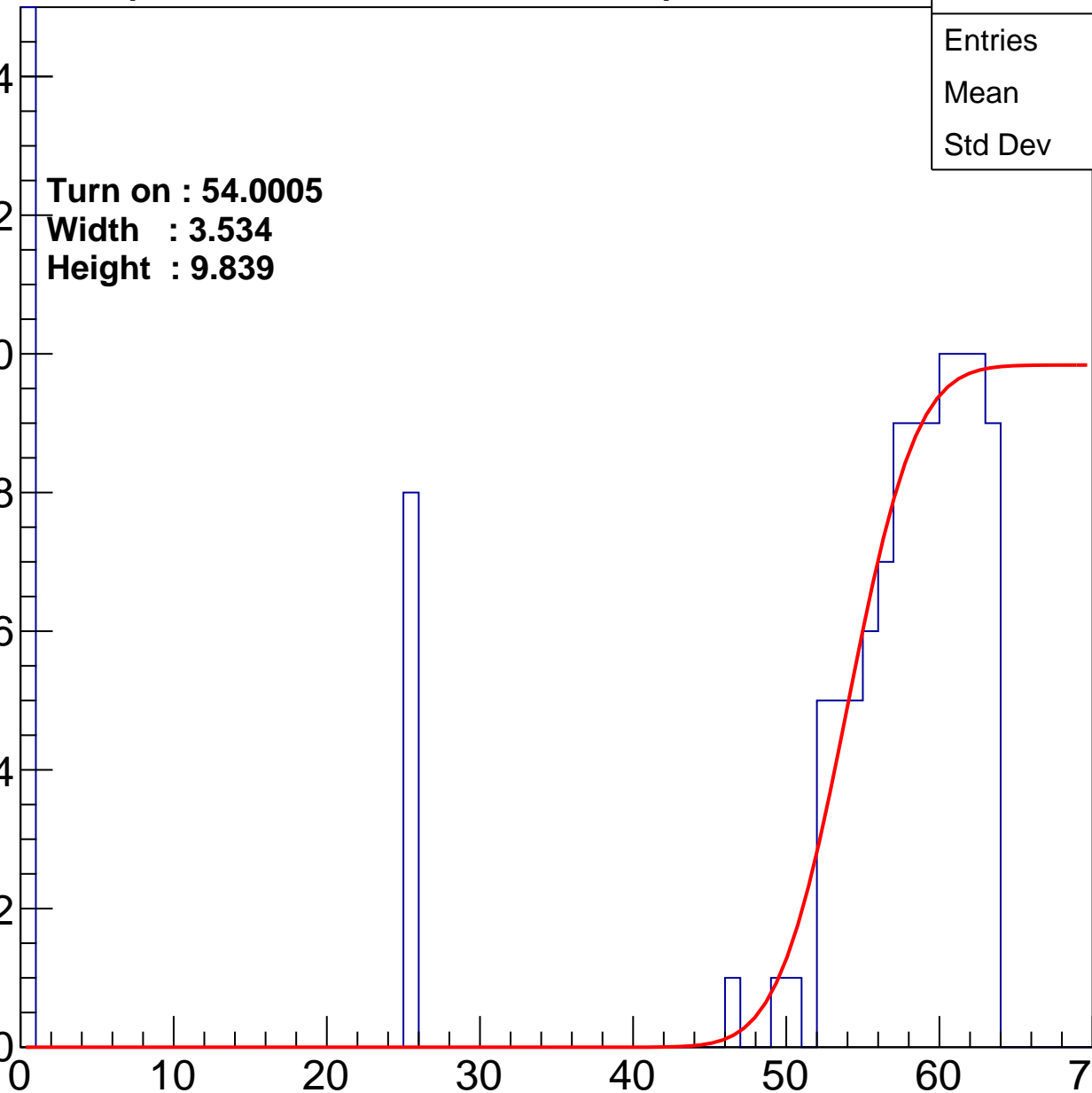
Width : 3.534

Height : 9.839

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	29.58
Std Dev	29.21

Turn on : 55.5909

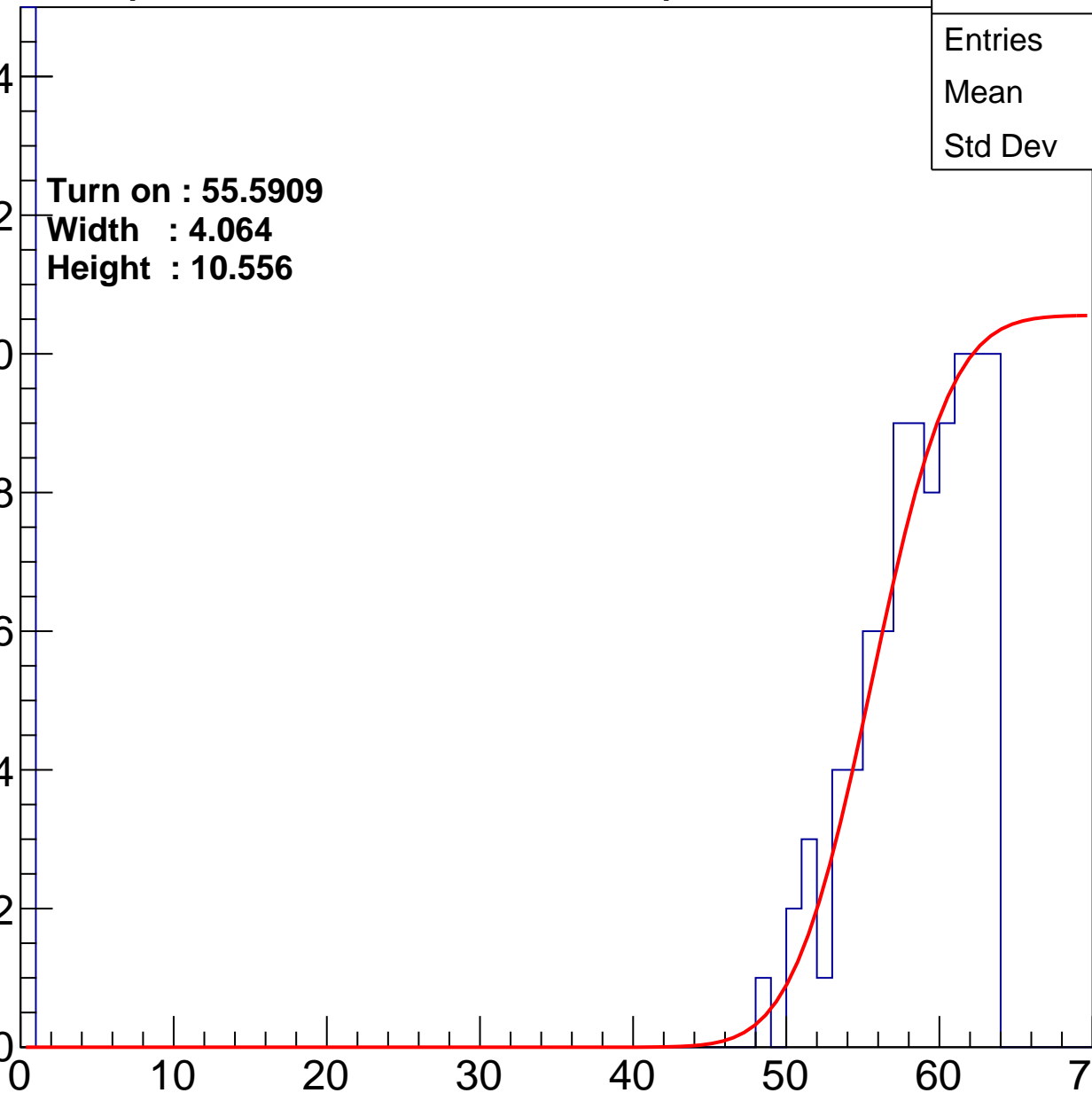
Width : 4.064

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	28.8
Std Dev	29.05

Turn on : 53.5635

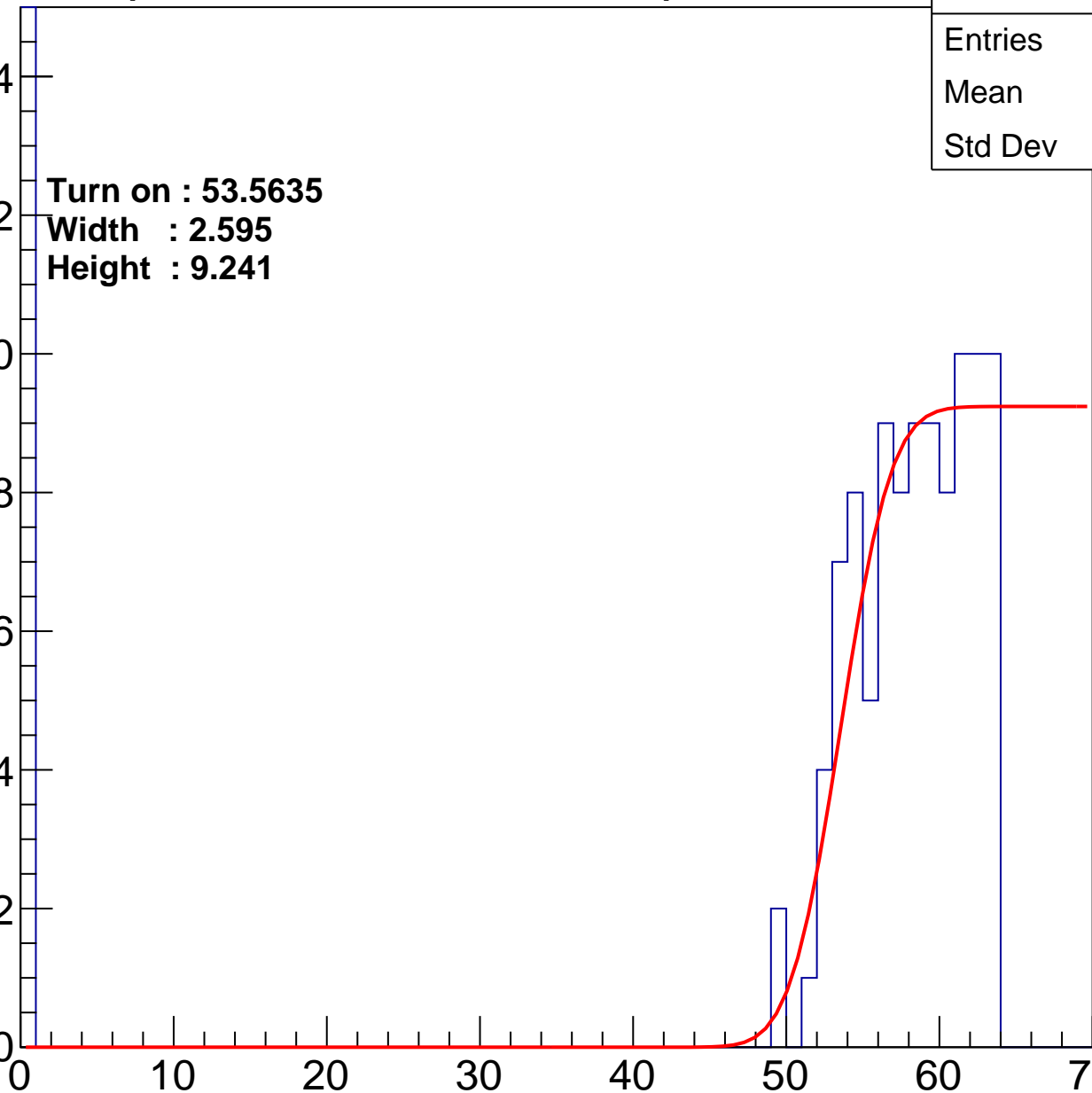
Width : 2.595

Height : 9.241

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	32.53
Std Dev	28.92

Turn on : 54.2027

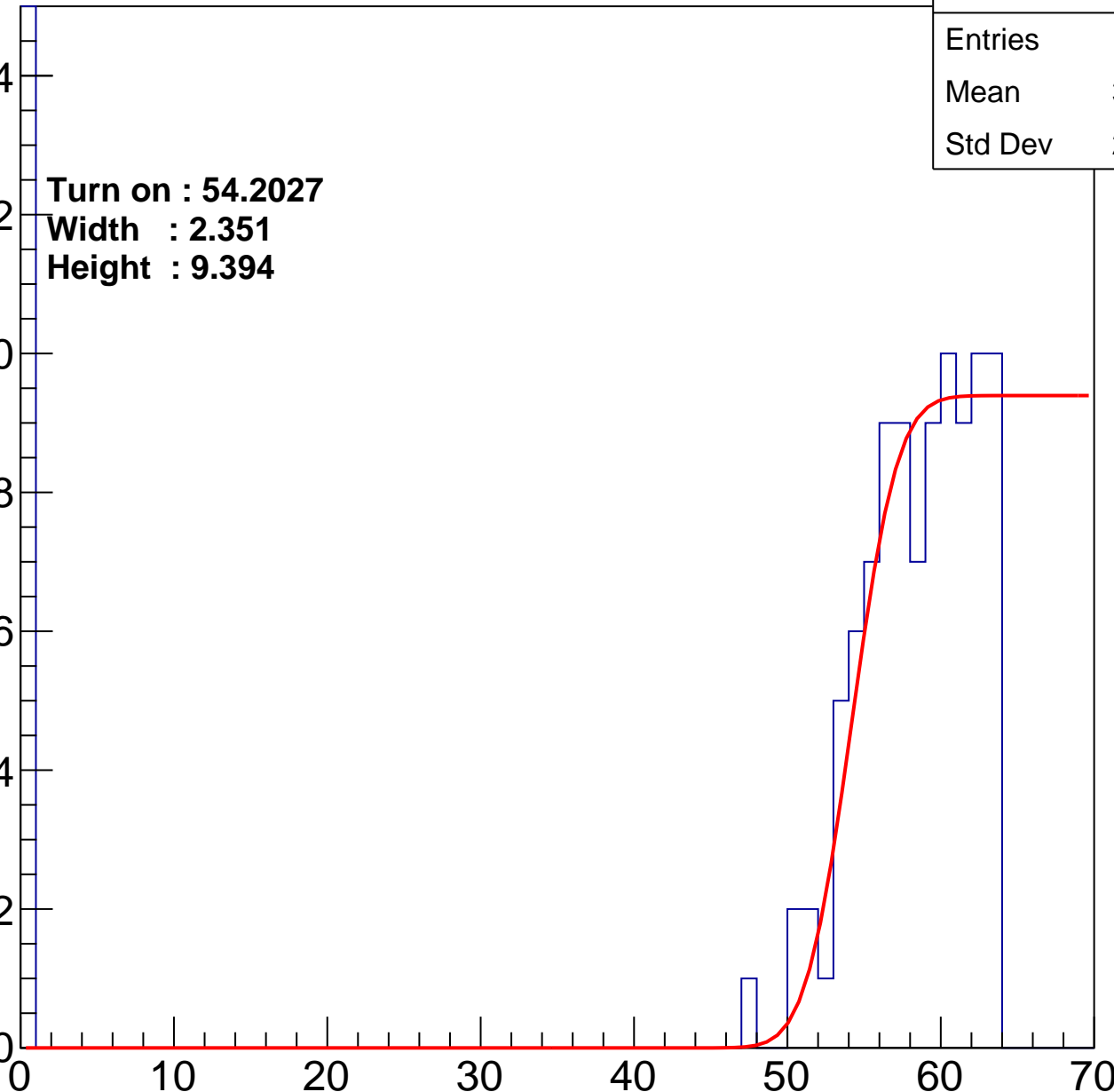
Width : 2.351

Height : 9.394

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.91
Std Dev	28.8

Turn on : 54.4586

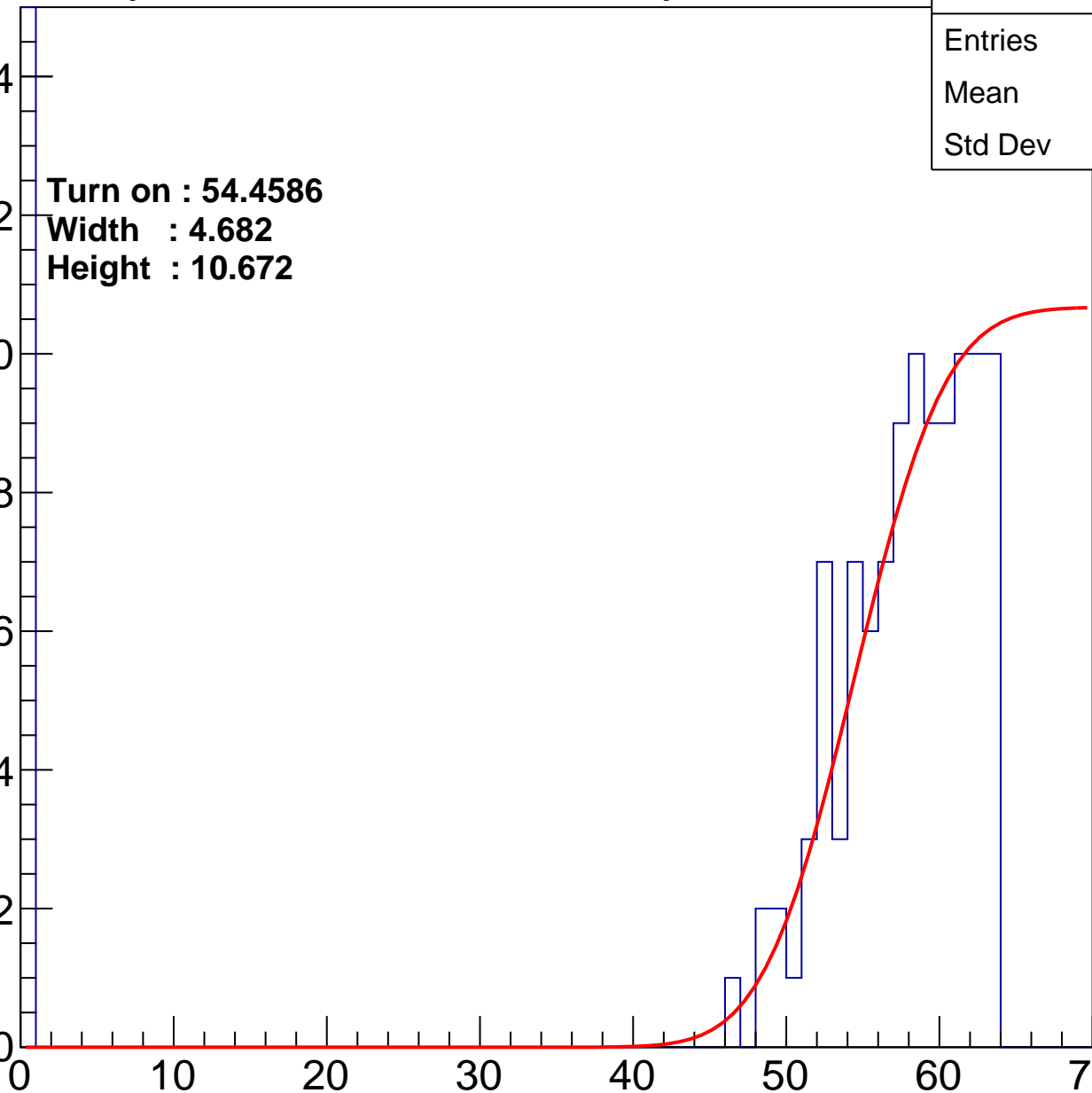
Width : 4.682

Height : 10.672

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch125

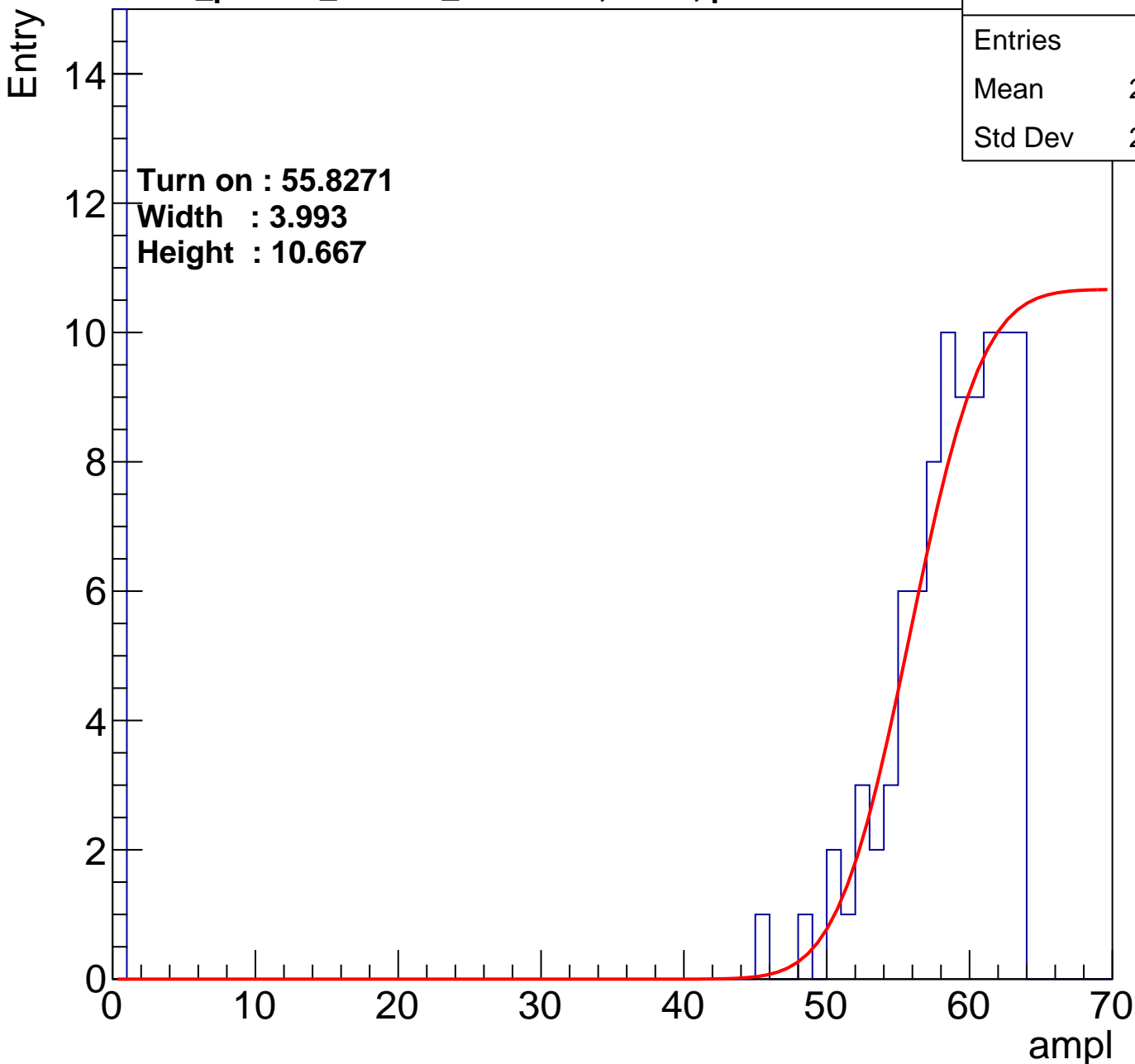
calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	27.47
Std Dev	29.19

Turn on : 55.8271

Width : 3.993

Height : 10.667



B1L104S, U23-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	31.57
Std Dev	28.73

Turn on : 54.4436

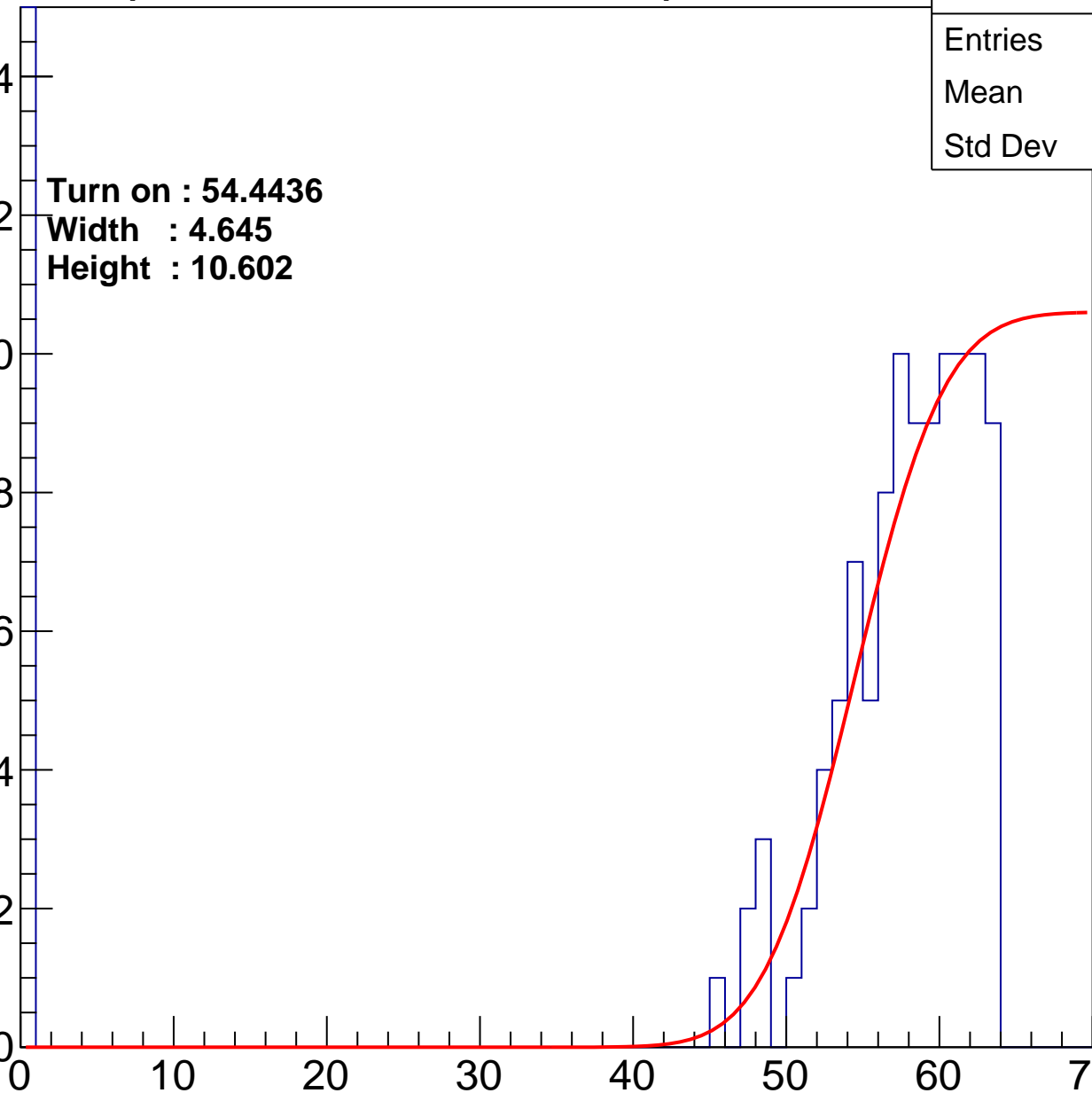
Width : 4.645

Height : 10.602

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	30.48
Std Dev	29.2

Turn on : 54.9918

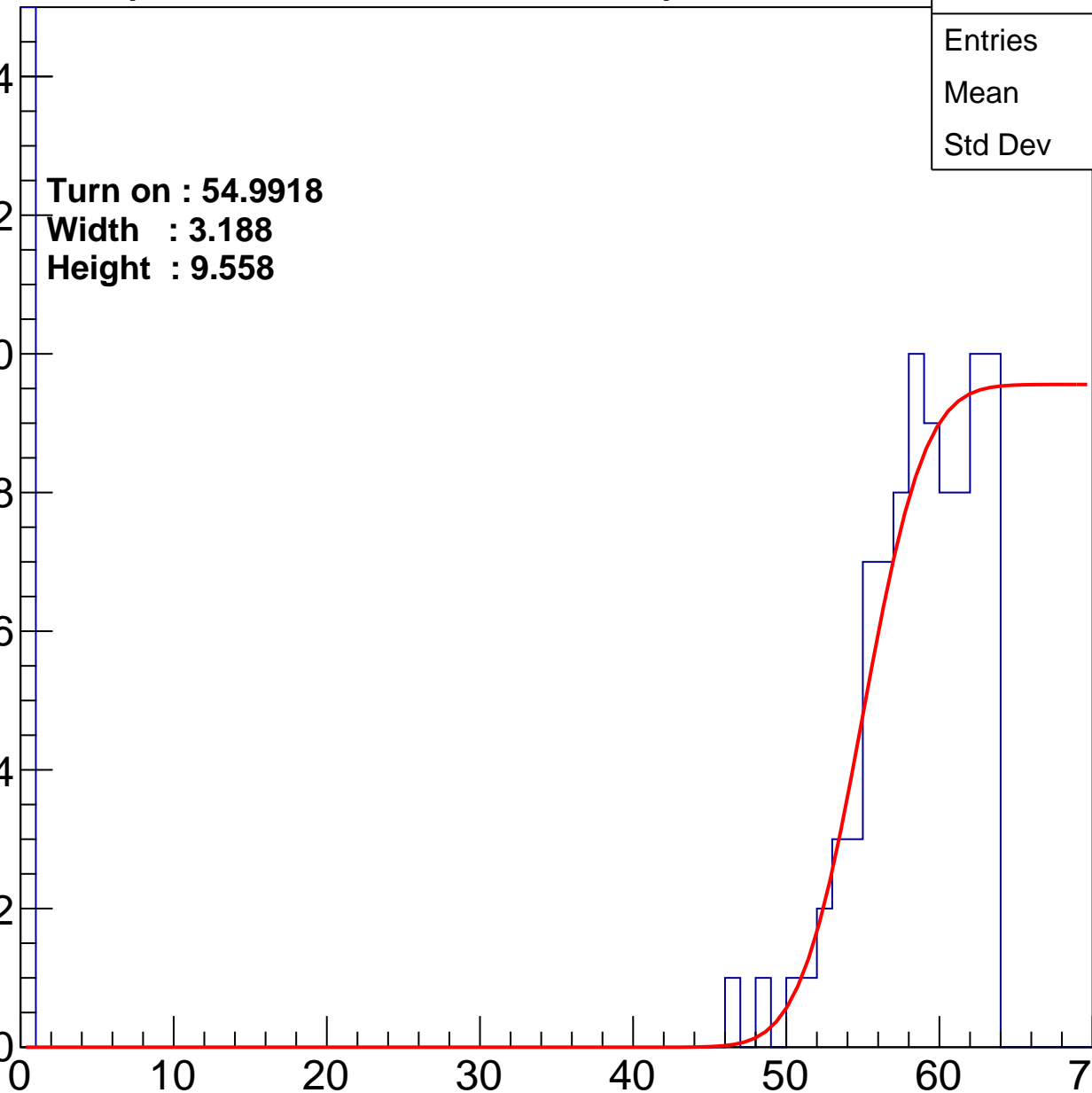
Width : 3.188

Height : 9.558

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U23-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	30.48
Std Dev	29.2

Turn on : 54.9918

Width : 3.188

Height : 9.558

Entry

14
12
10
8
6
4
2
0

ampl

