



# B0L002S, U23-ch0

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.3241

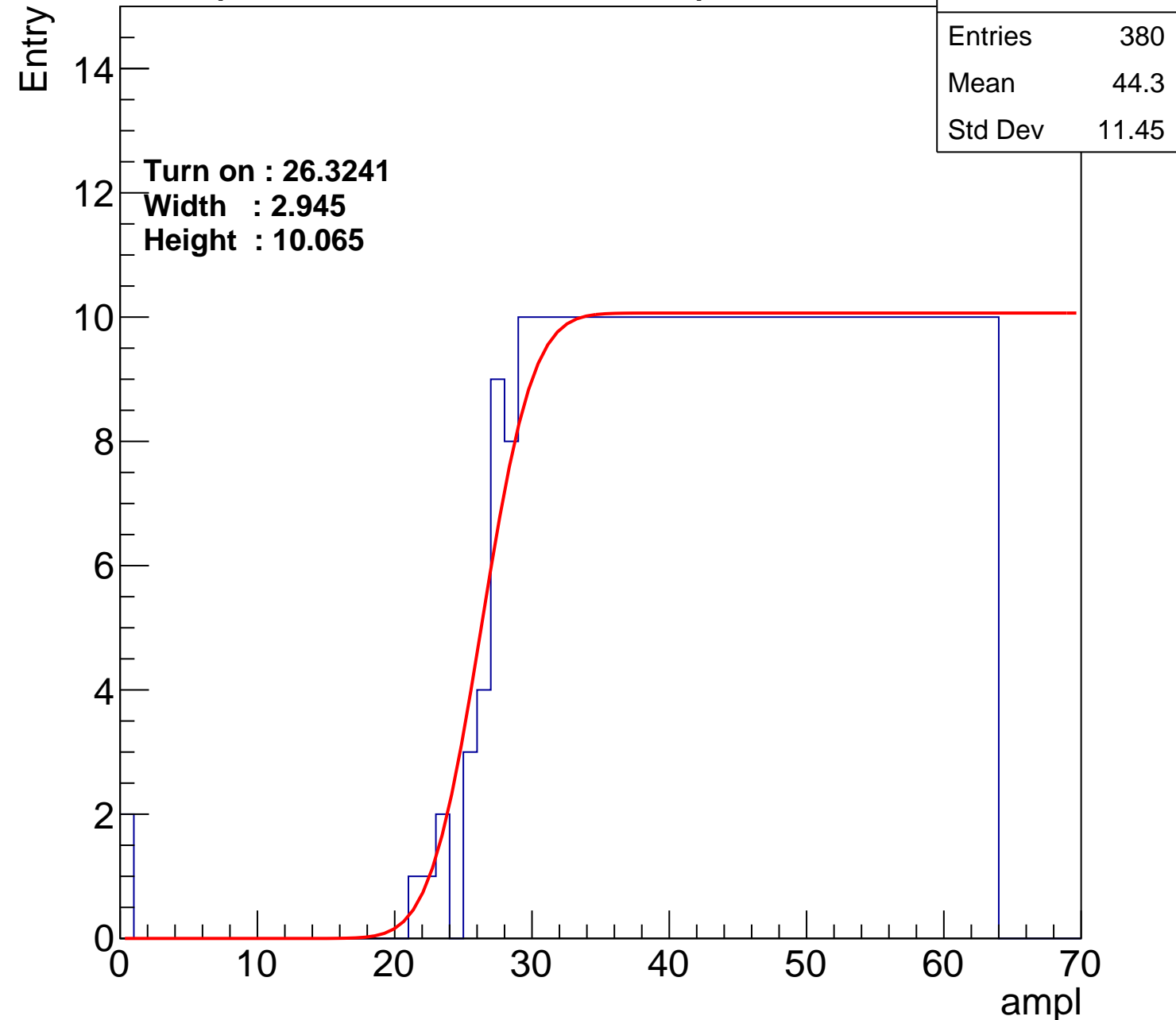
Width : 2.945

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch1

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.8116

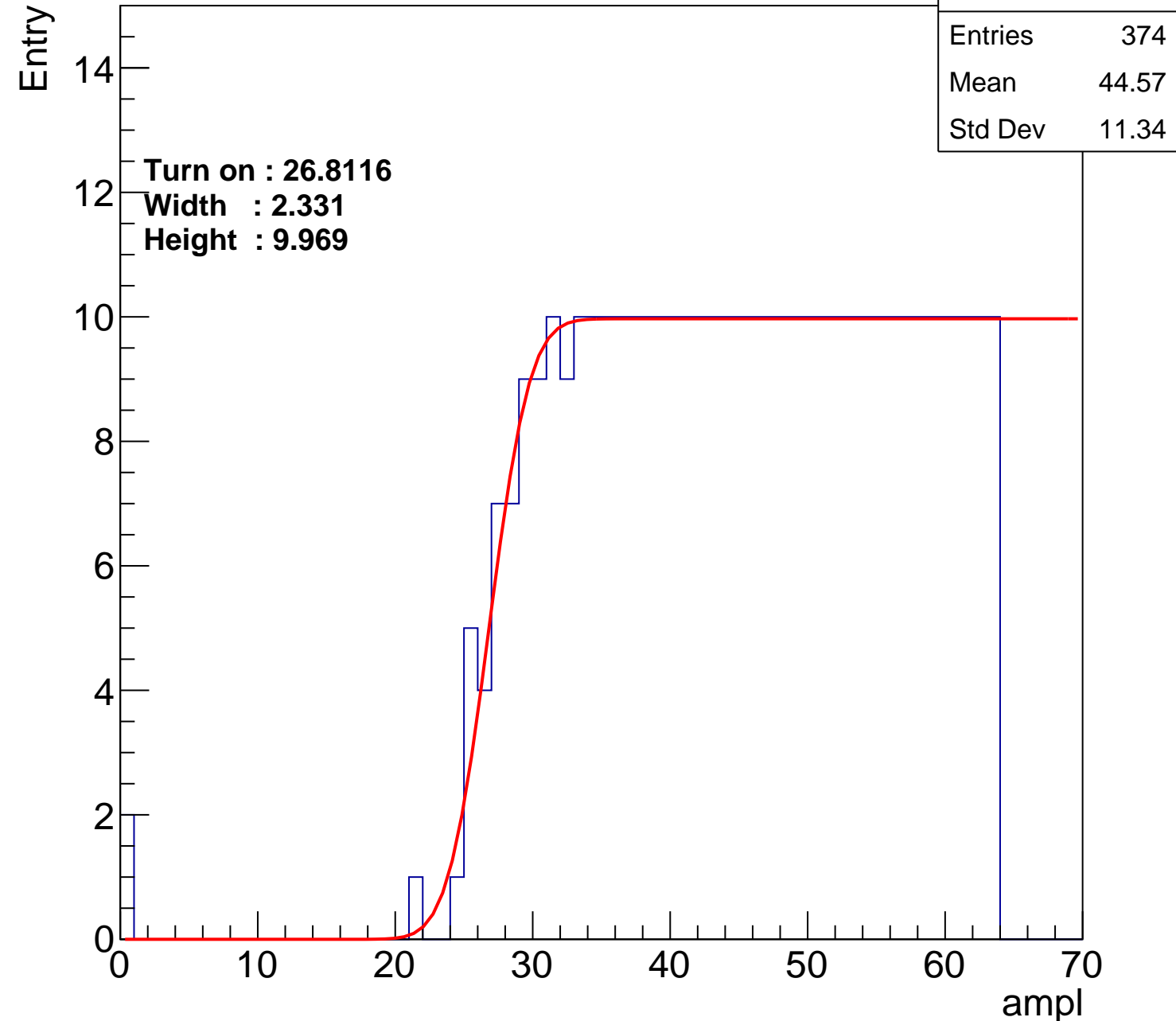
Width : 2.331

Height : 9.969

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.01
Std Dev	11.11

**Turn on : 27.8579**

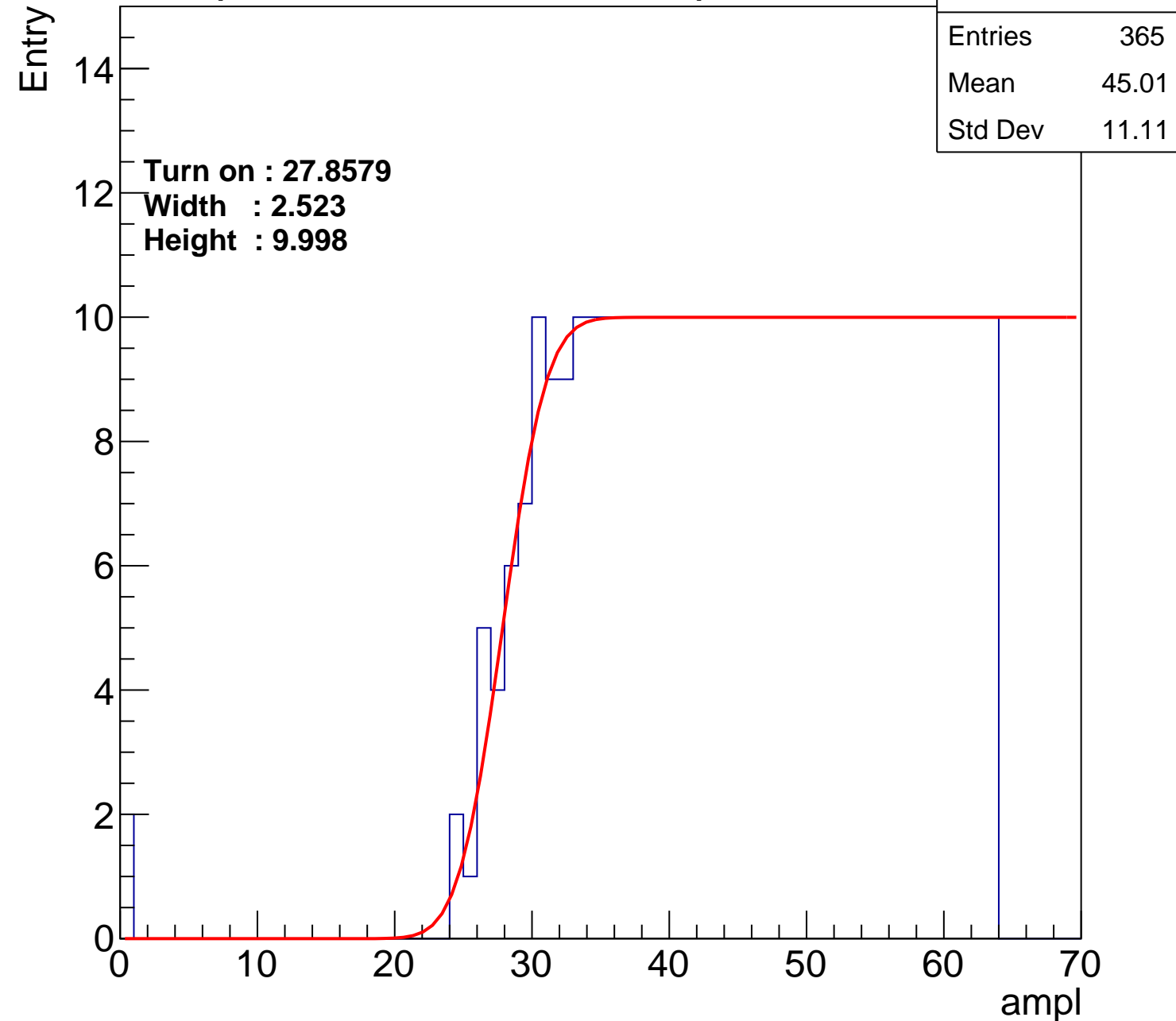
**Width : 2.523**

**Height : 9.998**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch3

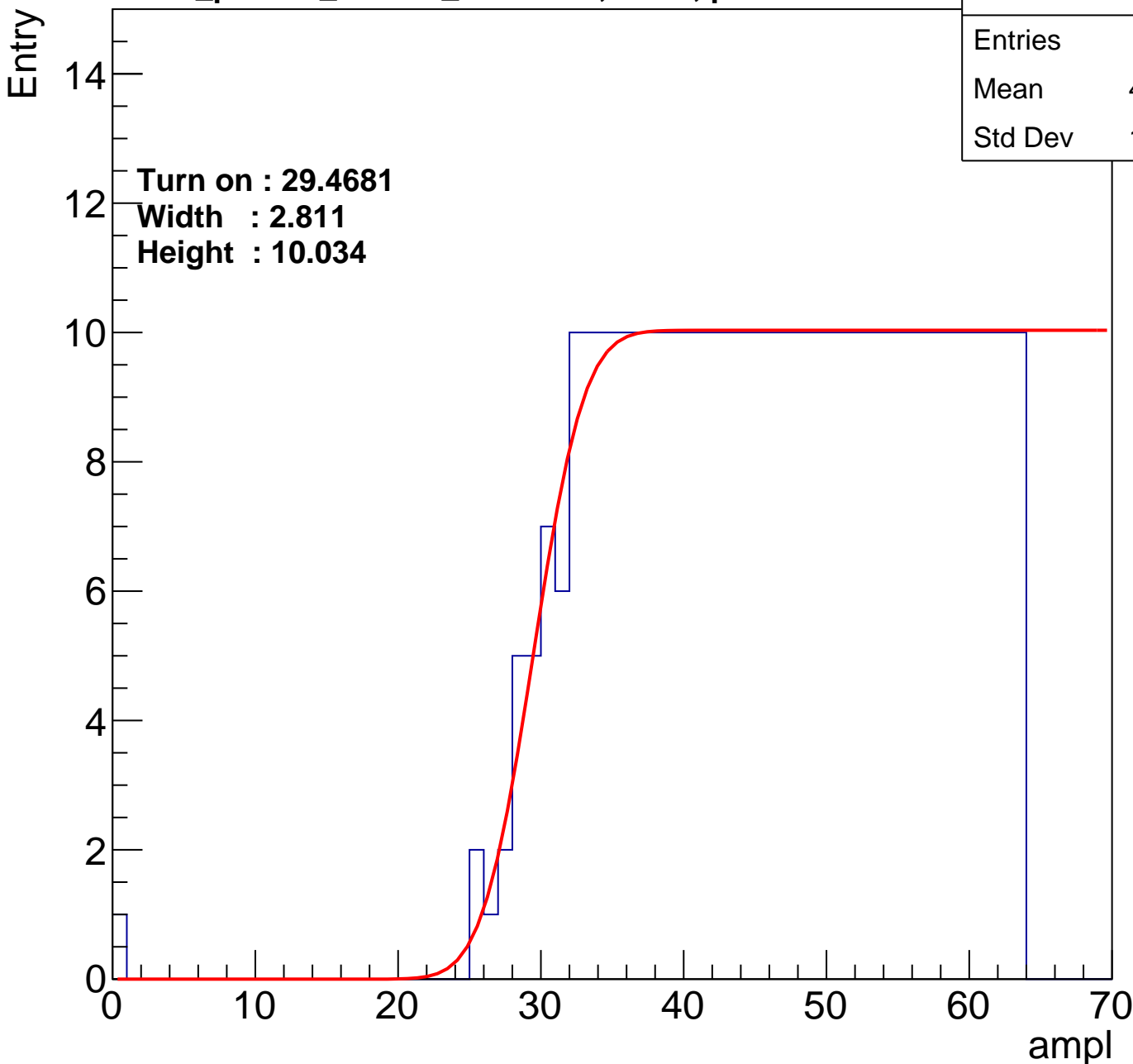
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	349
Mean	45.88
Std Dev	10.48

**Turn on : 29.4681**

**Width : 2.811**

**Height : 10.034**



# B0L002S, U23-ch4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.8
Std Dev	11.25

Turn on : 27.7791

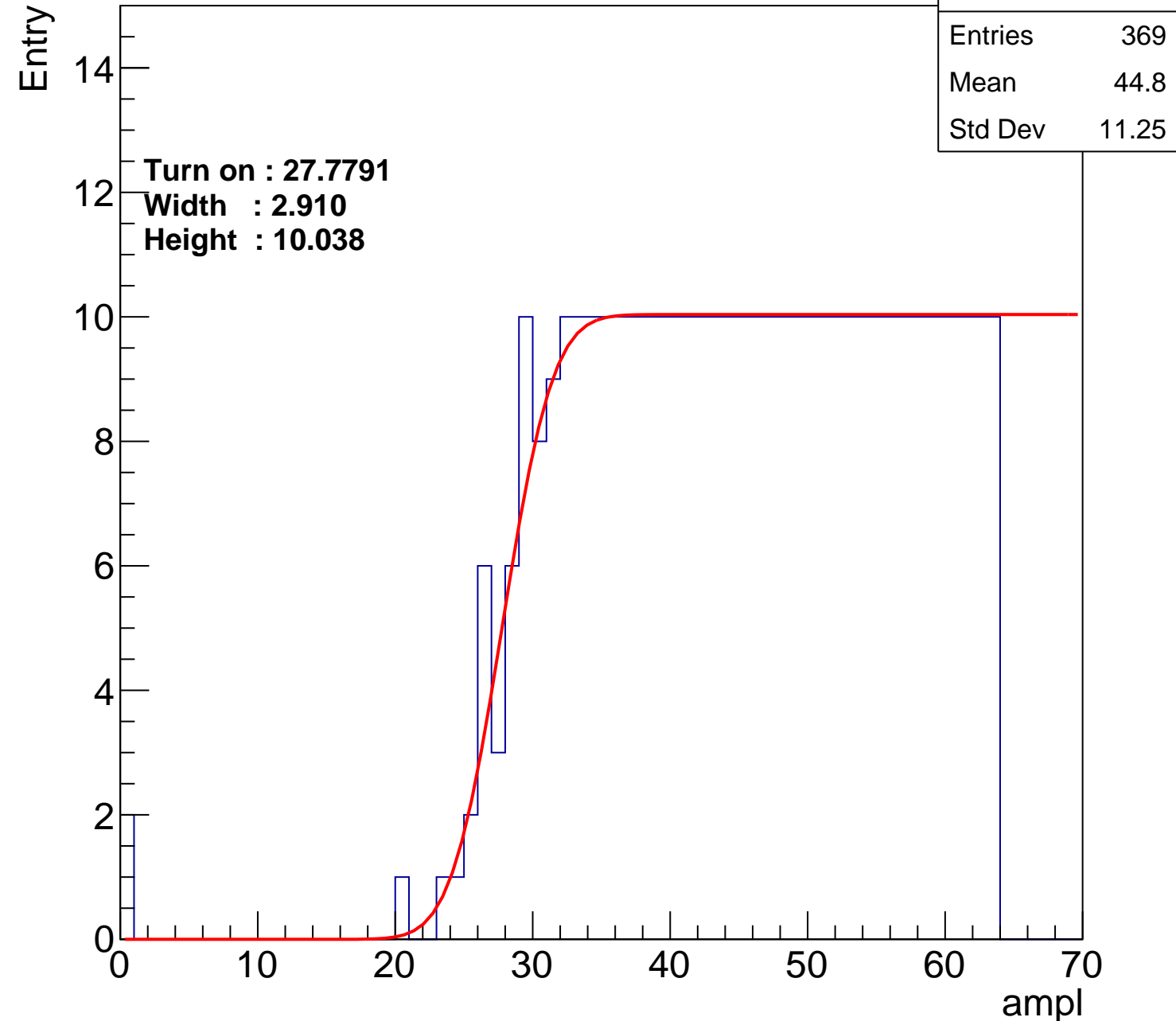
Width : 2.910

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.57
Std Dev	11.54

Turn on : 27.4191

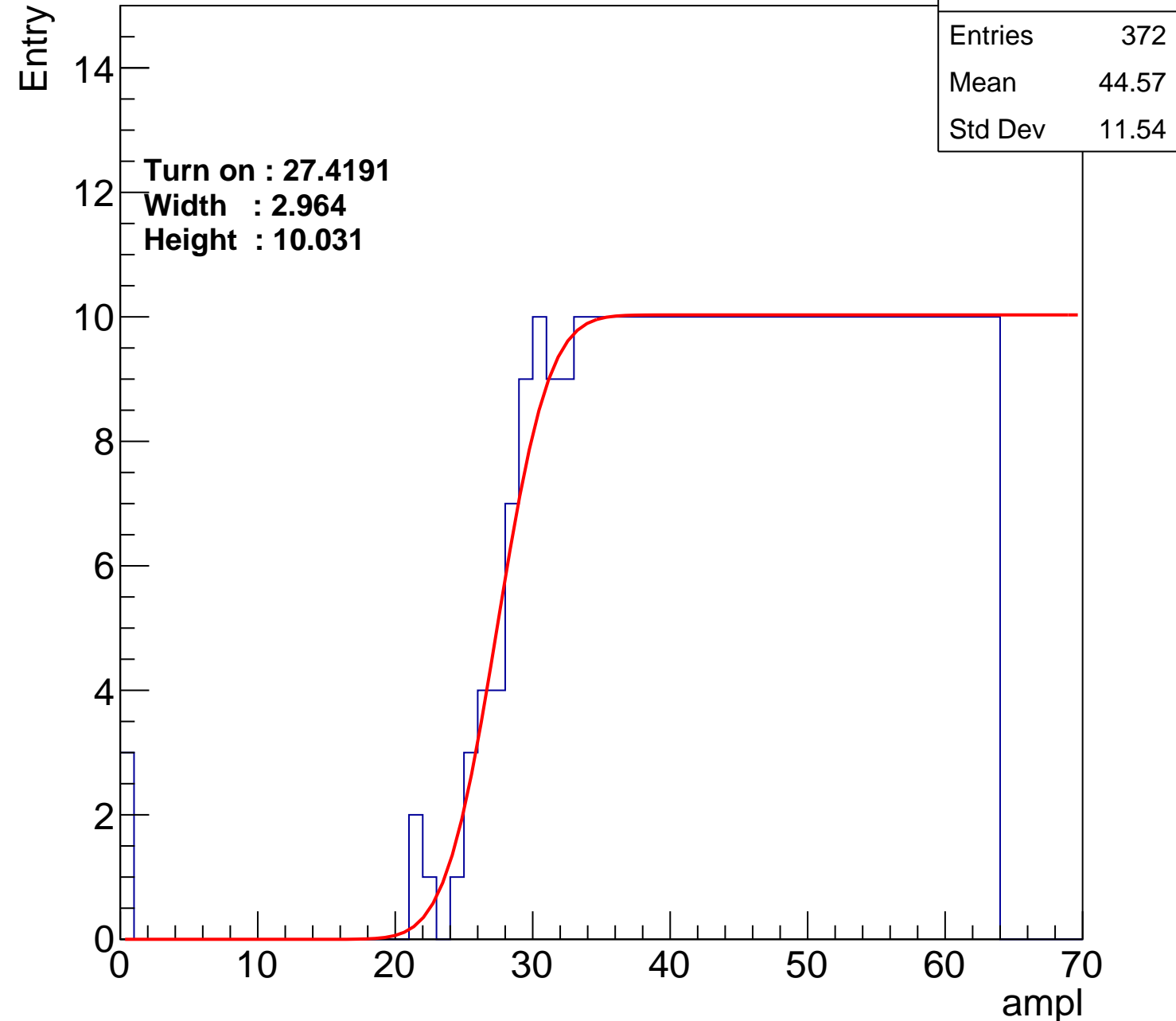
Width : 2.964

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch6

calib\_packv5\_042523\_0143.root, FC#8, port C1

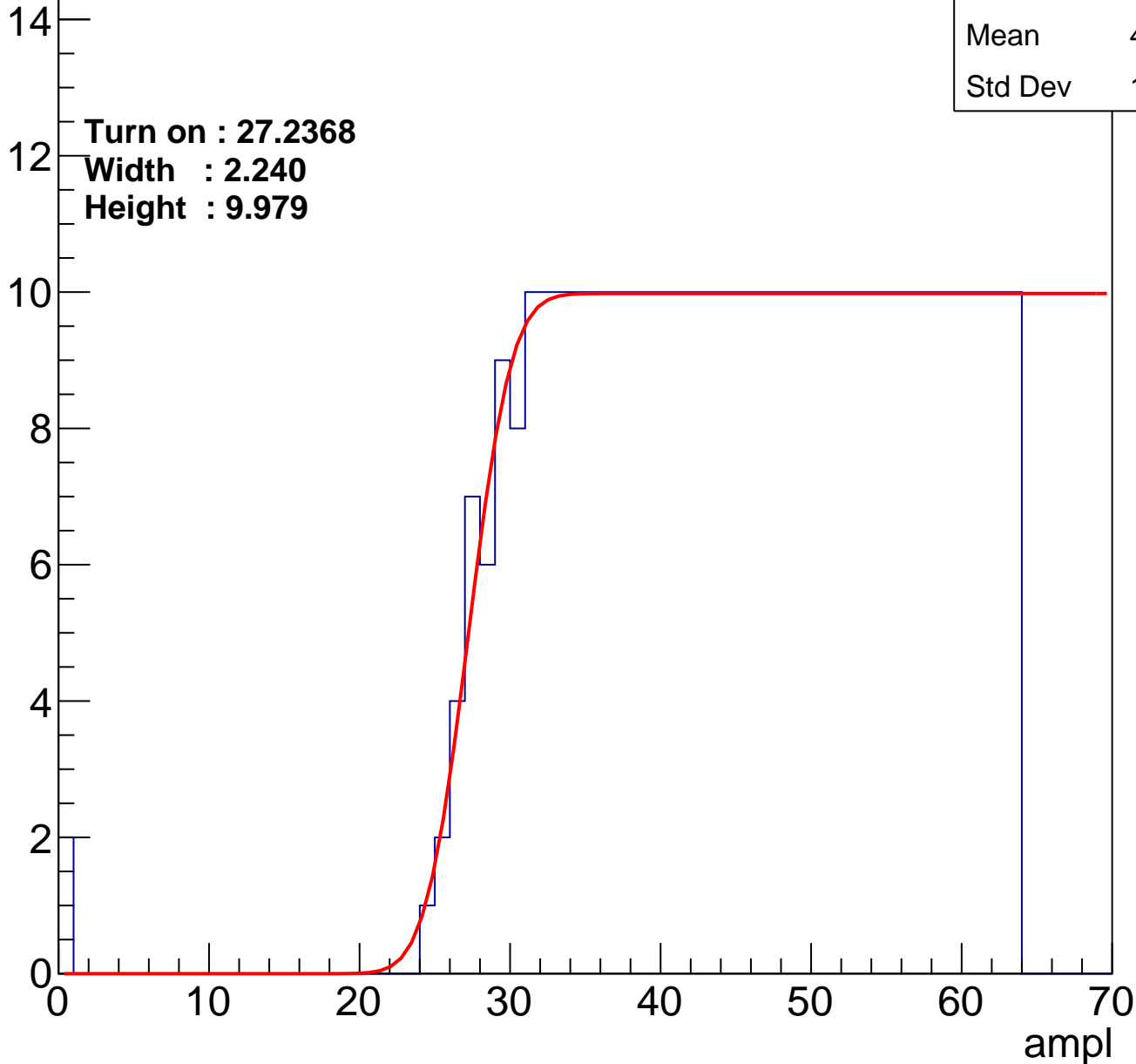
Entry

Entries	369
Mean	44.84
Std Dev	11.17

Turn on : 27.2368

Width : 2.240

Height : 9.979





# B0L002S, U23-ch7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.29
Std Dev	10.79

**Turn on : 28.3802**

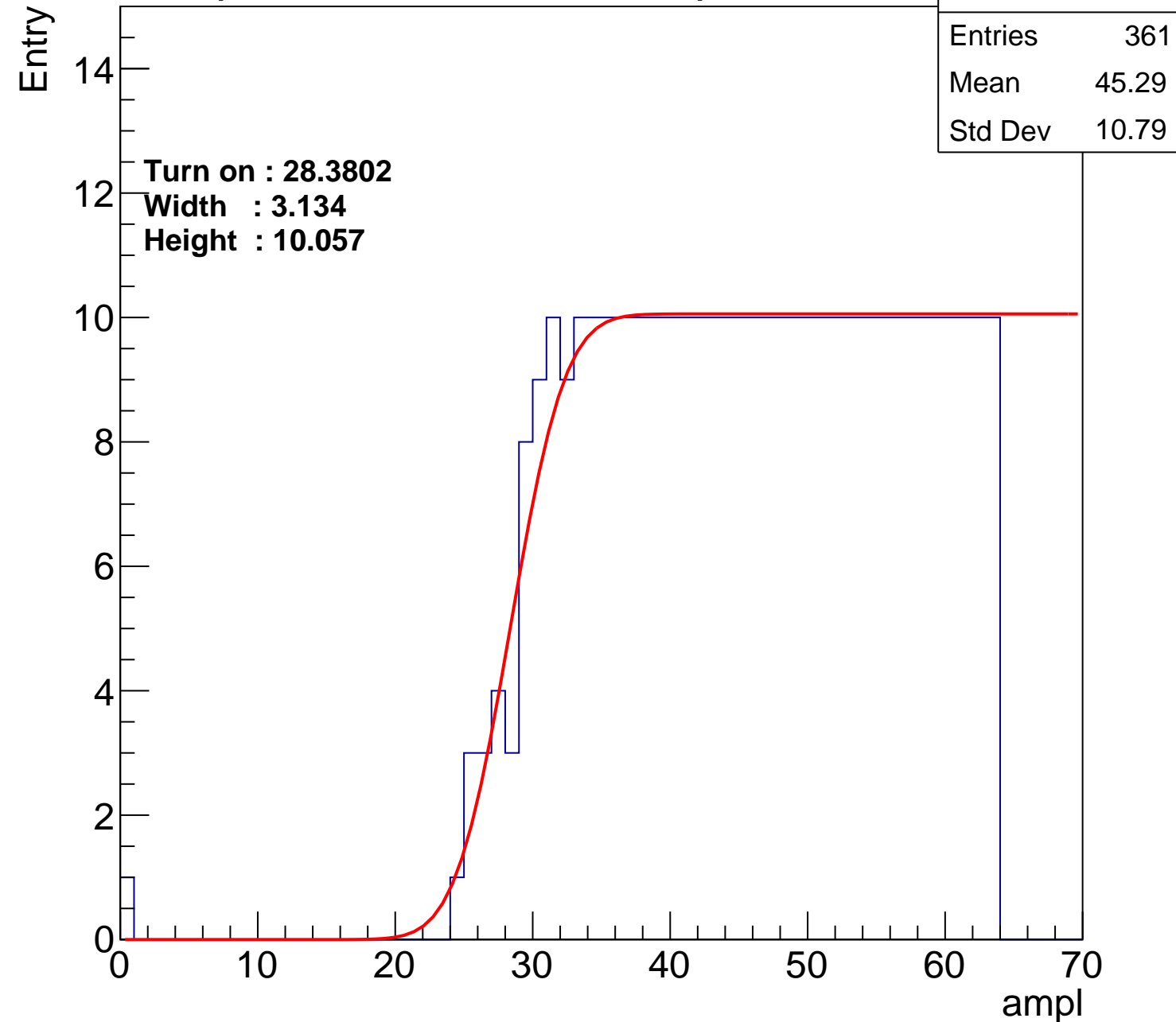
**Width : 3.134**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch8

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.9966

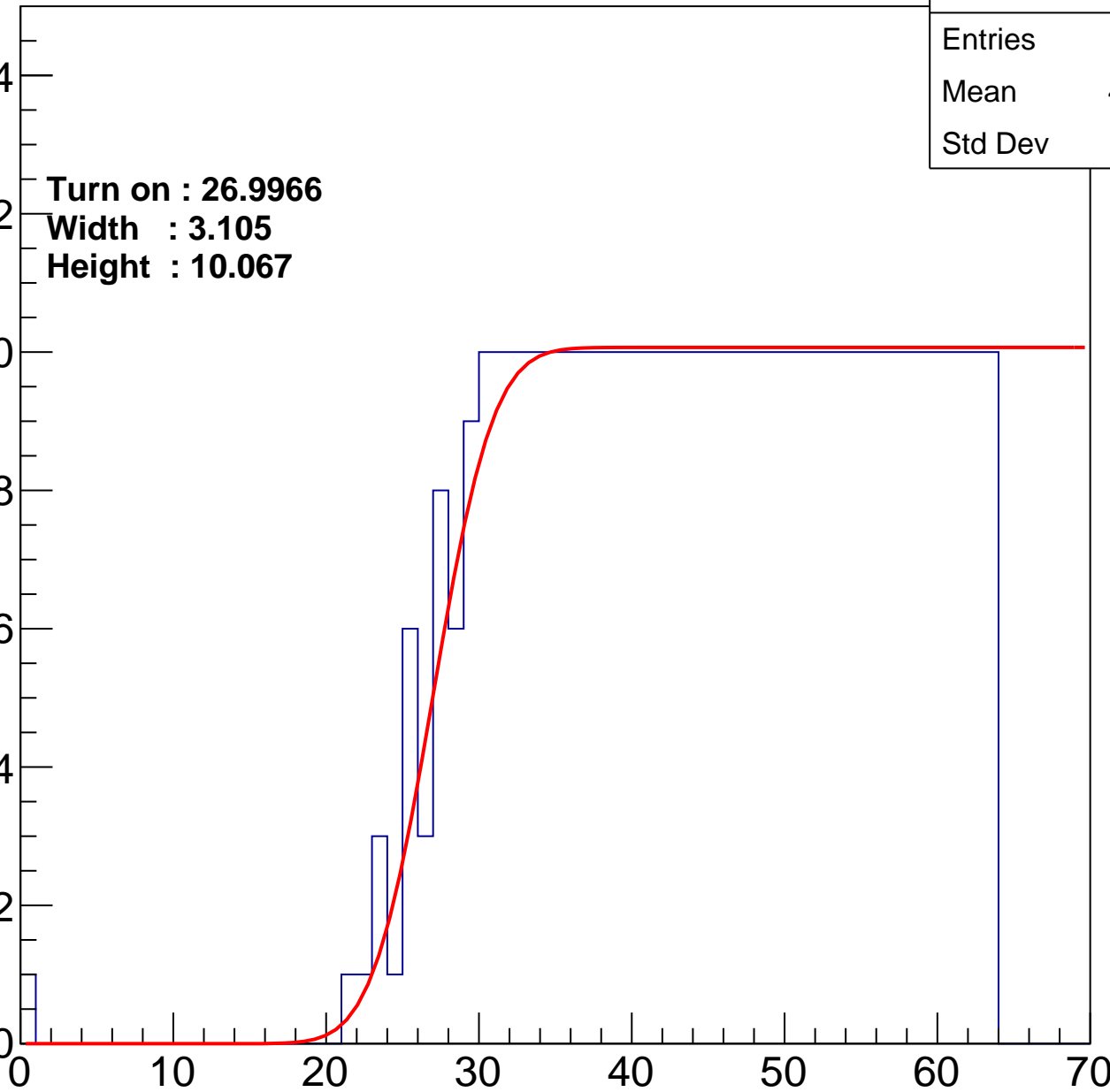
Width : 3.105

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch9

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.33
Std Dev	11.47

Turn on : 26.4518

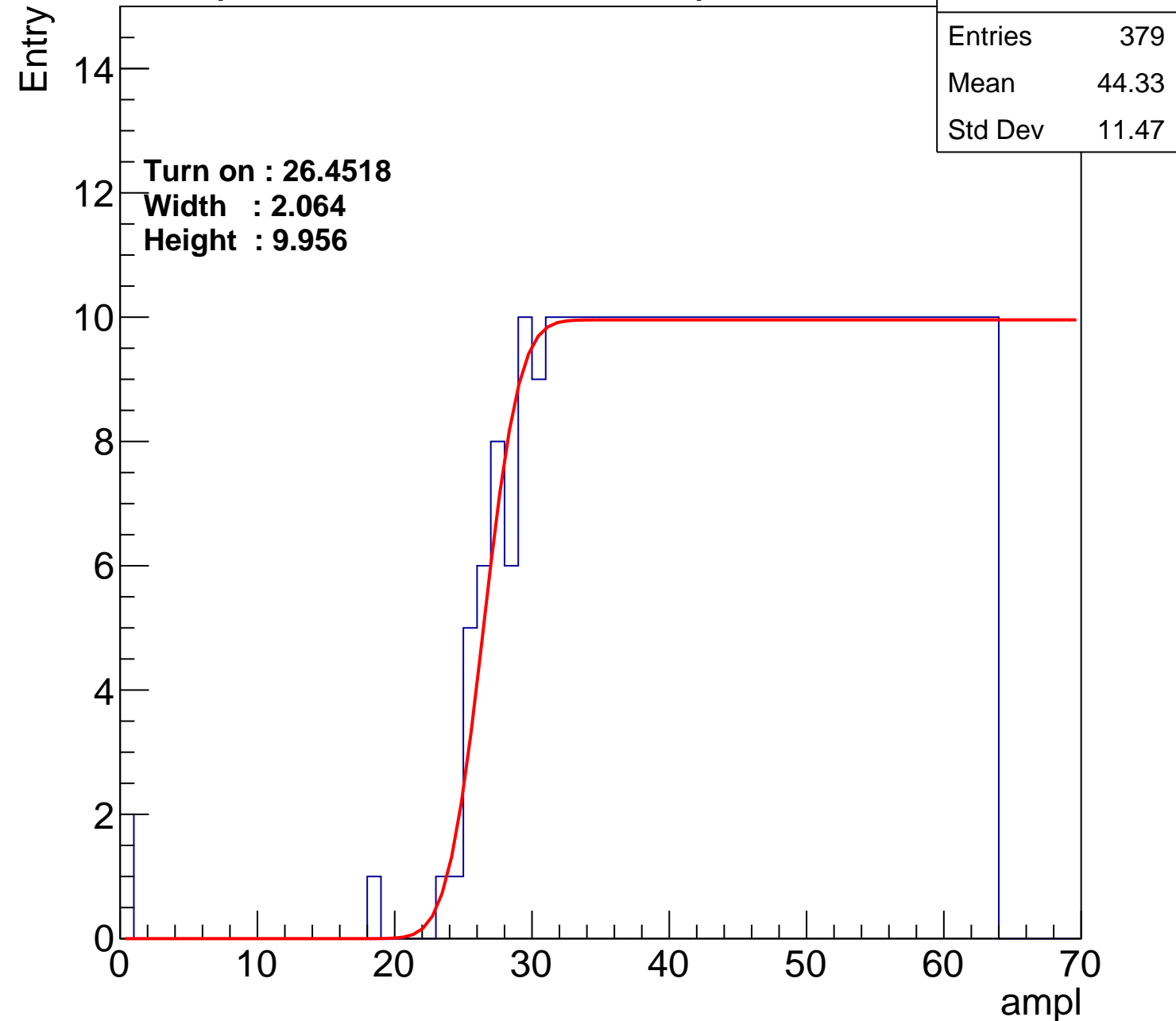
Width : 2.064

Height : 9.956

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch10

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.22
Std Dev	10.84

Turn on : 28.4172

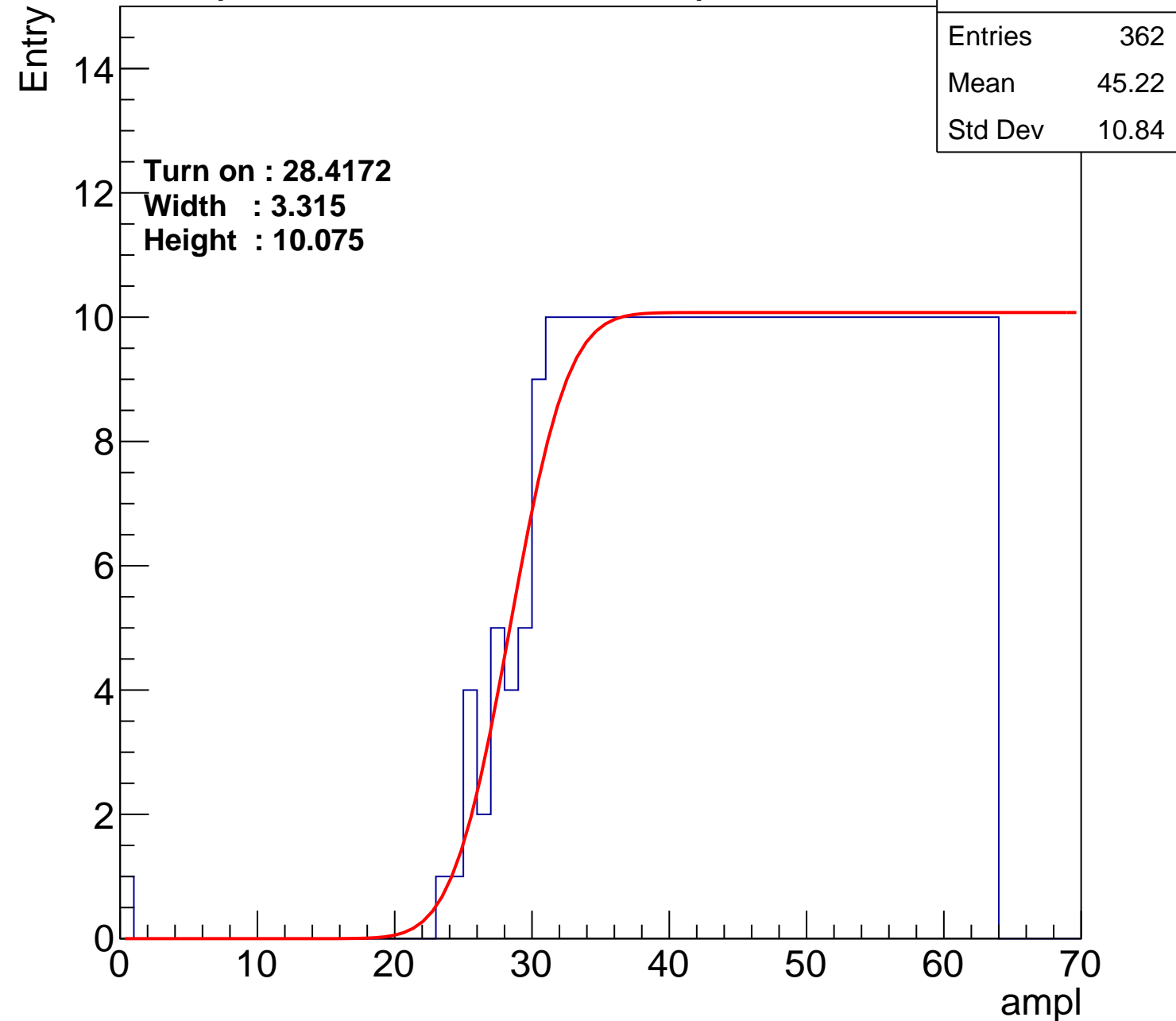
Width : 3.315

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch11

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 27.4578

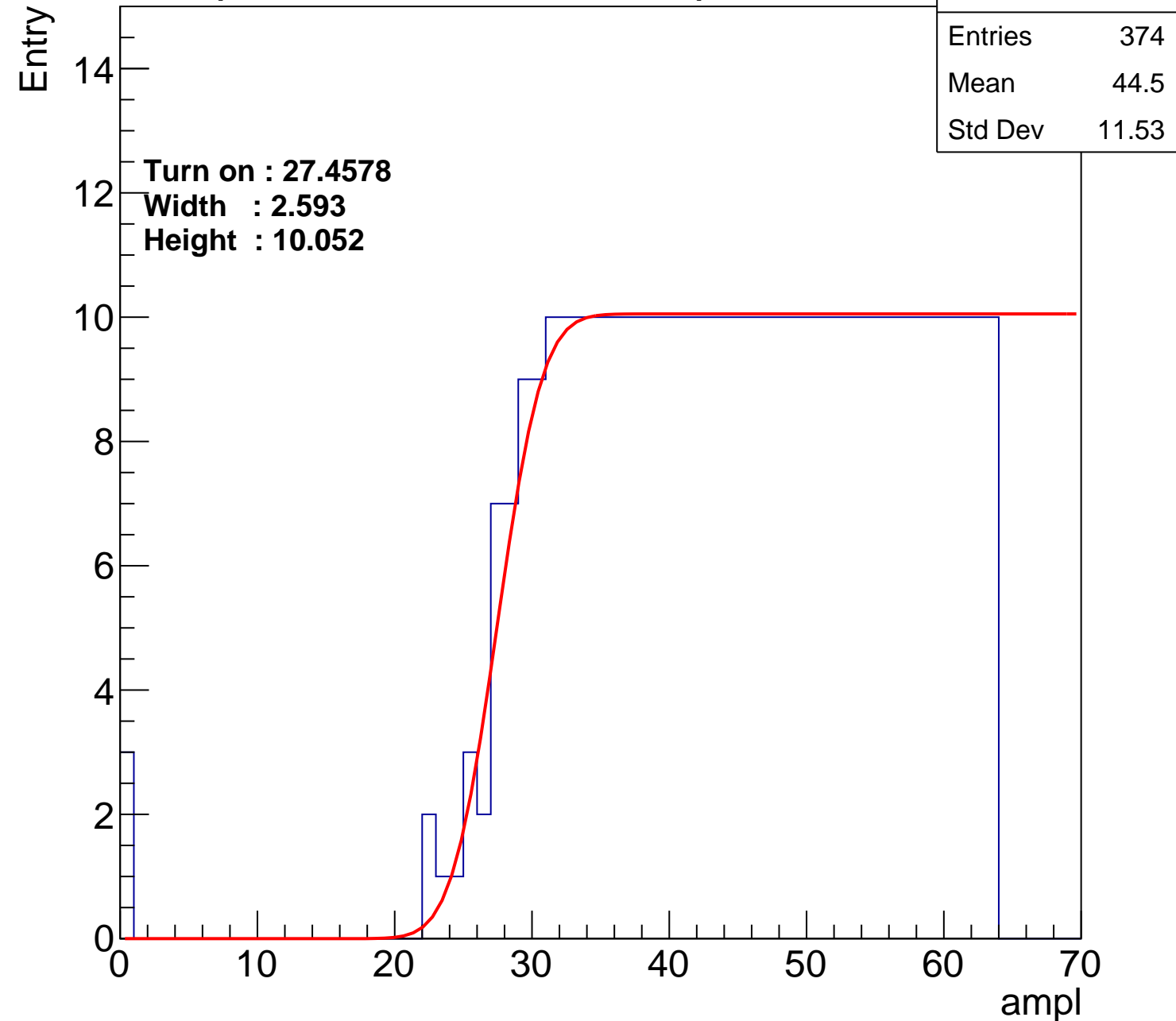
Width : 2.593

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch12

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.7714

Width : 1.350

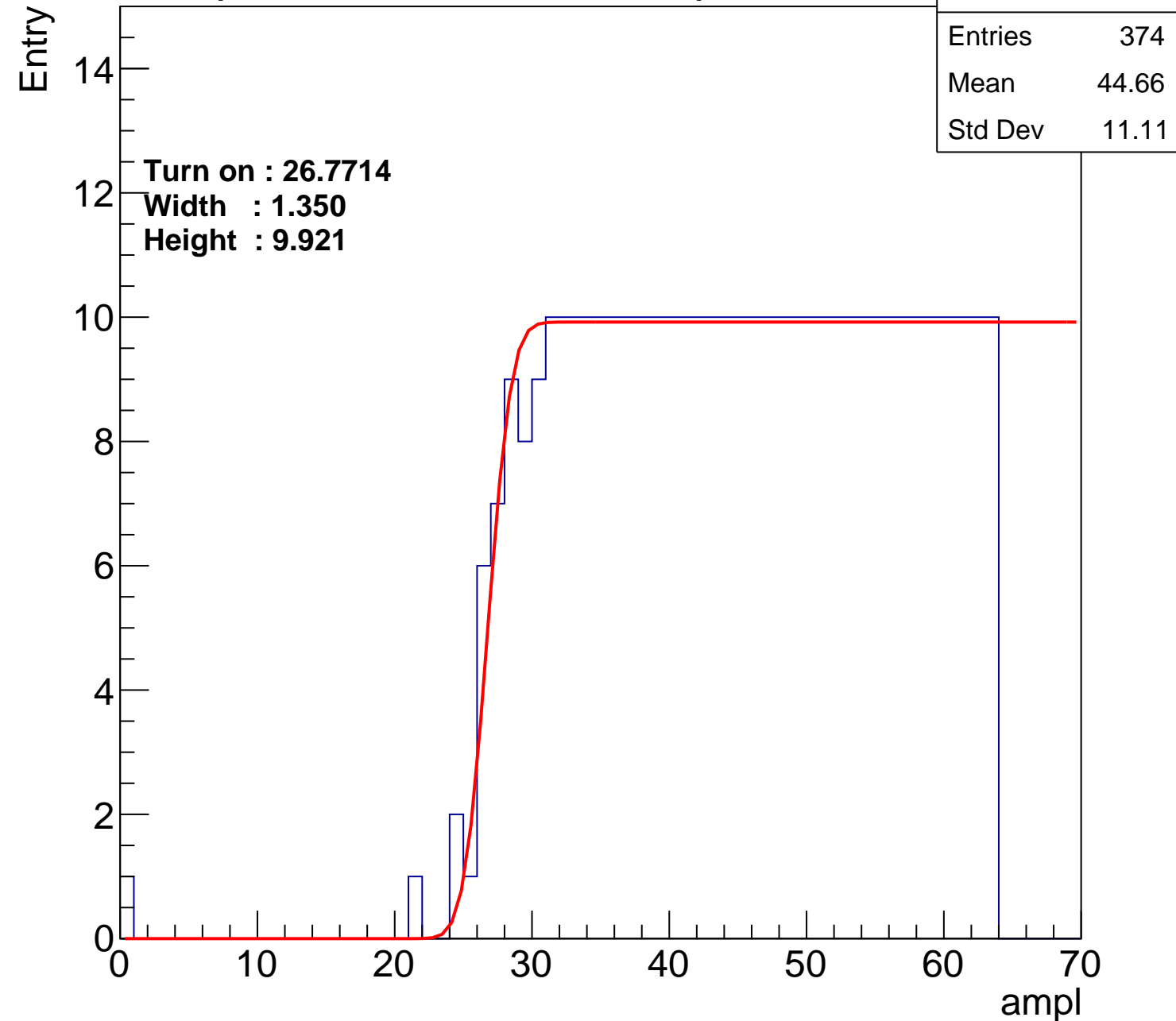
Height : 9.921

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch13

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.06
Std Dev	10.95

**Turn on : 27.9892**

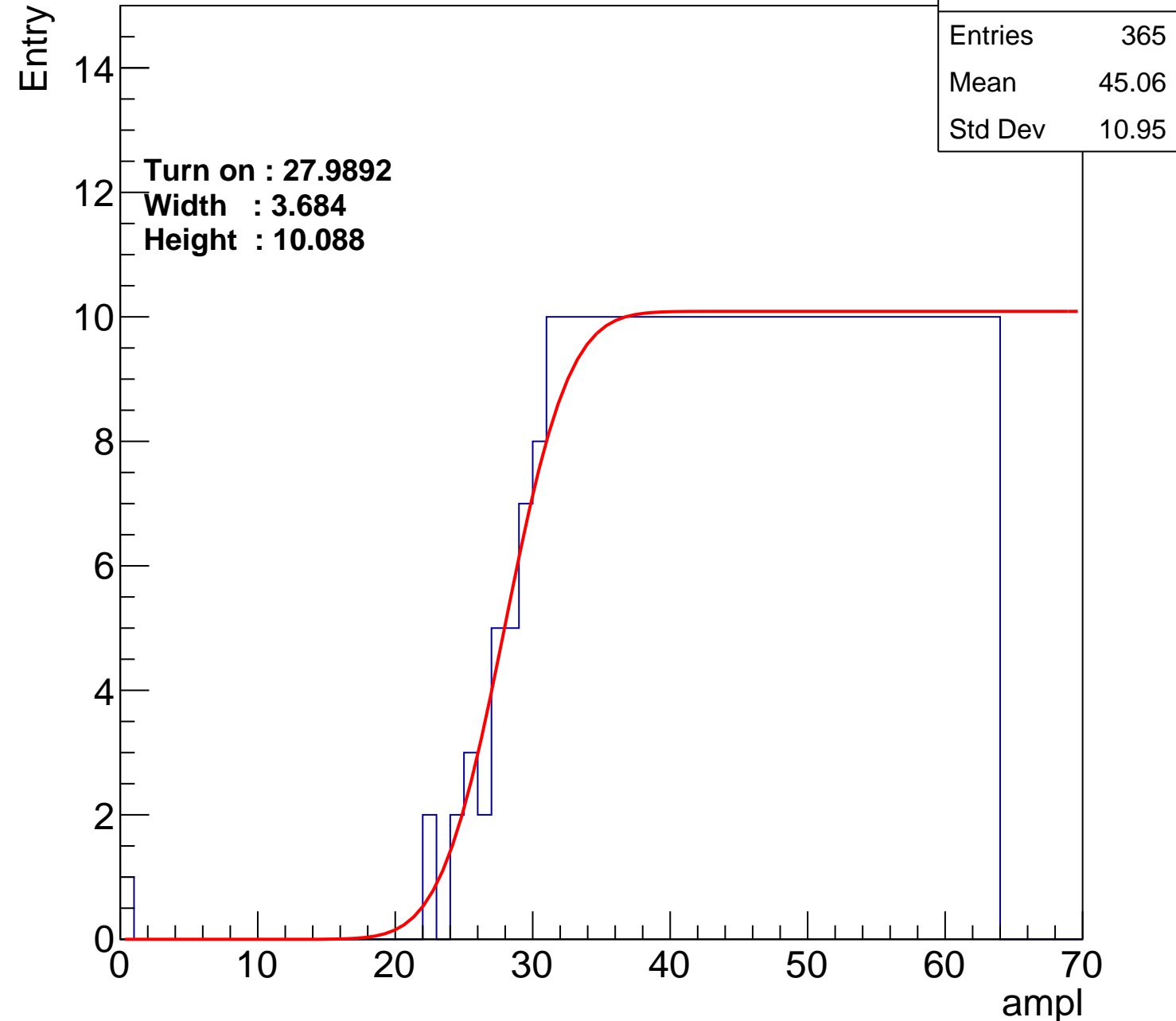
**Width : 3.684**

**Height : 10.088**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch14

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.35
Std Dev	10.95

Turn on : 28.4994

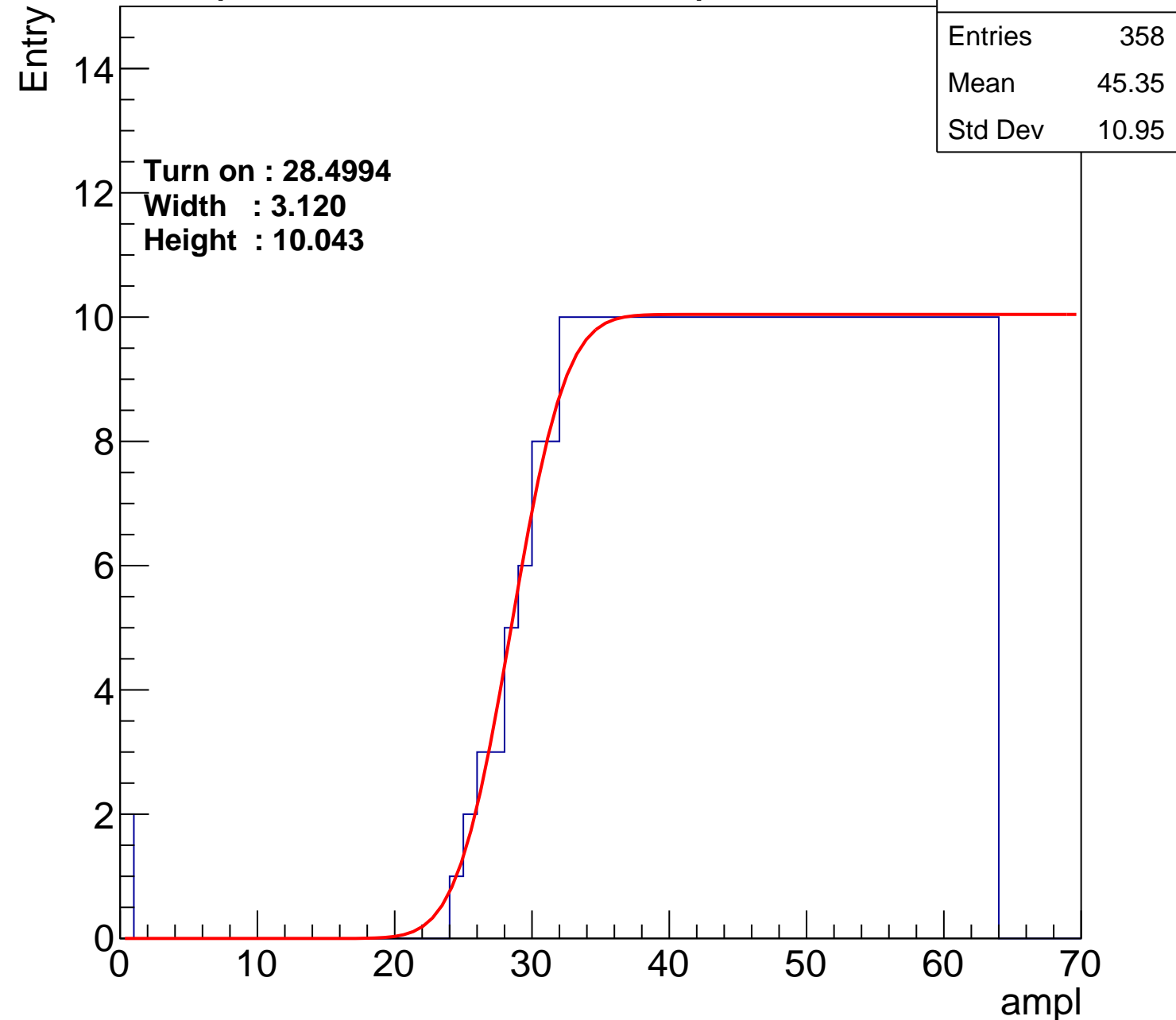
Width : 3.120

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch15

calib\_packv5\_042523\_0143.root, FC#8, port C1

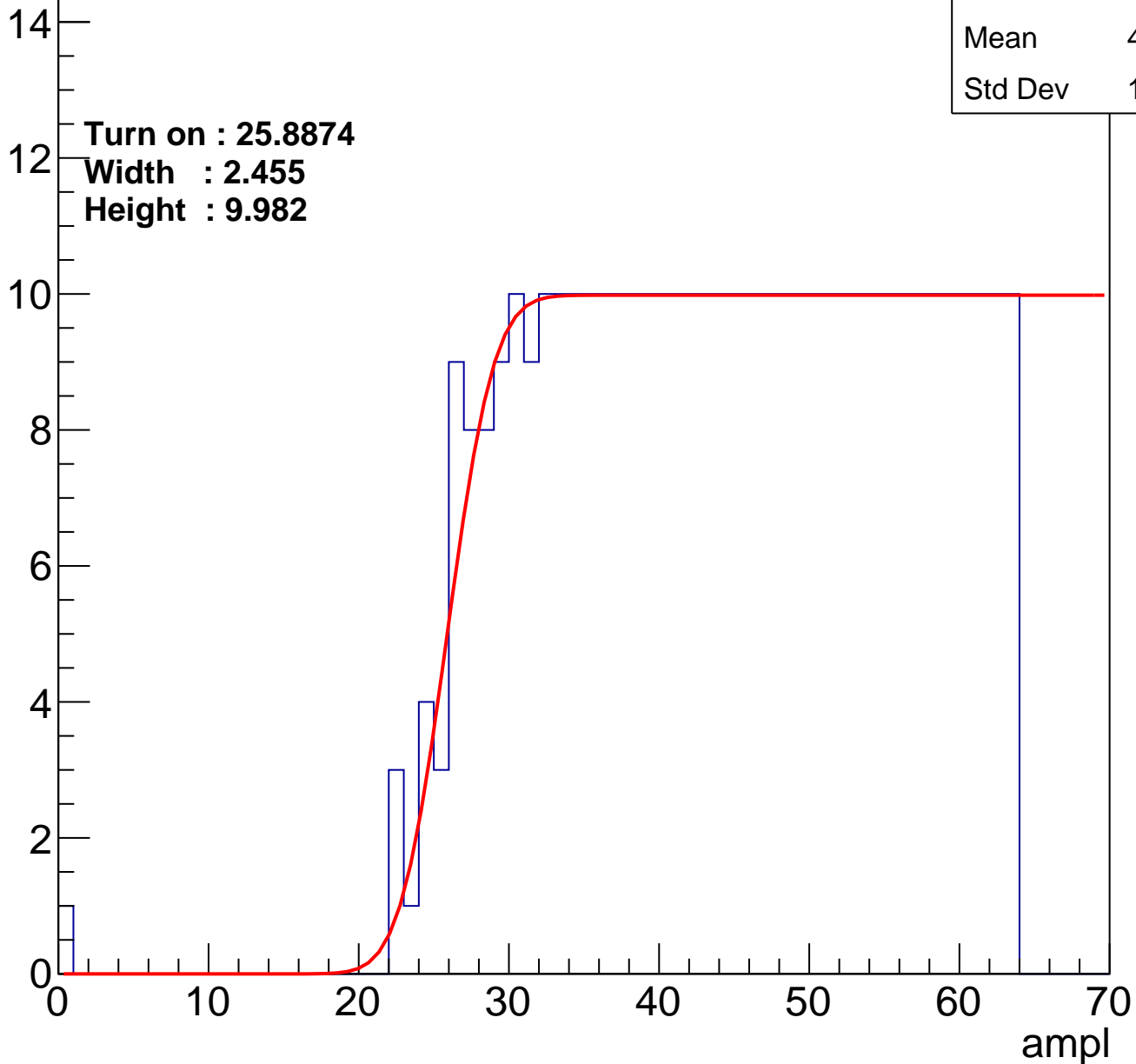
Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.8874

Width : 2.455

Height : 9.982

Entry



# B0L002S, U23-ch16

calib\_packv5\_042523\_0143.root, FC#8, port C1

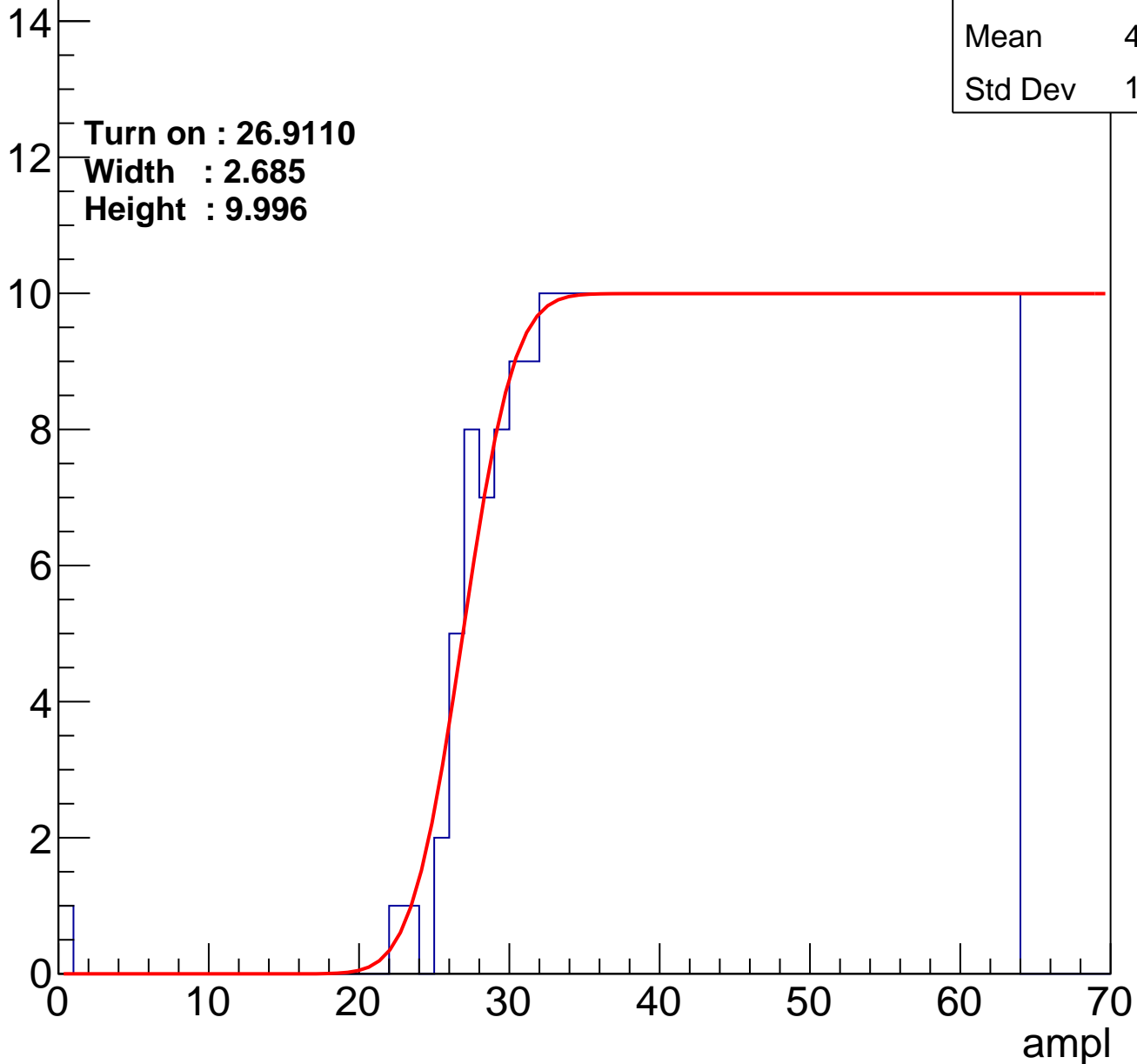
Entries	371
Mean	44.79
Std Dev	11.06

**Turn on : 26.9110**

**Width : 2.685**

**Height : 9.996**

Entry



# B0L002S, U23-ch17

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.2
Std Dev	10.82

Turn on : 28.0295

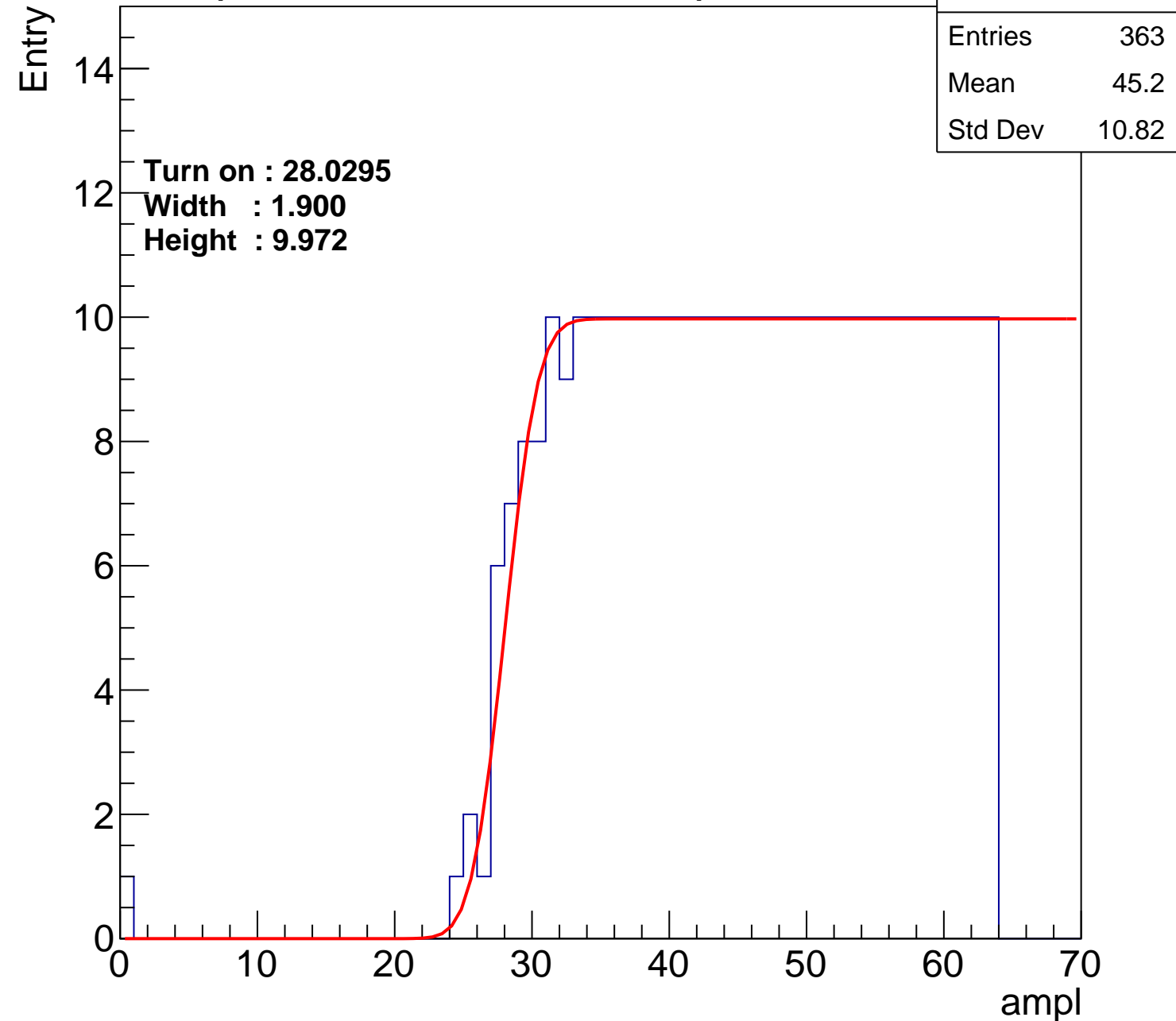
Width : 1.900

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch18

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 27.1184

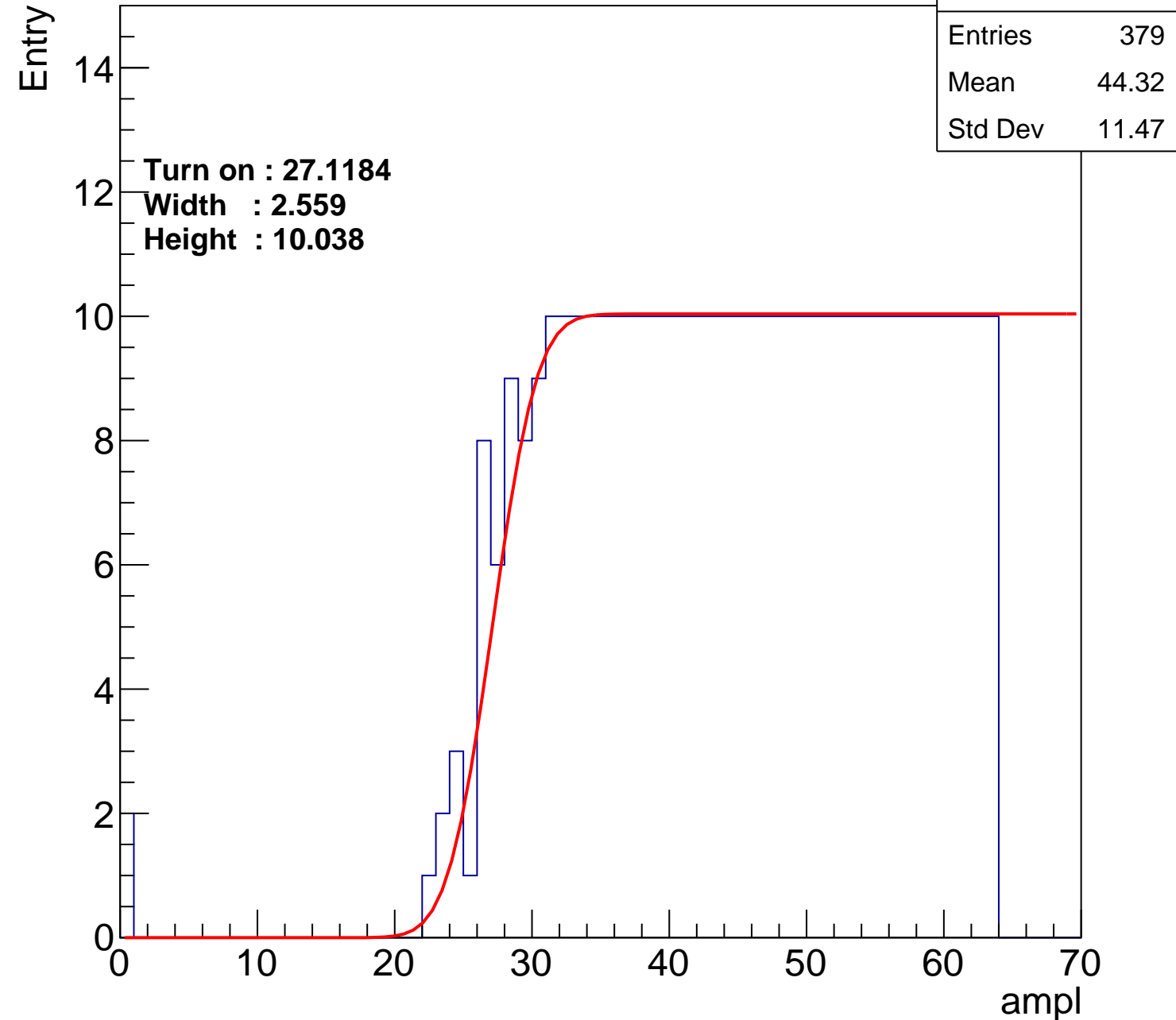
Width : 2.559

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch19

calib\_packv5\_042523\_0143.root, FC#8, port C1

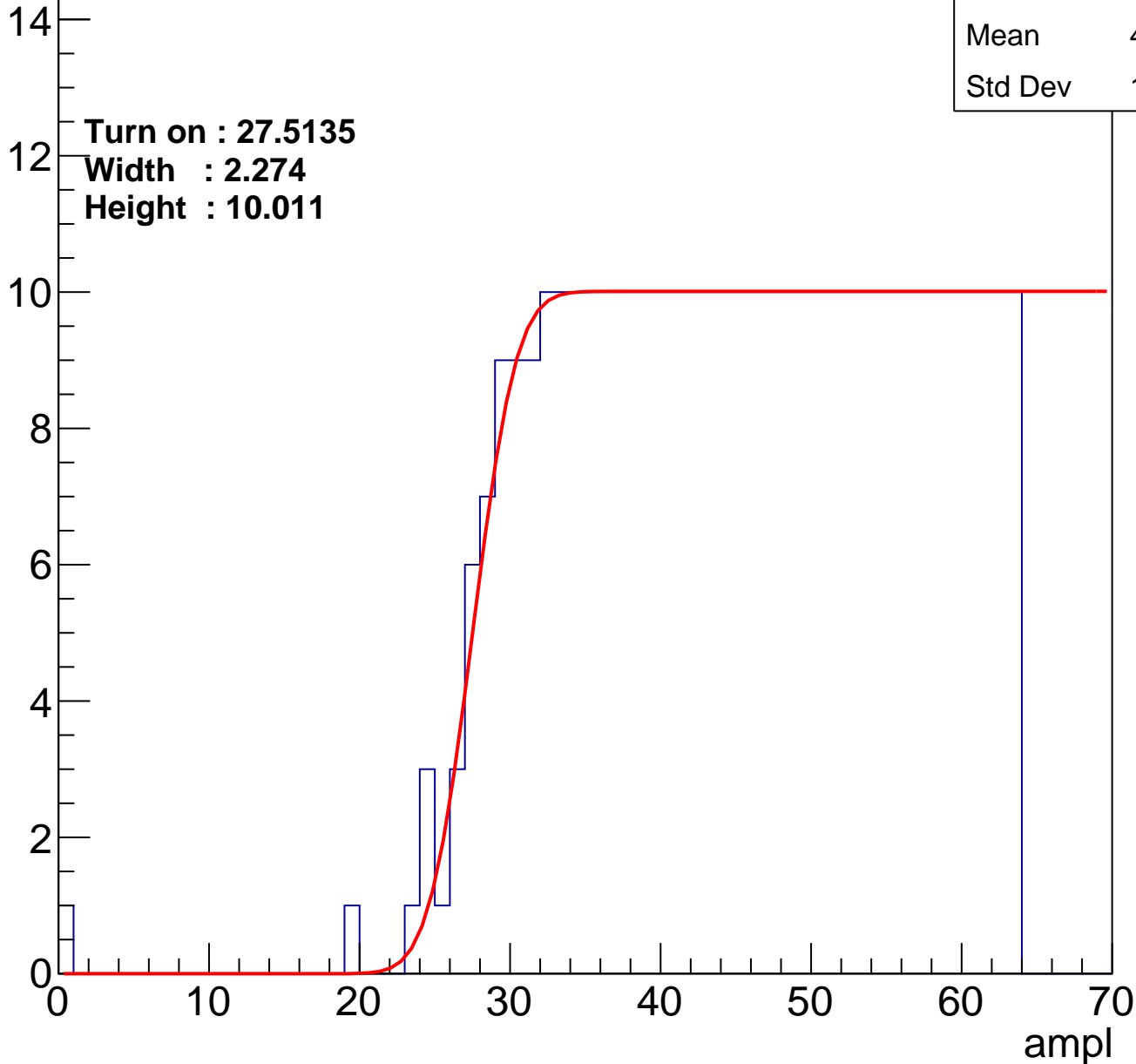
Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.5135

Width : 2.274

Height : 10.011

Entry



# B0L002S, U23-ch20

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.86
Std Dev	11.4

Turn on : 28.4848

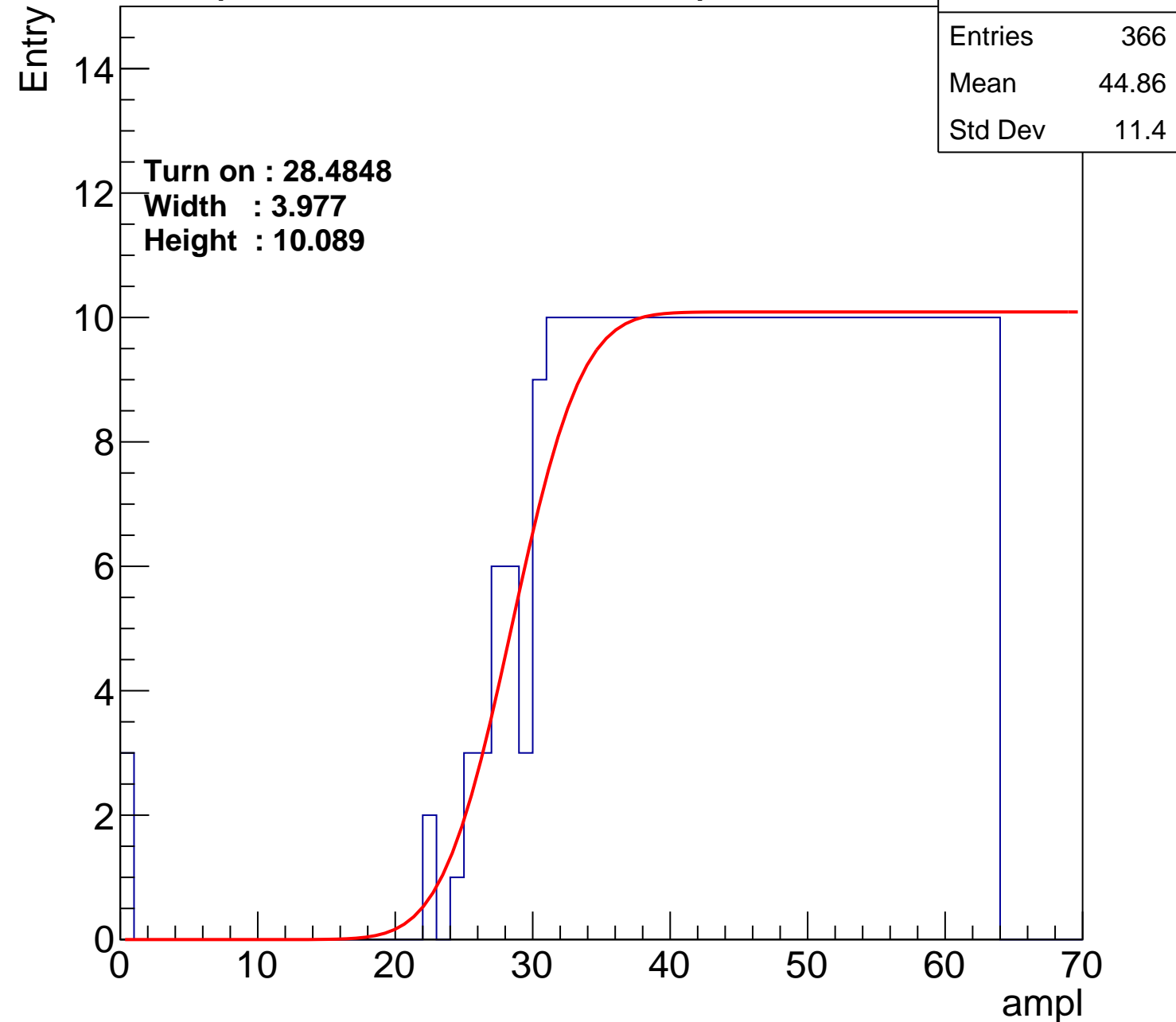
Width : 3.977

Height : 10.089

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch21

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.23
Std Dev	11.03

**Turn on : 28.2873**

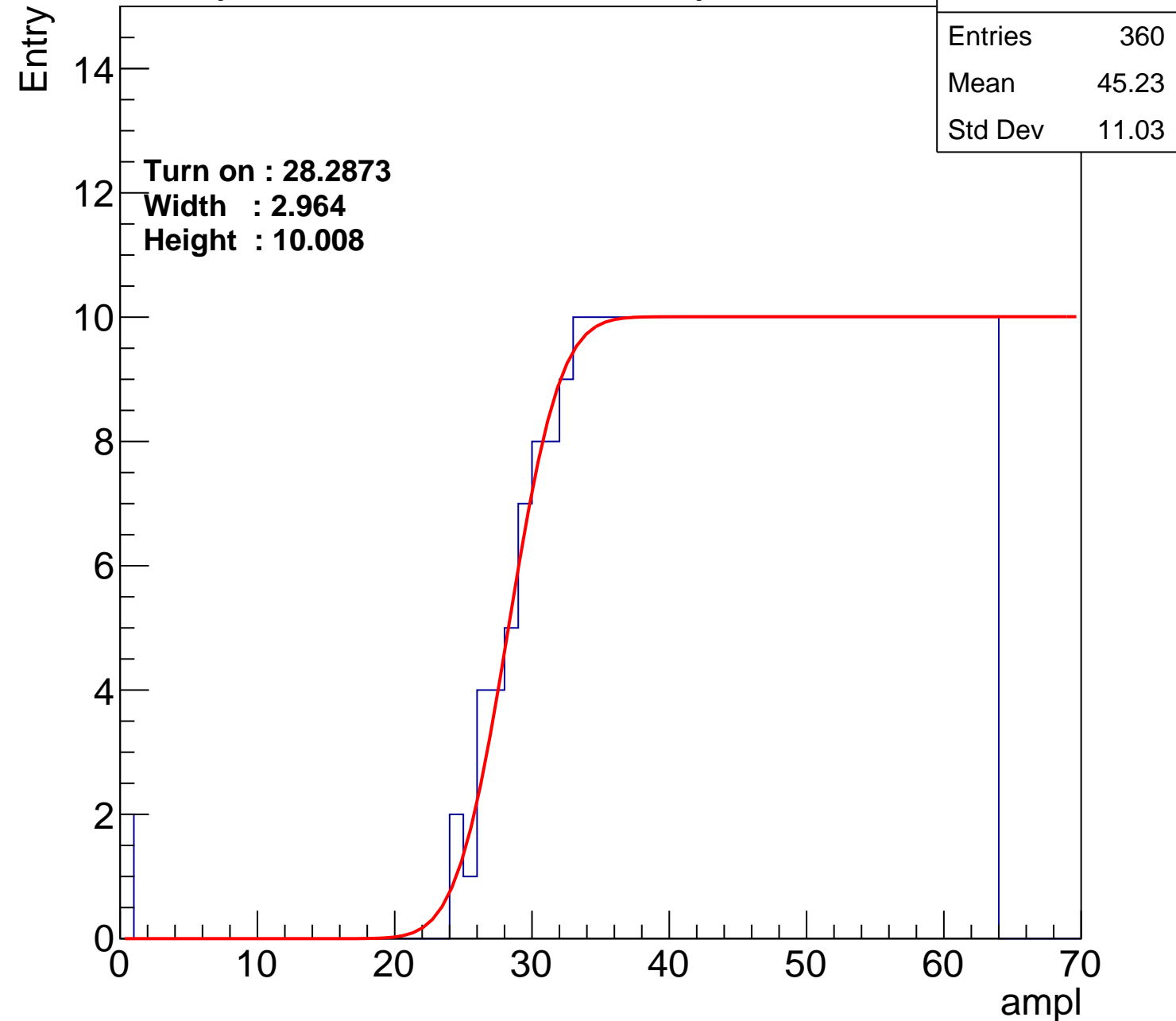
**Width : 2.964**

**Height : 10.008**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch22

calib\_packv5\_042523\_0143.root, FC#8, port C1

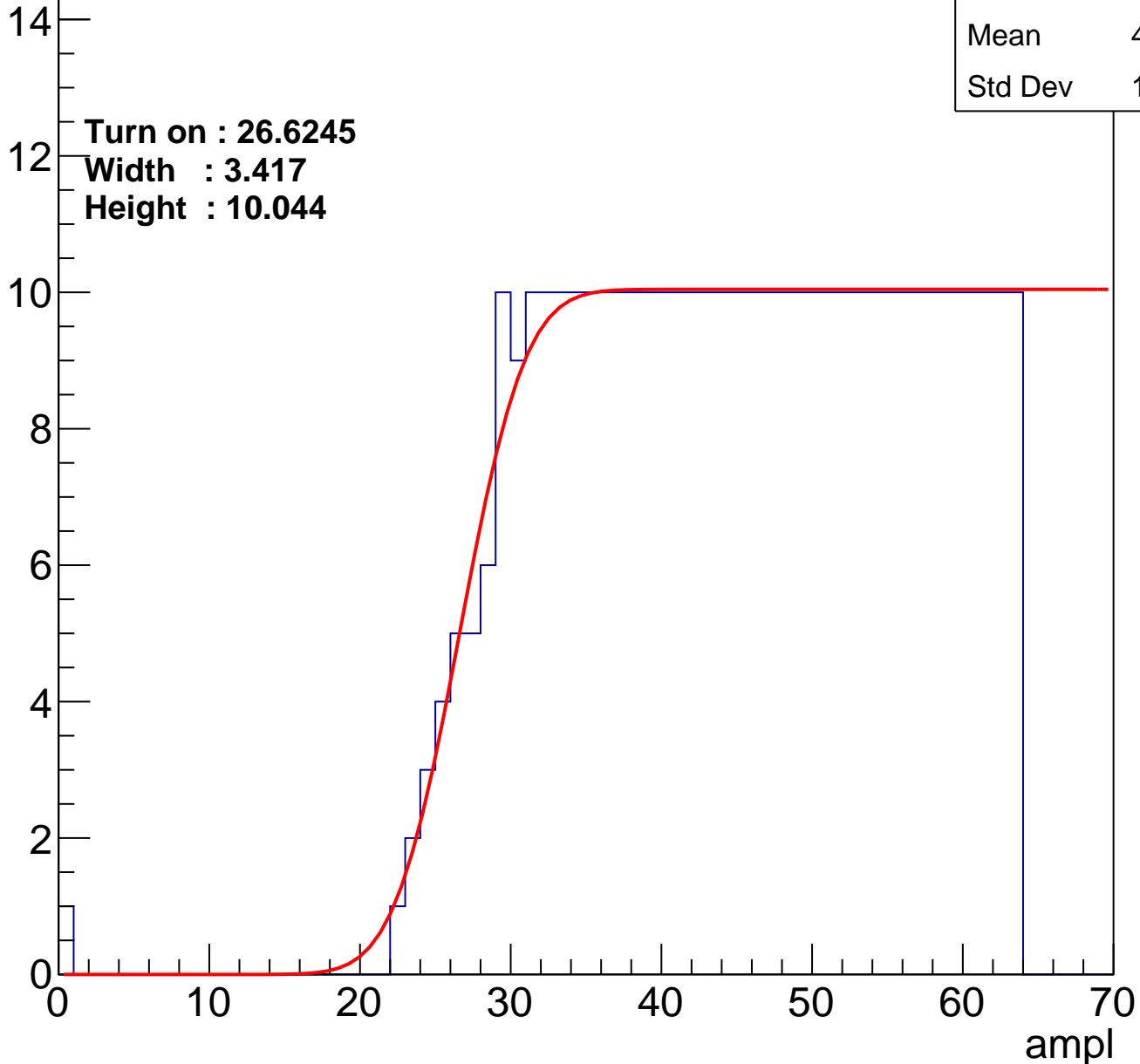
Entries	376
Mean	44.53
Std Dev	11.22

**Turn on : 26.6245**

**Width : 3.417**

**Height : 10.044**

Entry





# B0L002S, U23-ch23

calib\_packv5\_042523\_0143.root, FC#8, port C1

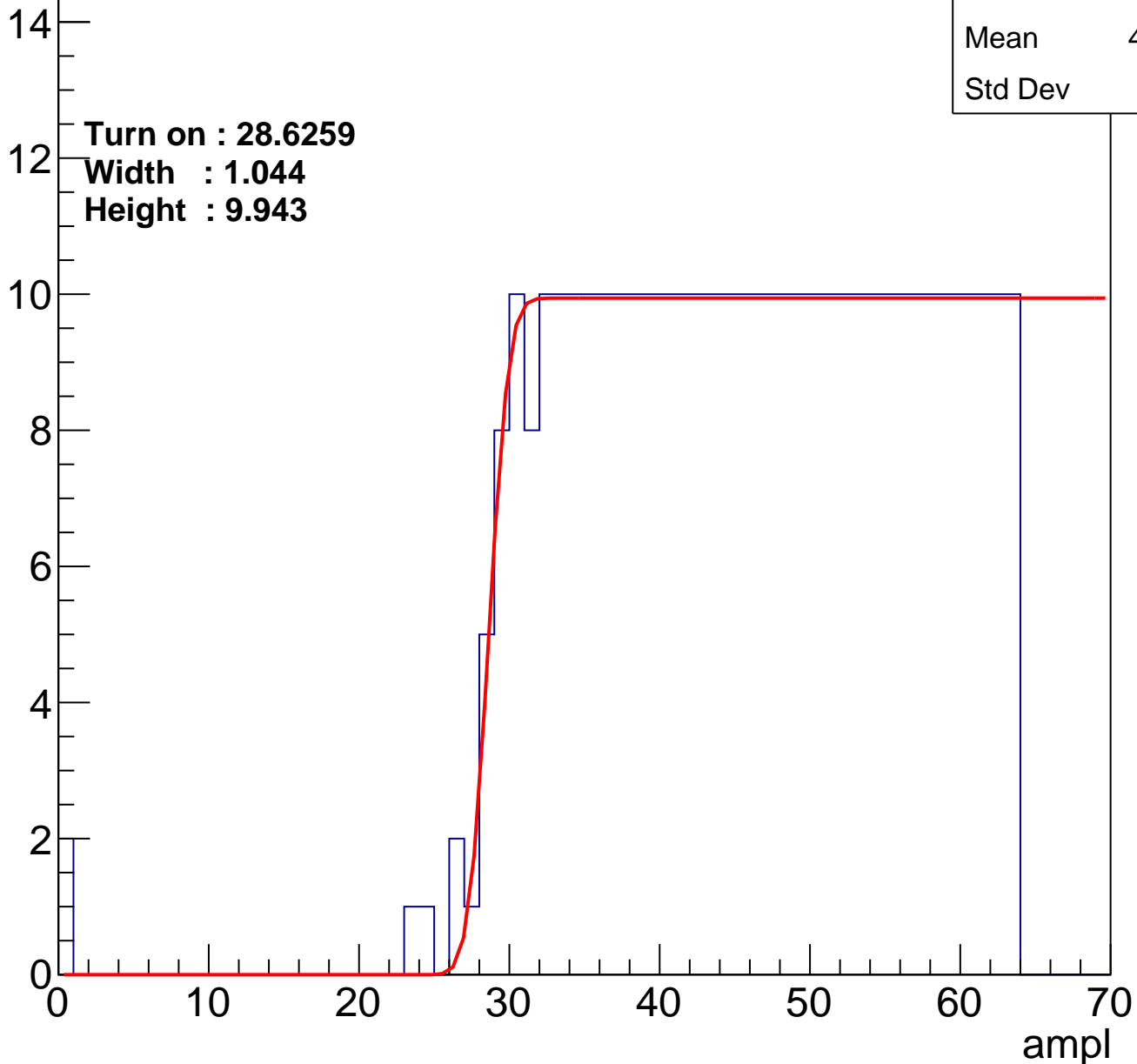
Entry

Entries	358
Mean	45.38
Std Dev	10.9

Turn on : 28.6259

Width : 1.044

Height : 9.943



# B0L002S, U23-ch24

calib\_packv5\_042523\_0143.root, FC#8, port C1

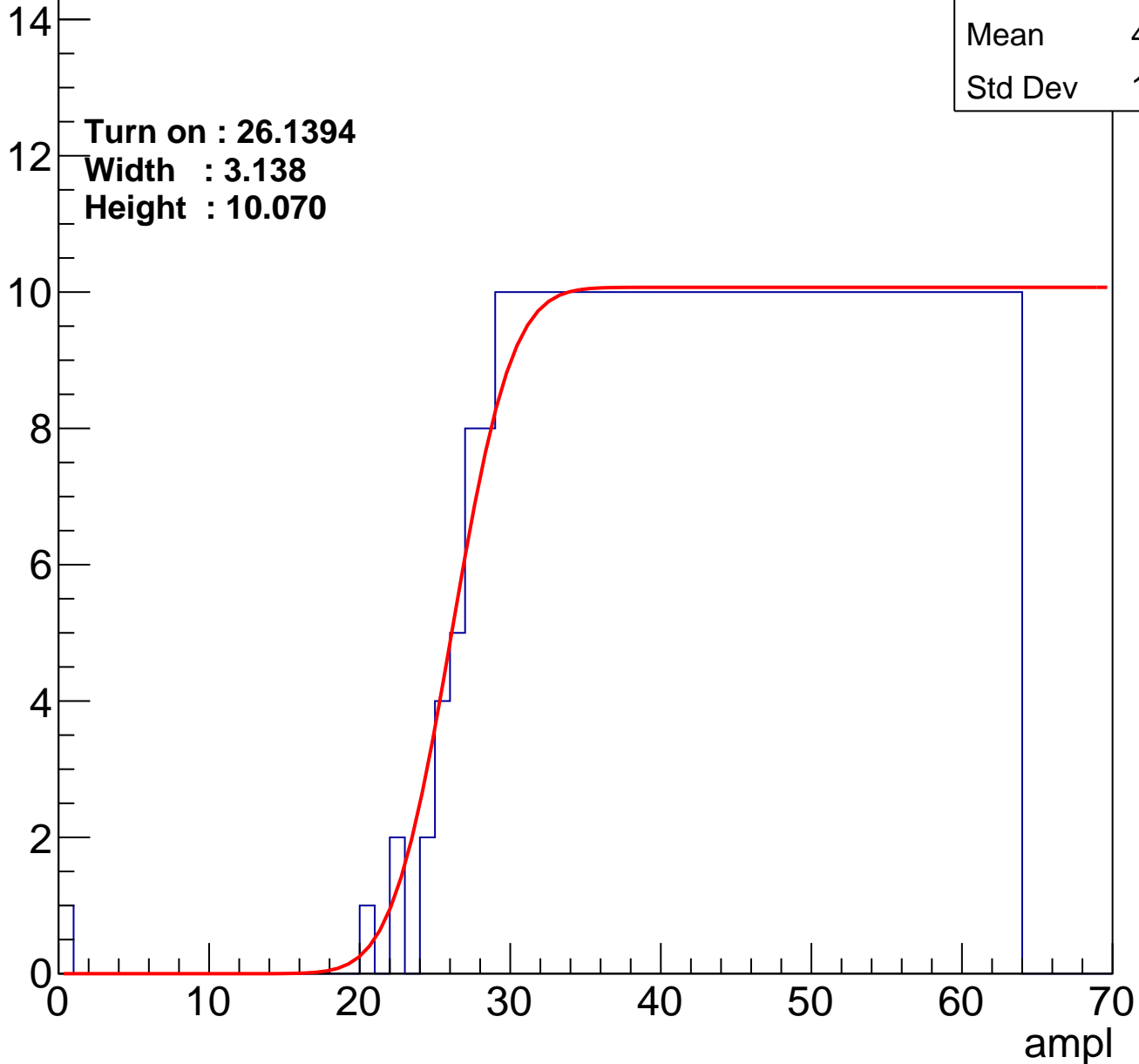
Entries	381
Mean	44.31
Std Dev	11.31

**Turn on : 26.1394**

**Width : 3.138**

**Height : 10.070**

Entry



# B0L002S, U23-ch25

calib\_packv5\_042523\_0143.root, FC#8, port C1

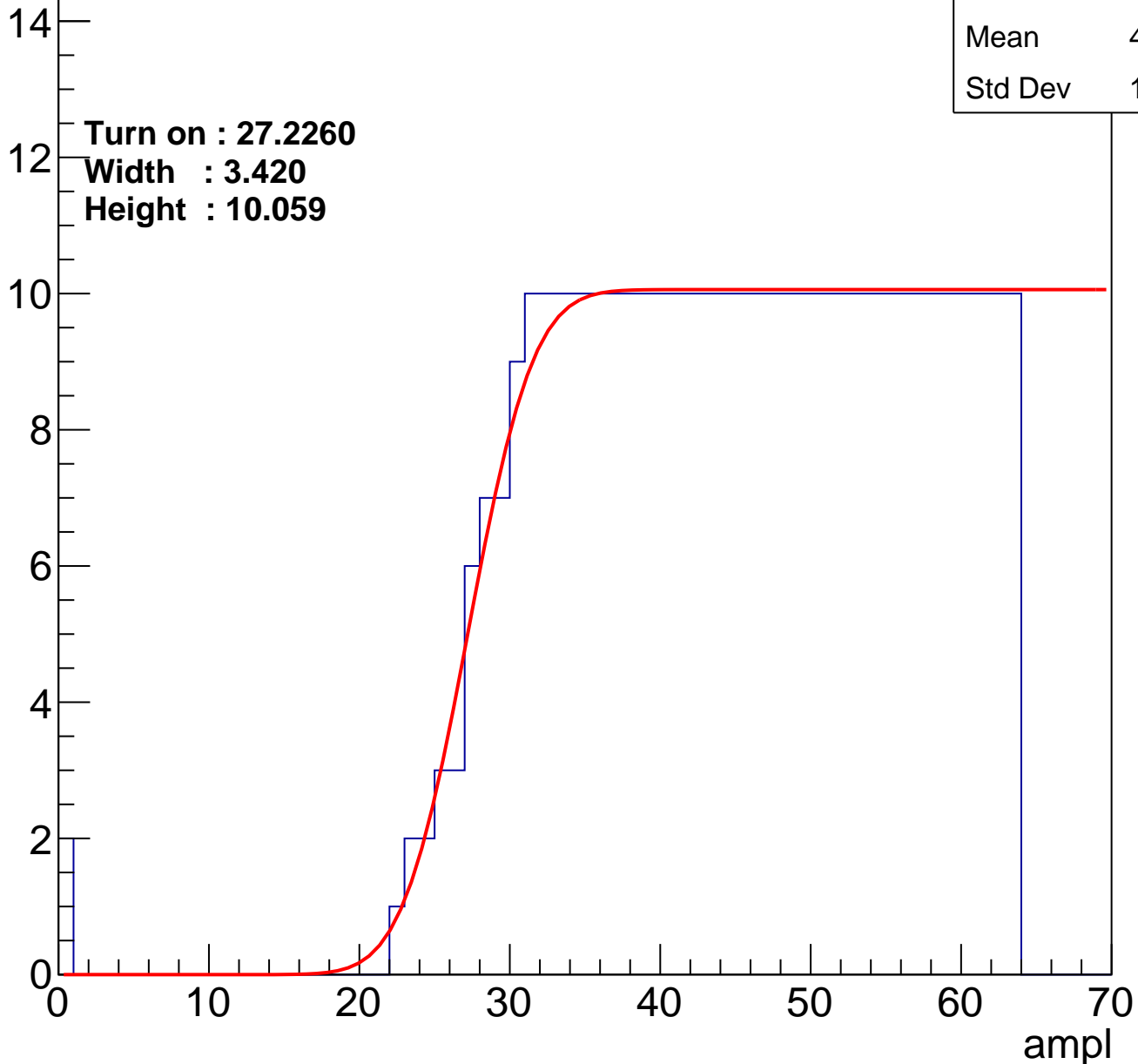
Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.2260

Width : 3.420

Height : 10.059

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.3816  
Width : 2.055  
Height : 10.027



# B0L002S, U23-ch27

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.34
Std Dev	11.58

Turn on : 26.5221

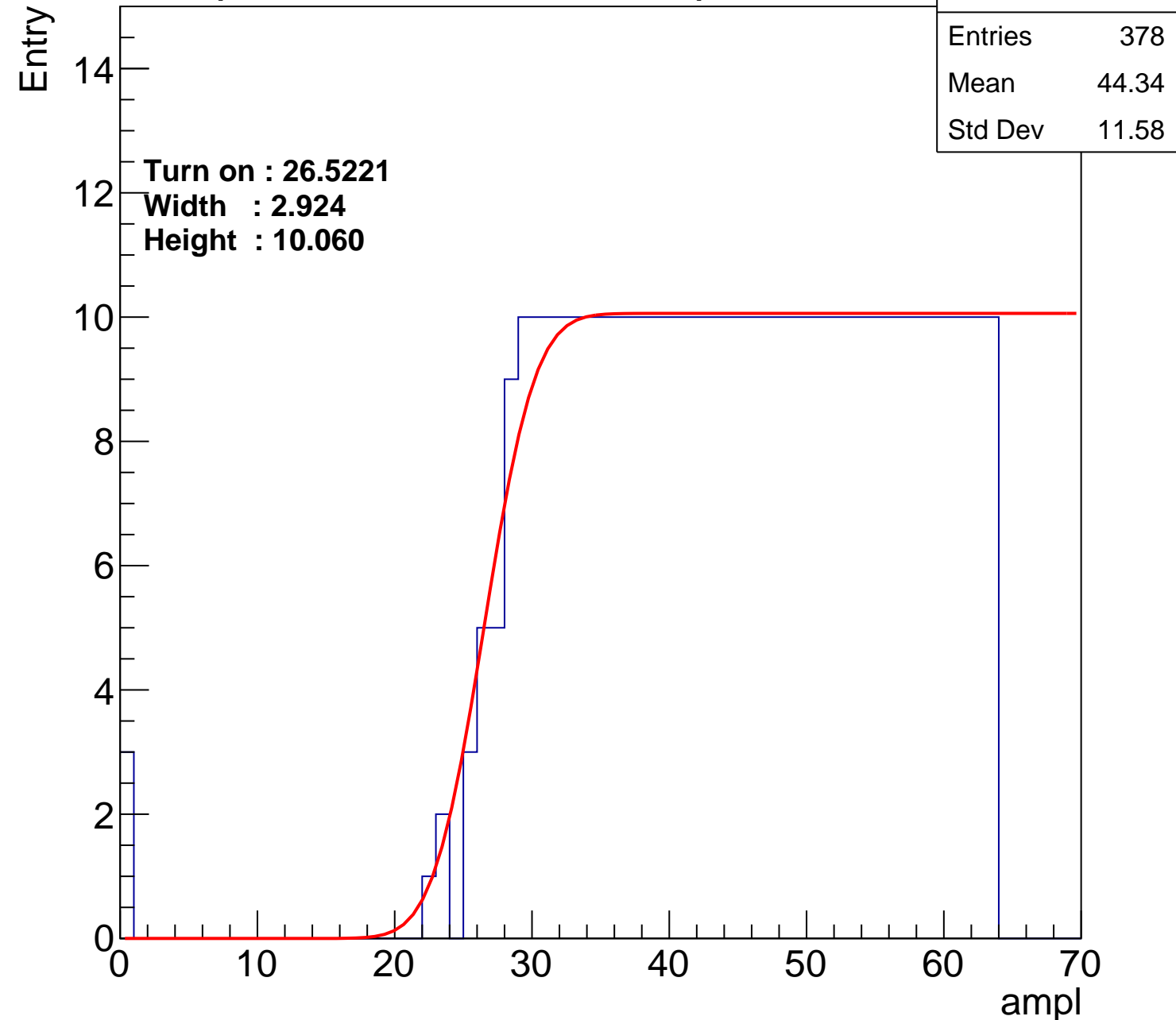
Width : 2.924

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch28

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.6182

Width : 2.696

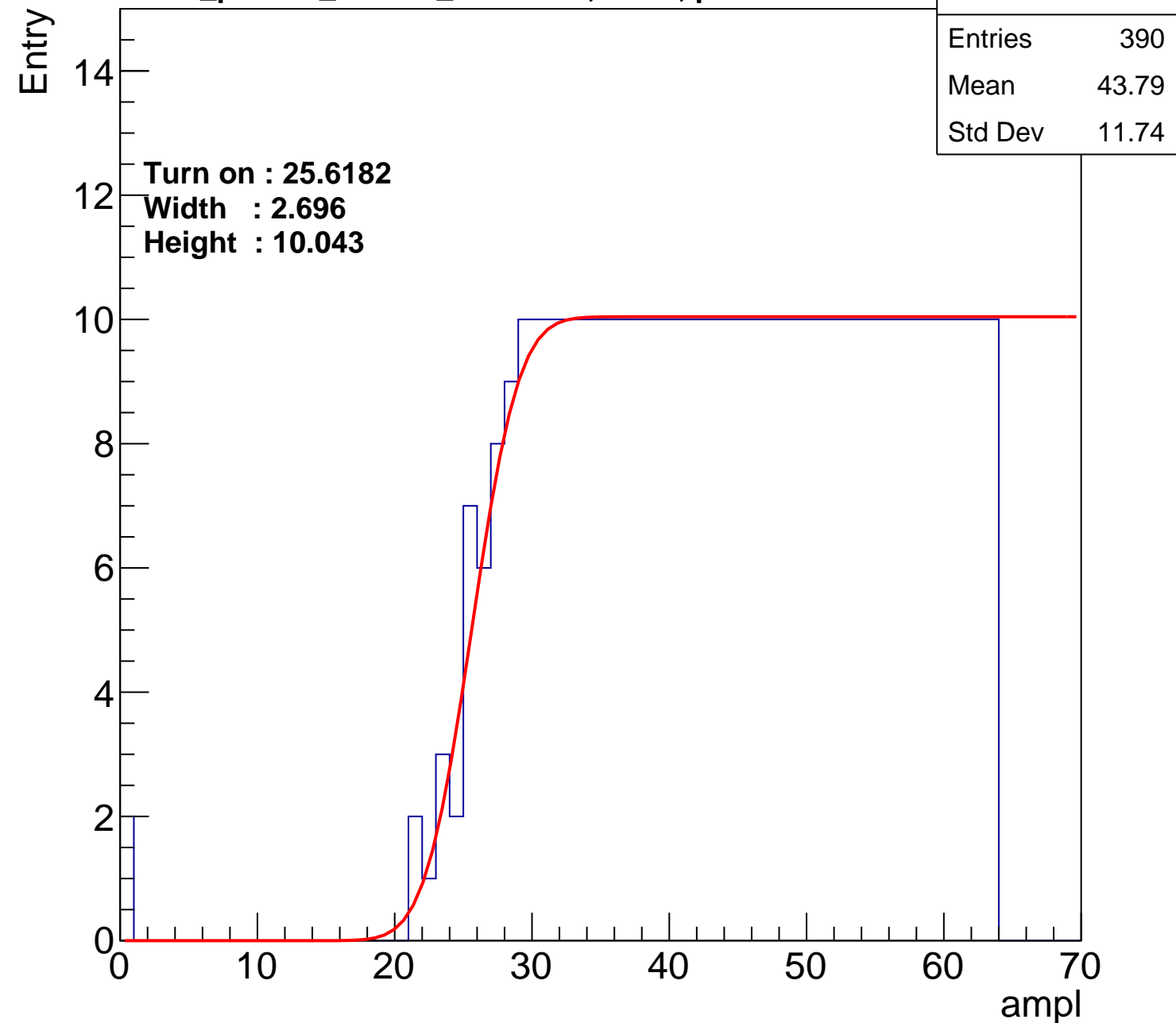
Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch29

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.29
Std Dev	10.78

Turn on : 27.9404

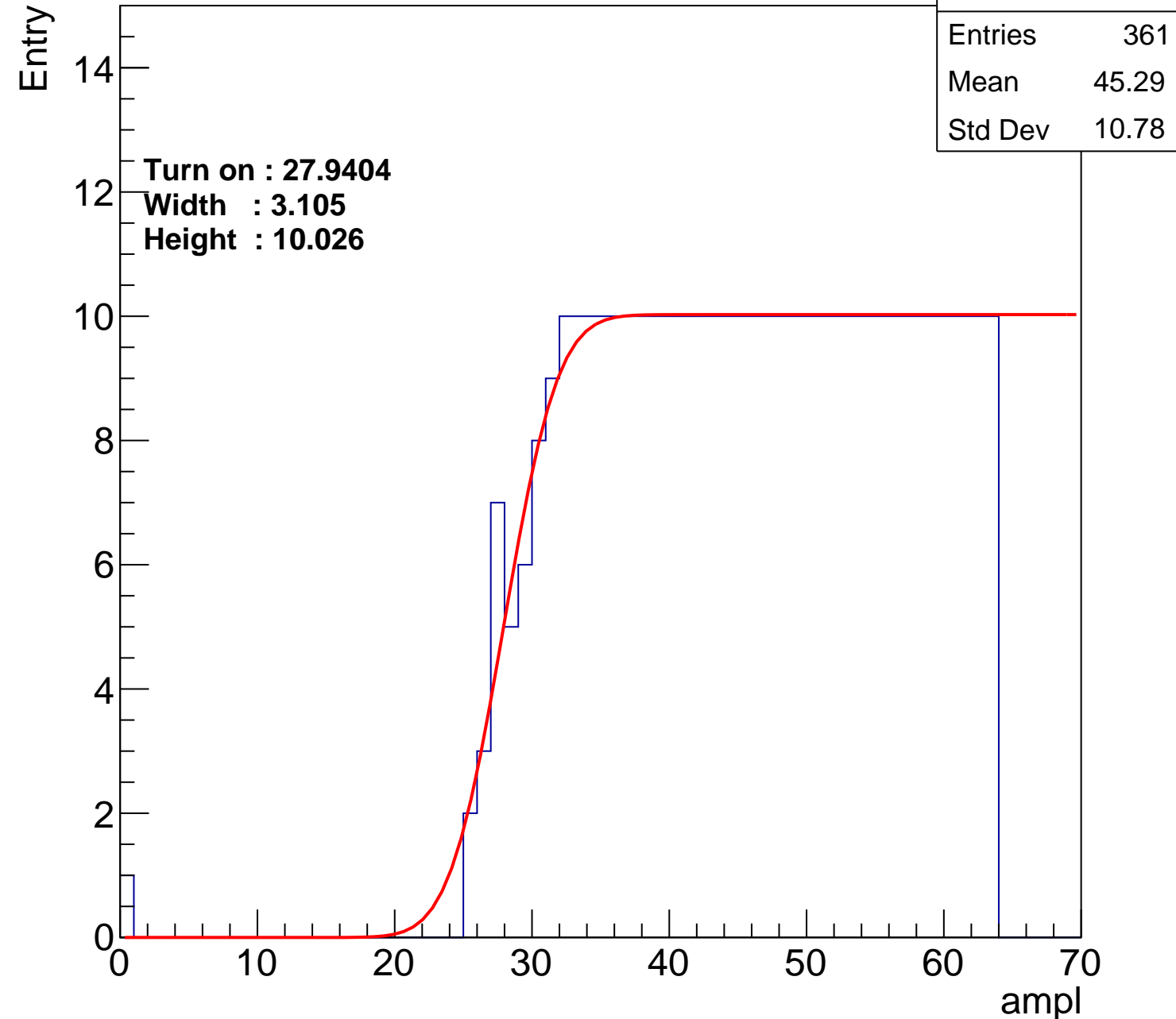
Width : 3.105

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch30

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	382
Mean	44.15
Std Dev	11.66

**Turn on : 26.4030**

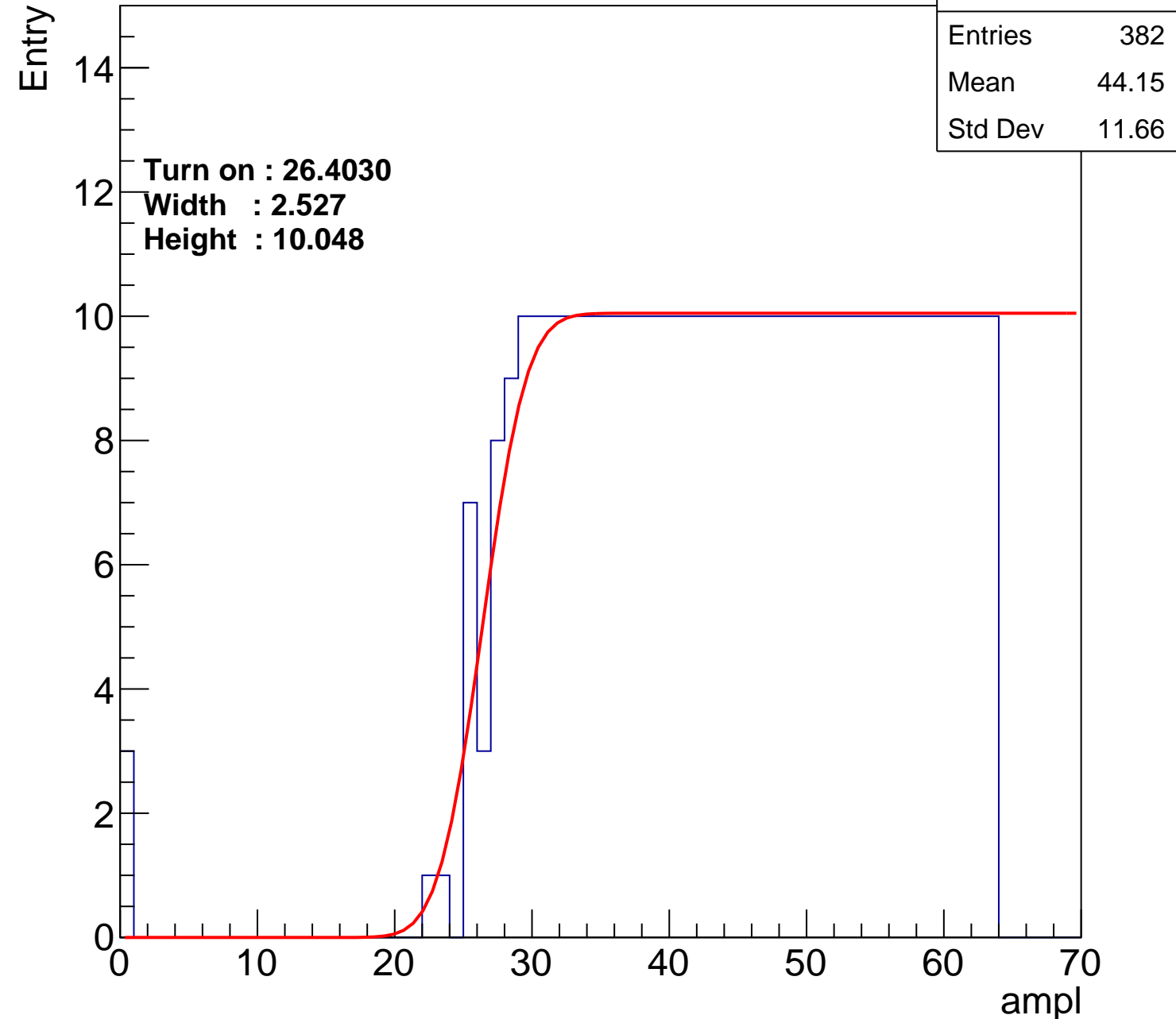
**Width : 2.527**

**Height : 10.048**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch31

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.7783

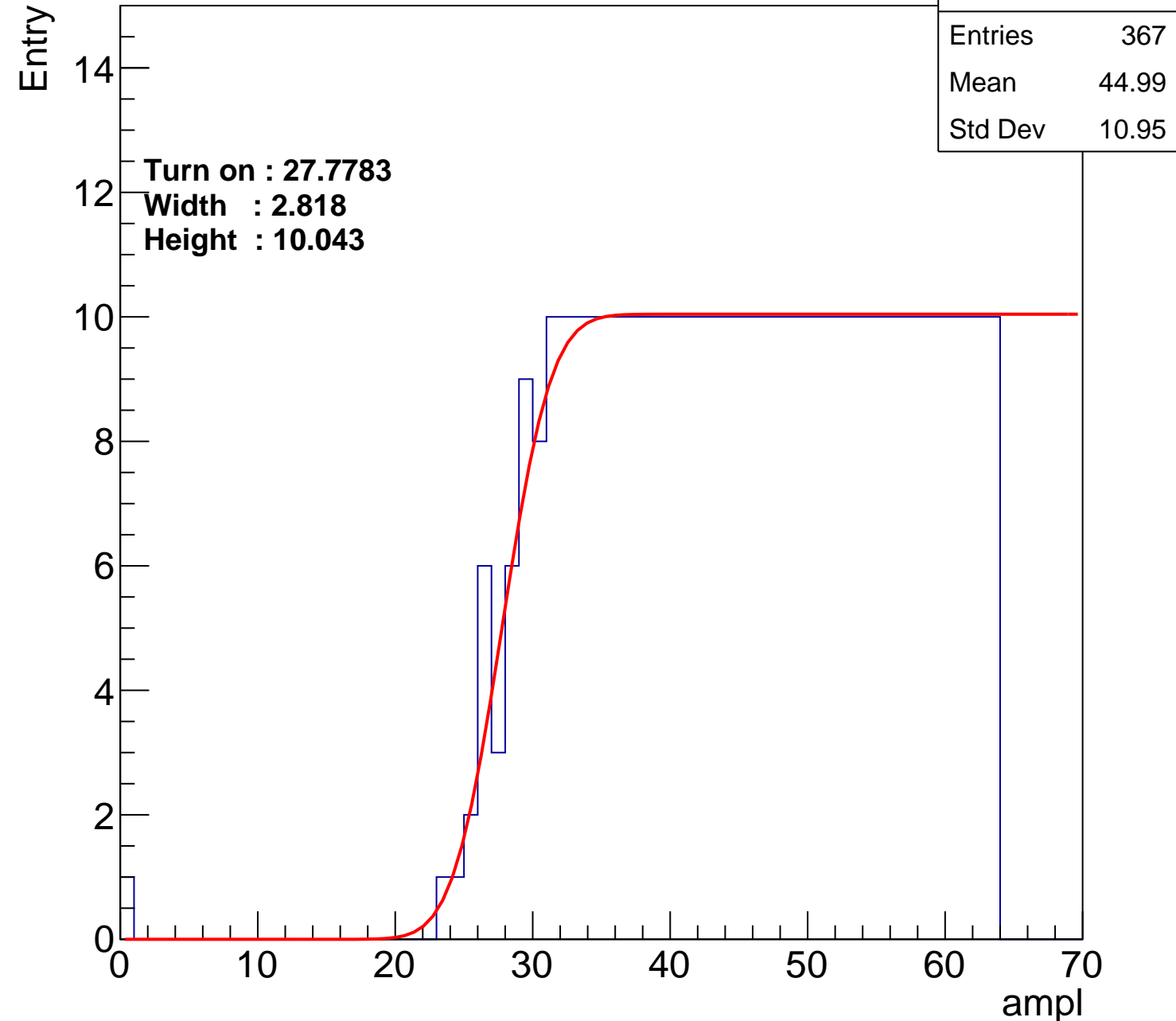
Width : 2.818

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch32

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	345
Mean	45.92
Std Dev	10.85

**Turn on : 29.9386**

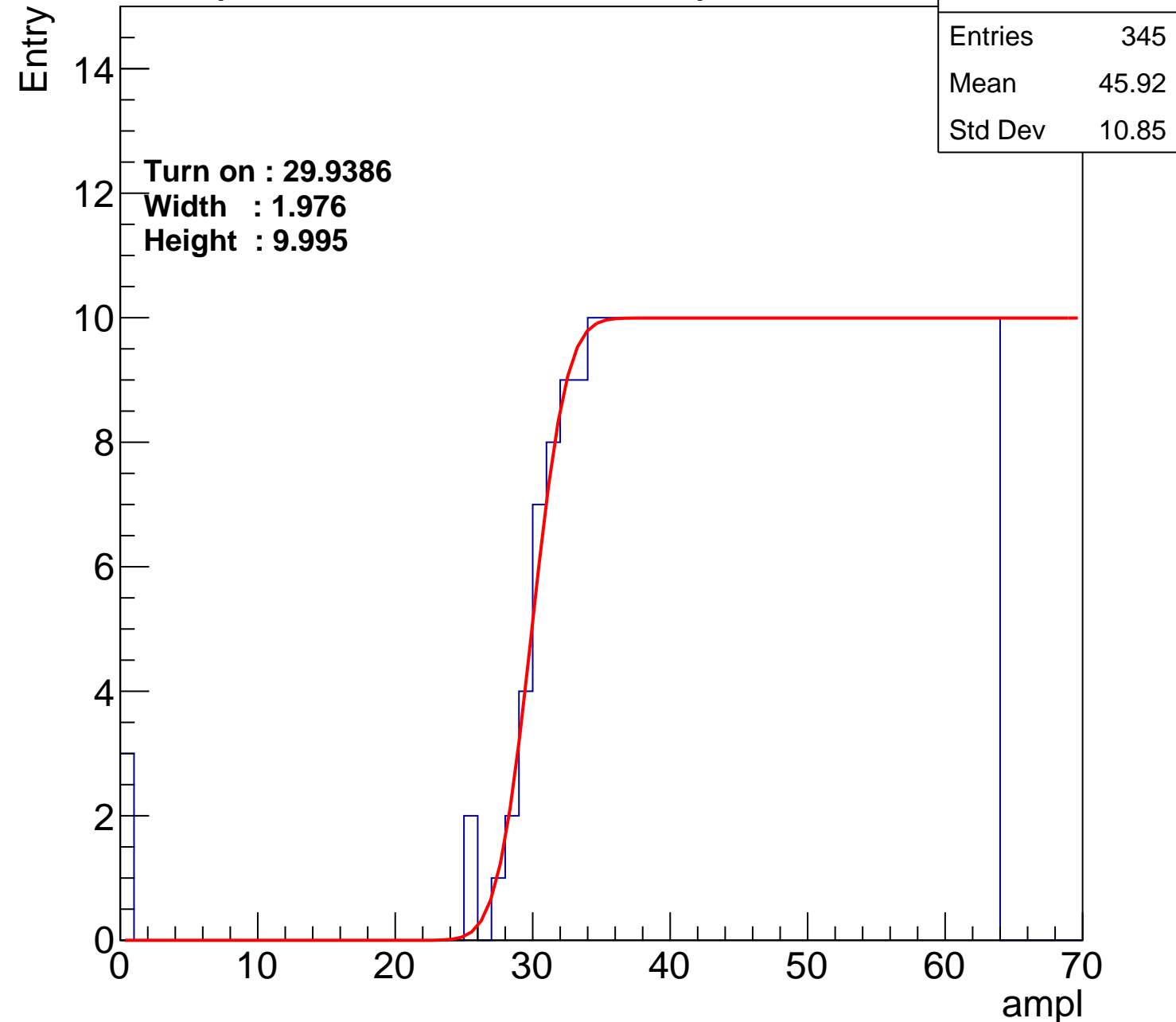
**Width : 1.976**

**Height : 9.995**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch33

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.01
Std Dev	11.18

**Turn on : 29.1298**

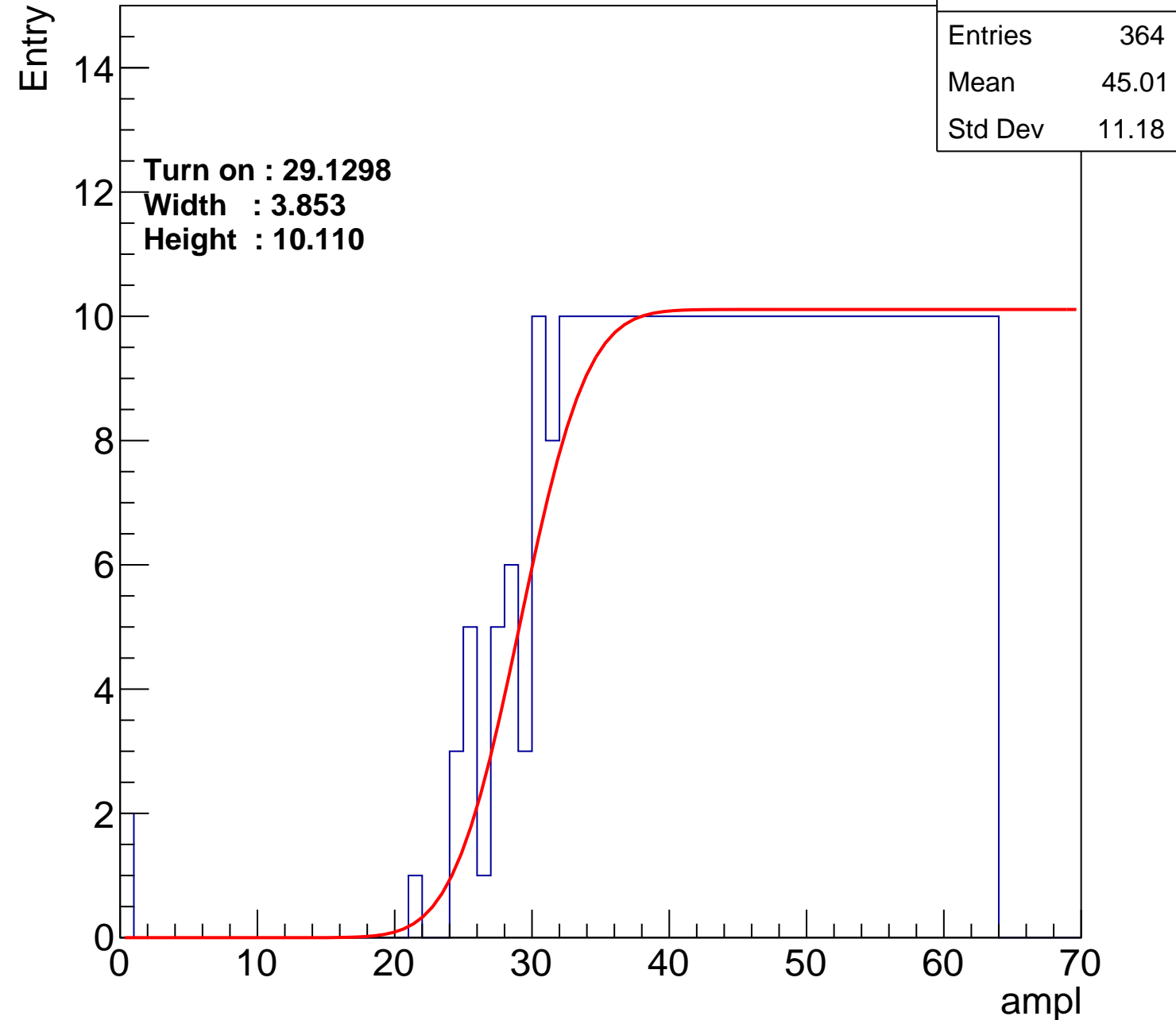
**Width : 3.853**

**Height : 10.110**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch34

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.4
Std Dev	11.27

Turn on : 26.4069

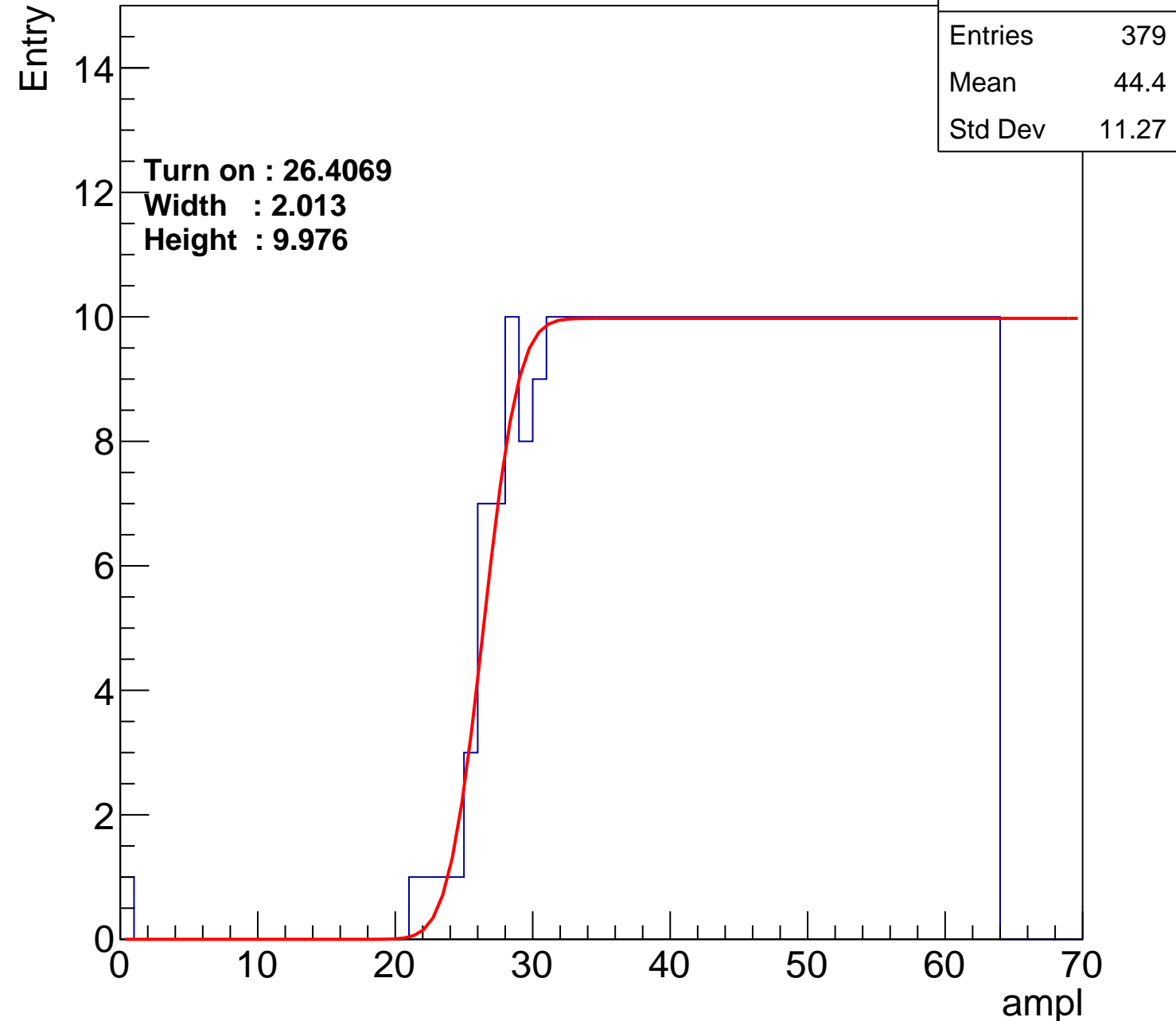
Width : 2.013

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch35

calib\_packv5\_042523\_0143.root, FC#8, port C1

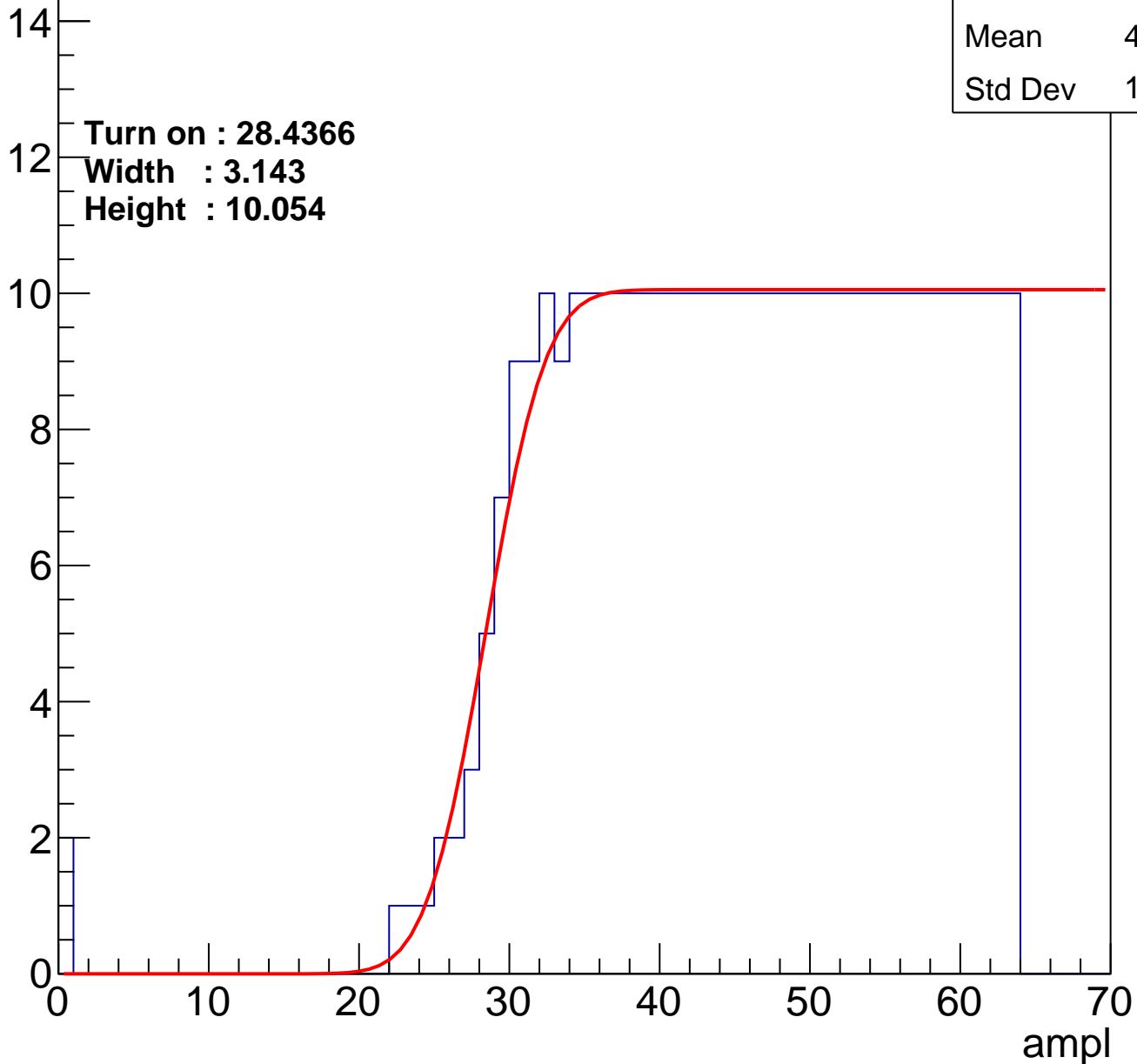
Entries	361
Mean	45.18
Std Dev	11.06

**Turn on : 28.4366**

**Width : 3.143**

**Height : 10.054**

Entry



# B0L002S, U23-ch36

calib\_packv5\_042523\_0143.root, FC#8, port C1

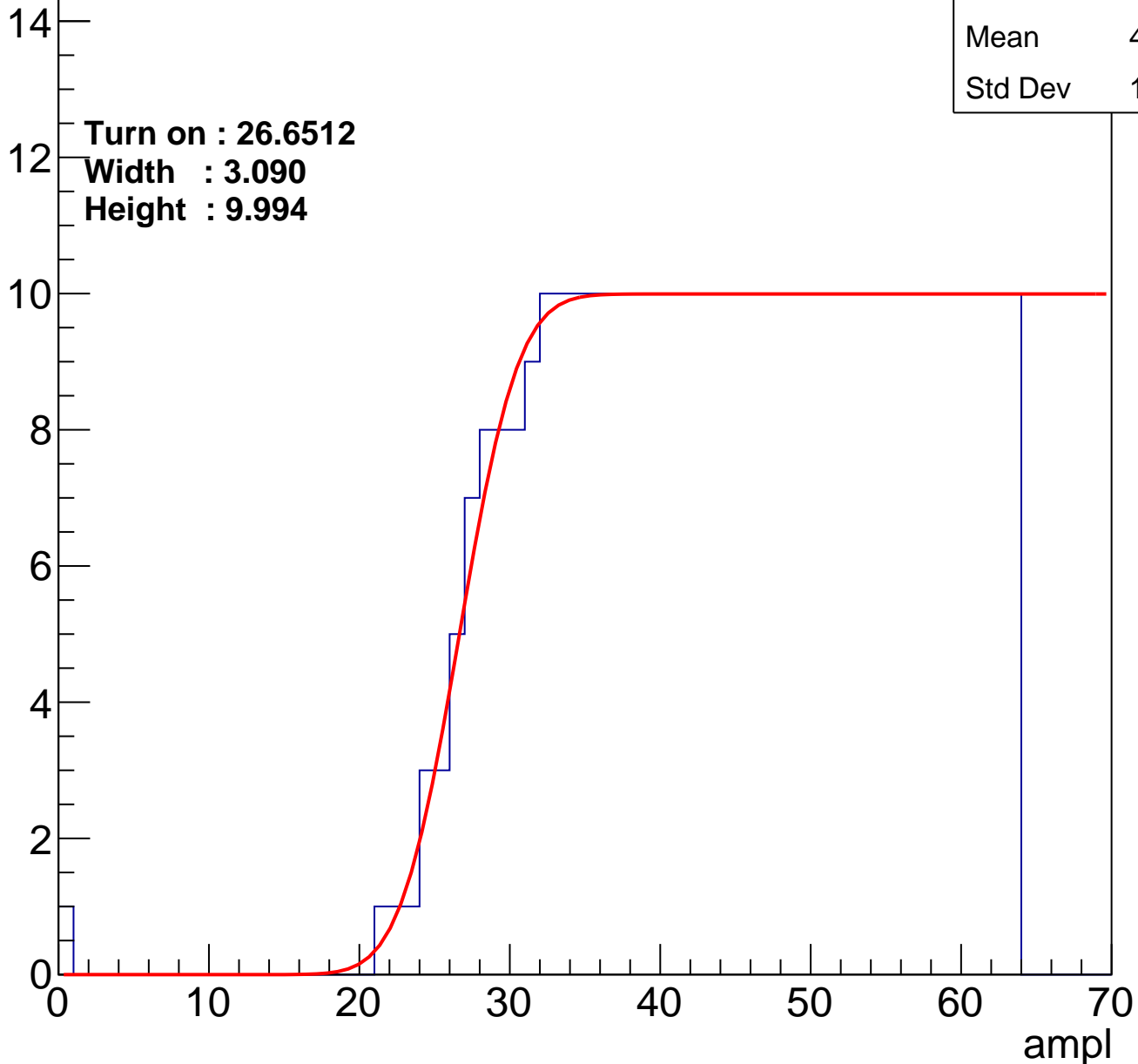
Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 26.6512

Width : 3.090

Height : 9.994

Entry



# B0L002S, U23-ch37

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.66
Std Dev	10.67

**Turn on : 29.1537**

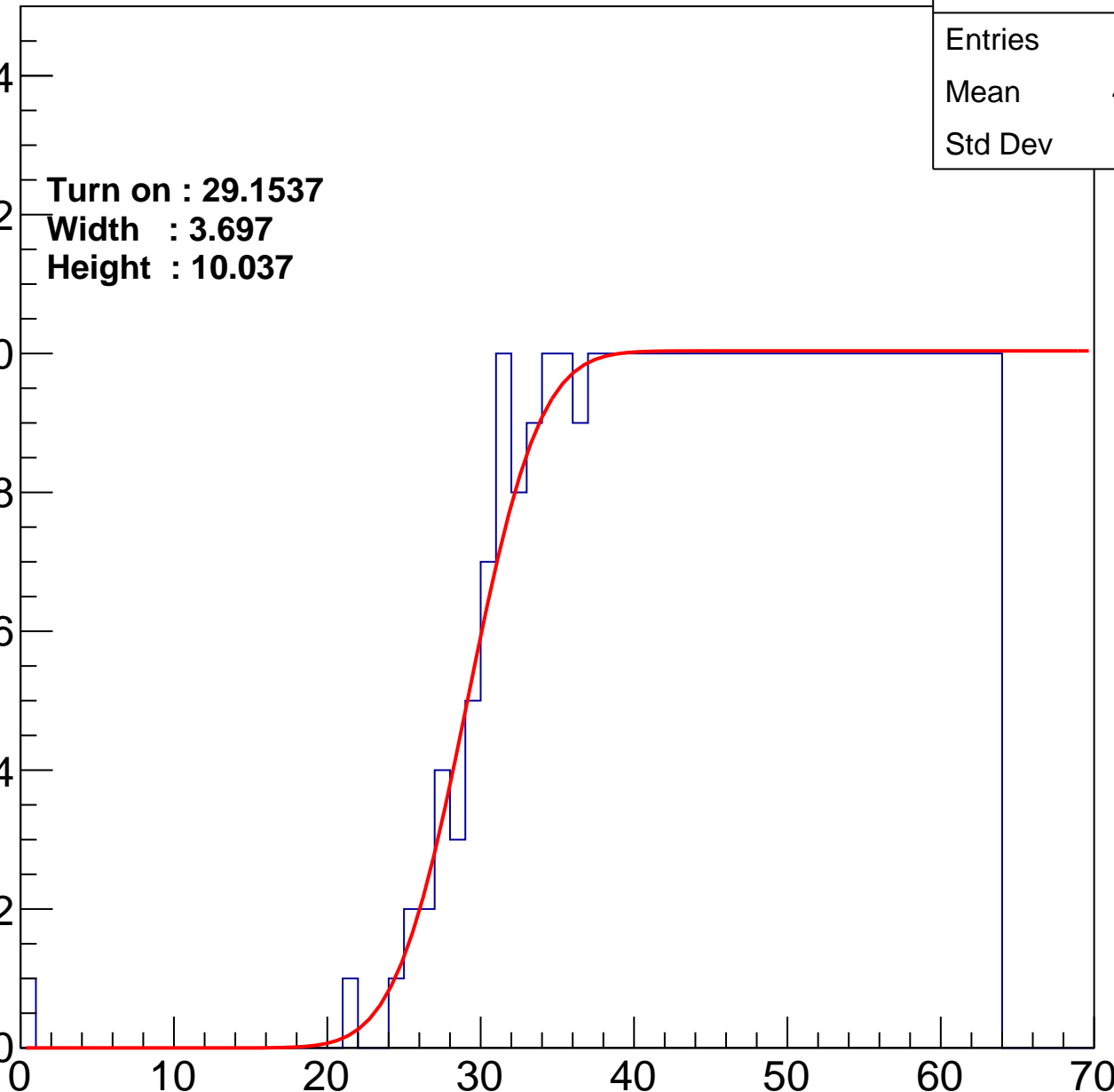
**Width : 3.697**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch38

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.39
Std Dev	11.73

Turn on : 26.9625

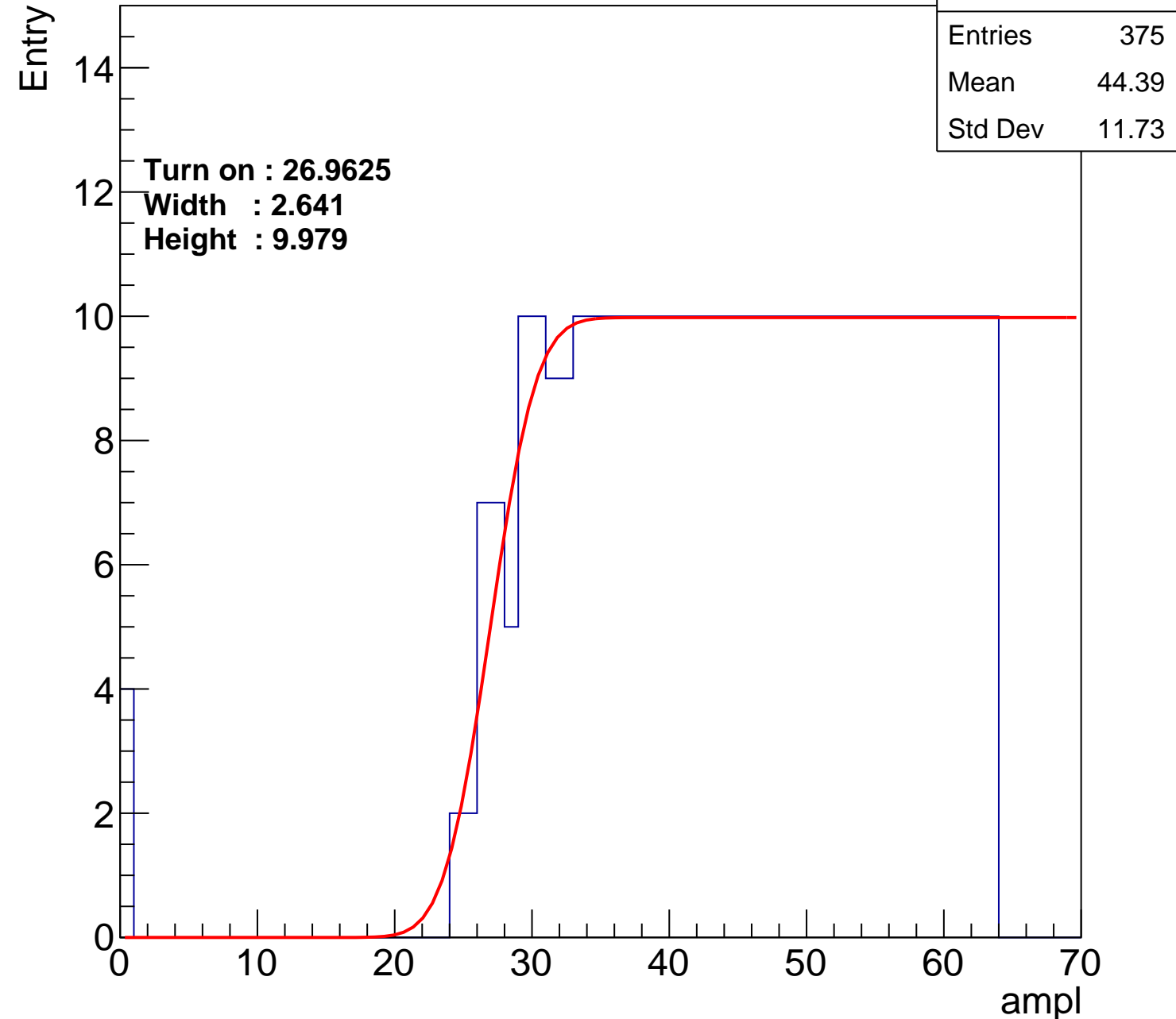
Width : 2.641

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch39

calib\_packv5\_042523\_0143.root, FC#8, port C1

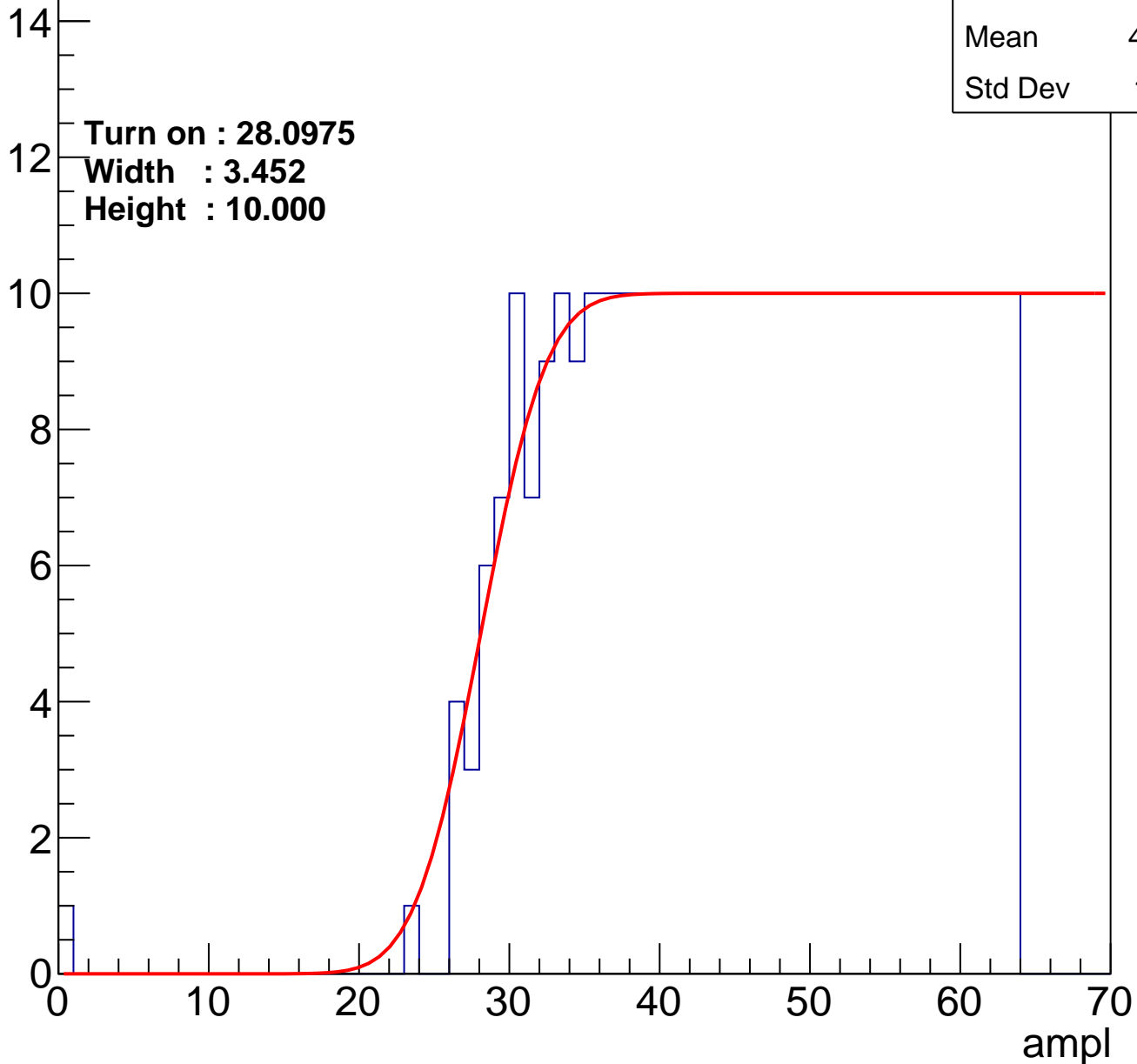
Entries	357
Mean	45.46
Std Dev	10.71

Turn on : 28.0975

Width : 3.452

Height : 10.000

Entry



# B0L002S, U23-ch40

calib\_packv5\_042523\_0143.root, FC#8, port C1

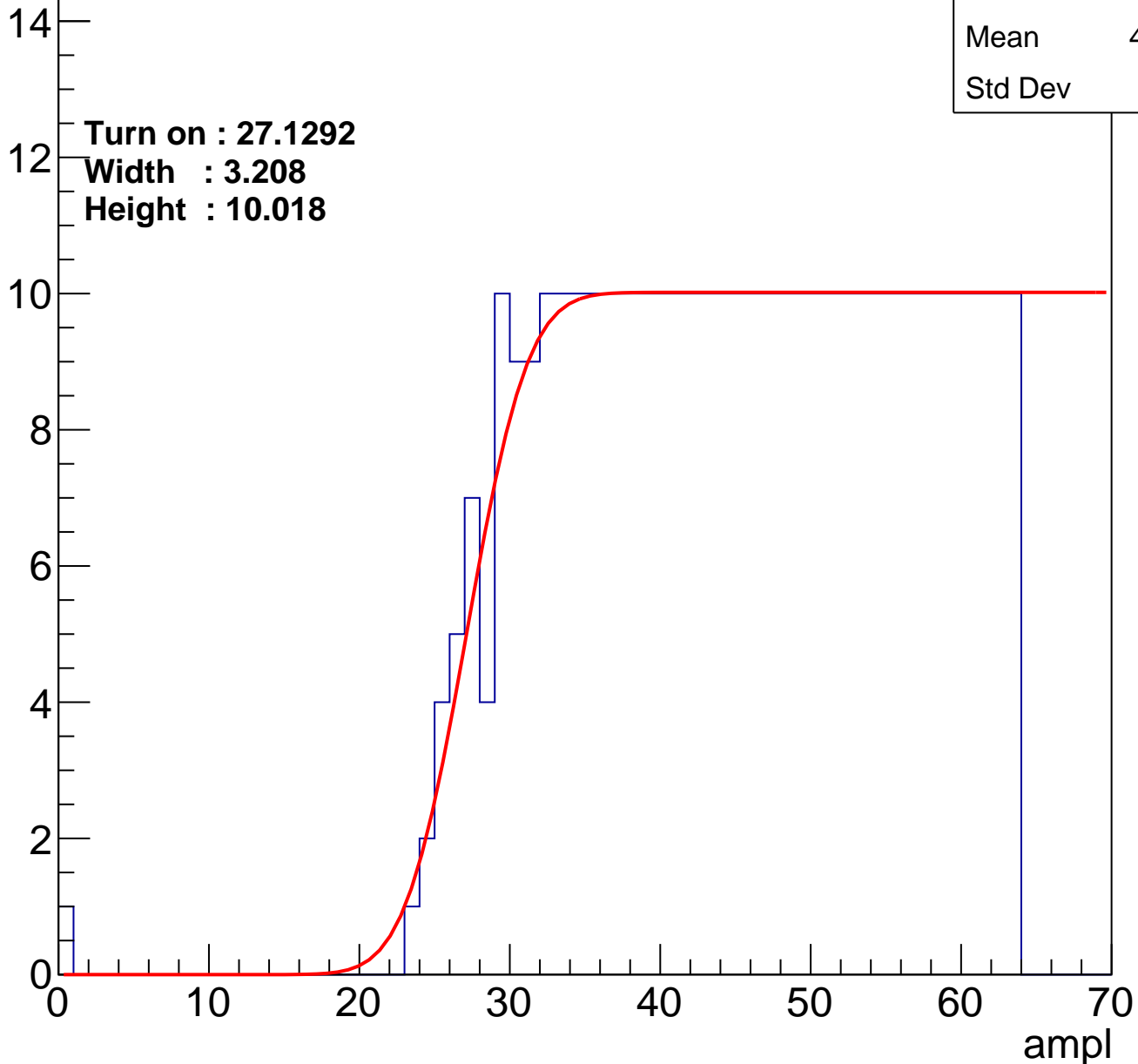
Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.1292

Width : 3.208

Height : 10.018

Entry



# B0L002S, U23-ch41

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	346
Mean	46.03
Std Dev	10.39

**Turn on : 29.7708**

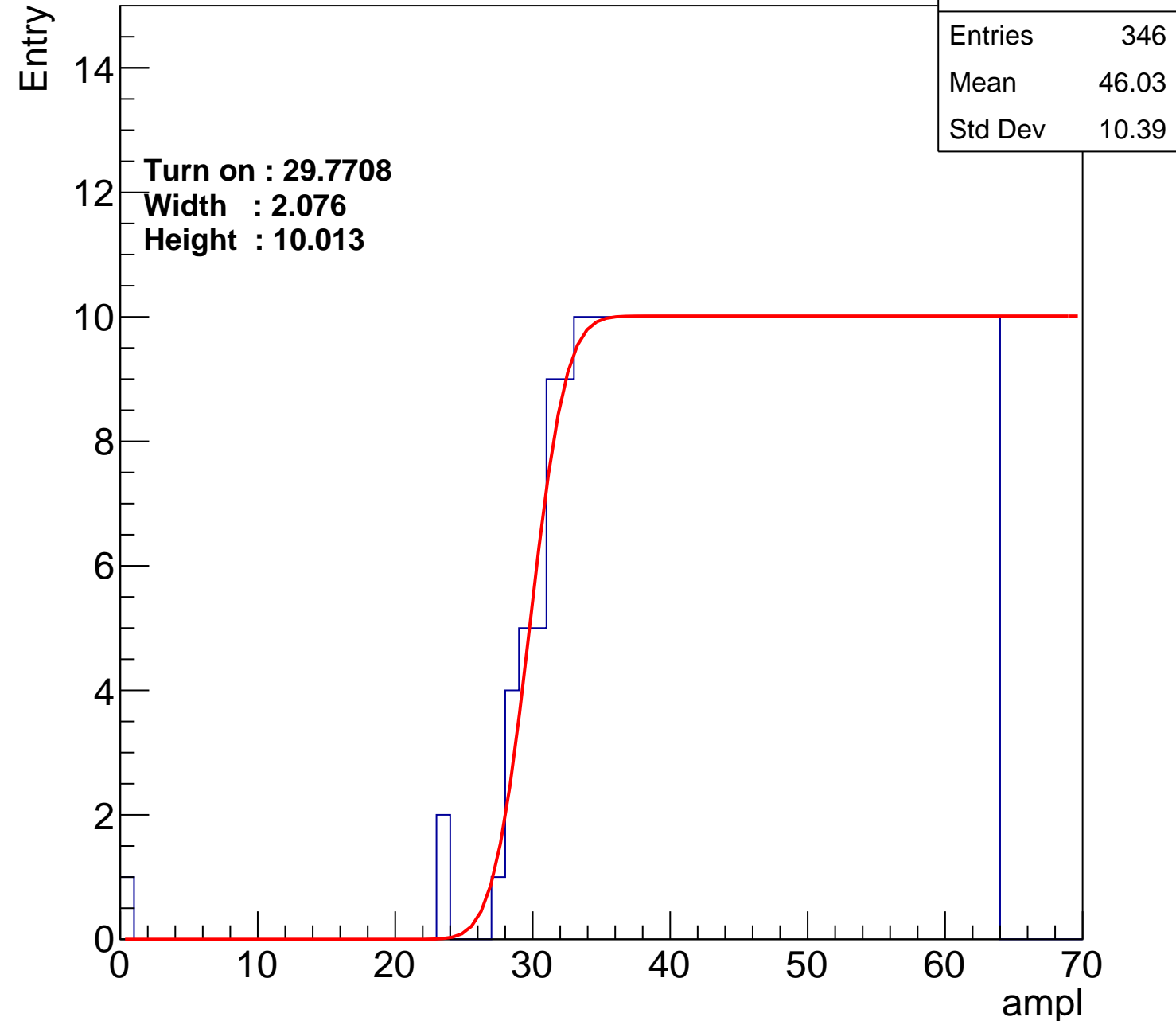
**Width : 2.076**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch42

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.18
Std Dev	11.06

Turn on : 28.1854

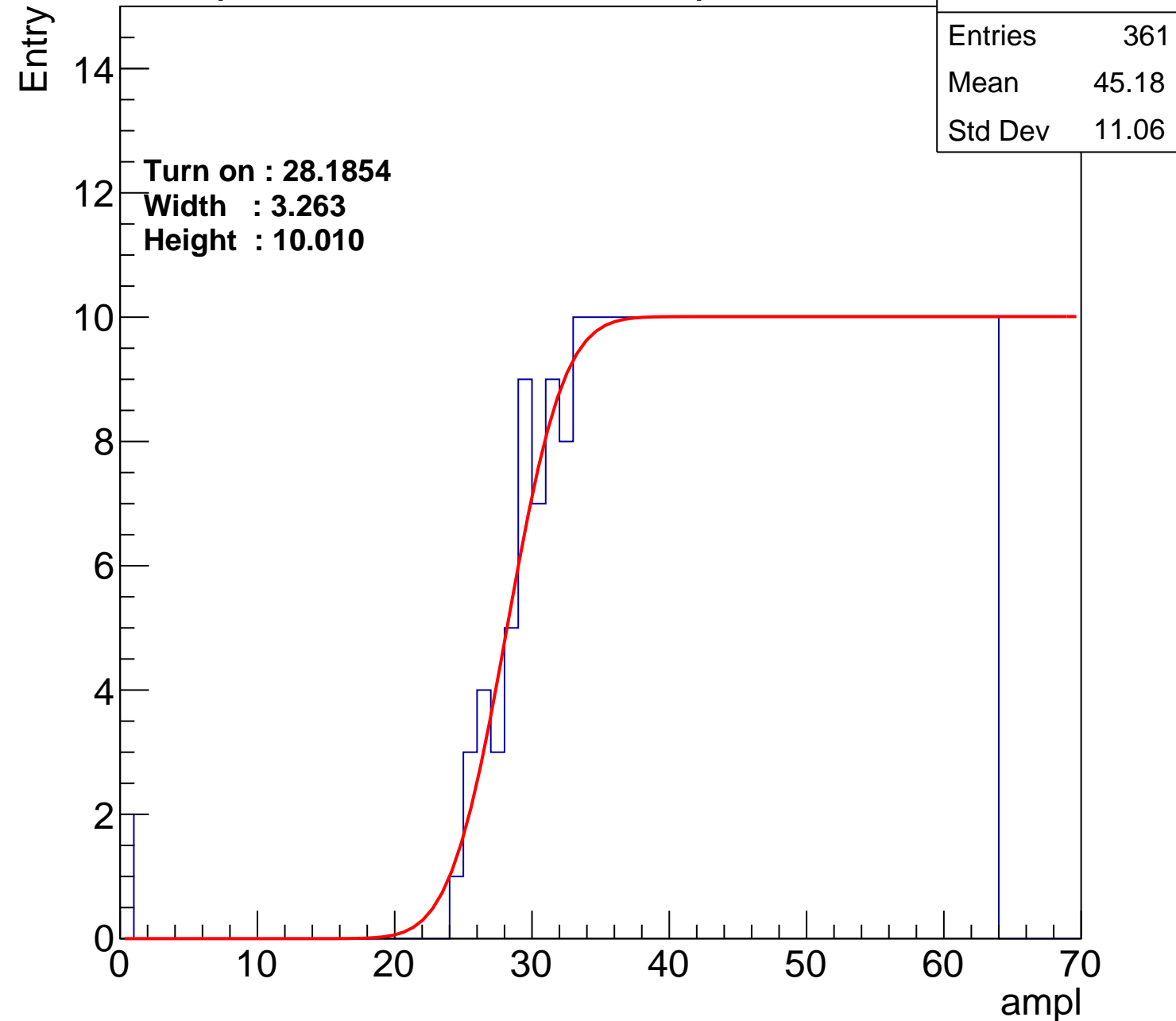
Width : 3.263

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch43

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.38
Std Dev	10.76

Turn on : 28.3665

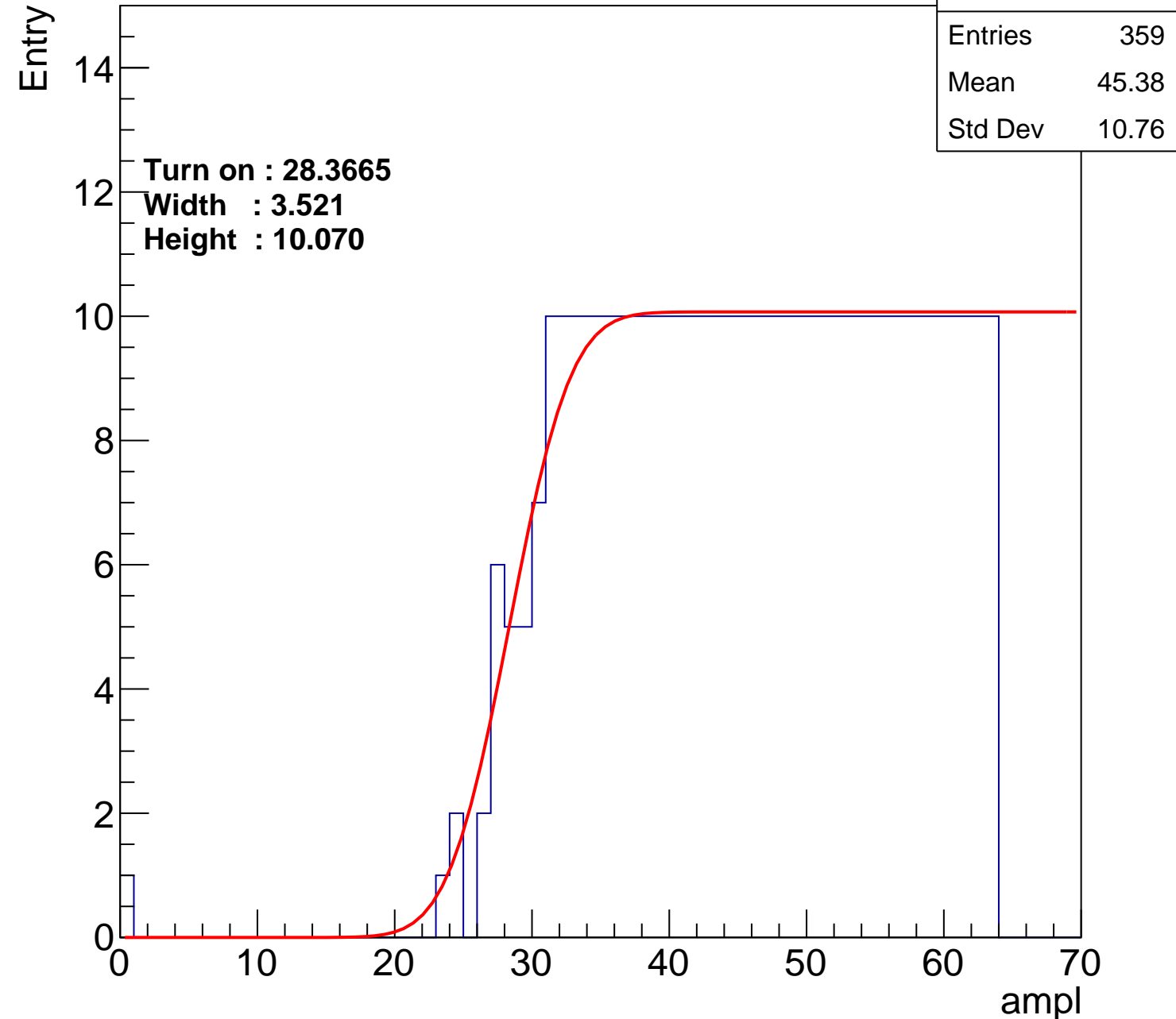
Width : 3.521

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch44

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.9
Std Dev	11.37

Turn on : 28.2399

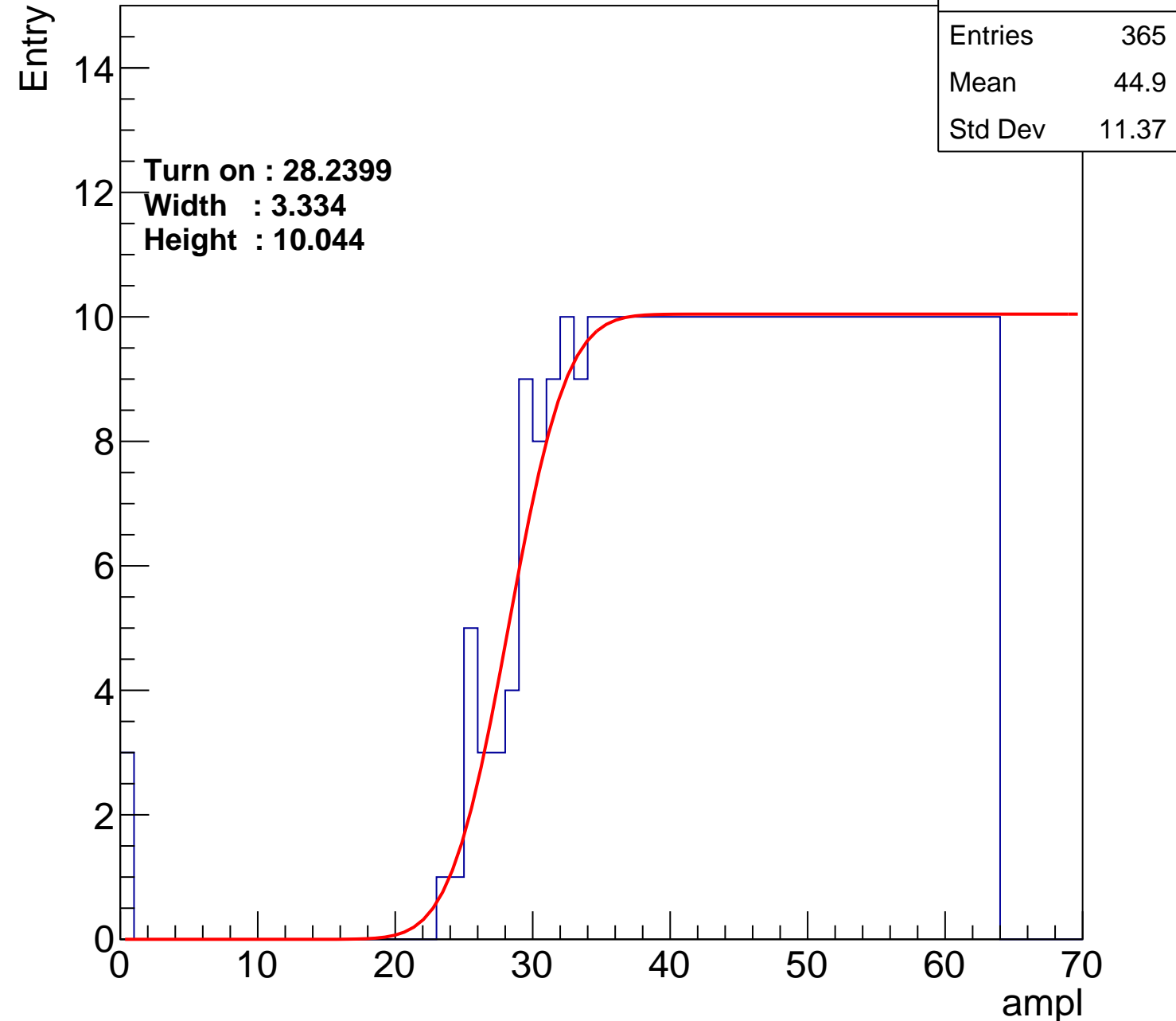
Width : 3.334

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch45

calib\_packv5\_042523\_0143.root, FC#8, port C1

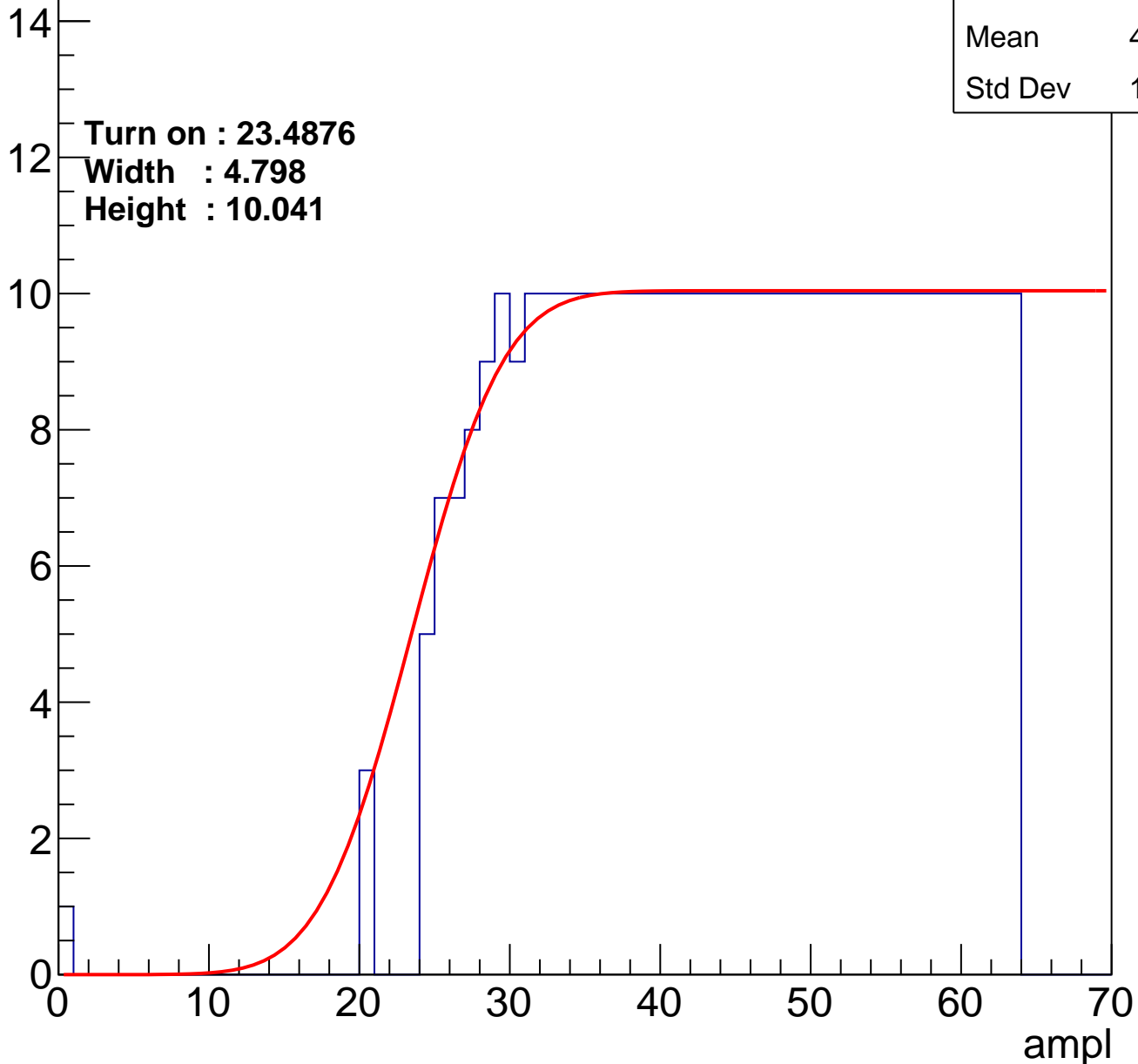
Entries	389
Mean	43.89
Std Dev	11.56

Turn on : 23.4876

Width : 4.798

Height : 10.041

Entry



# B0L002S, U23-ch46

calib\_packv5\_042523\_0143.root, FC#8, port C1

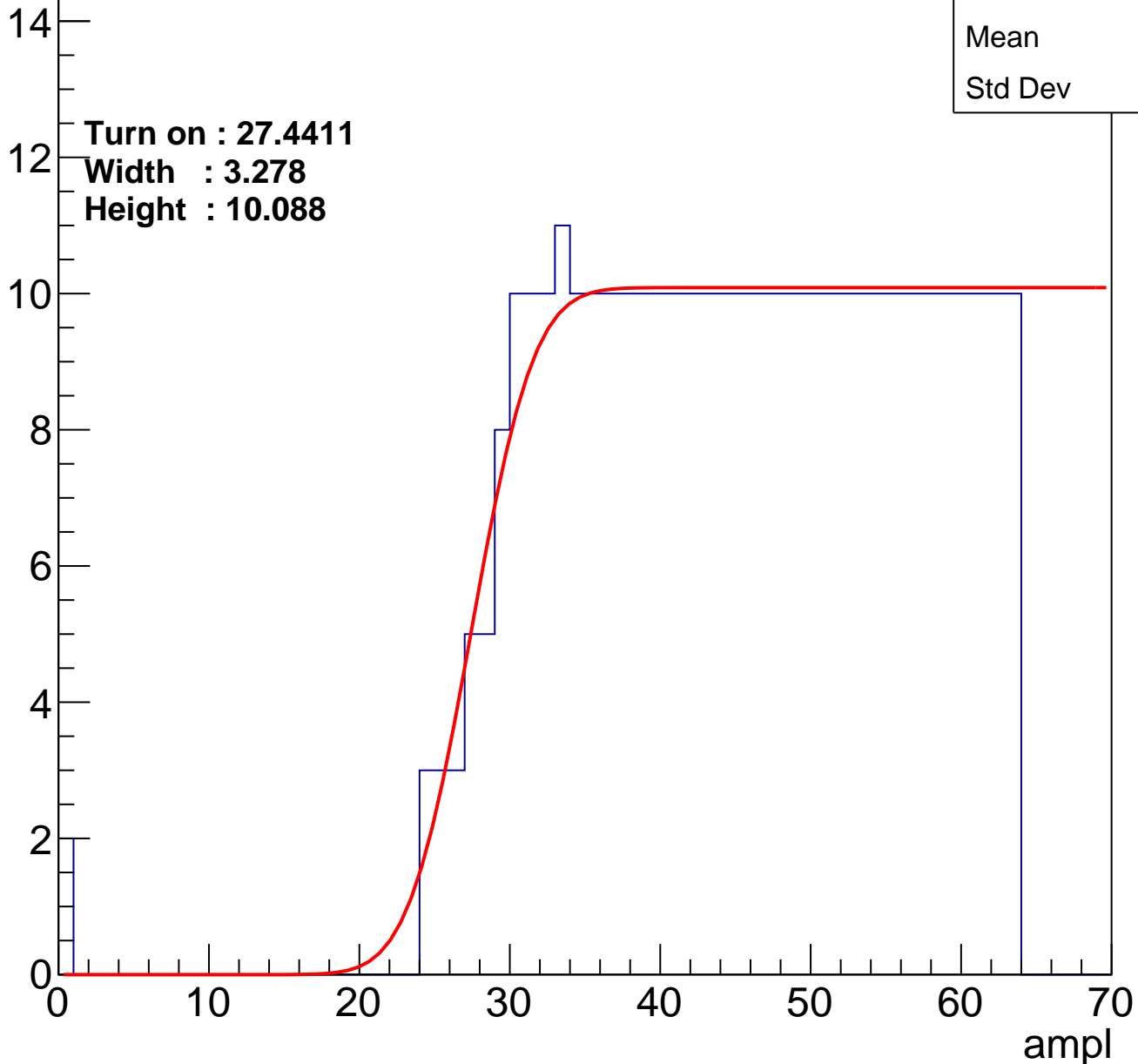
Entries	370
Mean	44.8
Std Dev	11.2

Turn on : 27.4411

Width : 3.278

Height : 10.088

Entry





# B0L002S, U23-ch47

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.78
Std Dev	11.37

Turn on : 27.7281

Width : 2.343

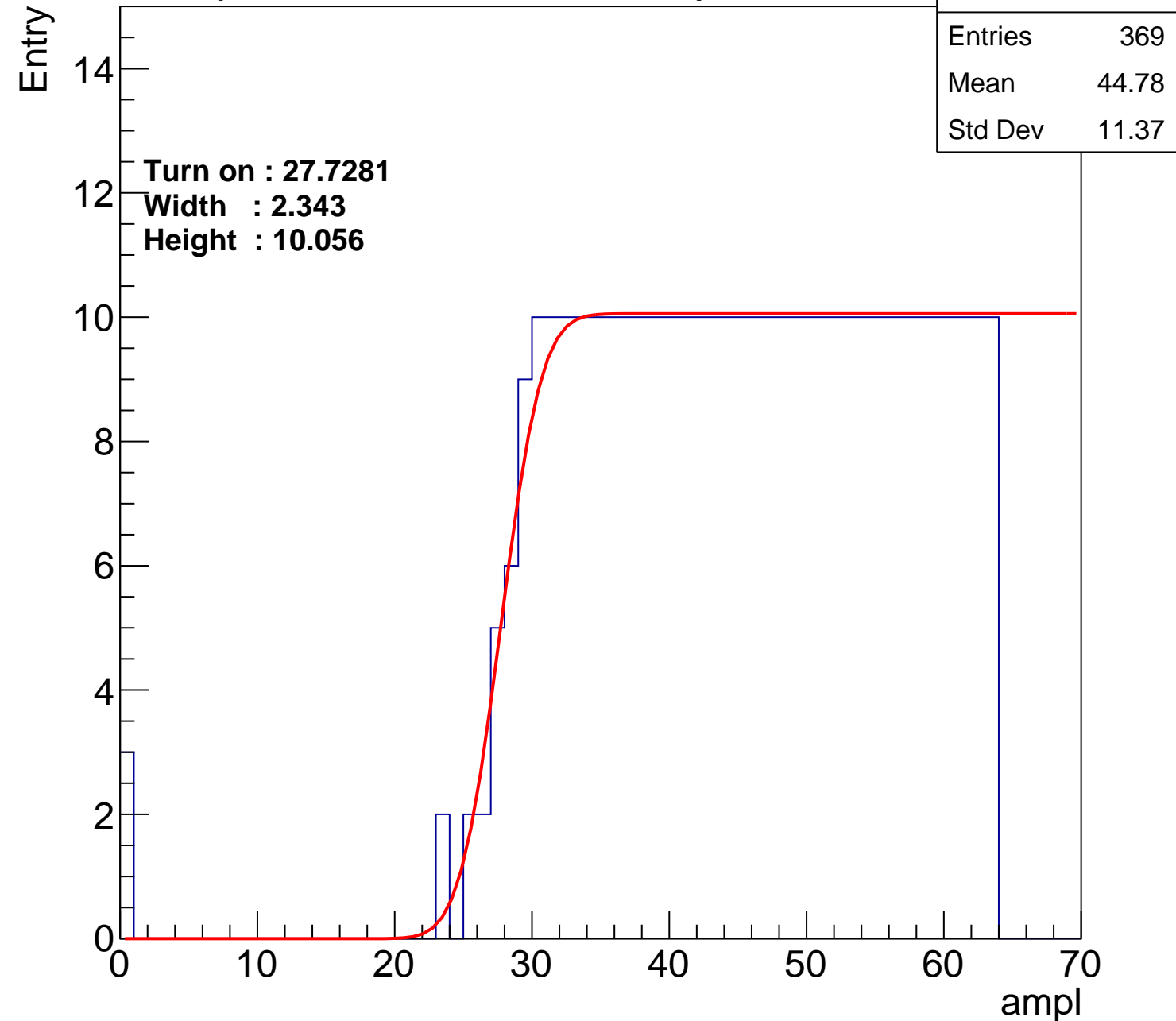
Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch48

calib\_packv5\_042523\_0143.root, FC#8, port C1

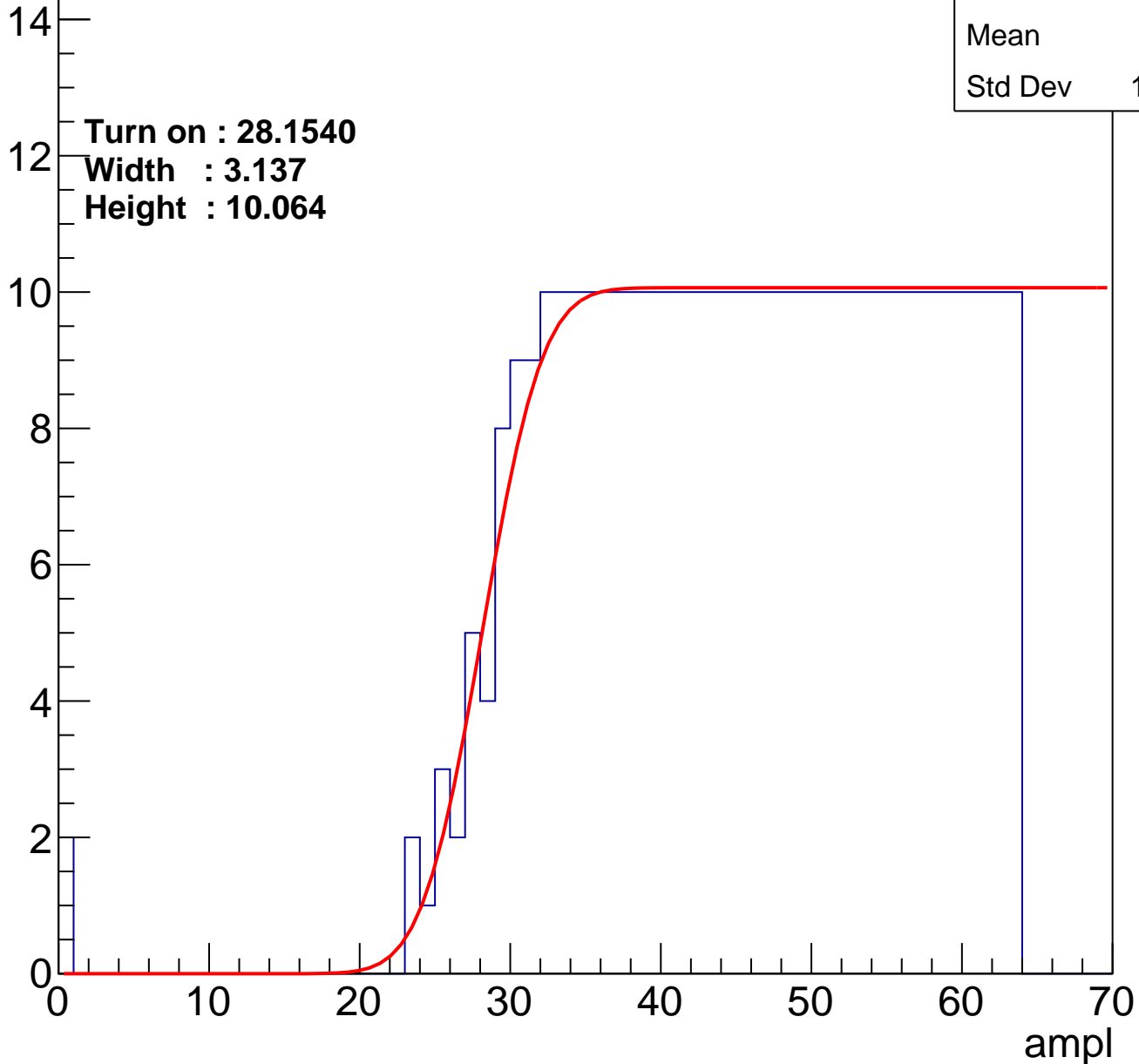
Entries	365
Mean	45
Std Dev	11.14

Turn on : 28.1540

Width : 3.137

Height : 10.064

Entry



# B0L002S, U23-ch49

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.44
Std Dev	10.91

**Turn on : 28.7336**

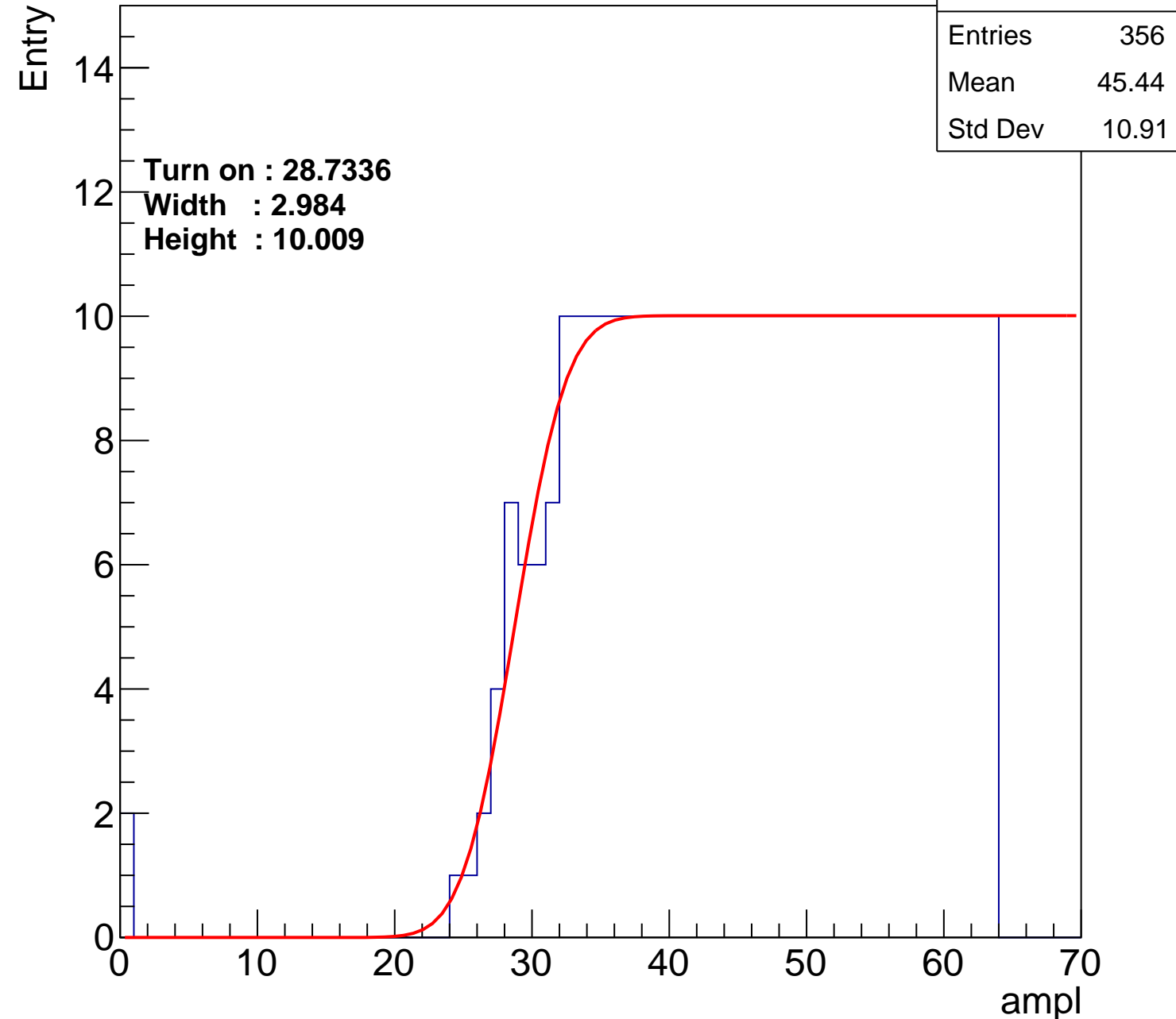
**Width : 2.984**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch50

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.36
Std Dev	11.14

**Turn on : 29.5998**

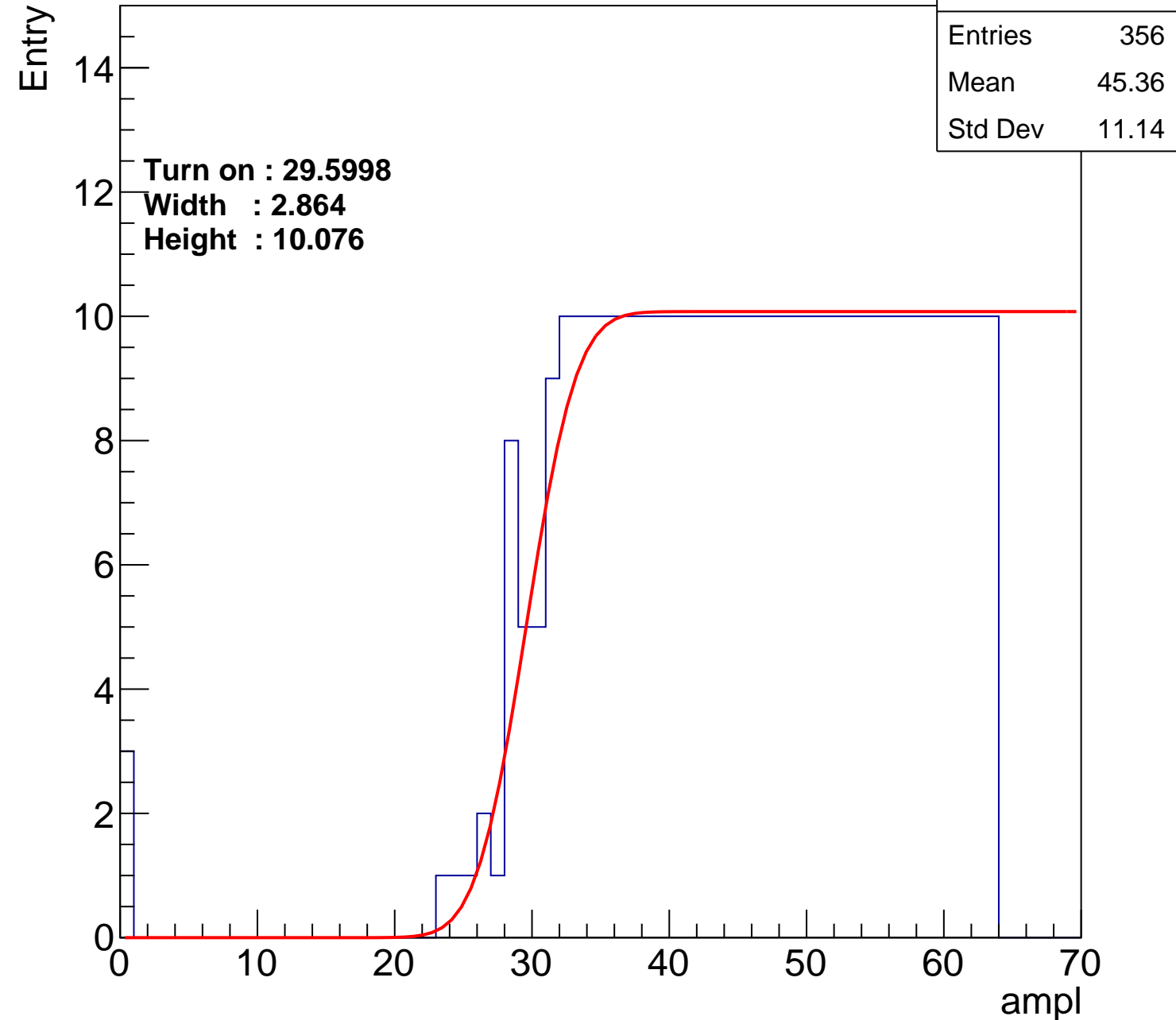
**Width : 2.864**

**Height : 10.076**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch51

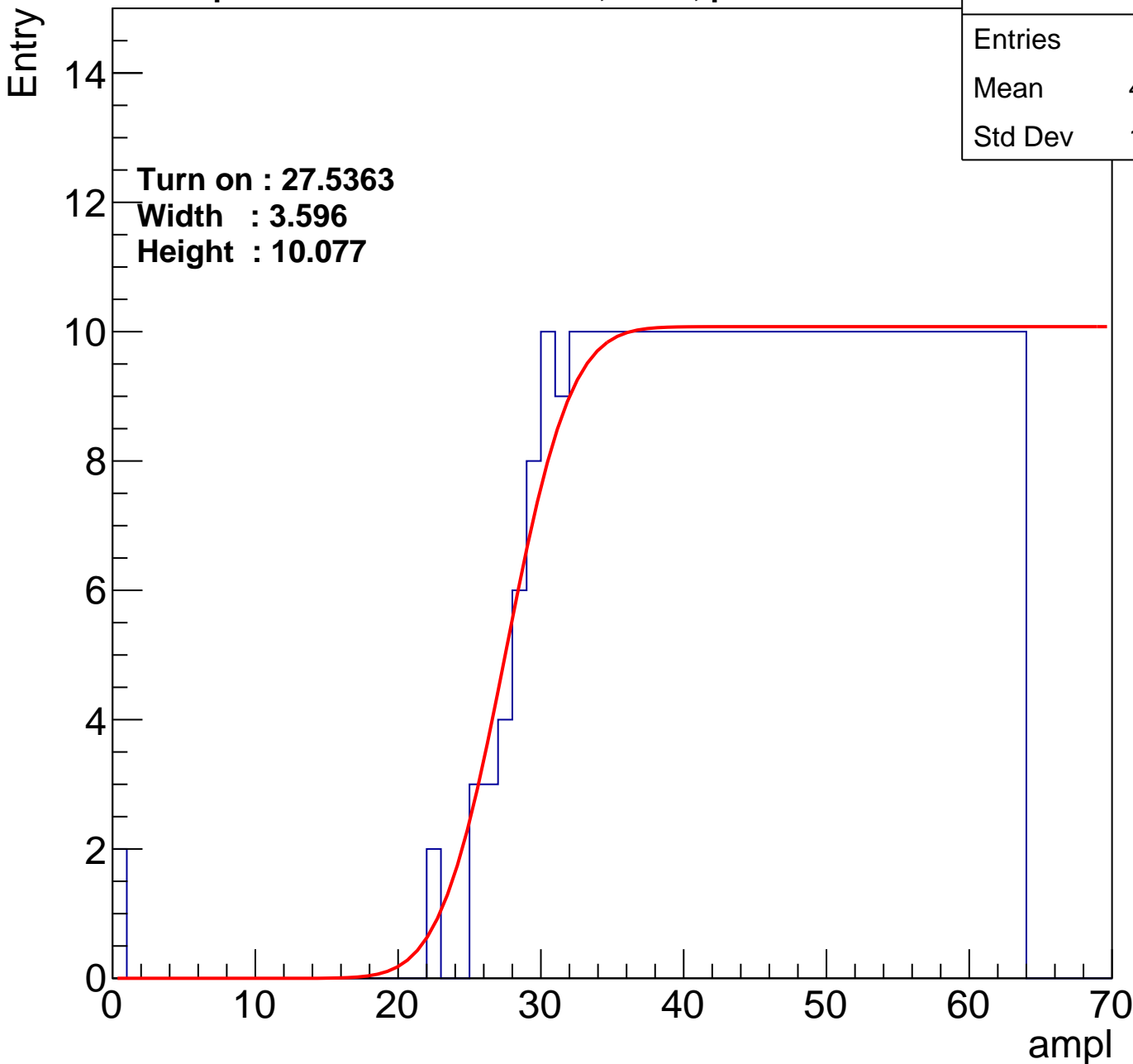
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	367
Mean	44.92
Std Dev	11.17

**Turn on : 27.5363**

**Width : 3.596**

**Height : 10.077**



# B0L002S, U23-ch52

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.79
Std Dev	11.2

Turn on : 27.4071

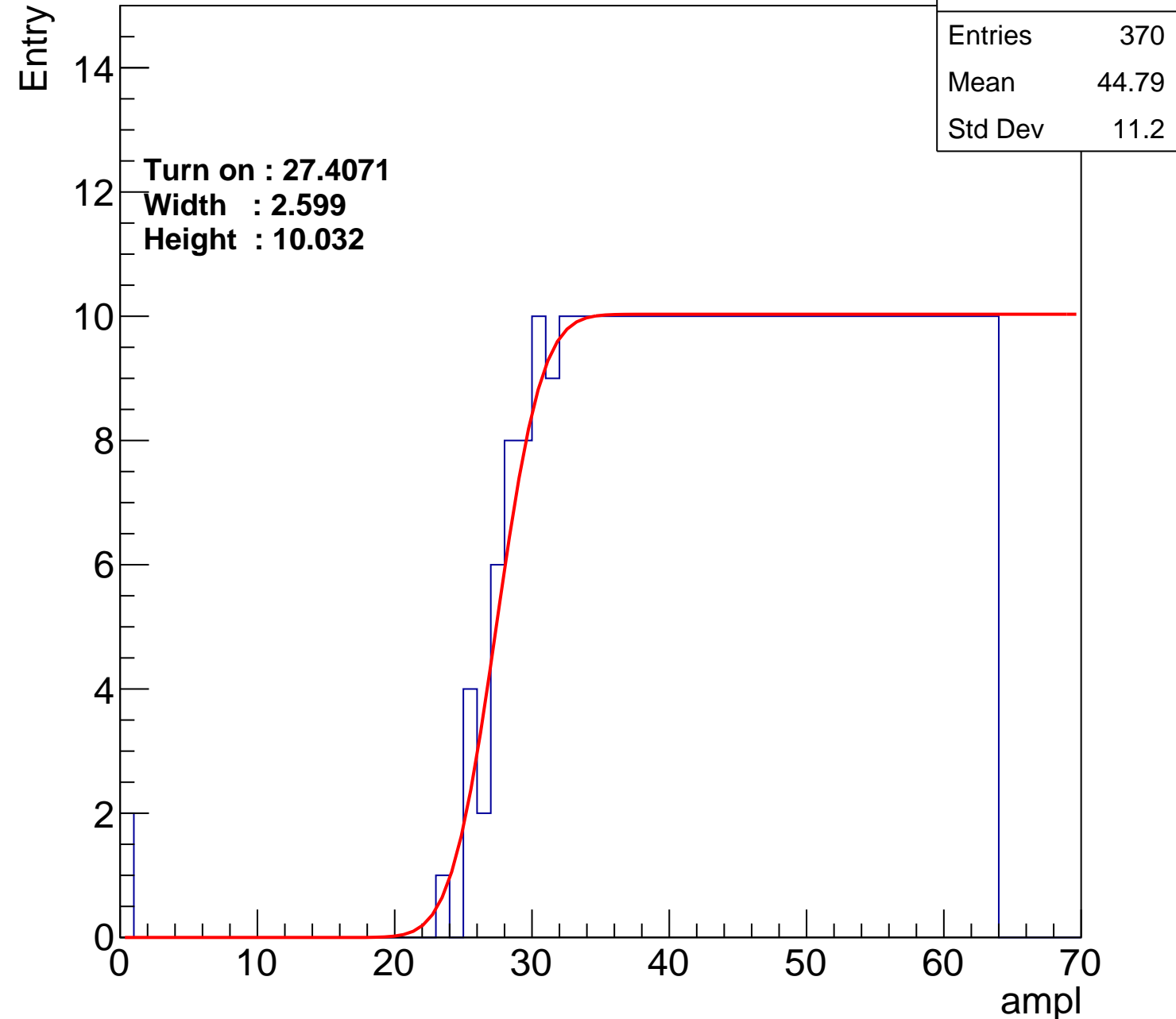
Width : 2.599

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch53

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.42
Std Dev	10.94

Turn on : 28.9038

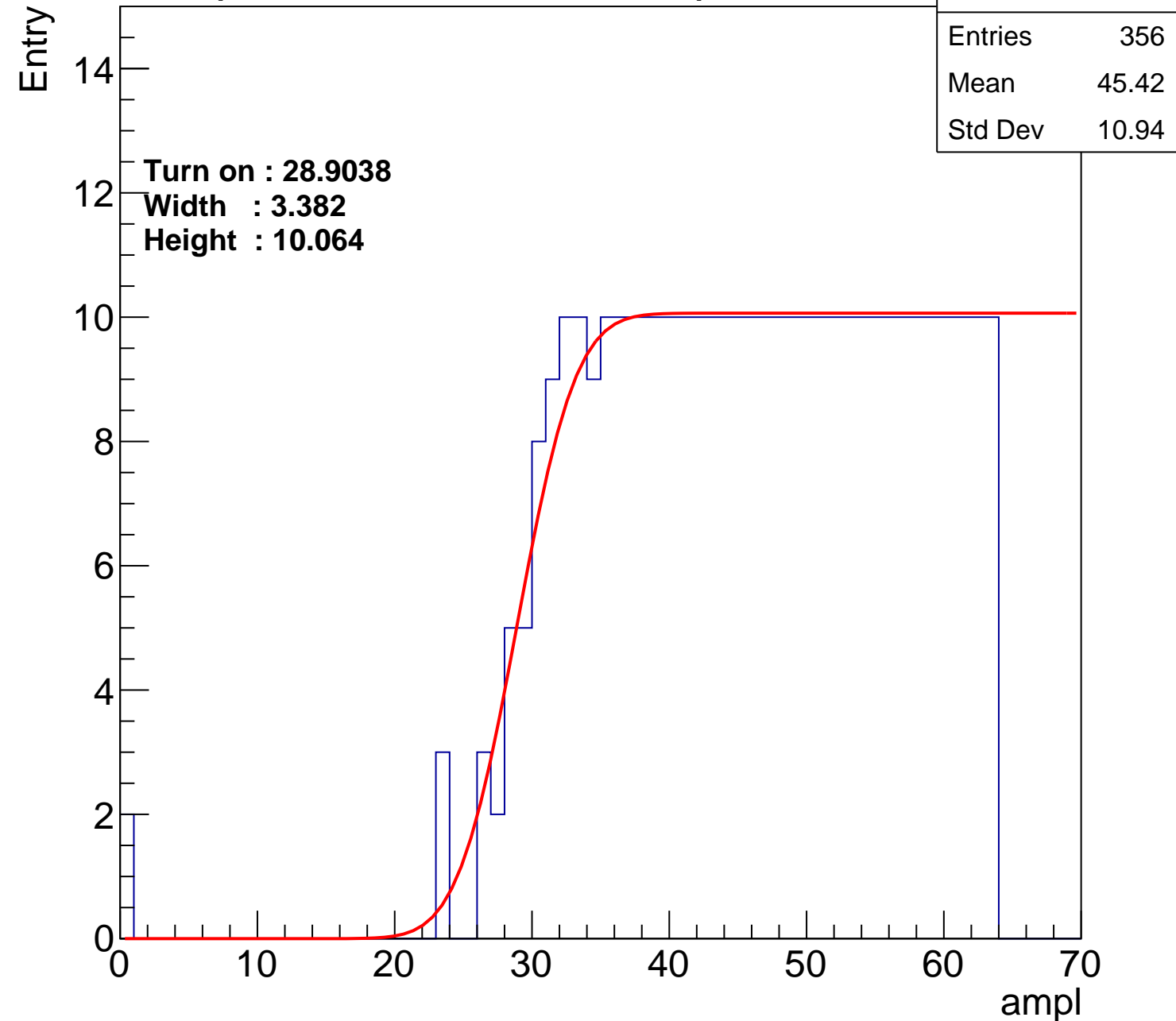
Width : 3.382

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch54

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.7134

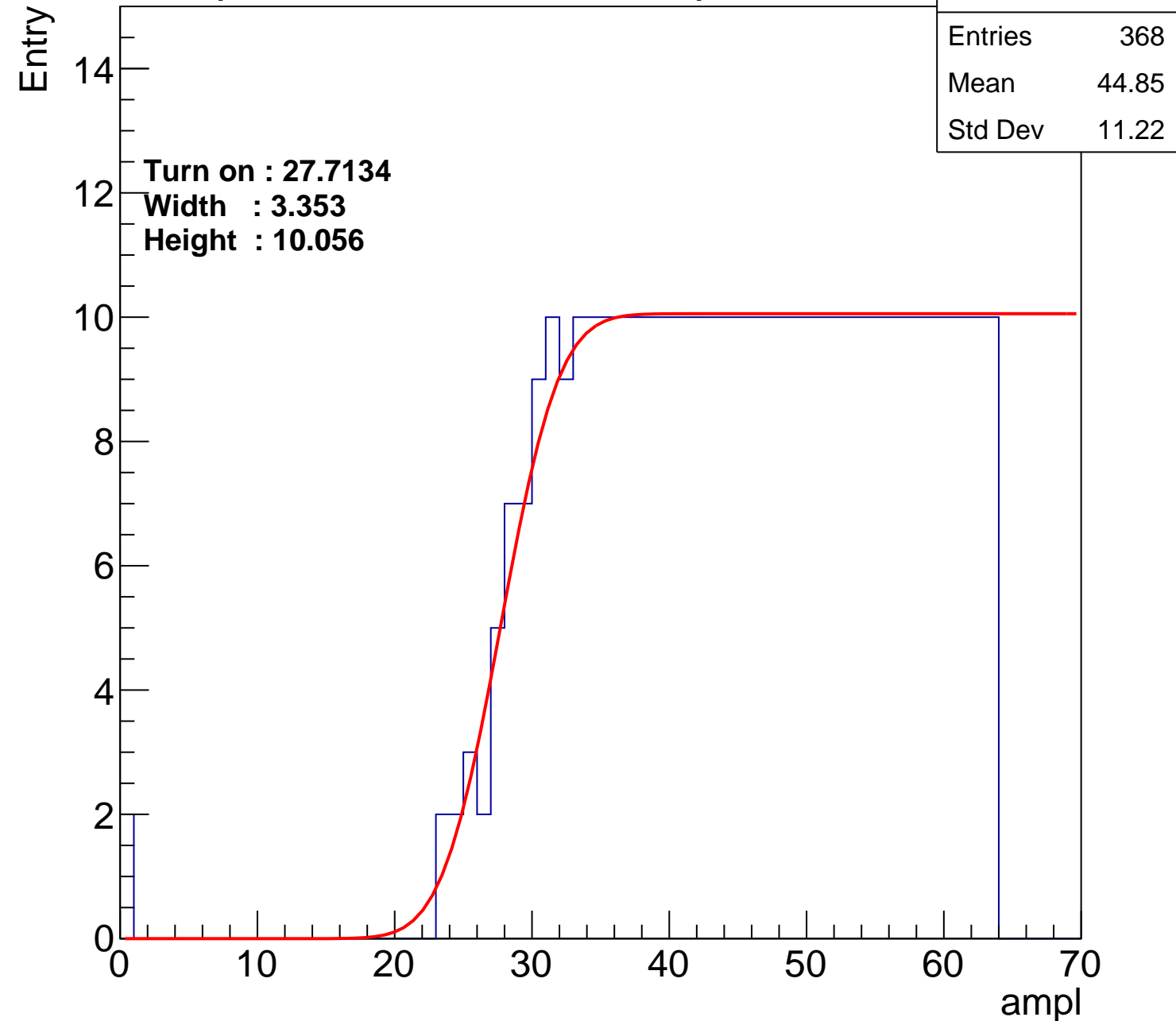
Width : 3.353

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch55

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.17
Std Dev	11.08

Turn on : 28.5952

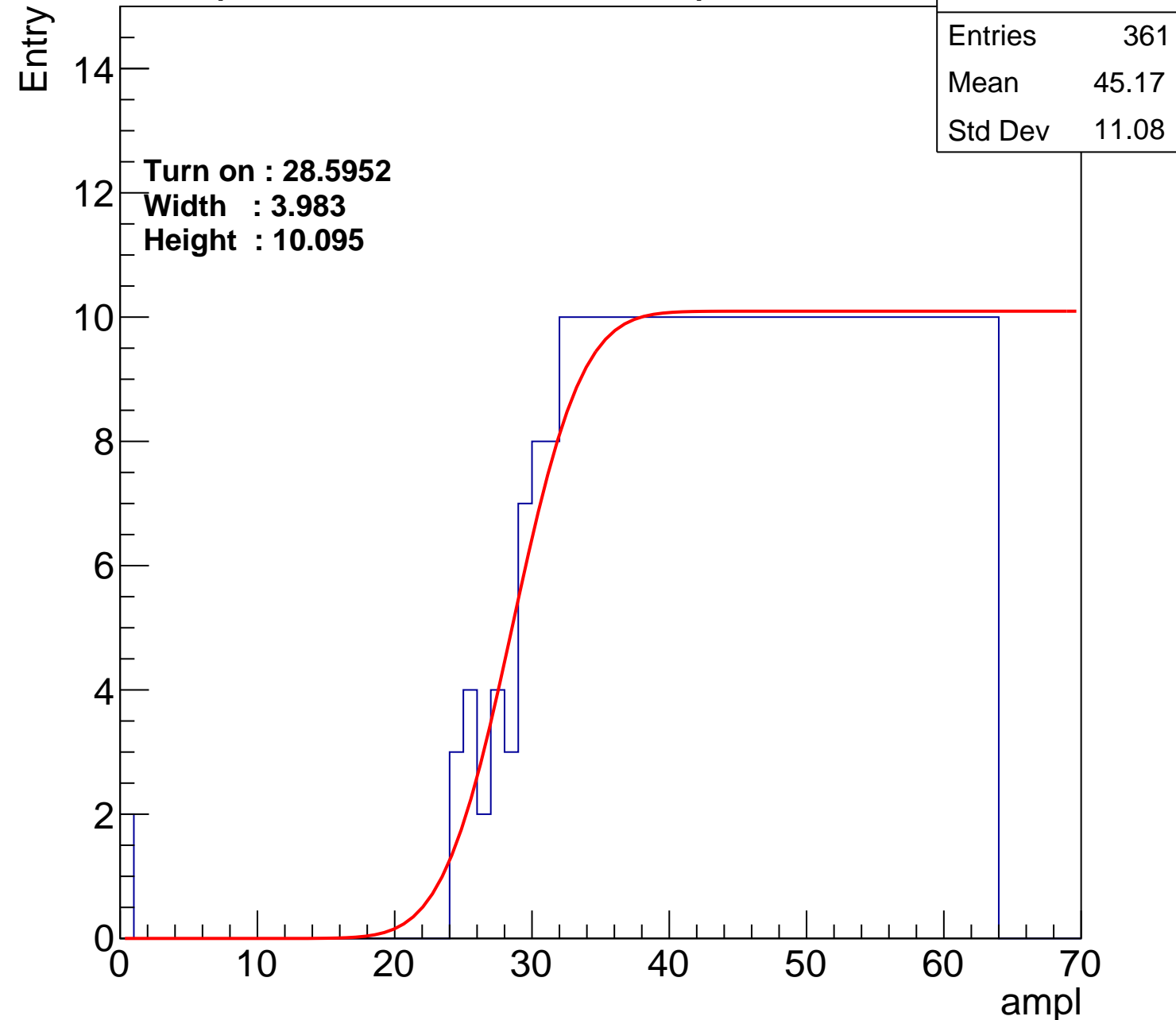
Width : 3.983

Height : 10.095

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch56

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.66
Std Dev	10.62

**Turn on : 29.2364**

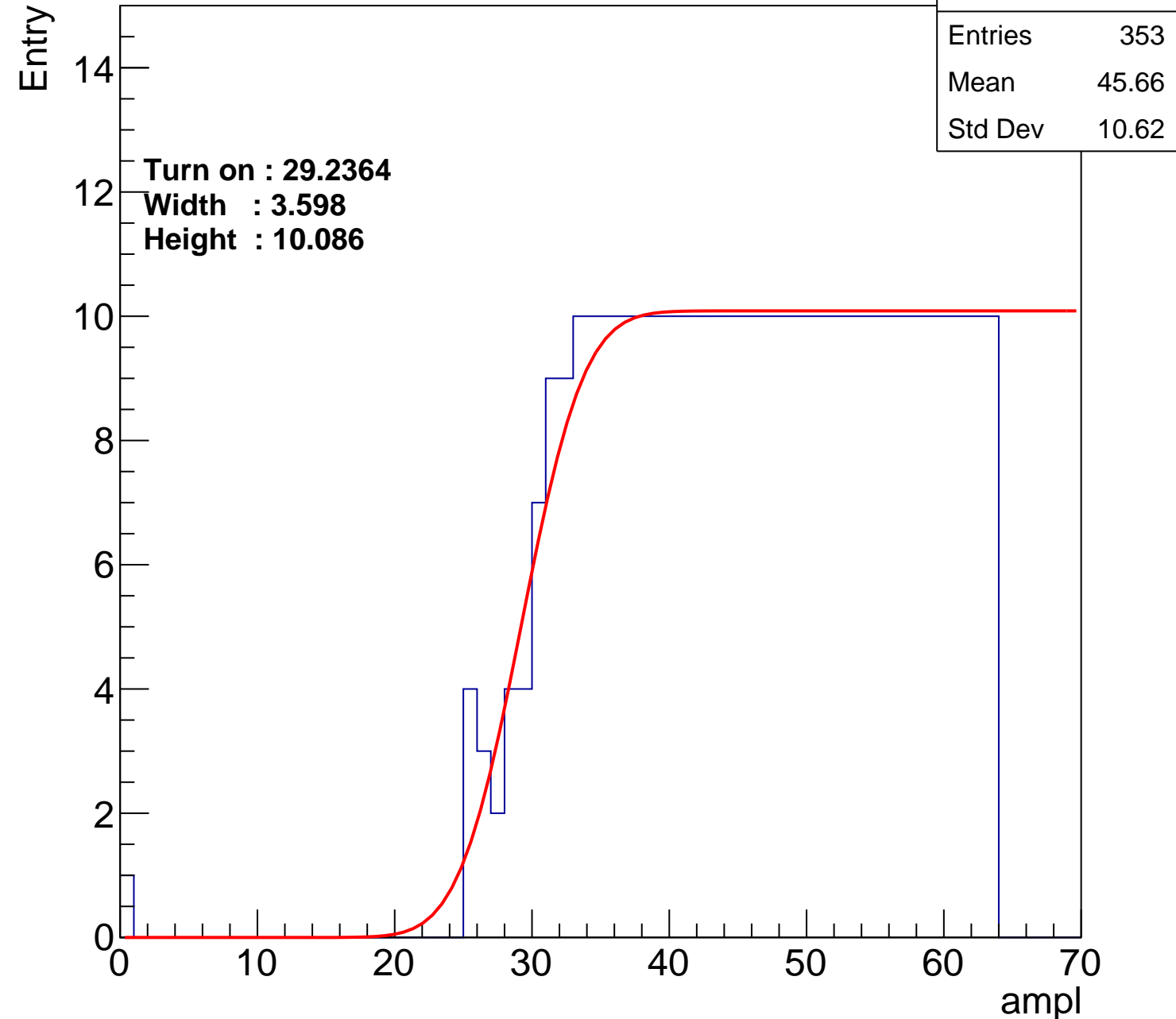
**Width : 3.598**

**Height : 10.086**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch57

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.63
Std Dev	11.49

**Turn on : 27.6083**

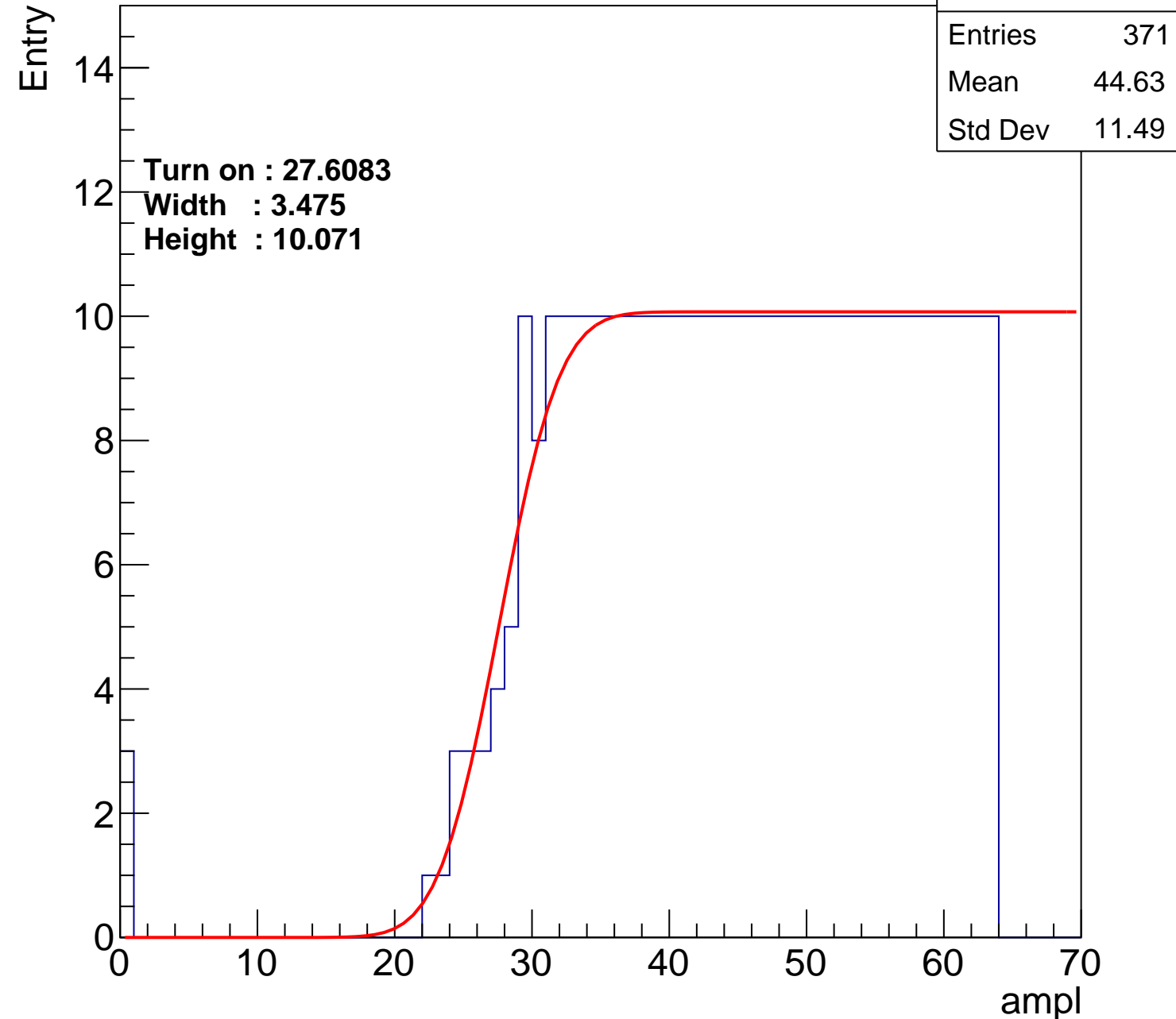
**Width : 3.475**

**Height : 10.071**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch58

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.95
Std Dev	11.3

**Turn on : 27.5690**

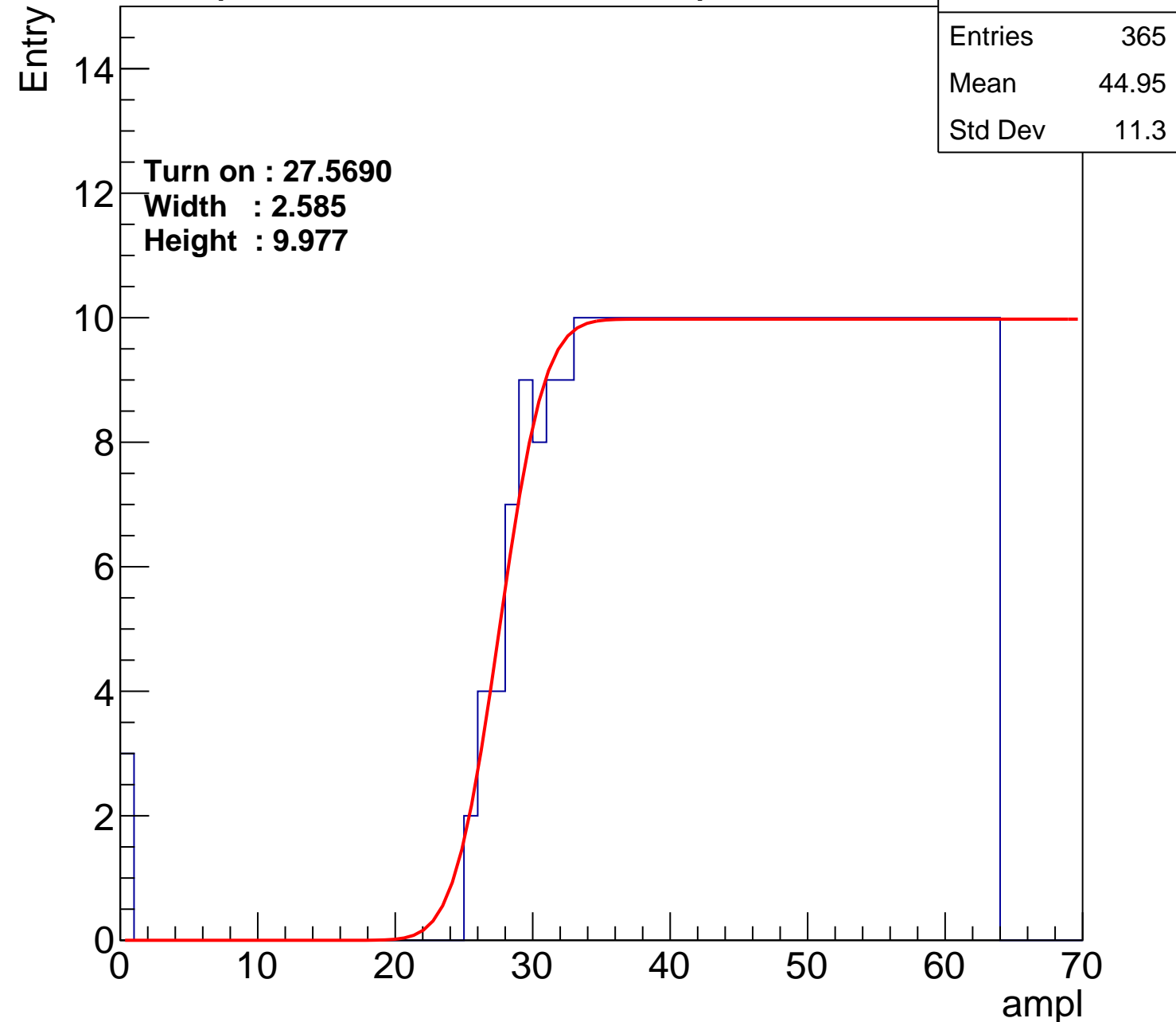
**Width : 2.585**

**Height : 9.977**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch59

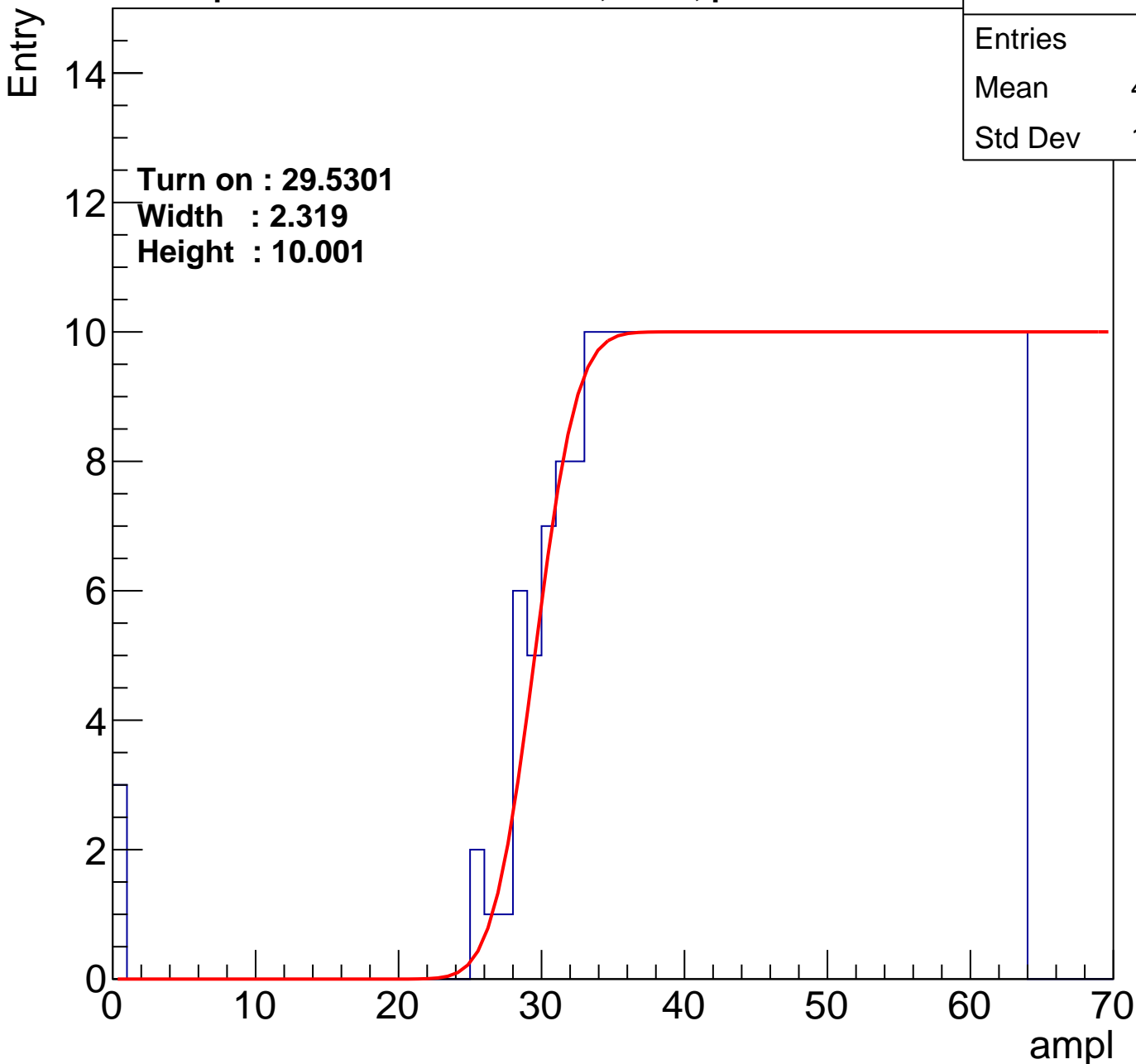
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	351
Mean	45.61
Std Dev	11.01

**Turn on : 29.5301**

**Width : 2.319**

**Height : 10.001**



# B0L002S, U23-ch60

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	350
Mean	45.76
Std Dev	10.72

**Turn on : 29.2767**

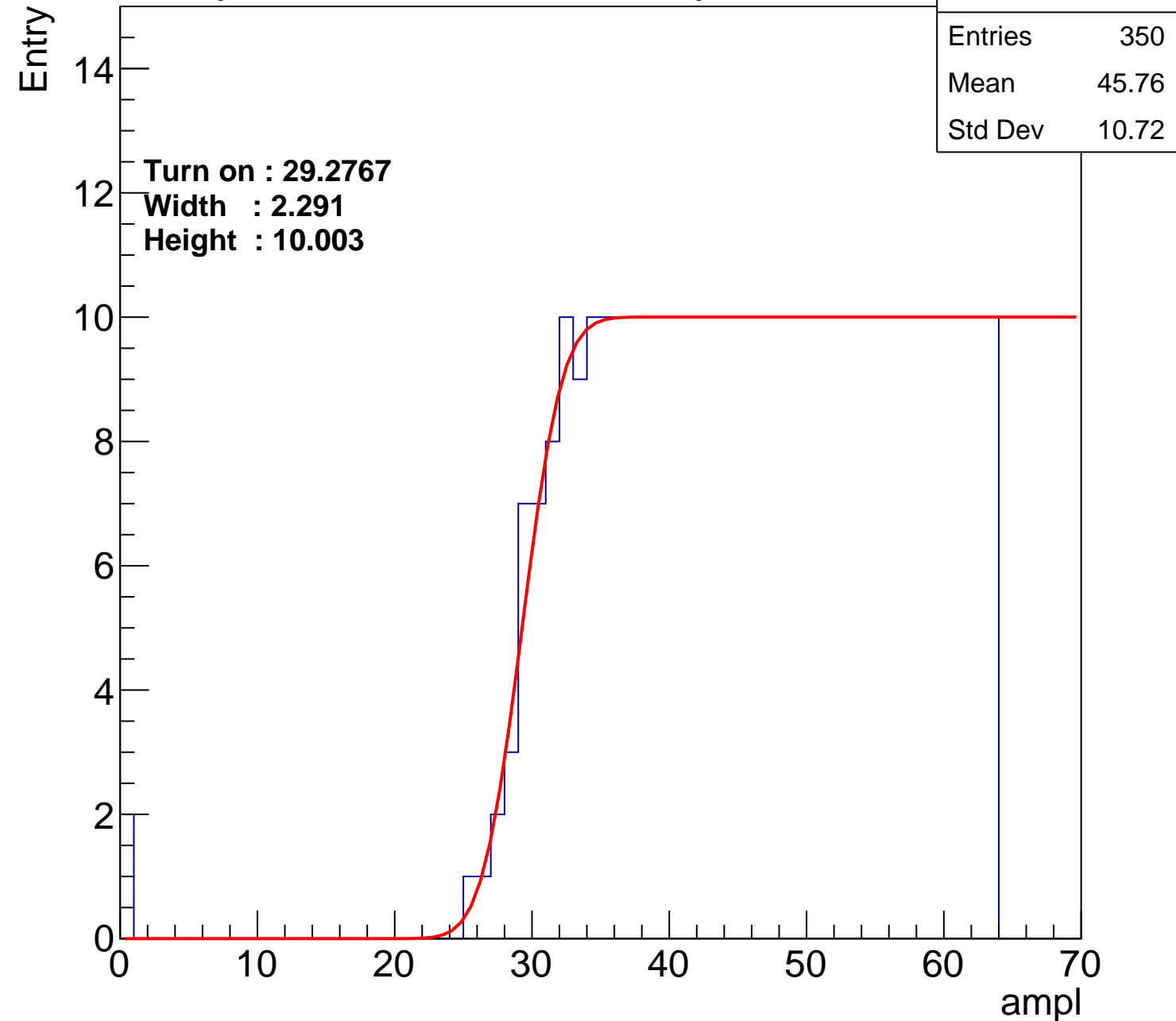
**Width : 2.291**

**Height : 10.003**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch61

calib\_packv5\_042523\_0143.root, FC#8, port C1

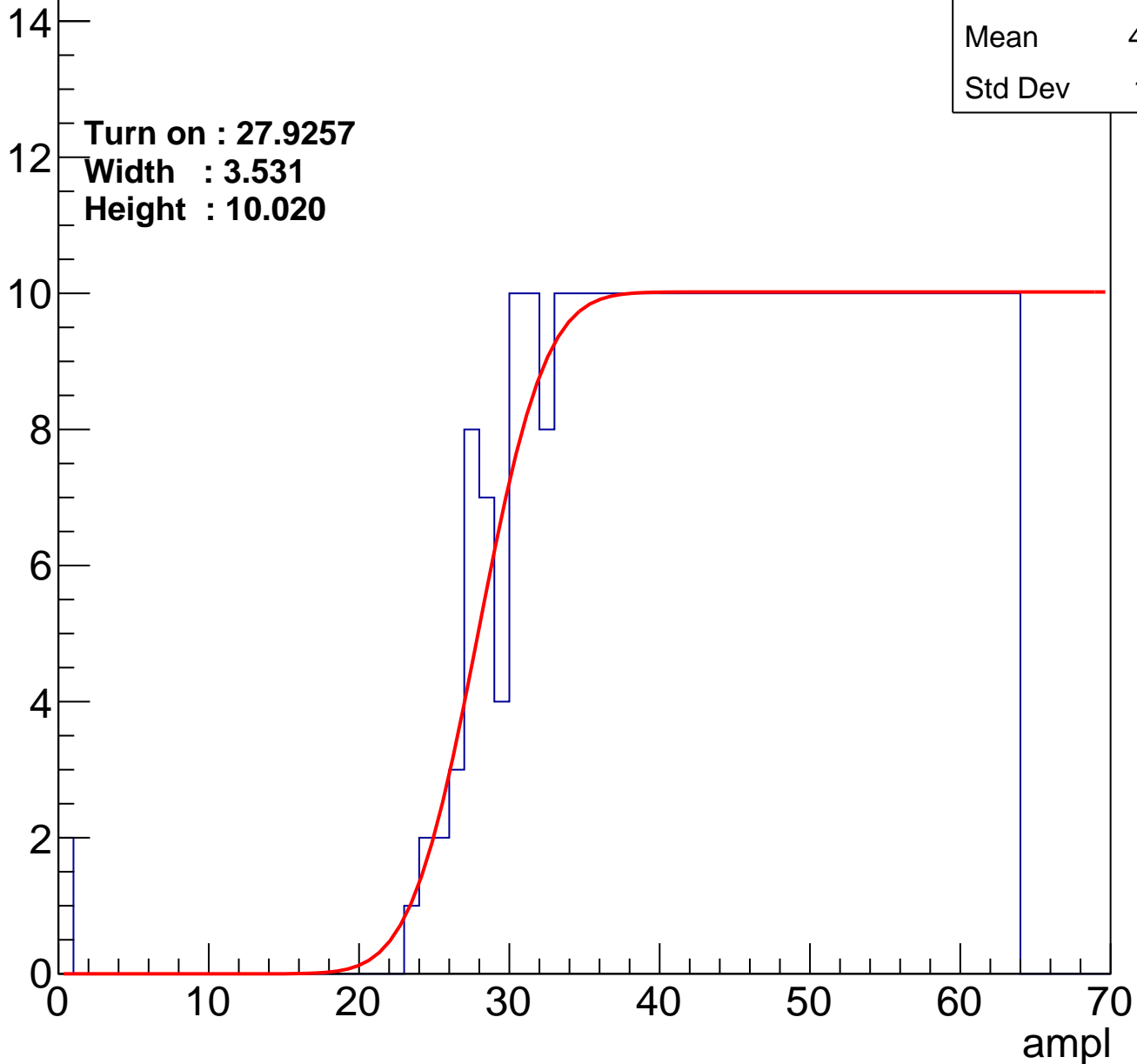
Entry

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.9257

Width : 3.531

Height : 10.020



# B0L002S, U23-ch62

calib\_packv5\_042523\_0143.root, FC#8, port C1

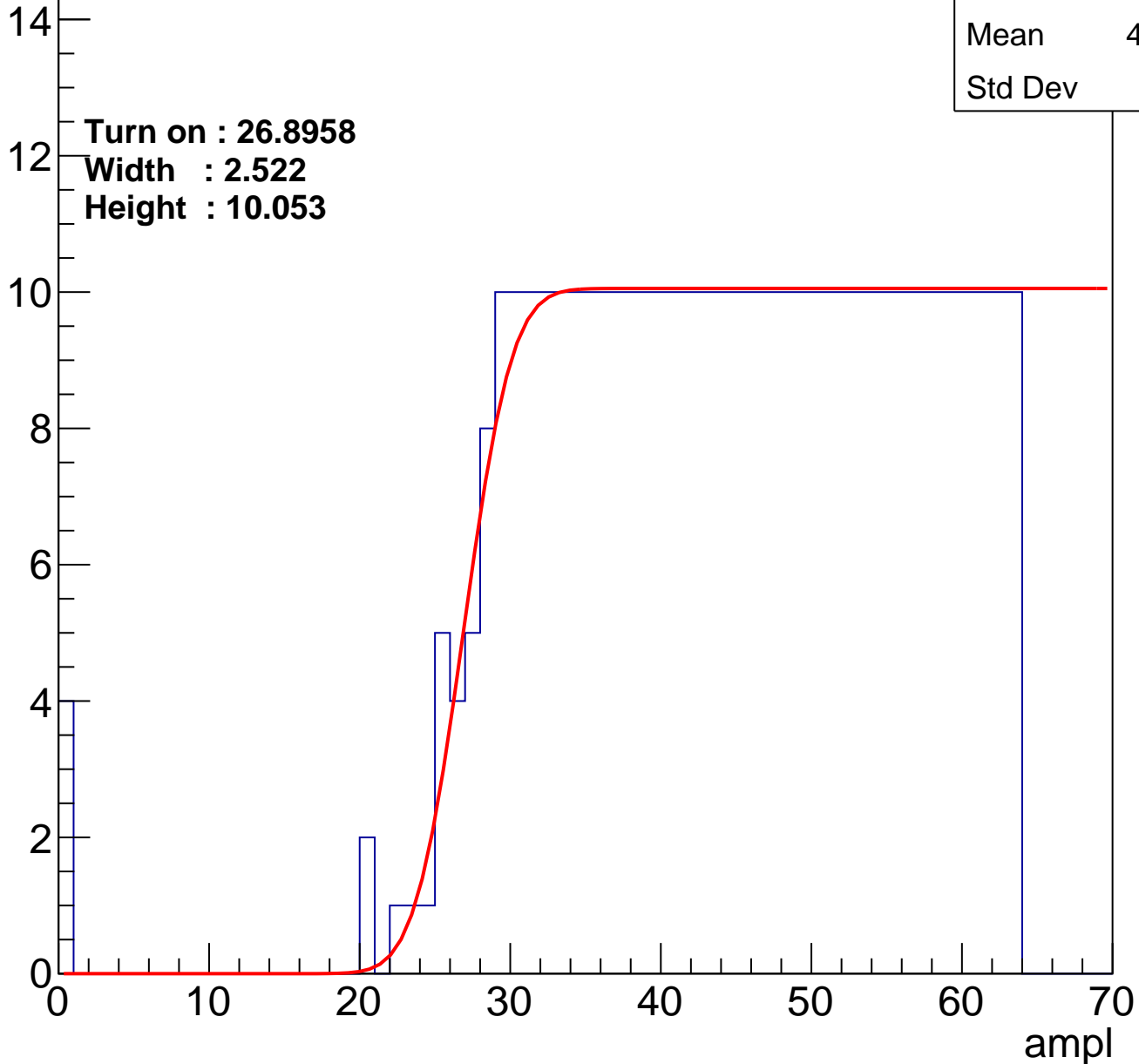
Entries	381
Mean	44.09
Std Dev	11.9

**Turn on : 26.8958**

**Width : 2.522**

**Height : 10.053**

Entry





# B0L002S, U23-ch63

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.84
Std Dev	11.57

Turn on : 28.3349

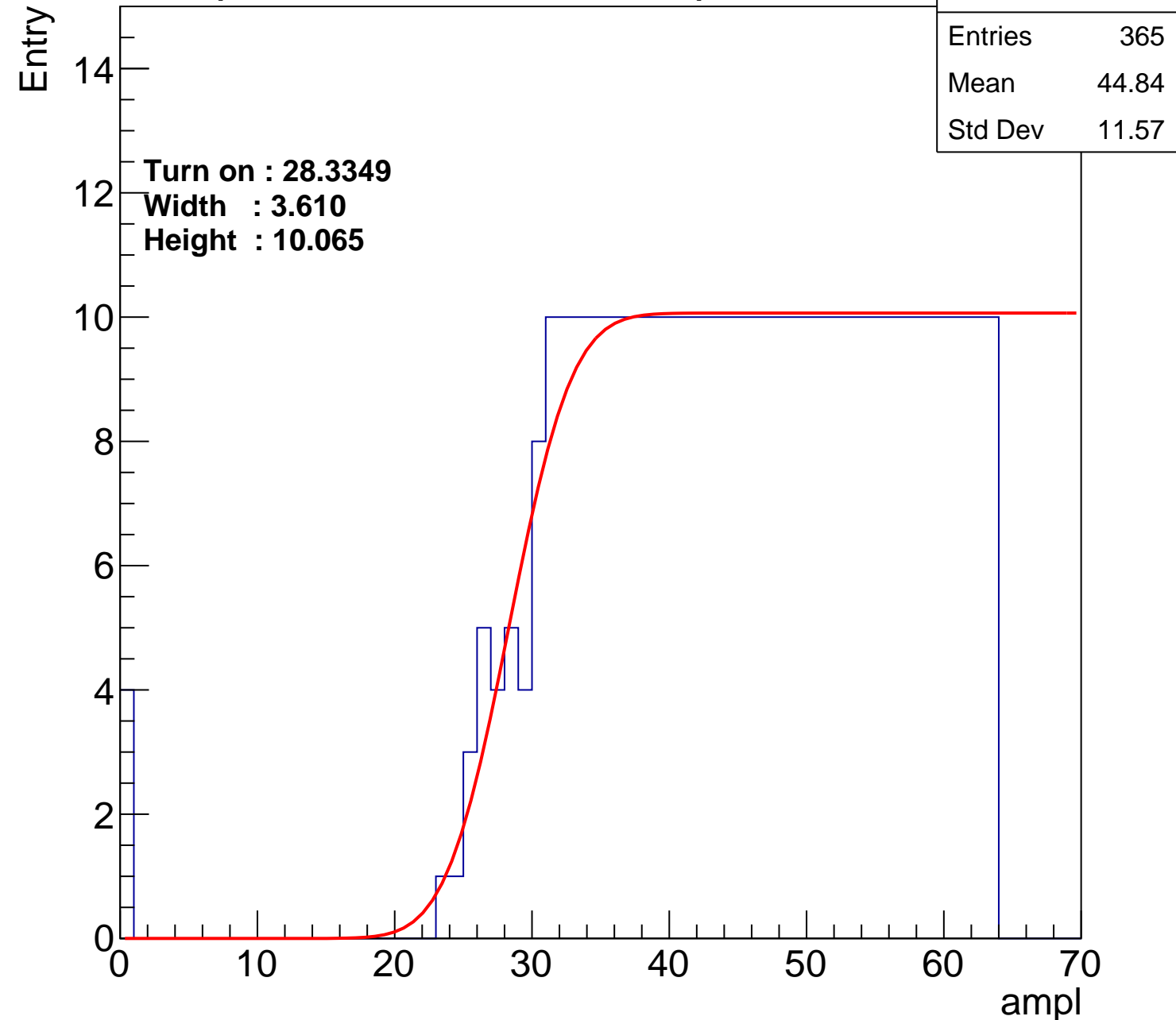
Width : 3.610

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch64

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.52
Std Dev	11.43

Turn on : 26.8551

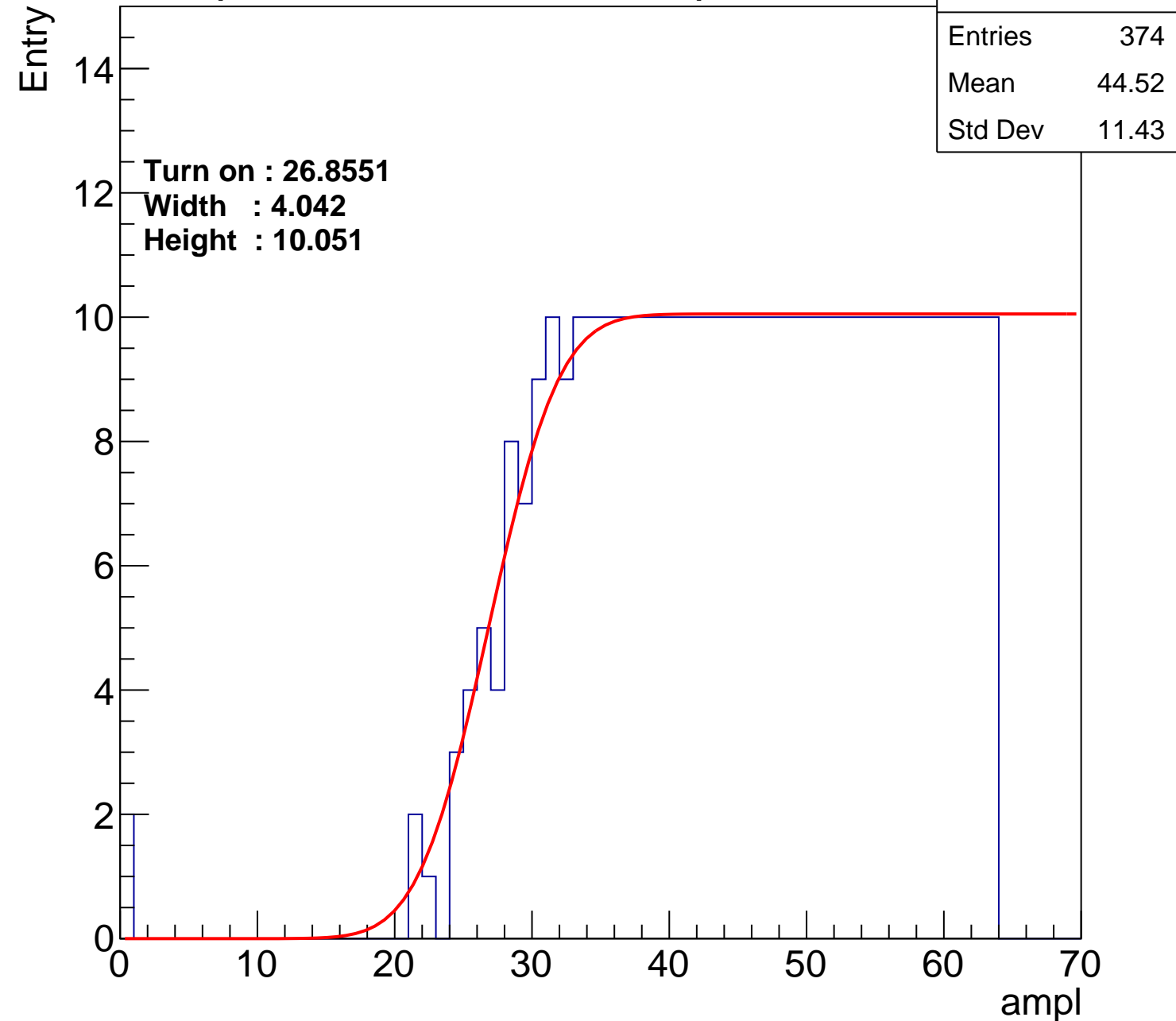
Width : 4.042

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch65

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	399
Mean	43.34
Std Dev	11.99

Turn on : 24.0344

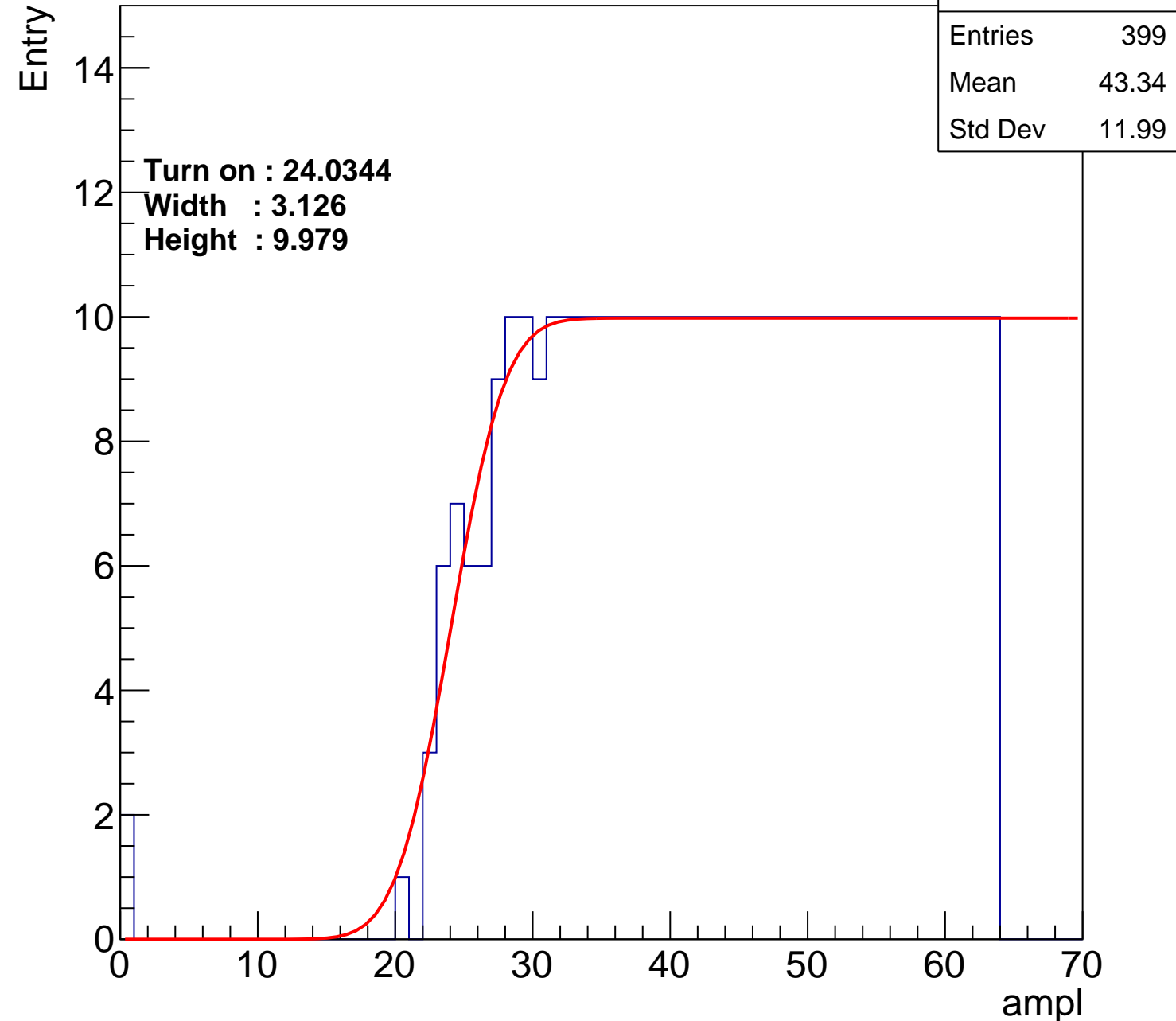
Width : 3.126

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch66

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	45.01
Std Dev	11.1

Turn on : 28.0829

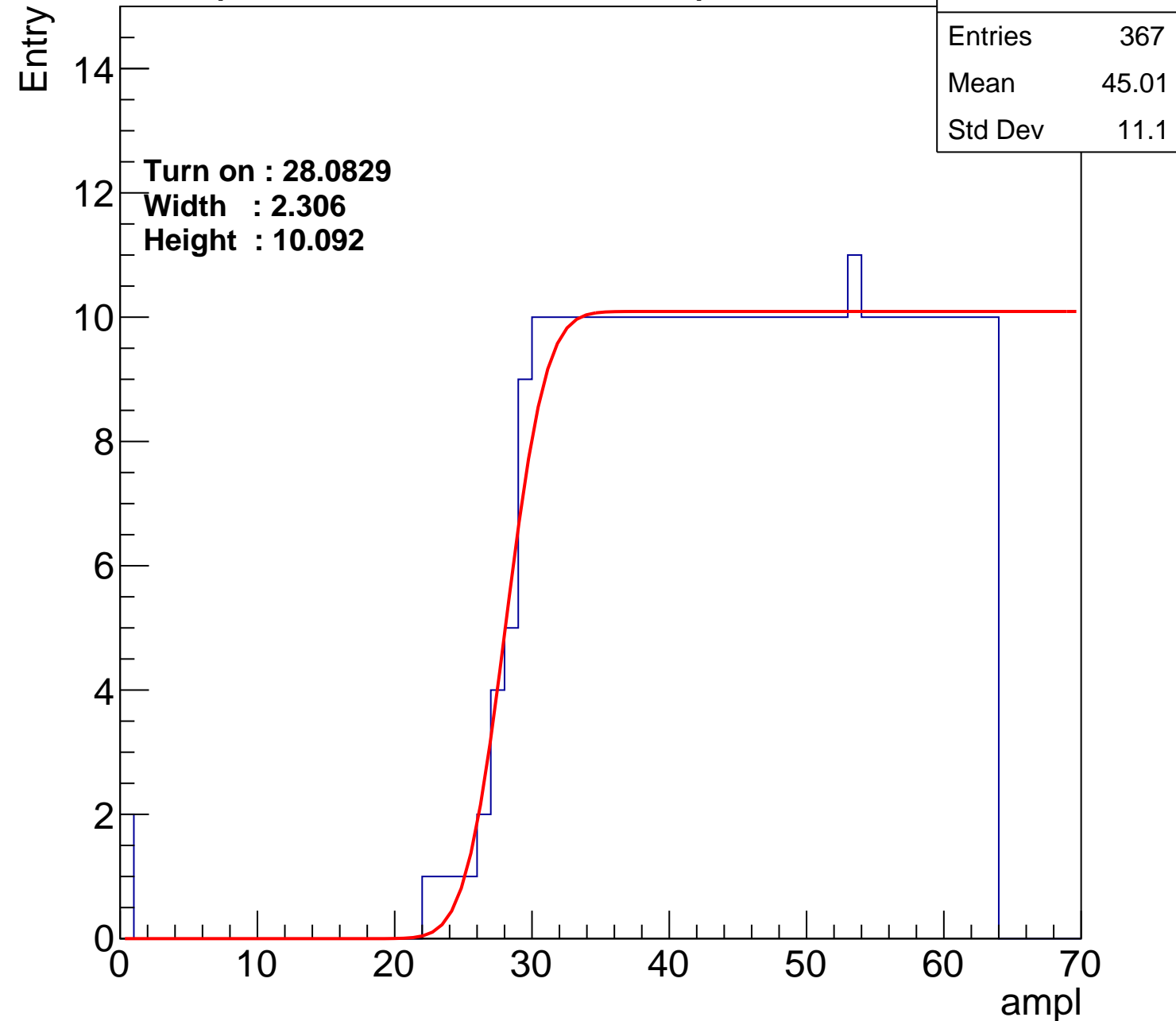
Width : 2.306

Height : 10.092

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch67

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.75
Std Dev	11.27

Turn on : 27.3815

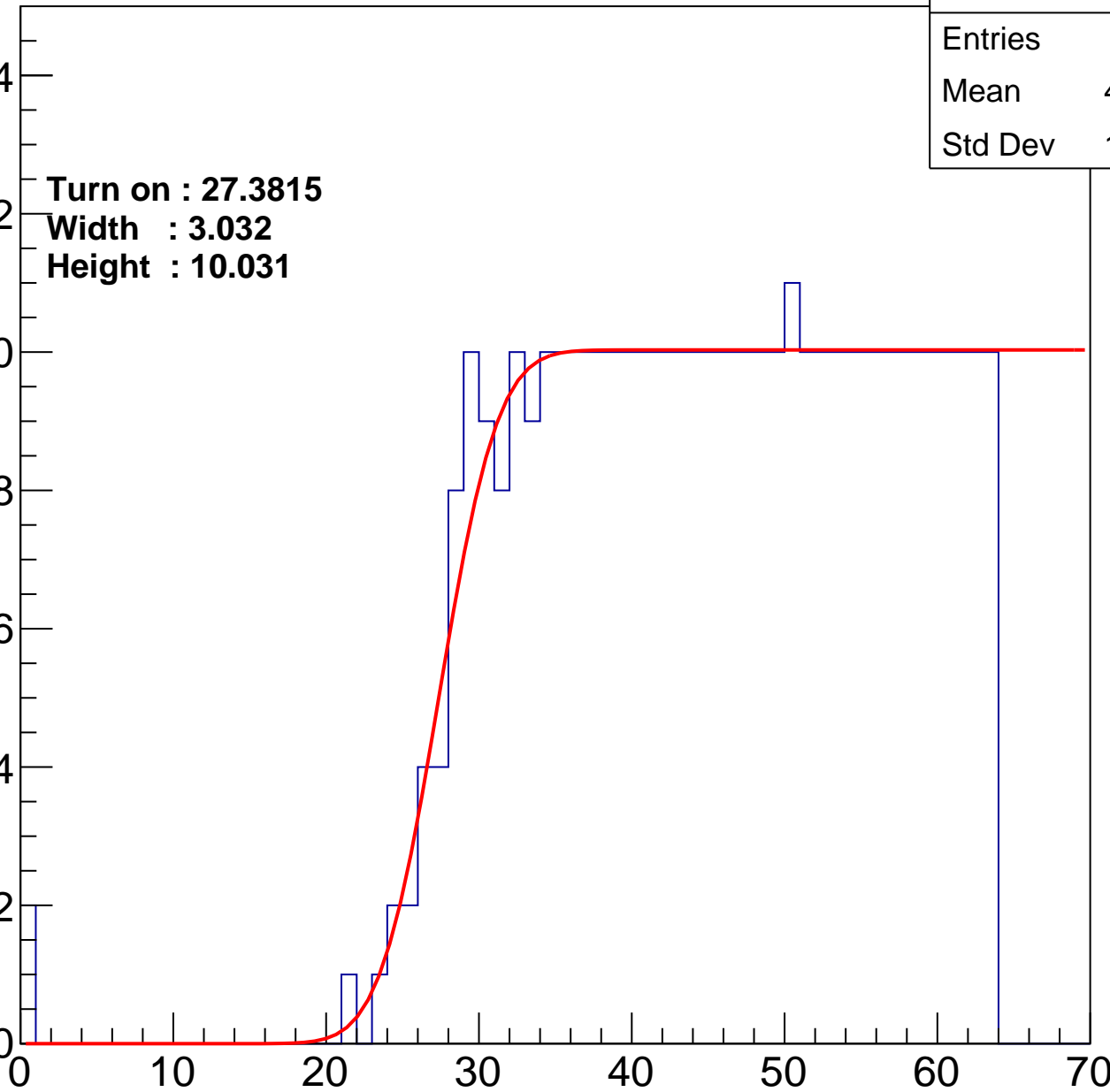
Width : 3.032

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch68

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.5
Std Dev	10.67

**Turn on : 28.9778**

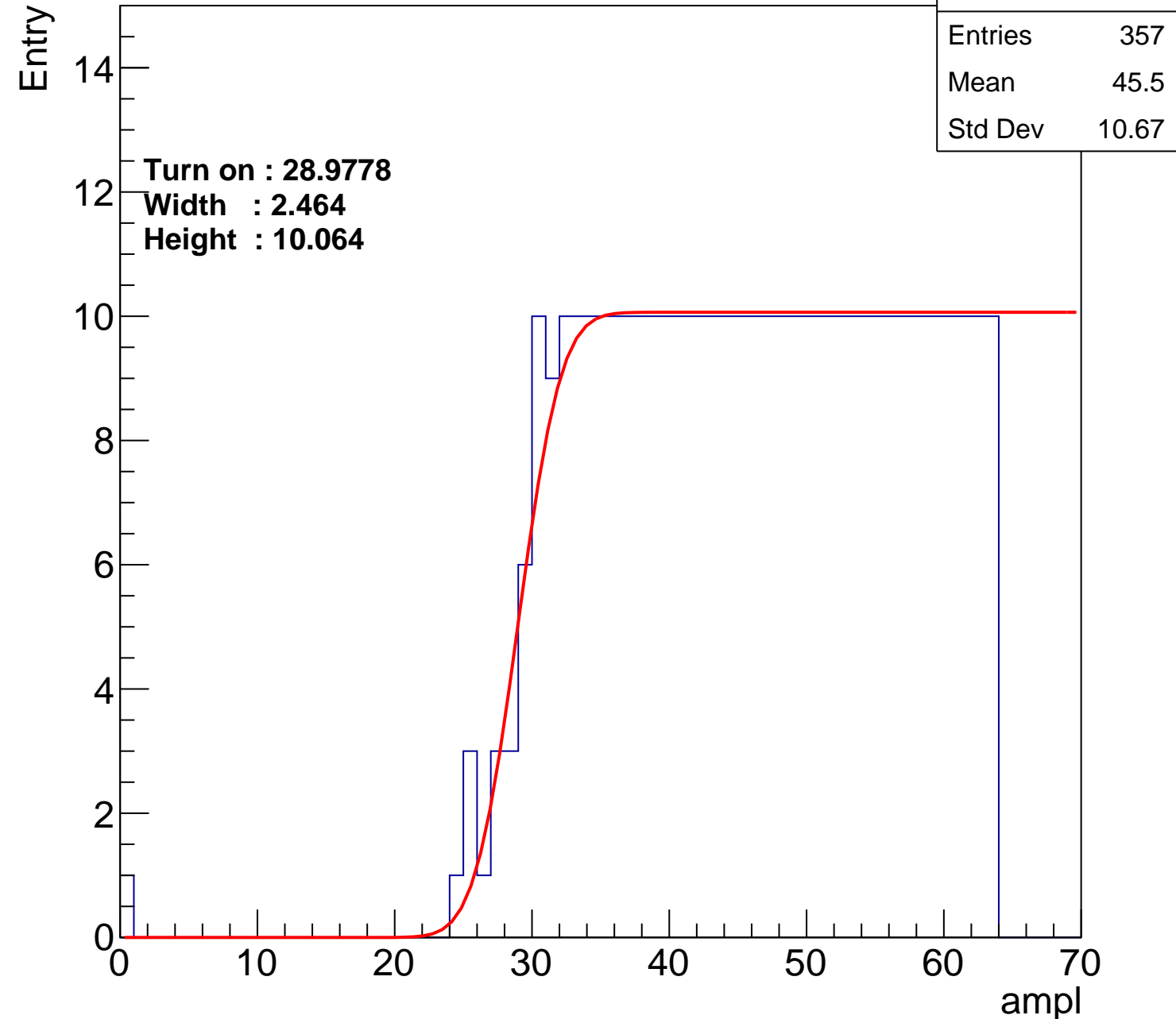
**Width : 2.464**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch69

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.5519

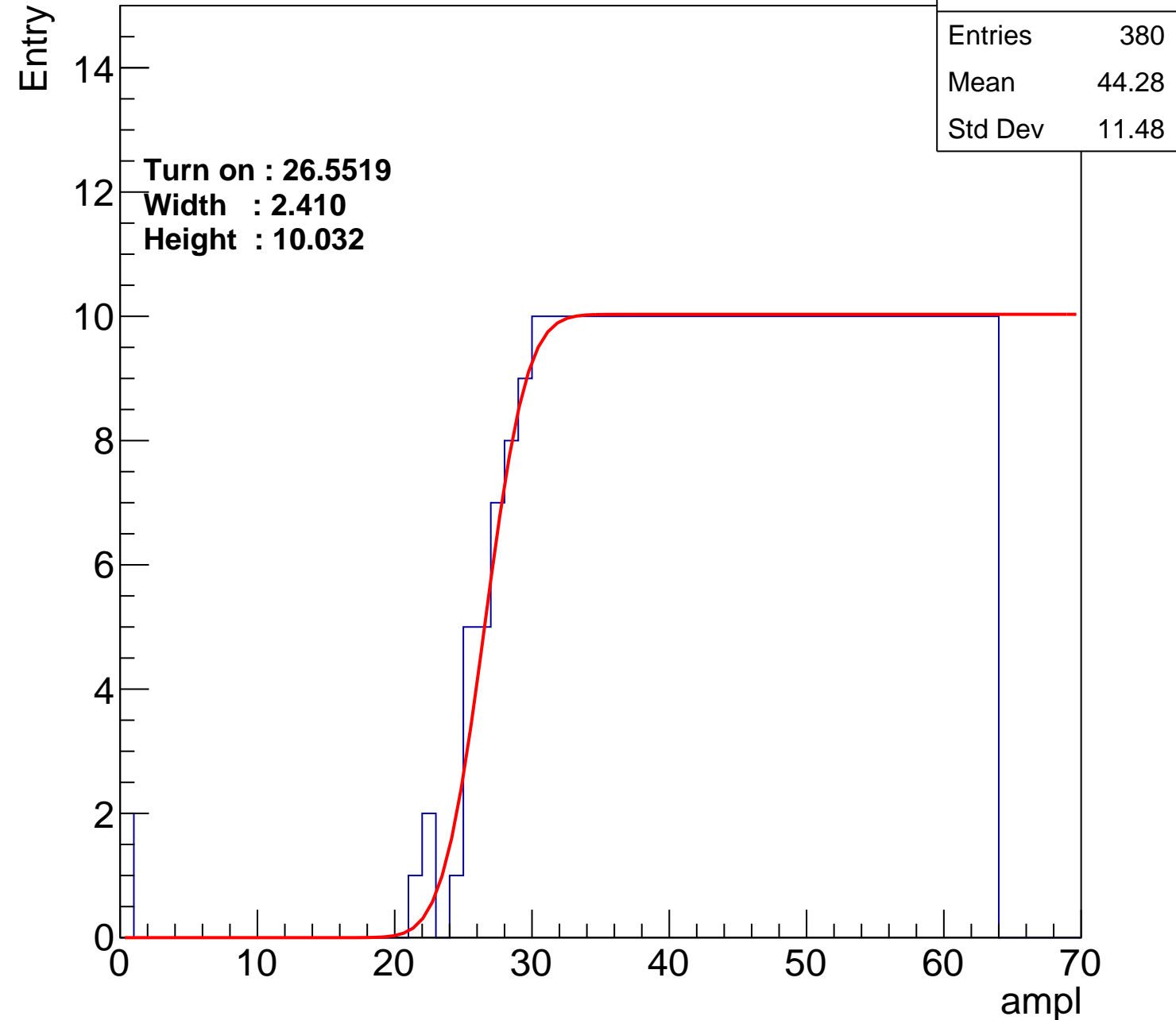
Width : 2.410

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch70

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.71
Std Dev	11.42

Turn on : 27.6797

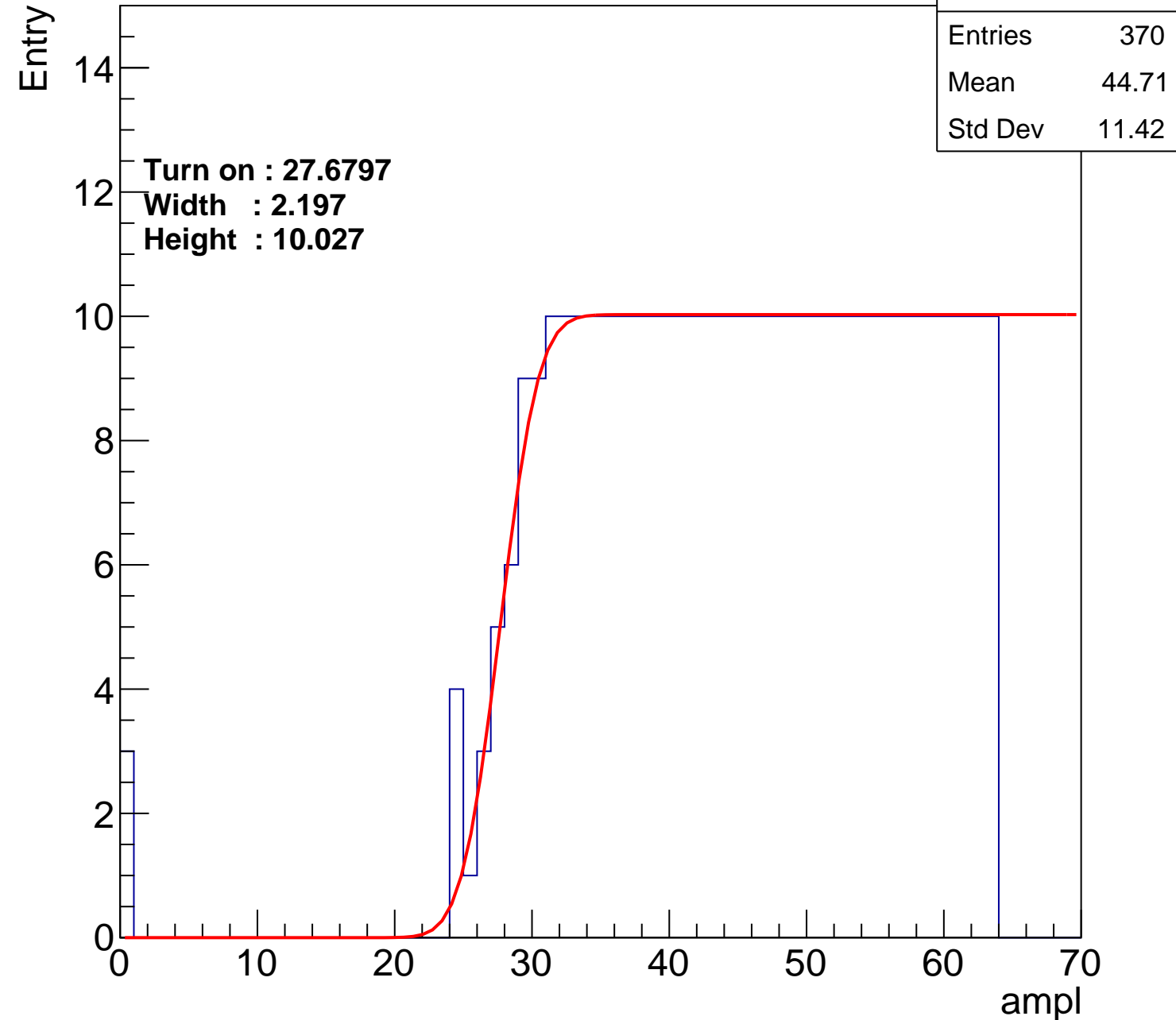
Width : 2.197

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch71

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.43
Std Dev	10.77

Turn on : 29.0947

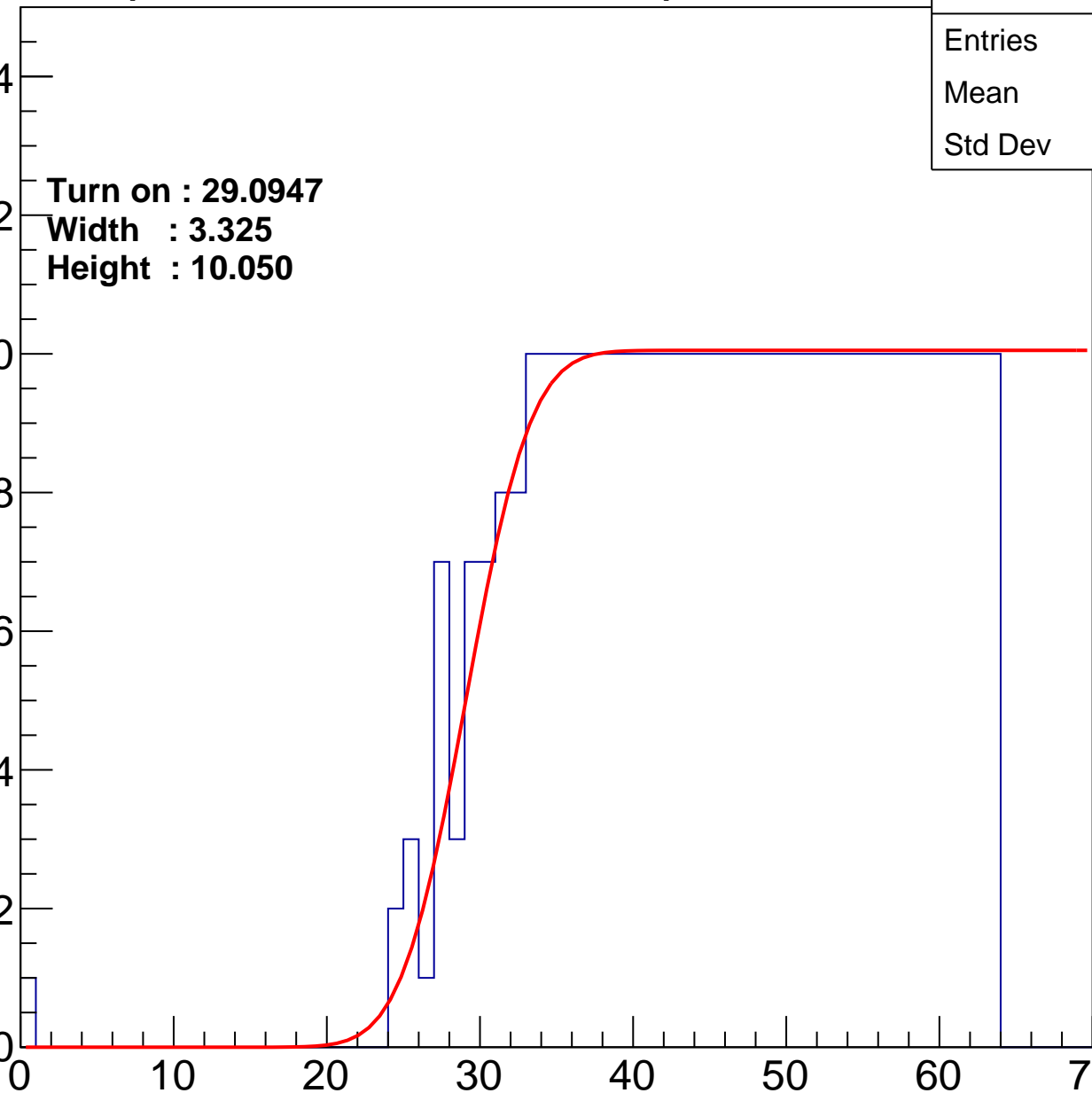
Width : 3.325

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch72

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.18
Std Dev	11.54

Turn on : 28.7299

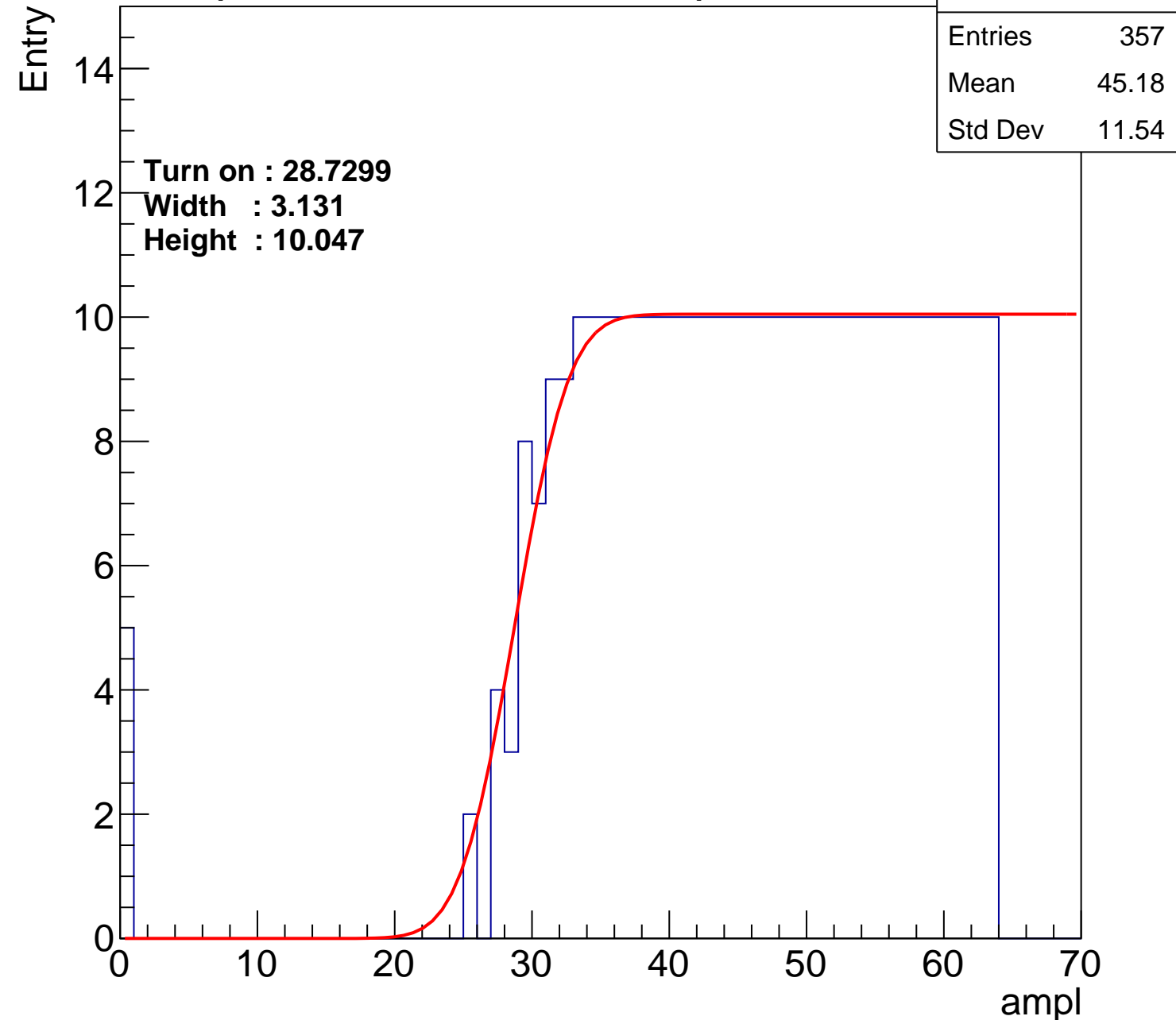
Width : 3.131

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch73

calib\_packv5\_042523\_0143.root, FC#8, port C1

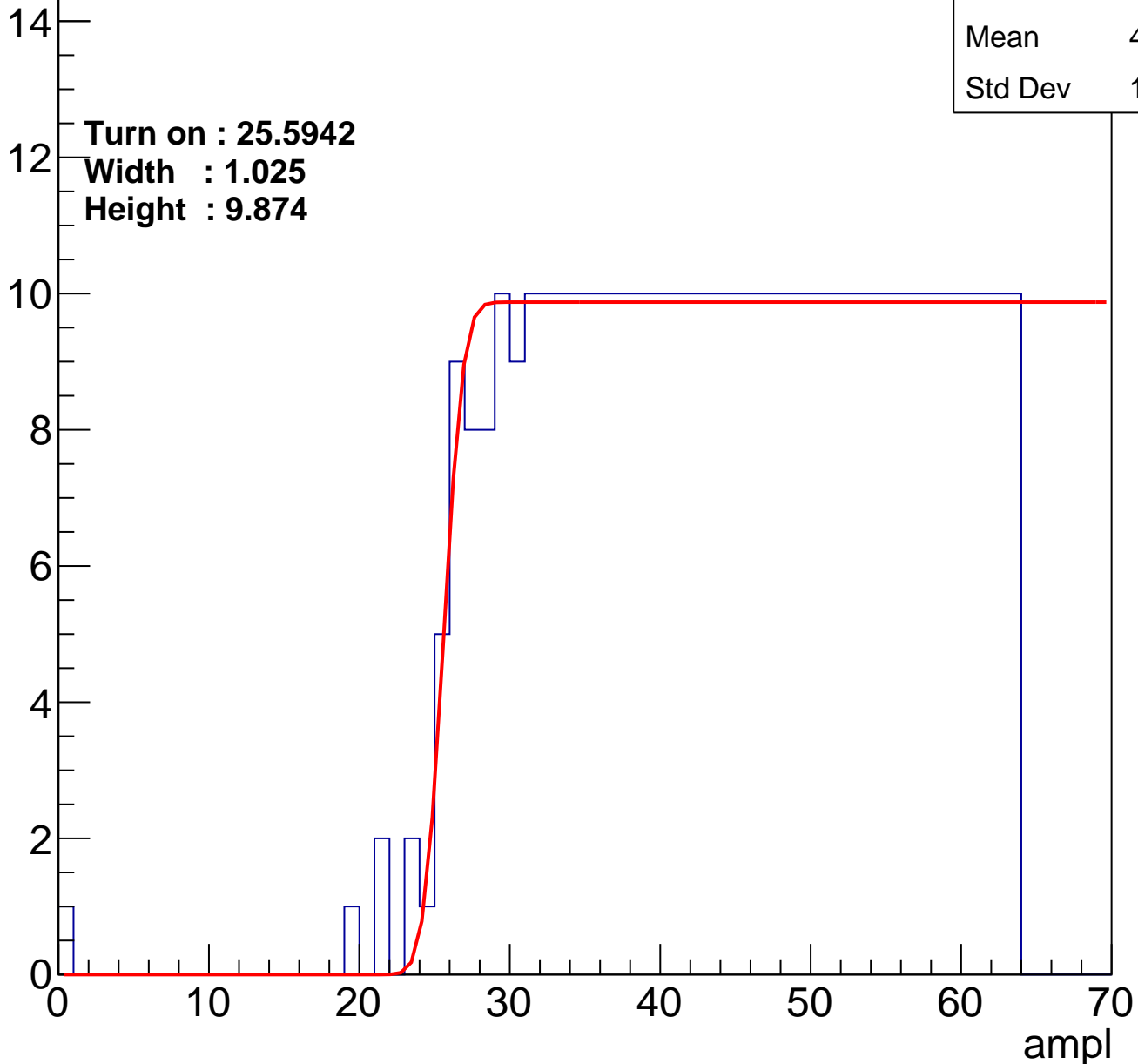
Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.5942

Width : 1.025

Height : 9.874

Entry



# B0L002S, U23-ch74

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	355
Mean	45.53
Std Dev	10.82

**Turn on : 28.9250**

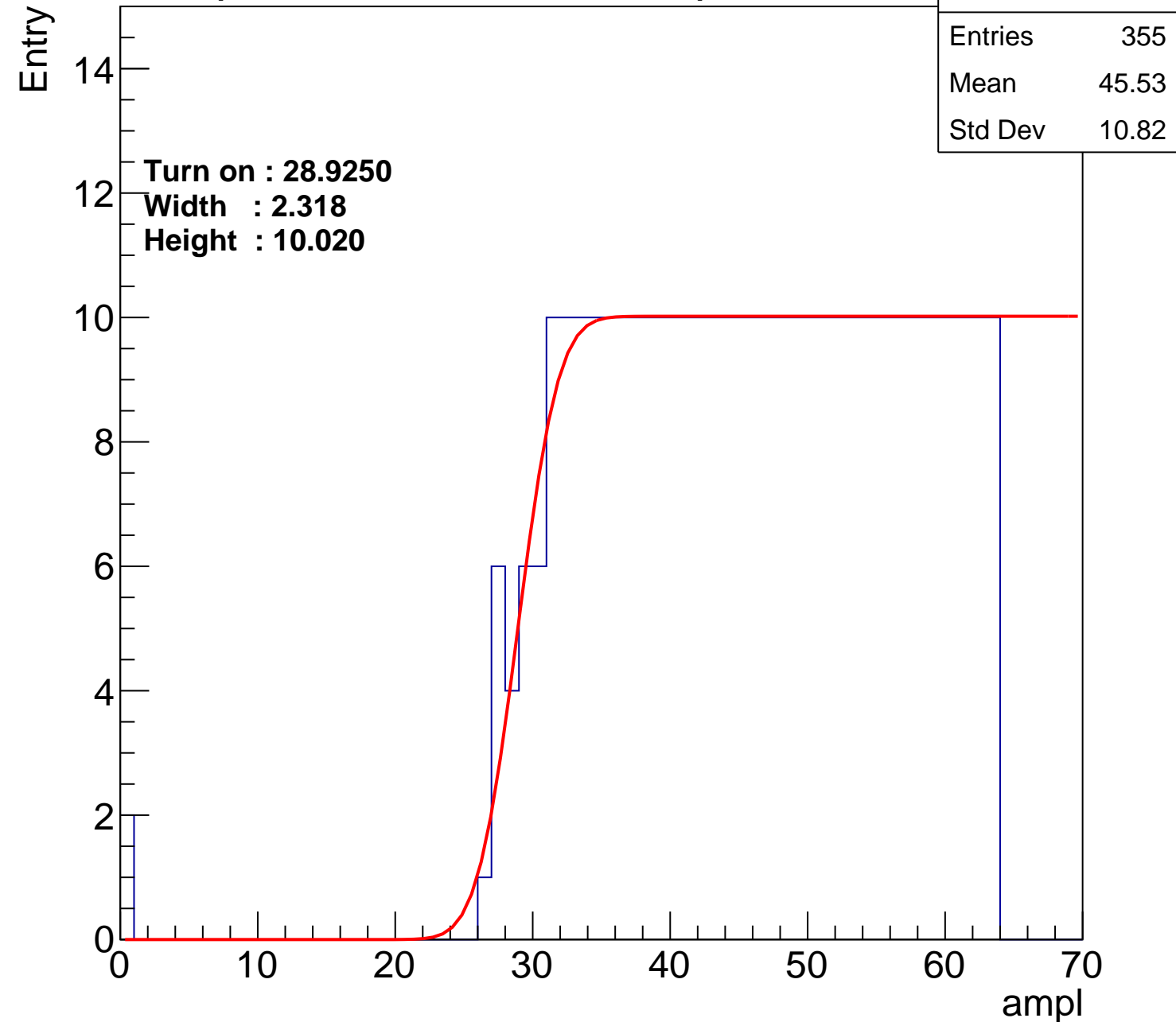
**Width : 2.318**

**Height : 10.020**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch75

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	383
Mean	44.04
Std Dev	11.78

Turn on : 26.2604

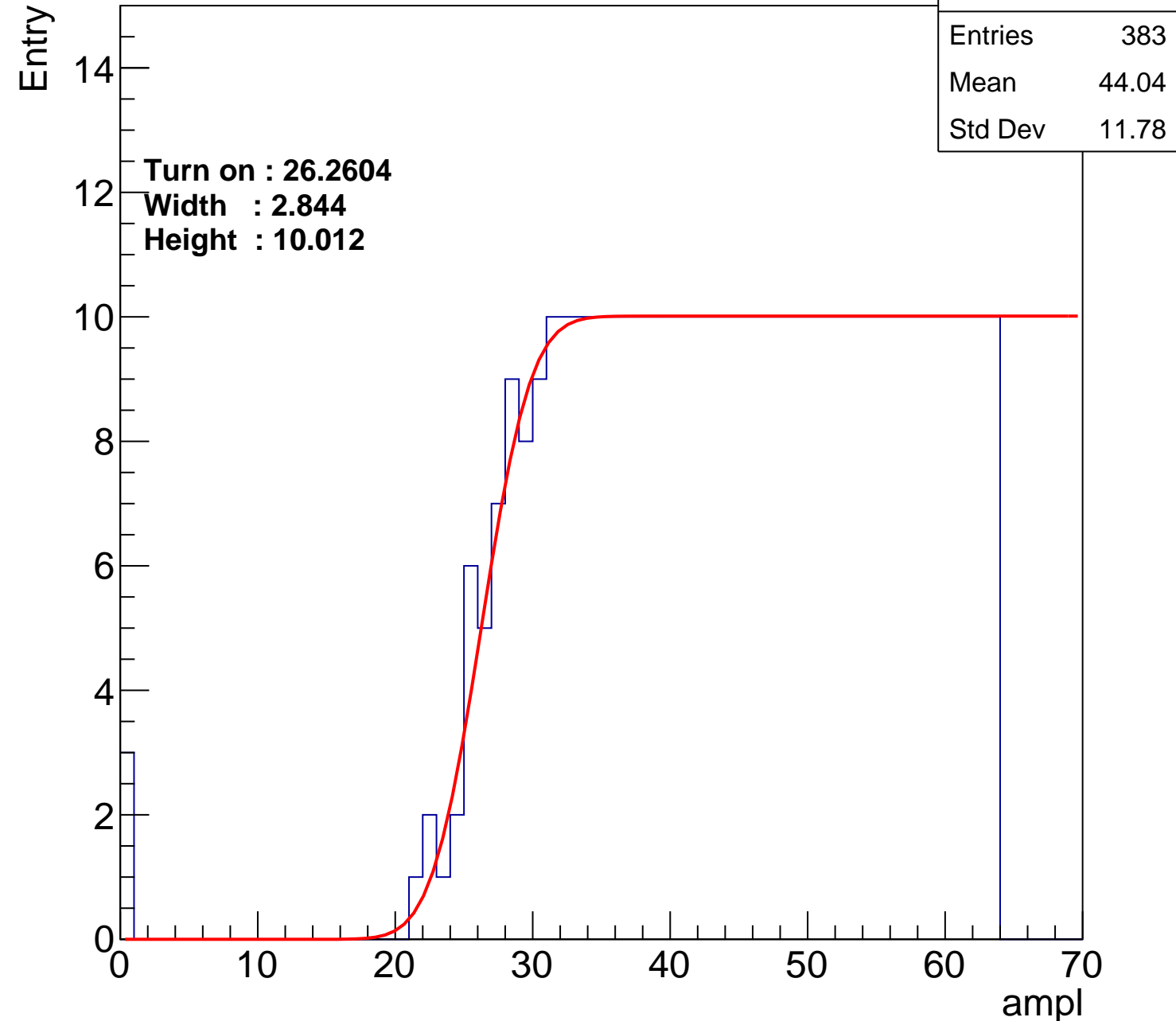
Width : 2.844

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch76

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.89
Std Dev	11.21

Turn on : 27.4479

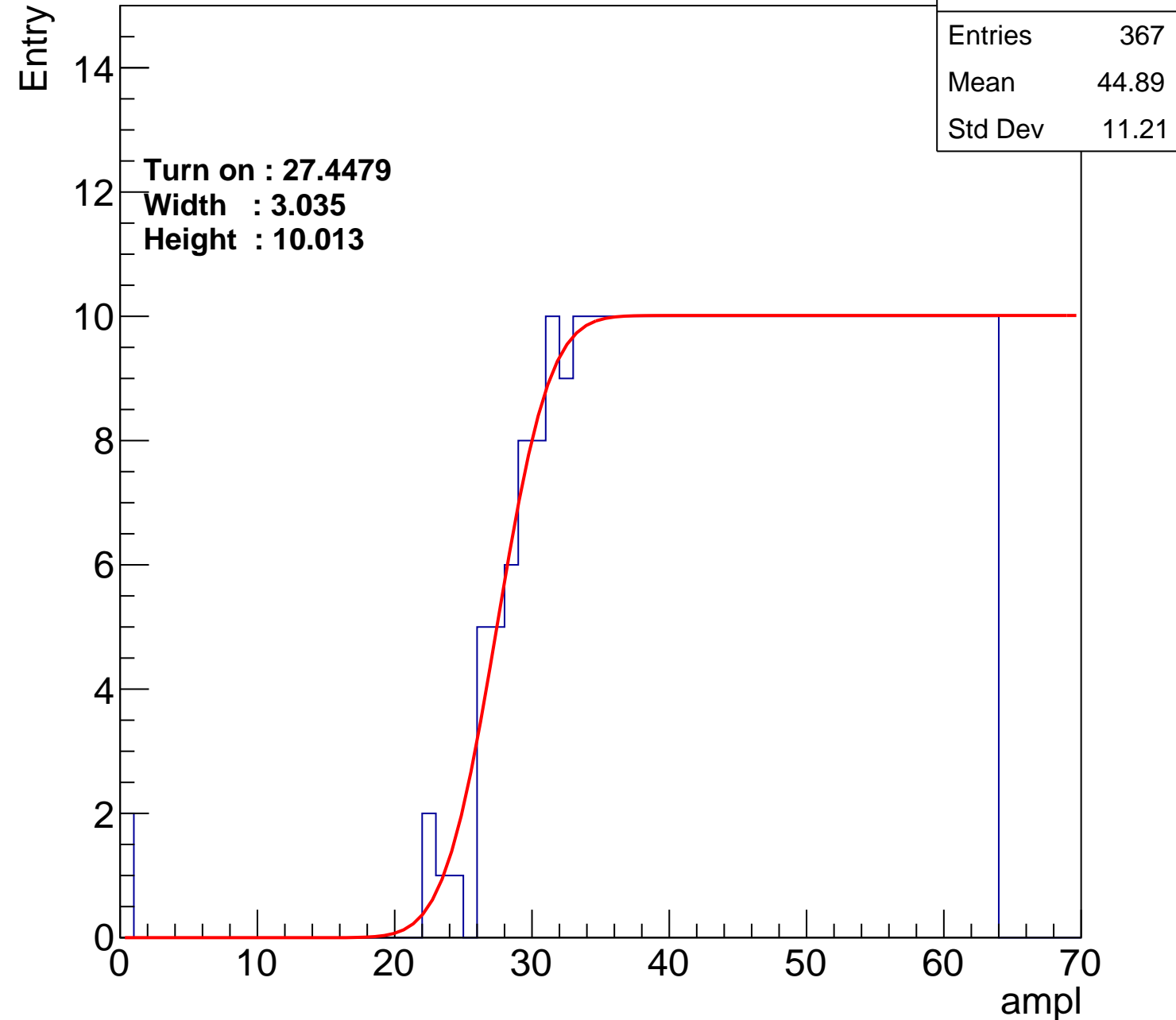
Width : 3.035

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch77

calib\_packv5\_042523\_0143.root, FC#8, port C1

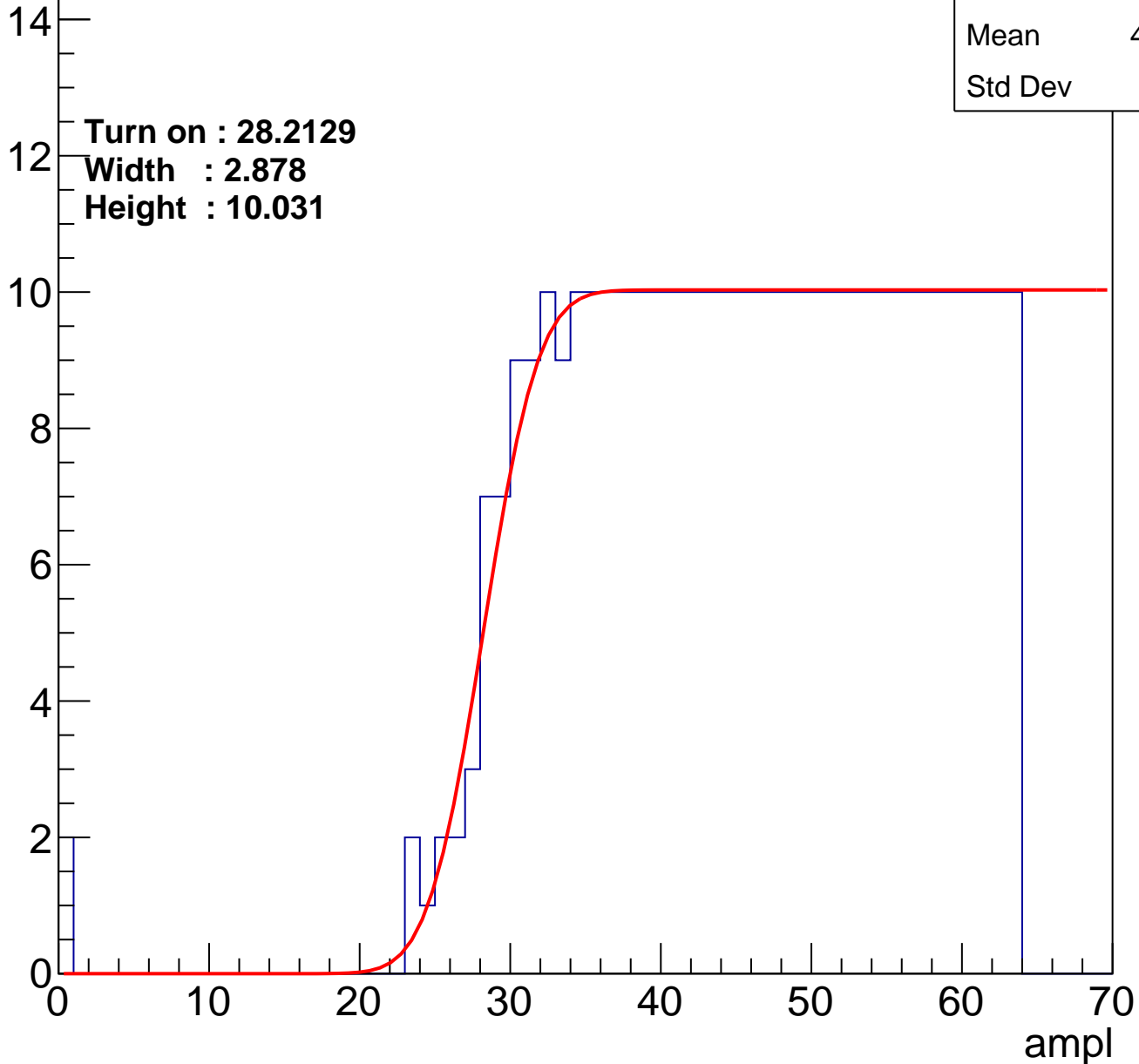
Entries	363
Mean	45.09
Std Dev	11.1

Turn on : 28.2129

Width : 2.878

Height : 10.031

Entry



# B0L002S, U23-ch78

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.96
Std Dev	11.15

**Turn on : 28.4416**

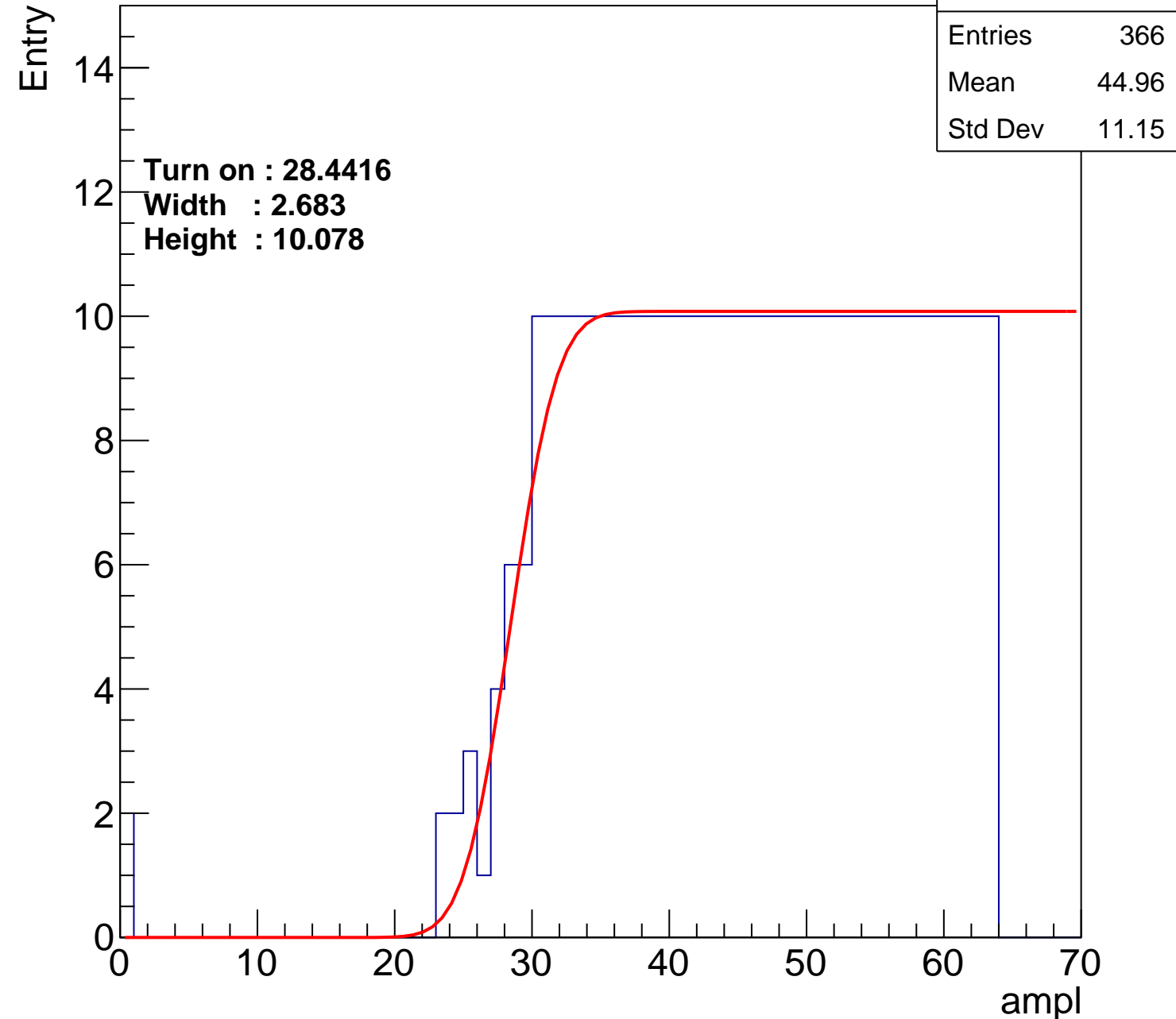
**Width : 2.683**

**Height : 10.078**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch79

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.65
Std Dev	11.5

**Turn on : 26.8348**

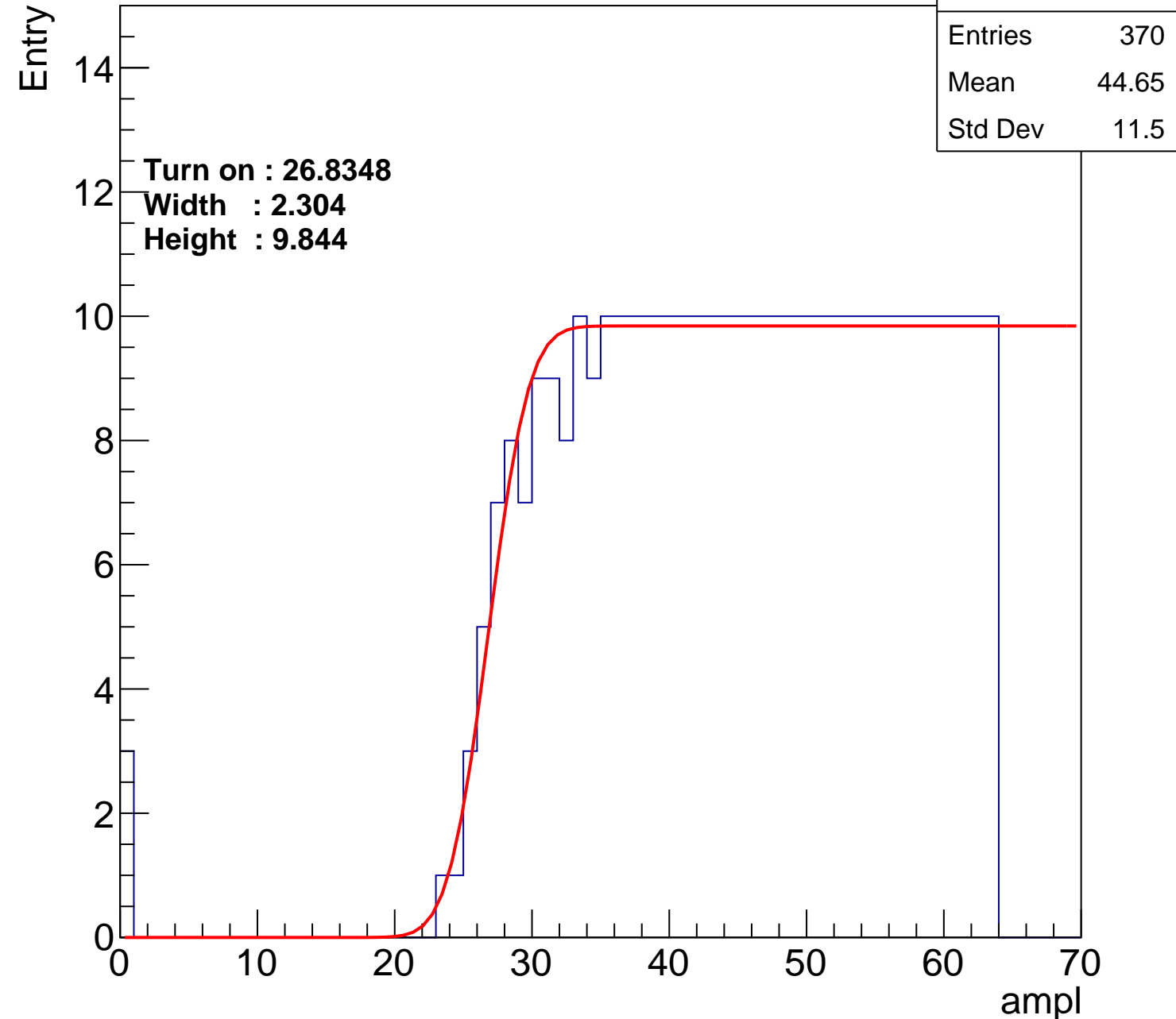
**Width : 2.304**

**Height : 9.844**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch80

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	381
Mean	44.05
Std Dev	12.01

Turn on : 26.7649

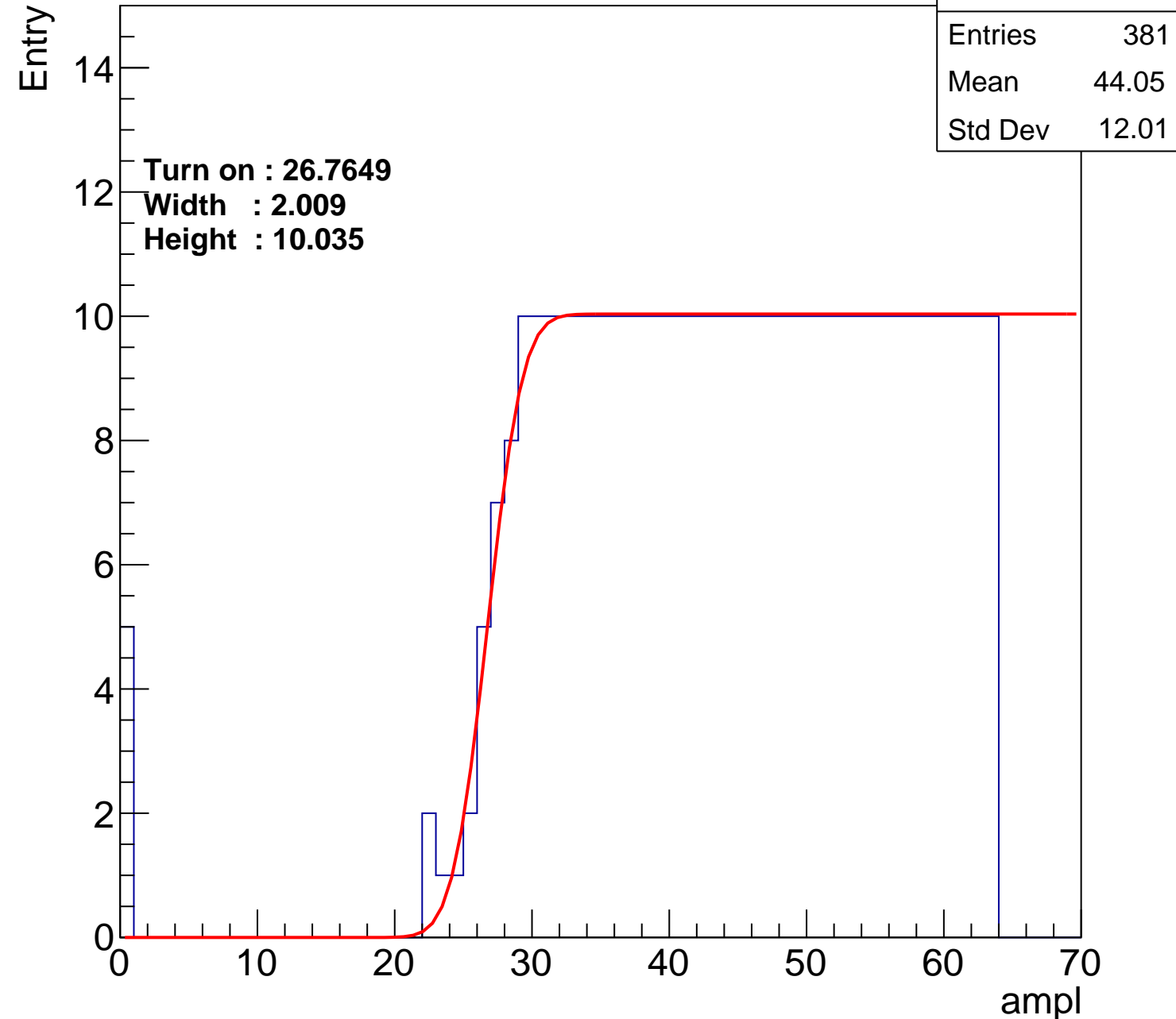
Width : 2.009

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch81

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.2912

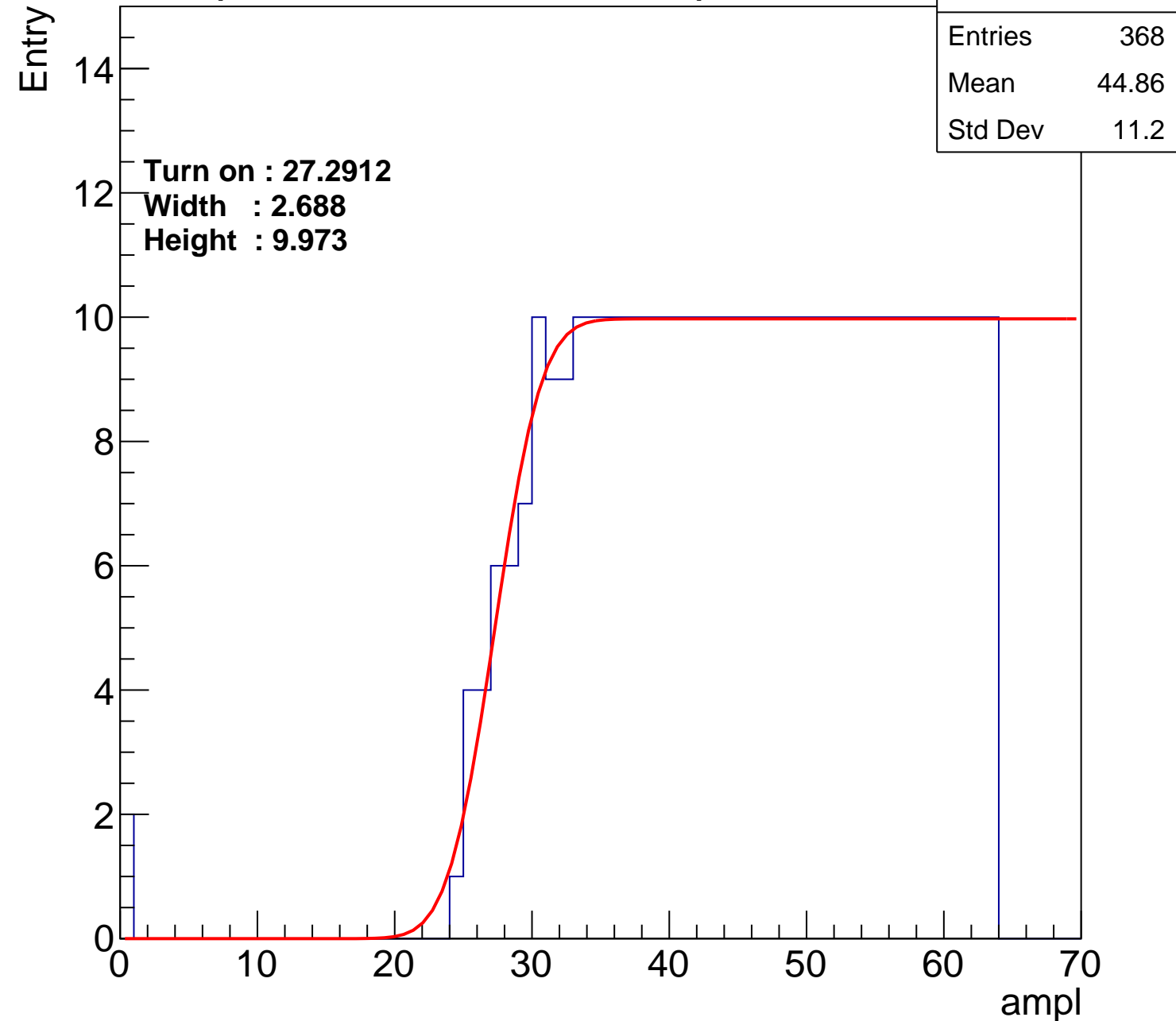
Width : 2.688

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch82

calib\_packv5\_042523\_0143.root, FC#8, port C1

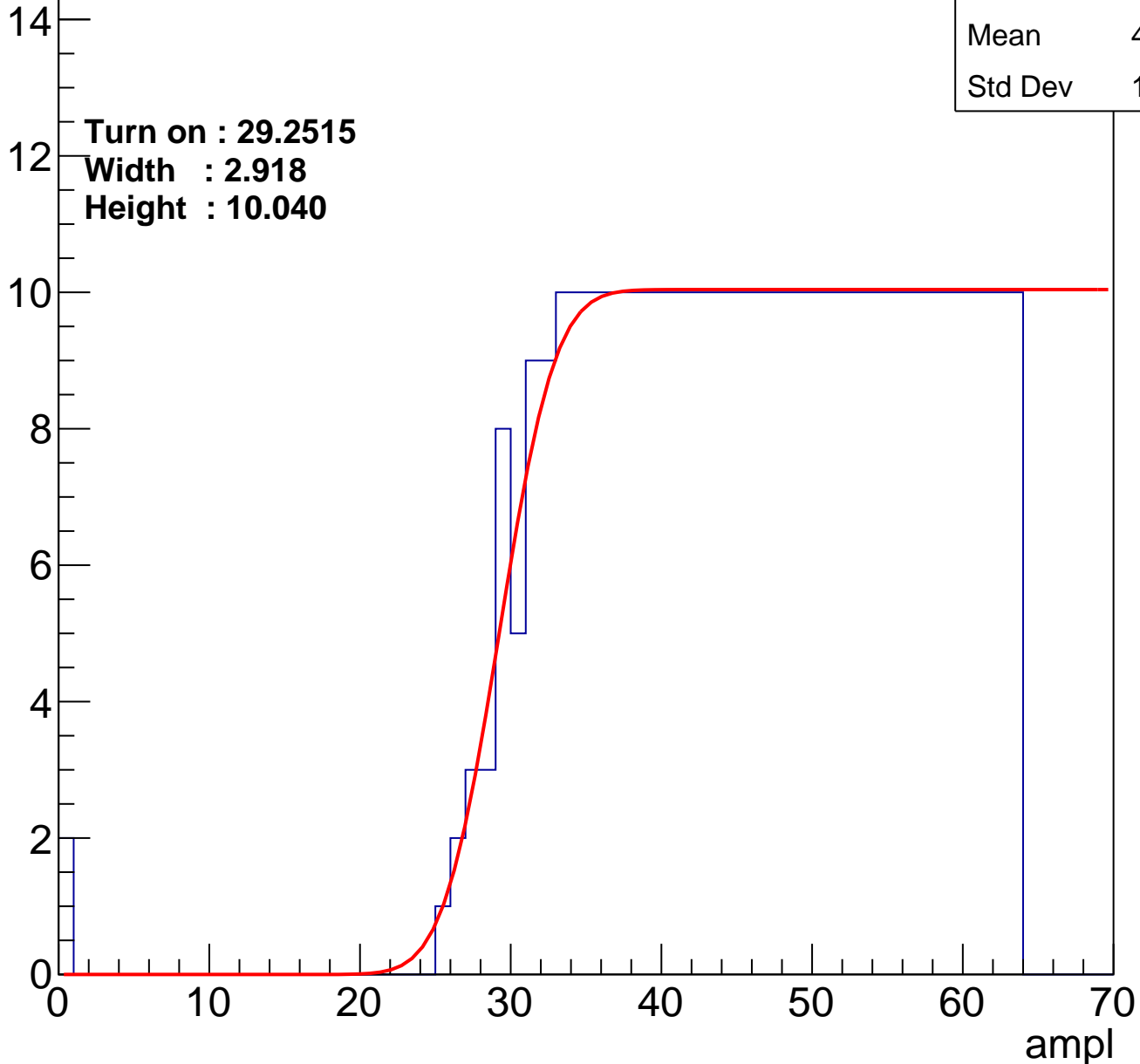
Entries	352
Mean	45.66
Std Dev	10.78

**Turn on : 29.2515**

**Width : 2.918**

**Height : 10.040**

Entry



# B0L002S, U23-ch83

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.02
Std Dev	11.11

**Turn on : 27.9009**

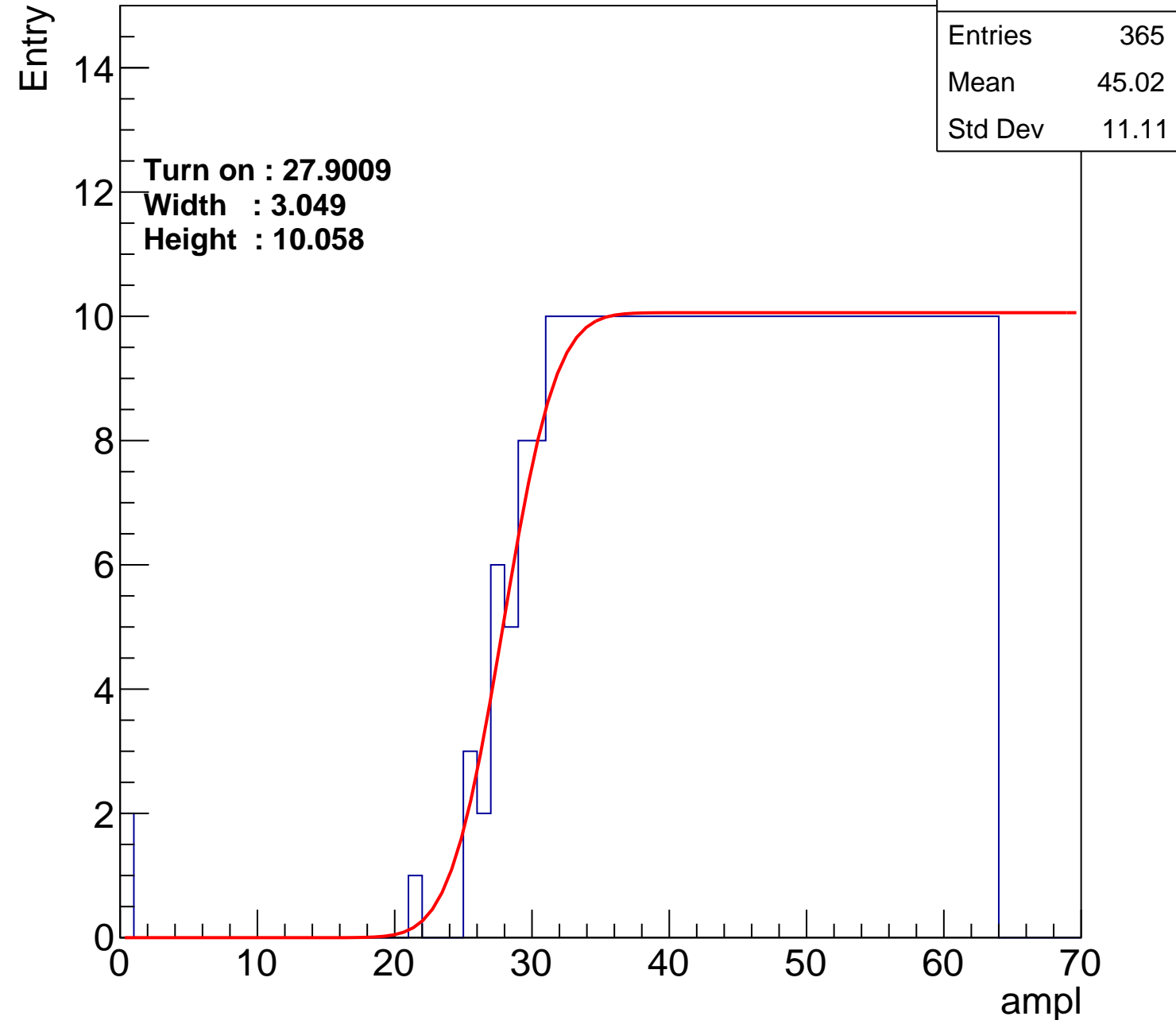
**Width : 3.049**

**Height : 10.058**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch84

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.02
Std Dev	11.09

**Turn on : 27.0073**

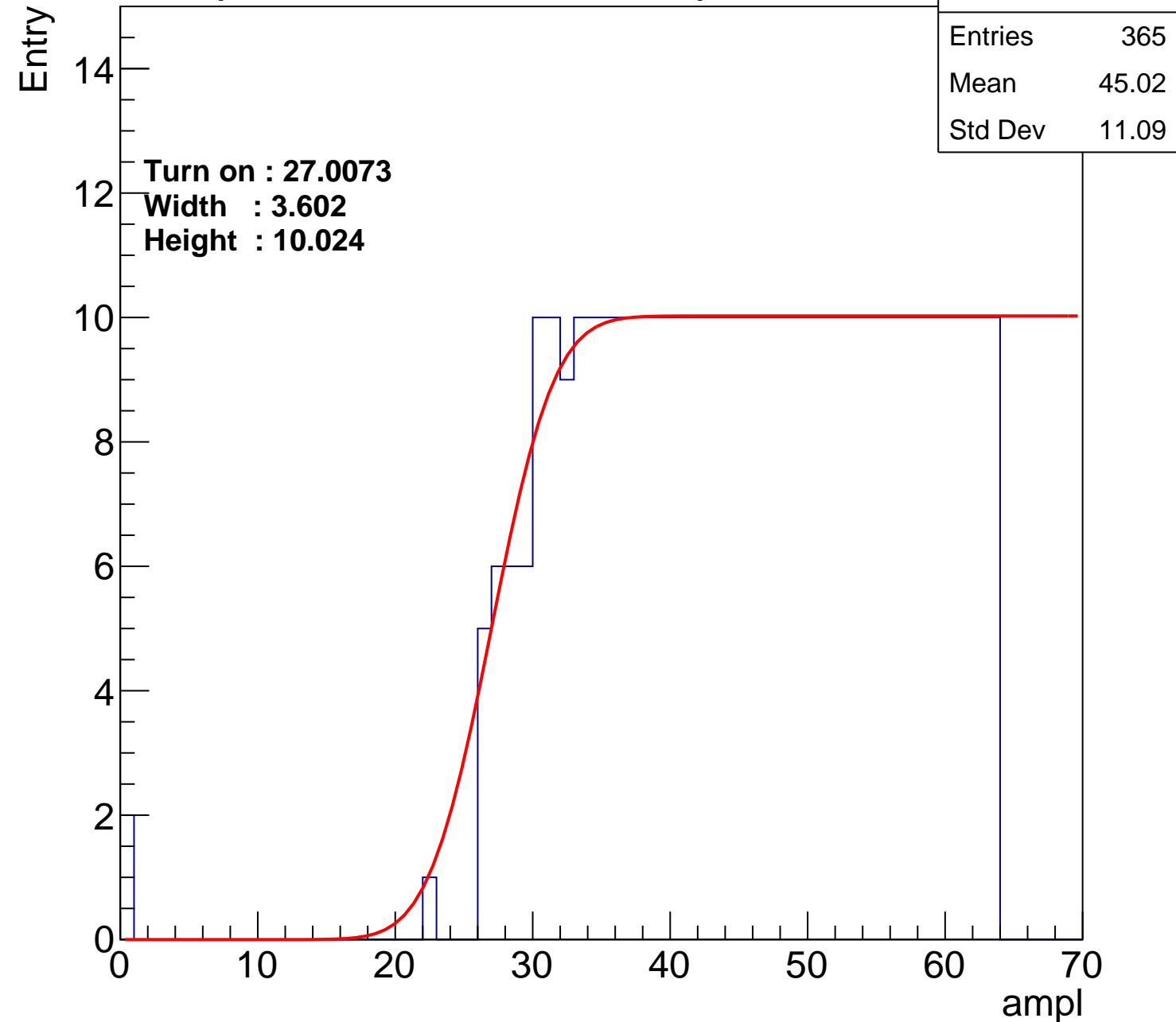
**Width : 3.602**

**Height : 10.024**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch85

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.04
Std Dev	11.6

Turn on : 28.3212

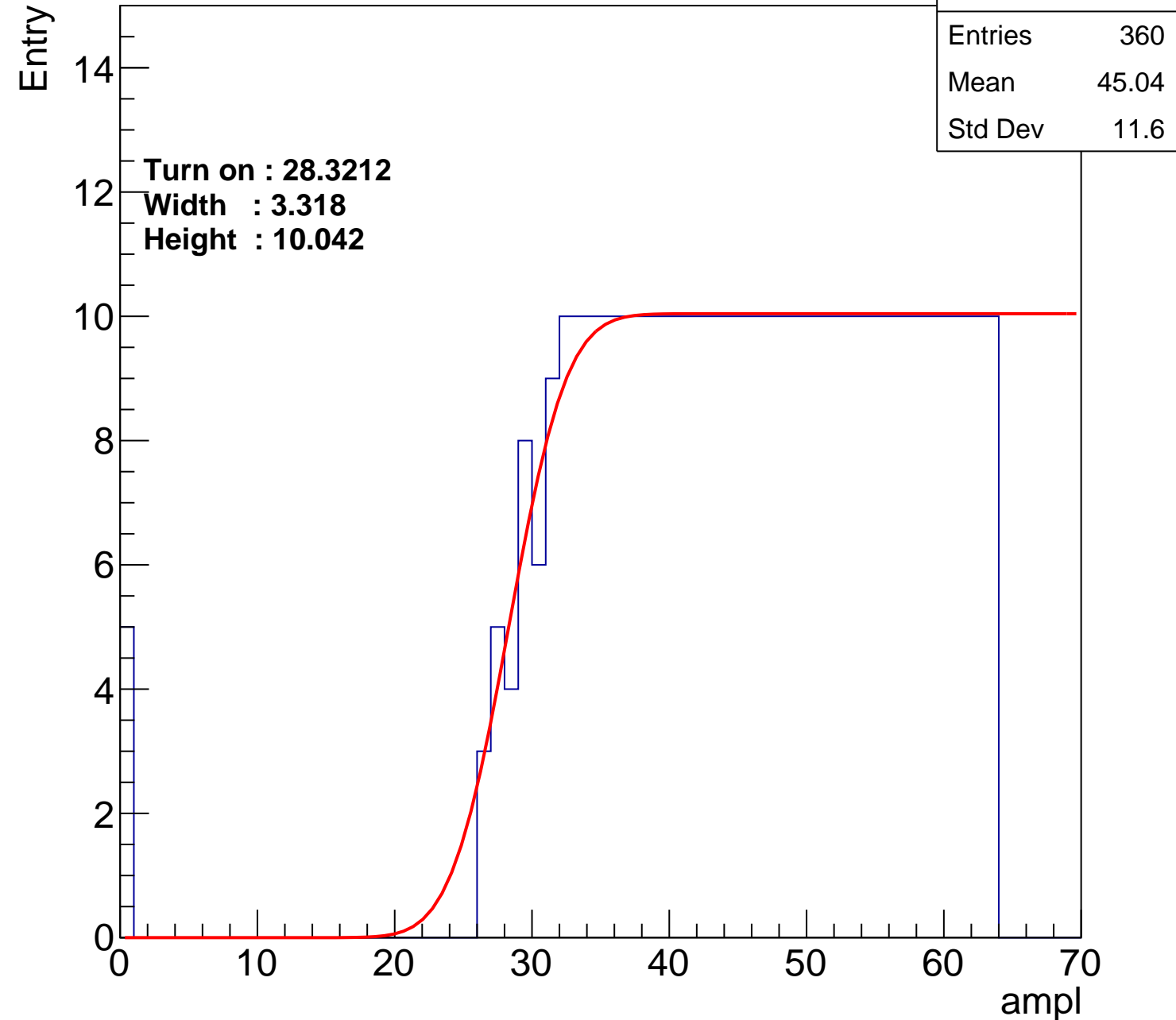
Width : 3.318

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch86

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.16
Std Dev	11.1

Turn on : 28.7698

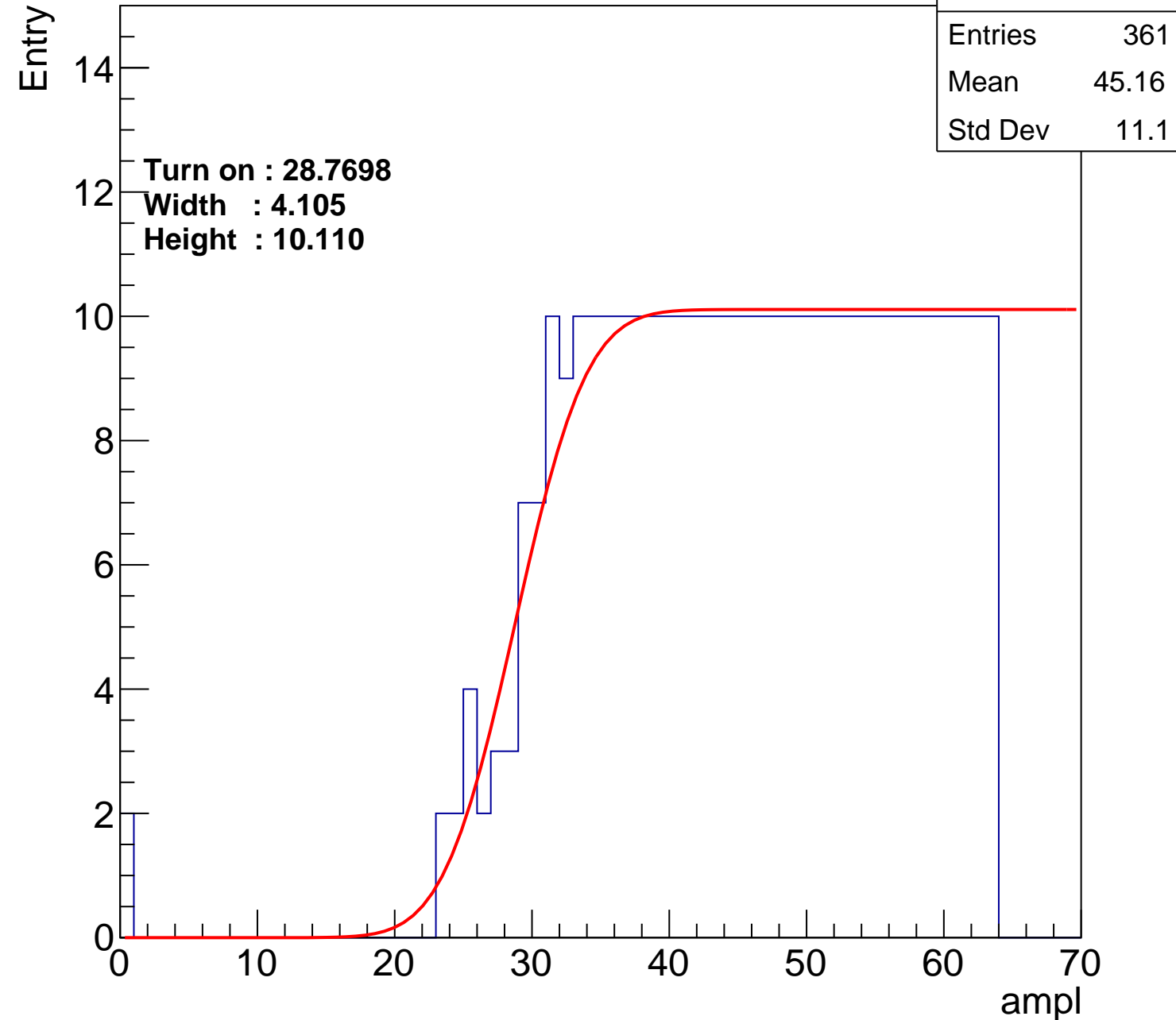
Width : 4.105

Height : 10.110

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch87

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	44.94
Std Dev	11.55

Turn on : 28.4041

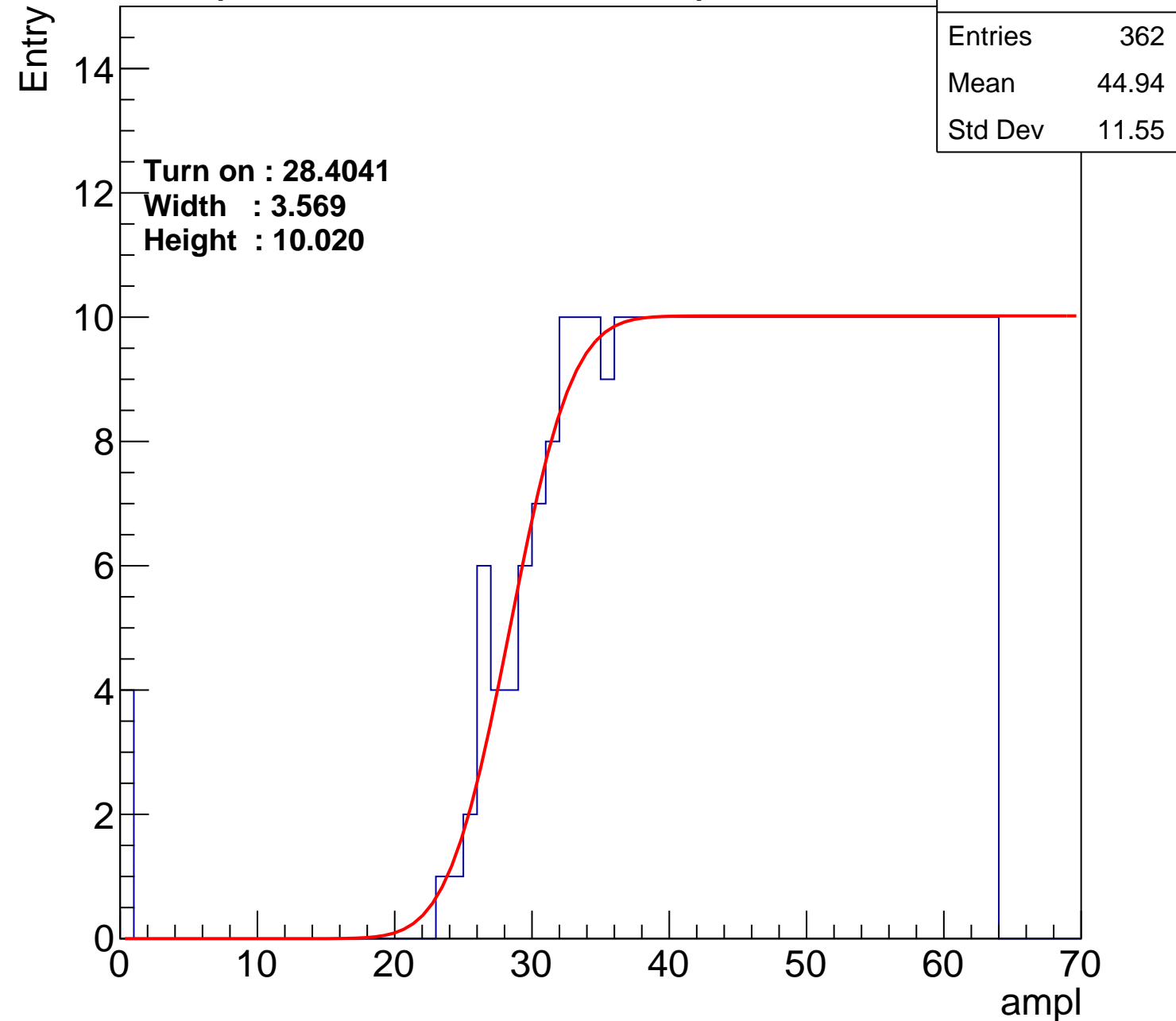
Width : 3.569

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch88

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	387
Mean	43.79
Std Dev	12.03

Turn on : 25.6025

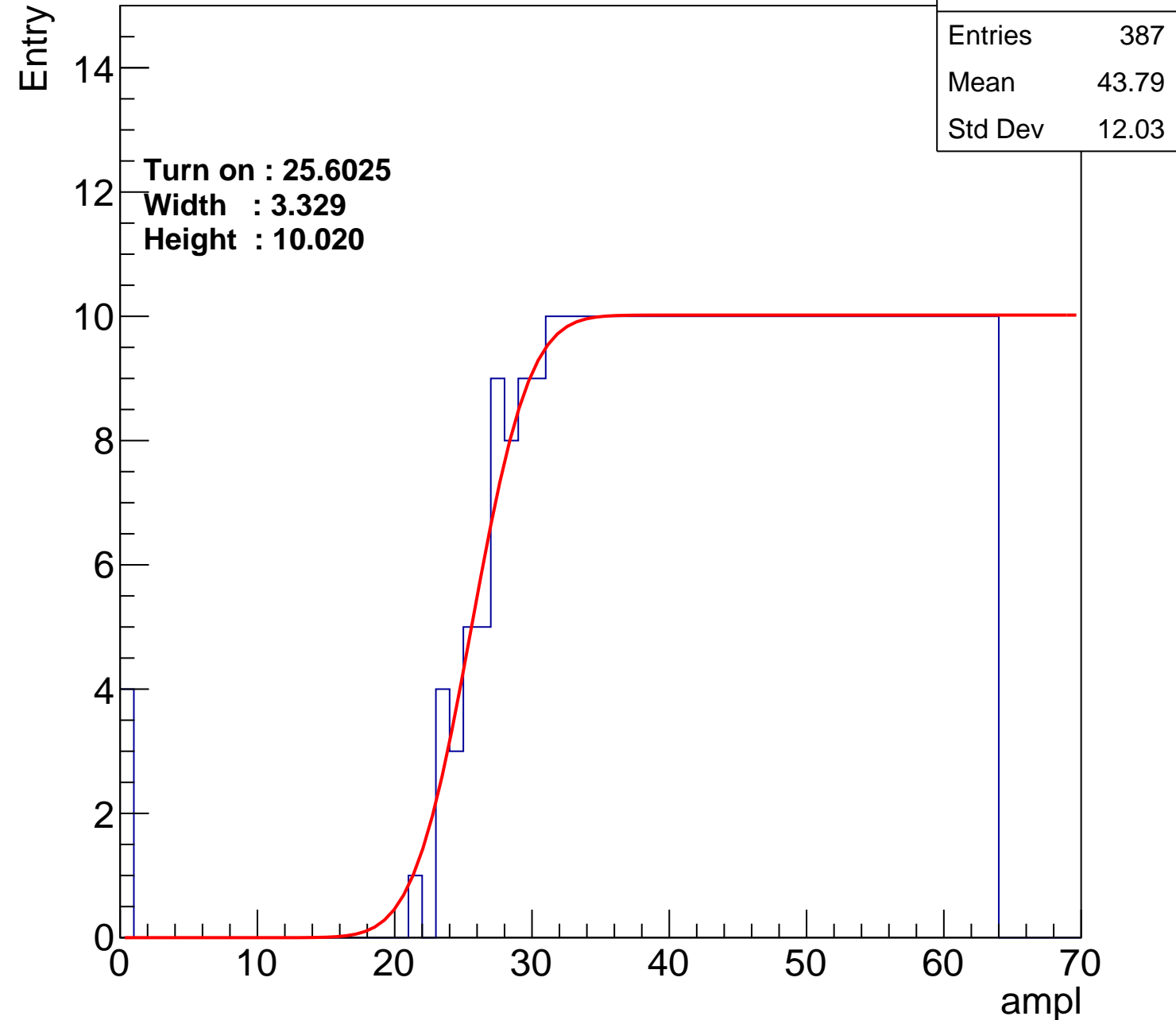
Width : 3.329

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch89

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	355
Mean	45.5
Std Dev	10.87

**Turn on : 28.9008**

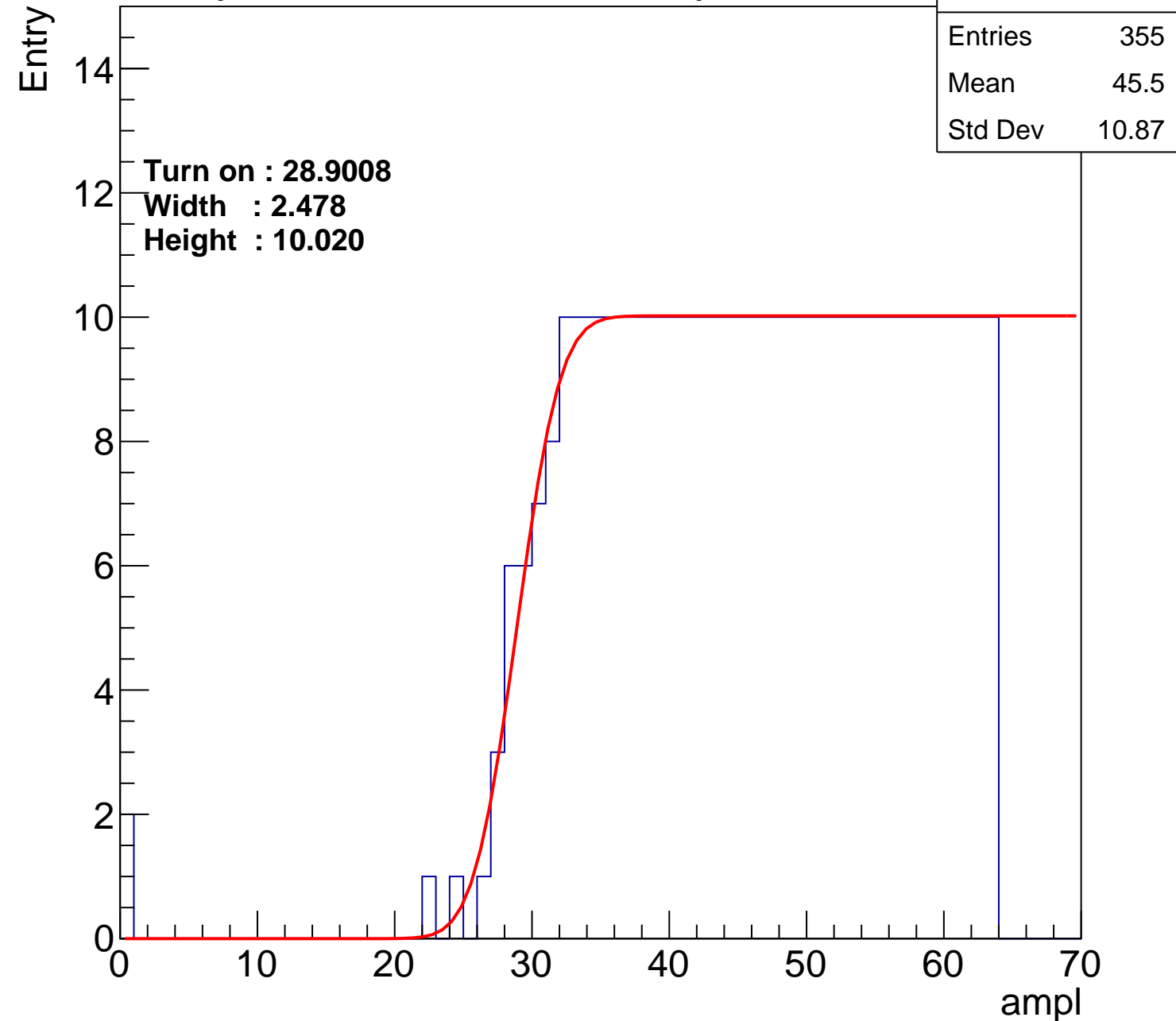
**Width : 2.478**

**Height : 10.020**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch90

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.88
Std Dev	11.5

Turn on : 28.1521

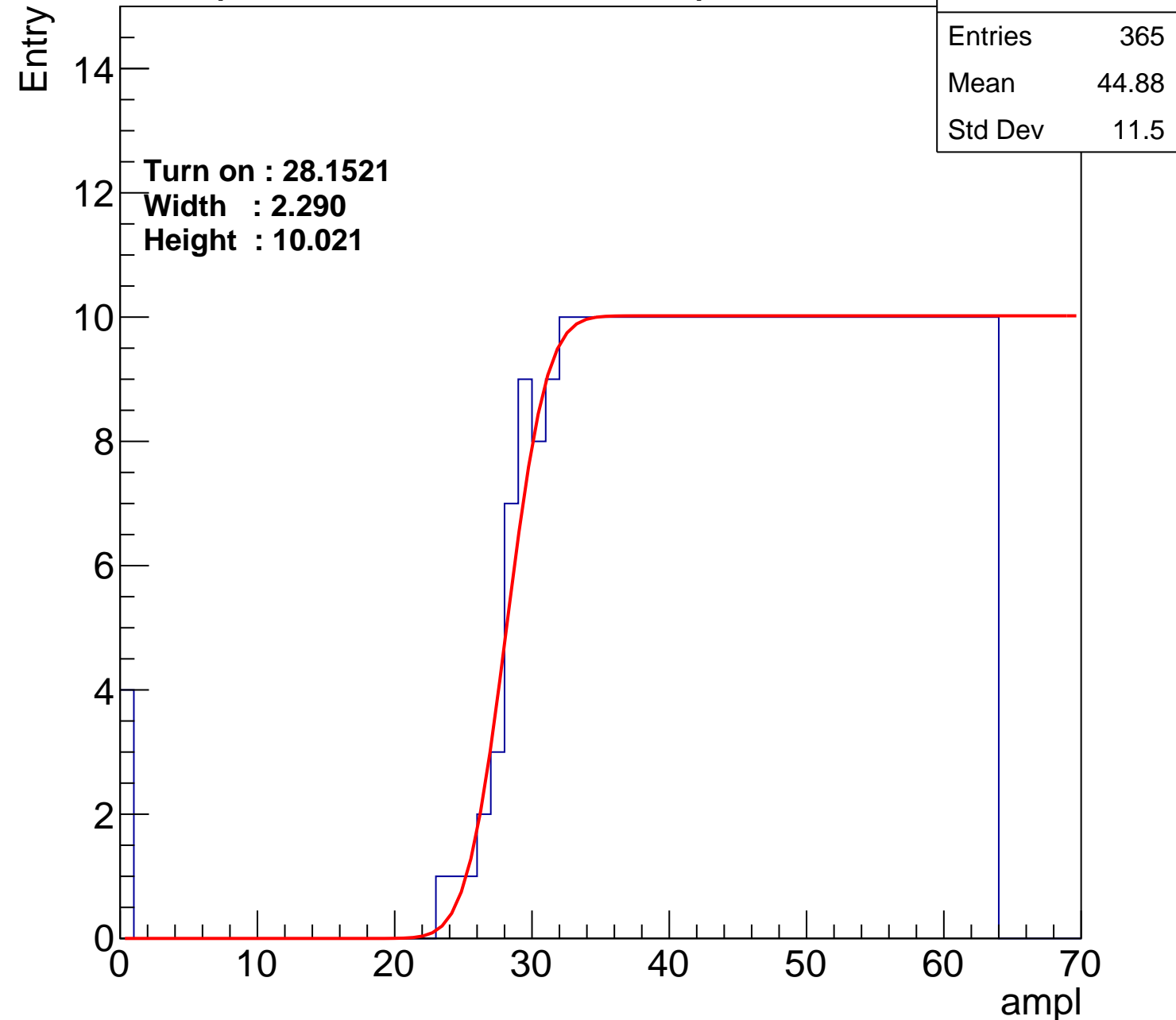
Width : 2.290

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch91

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	367
Mean	44.77
Std Dev	11.47

**Turn on : 27.5967**

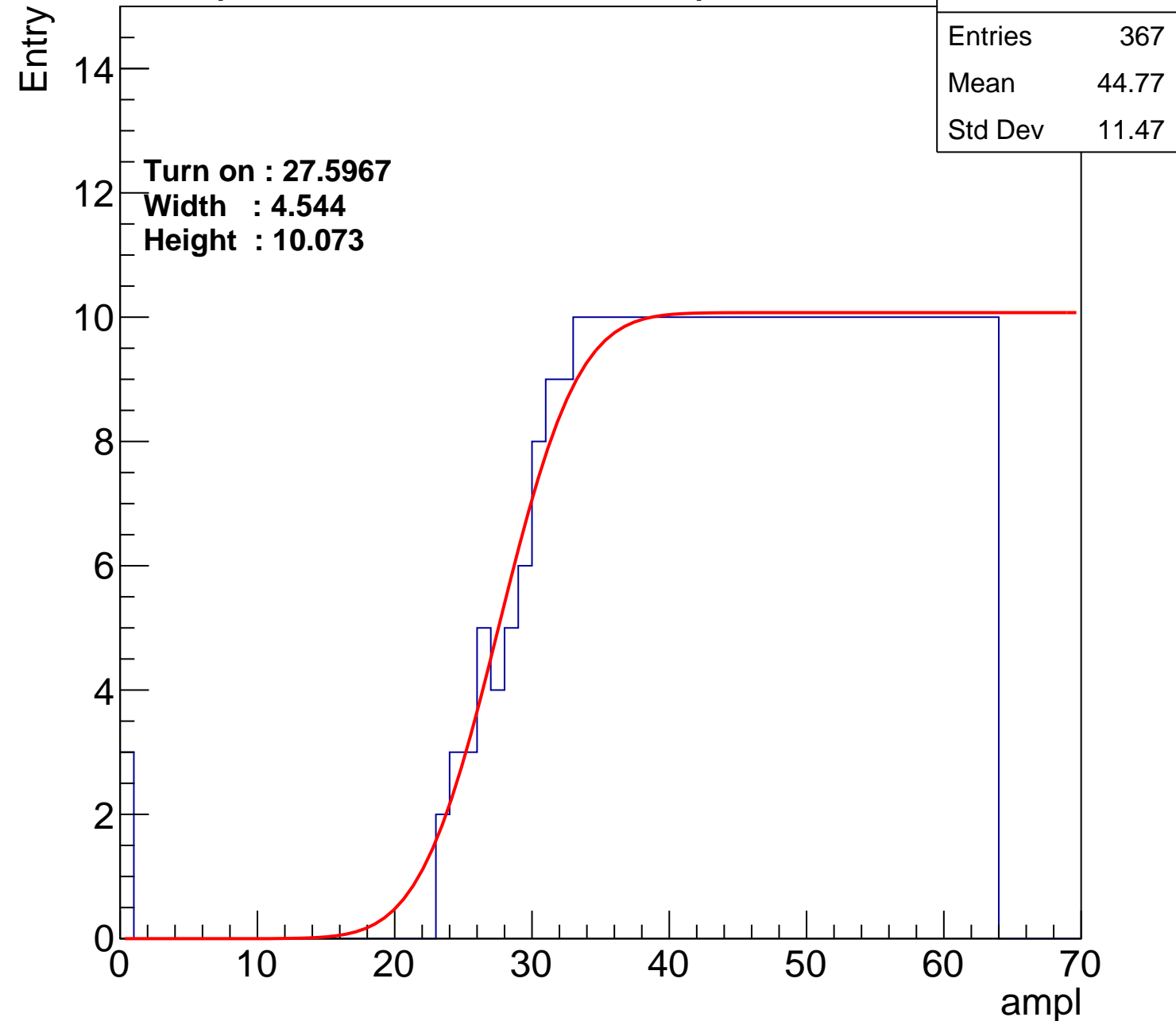
**Width : 4.544**

**Height : 10.073**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch92

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	347
Mean	45.89
Std Dev	10.67

Turn on : 29.2429

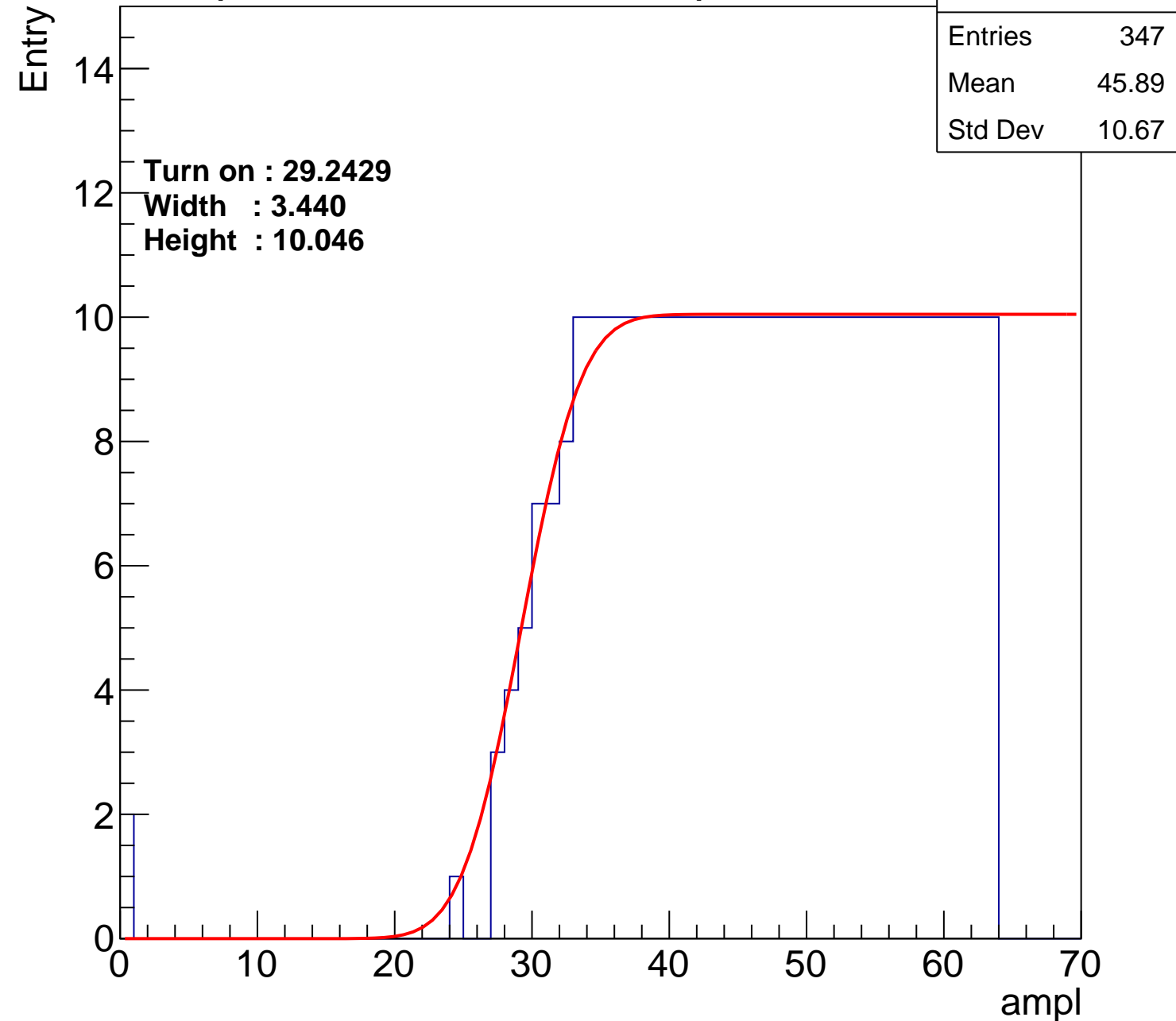
Width : 3.440

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch93

calib\_packv5\_042523\_0143.root, FC#8, port C1

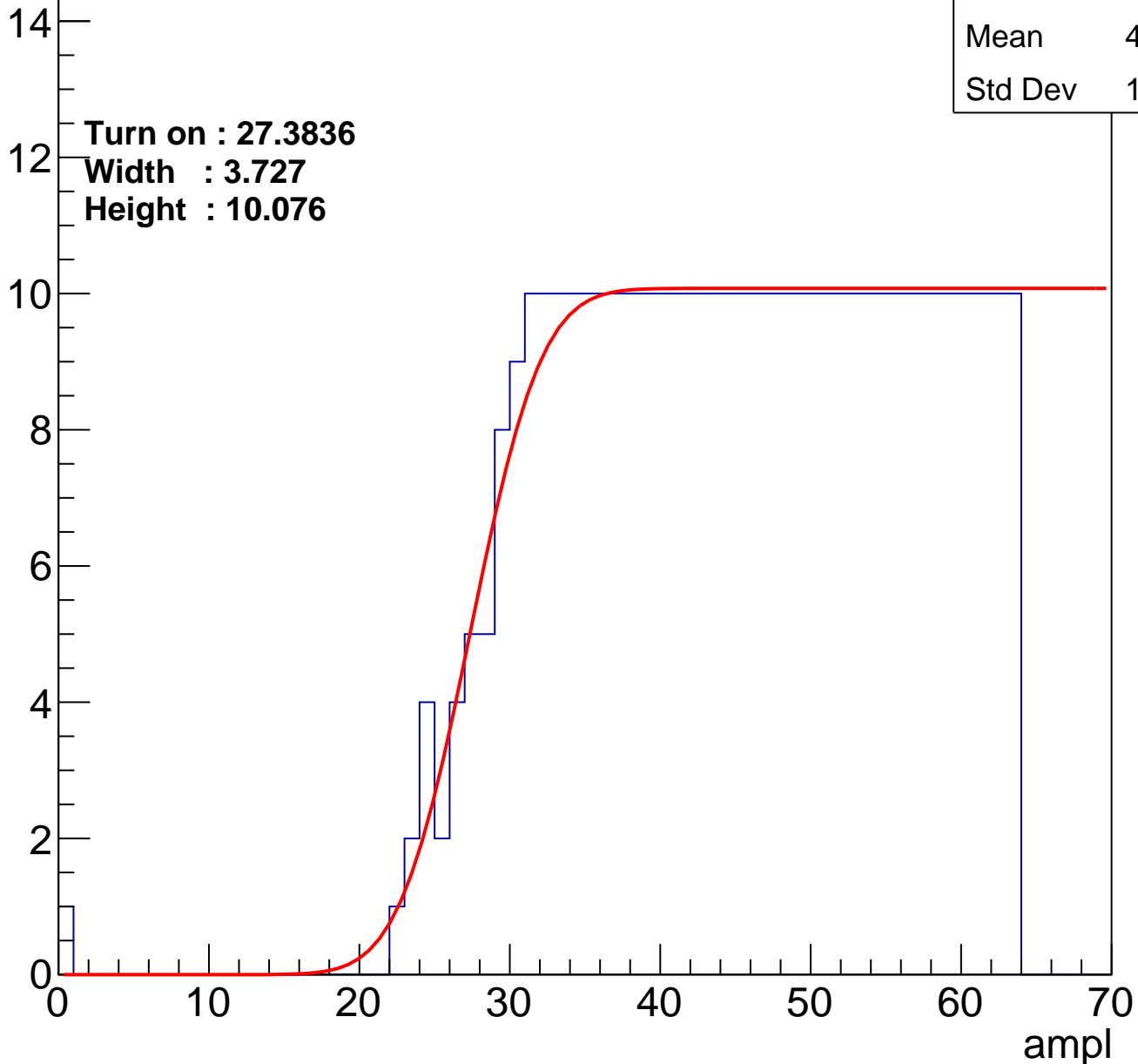
Entries	371
Mean	44.76
Std Dev	11.12

Turn on : 27.3836

Width : 3.727

Height : 10.076

Entry



# B0L002S, U23-ch94

calib\_packv5\_042523\_0143.root, FC#8, port C1

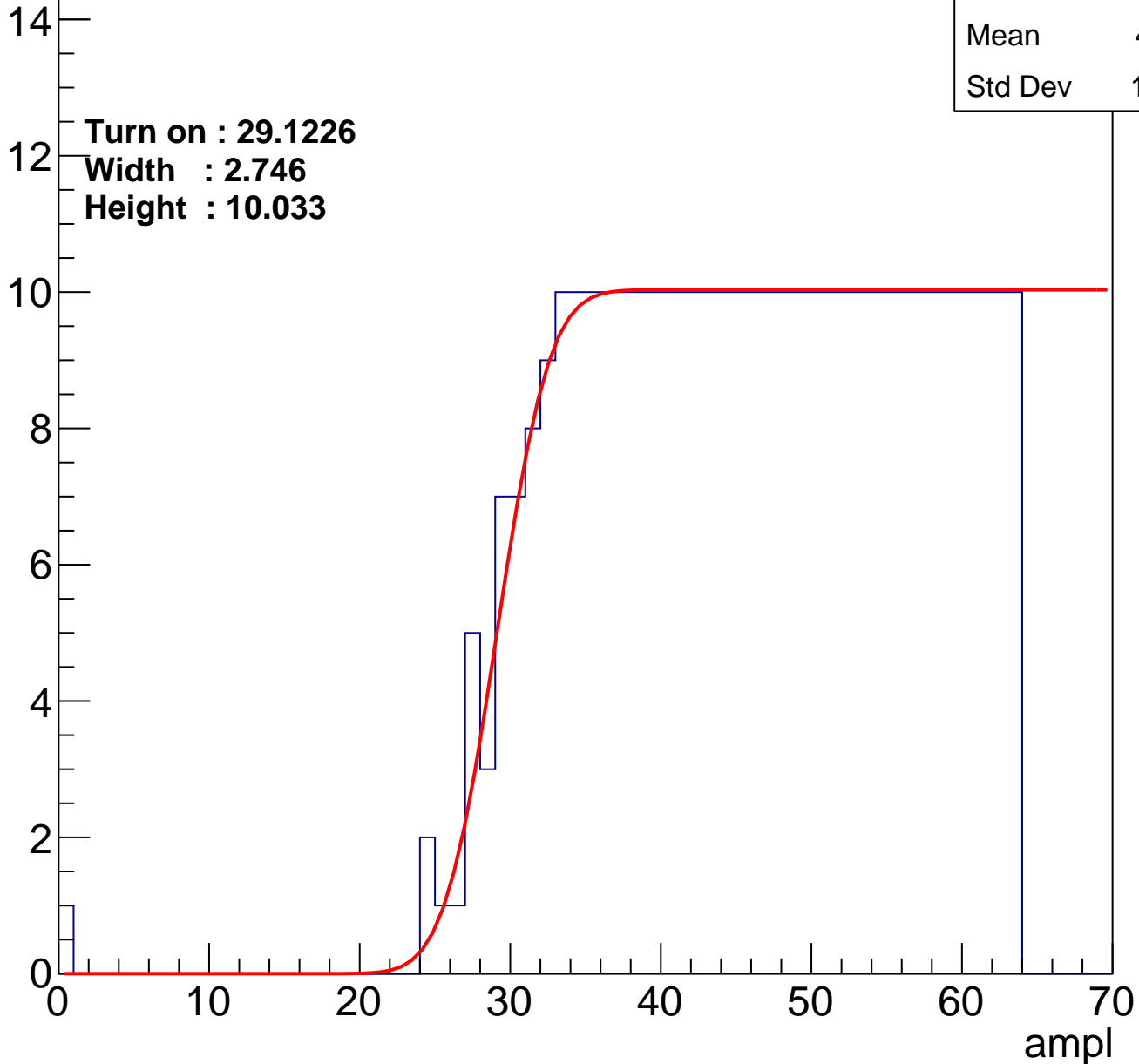
Entries	354
Mean	45.61
Std Dev	10.64

Turn on : 29.1226

Width : 2.746

Height : 10.033

Entry





# B0L002S, U23-ch95

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.65
Std Dev	10.79

Turn on : 29.5252

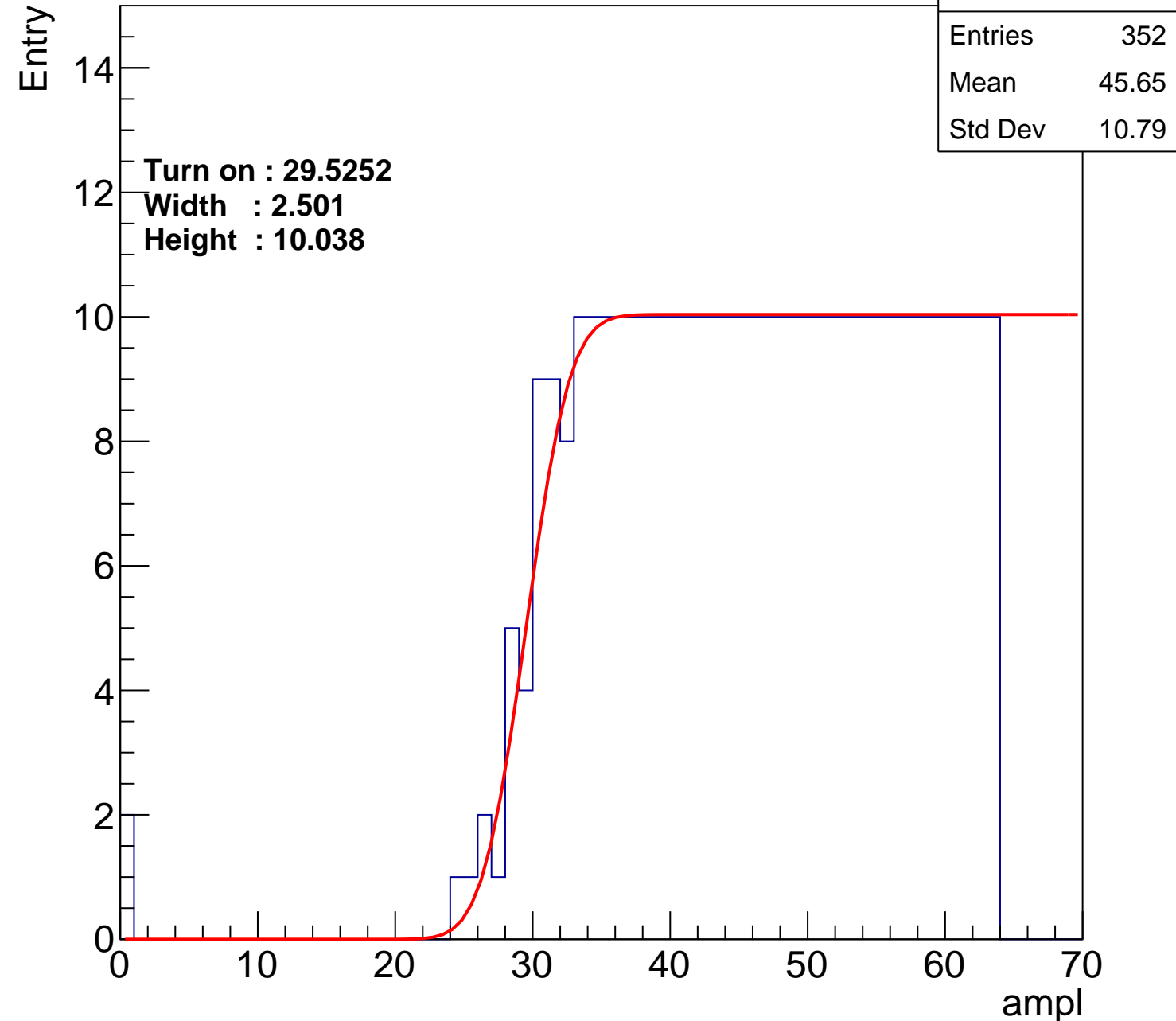
Width : 2.501

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch96

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.64
Std Dev	11.33

Turn on : 27.0503

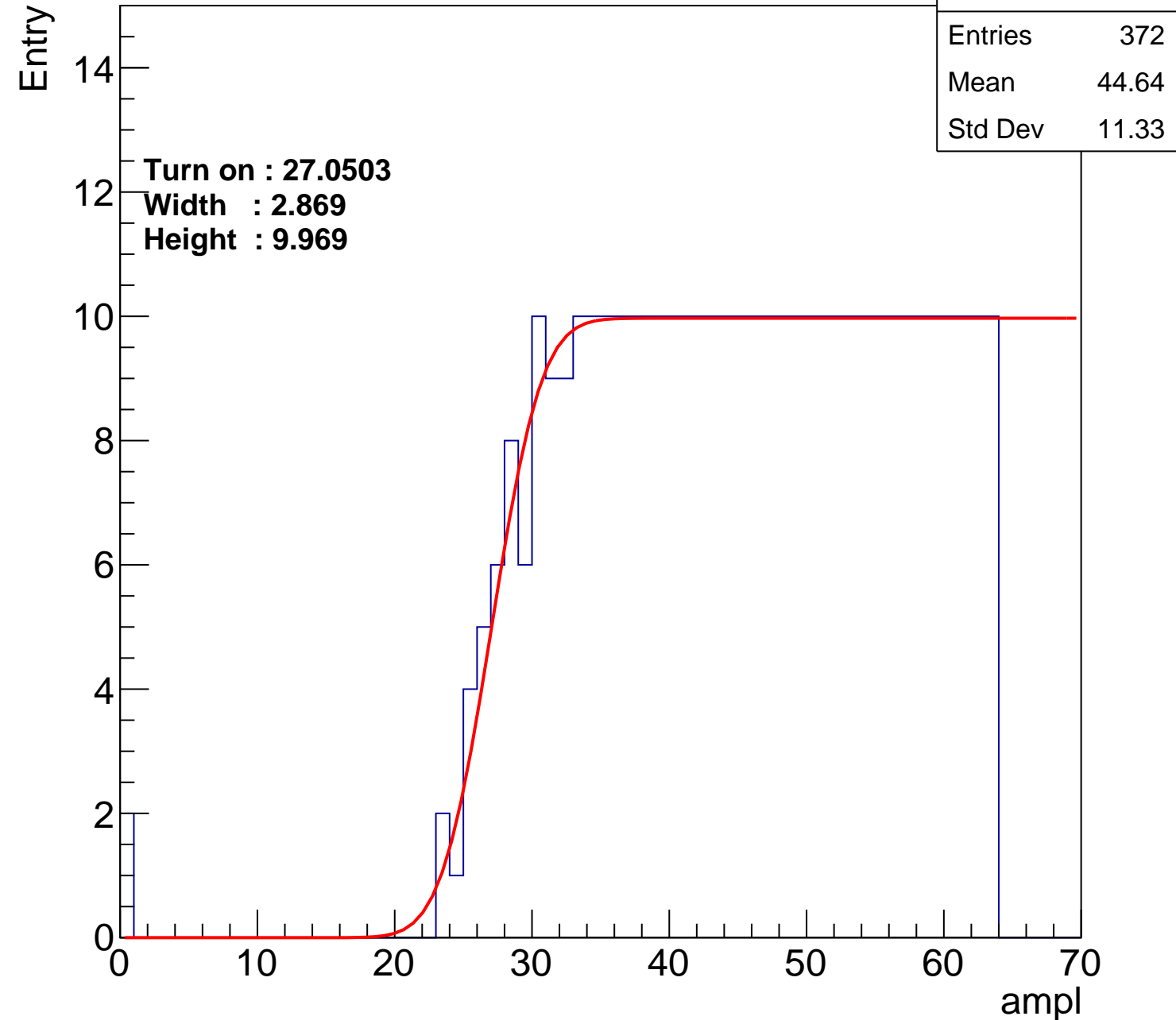
Width : 2.869

Height : 9.969

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch97

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.13
Std Dev	11.23

**Turn on : 28.3497**

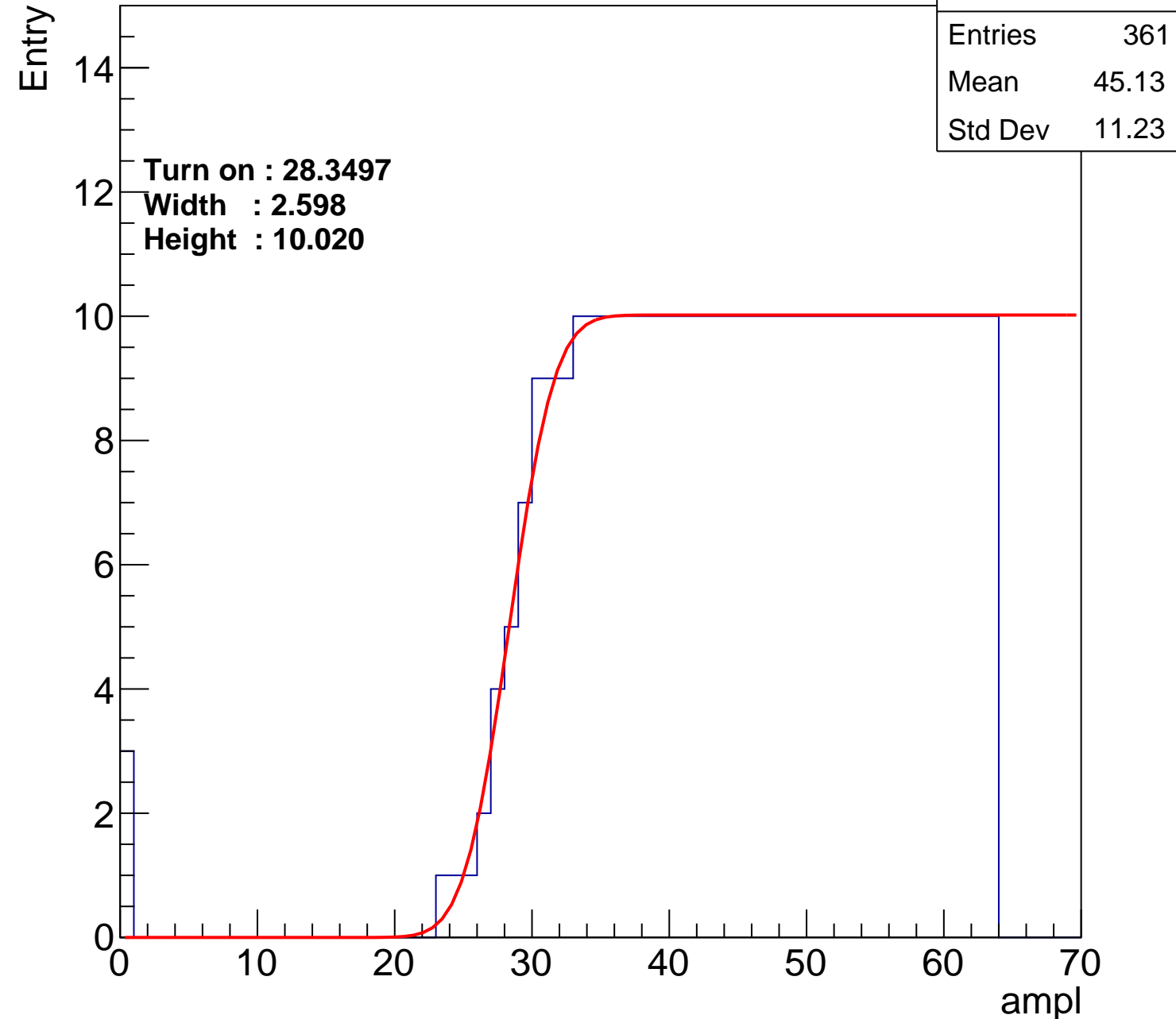
**Width : 2.598**

**Height : 10.020**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch98

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	44.94
Std Dev	11.52

Turn on : 28.2884

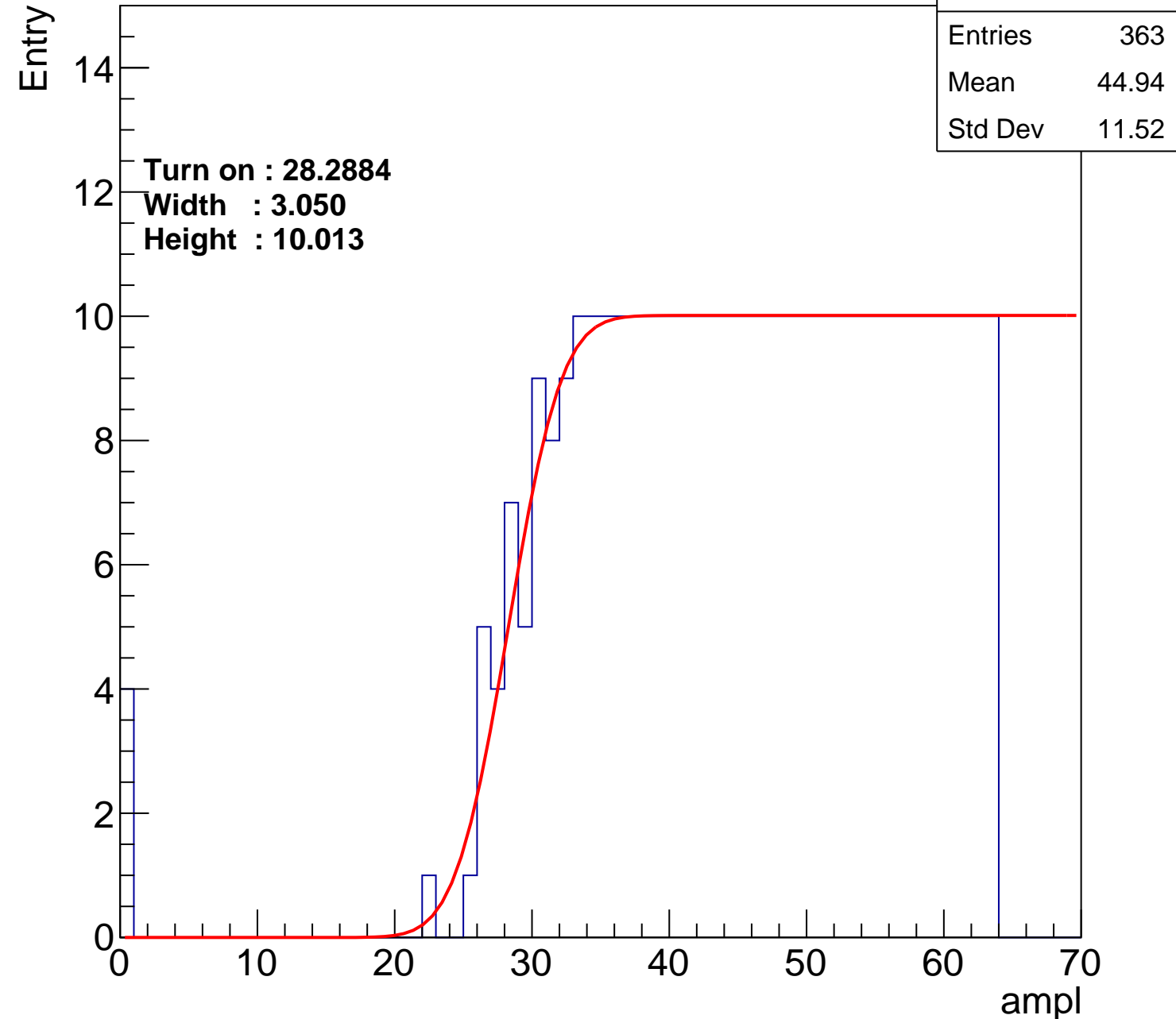
Width : 3.050

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch99

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.43
Std Dev	11.63

**Turn on : 27.0058**

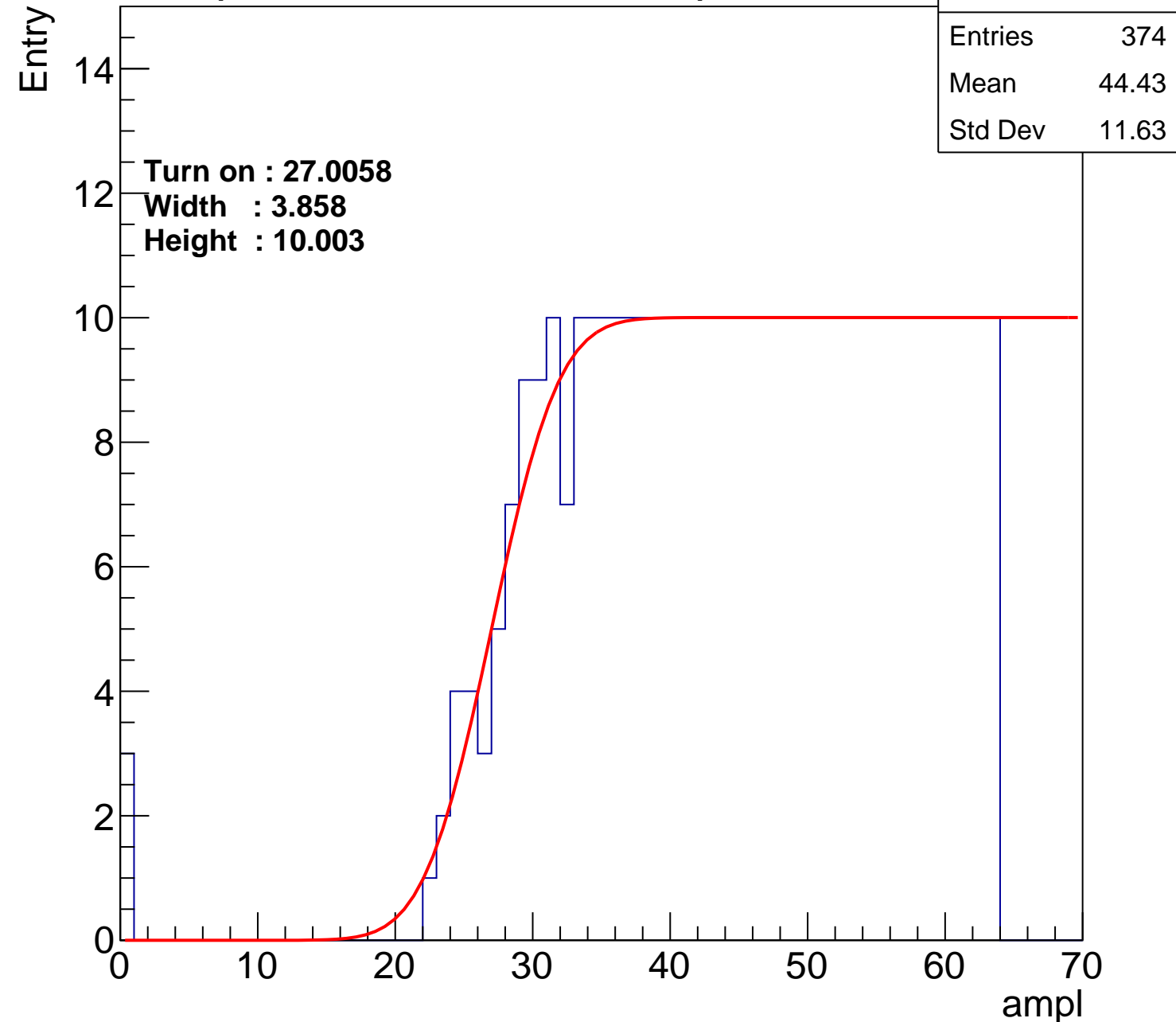
**Width : 3.858**

**Height : 10.003**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch100

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	384
Mean	43.87
Std Dev	12.14

**Turn on : 26.2937**

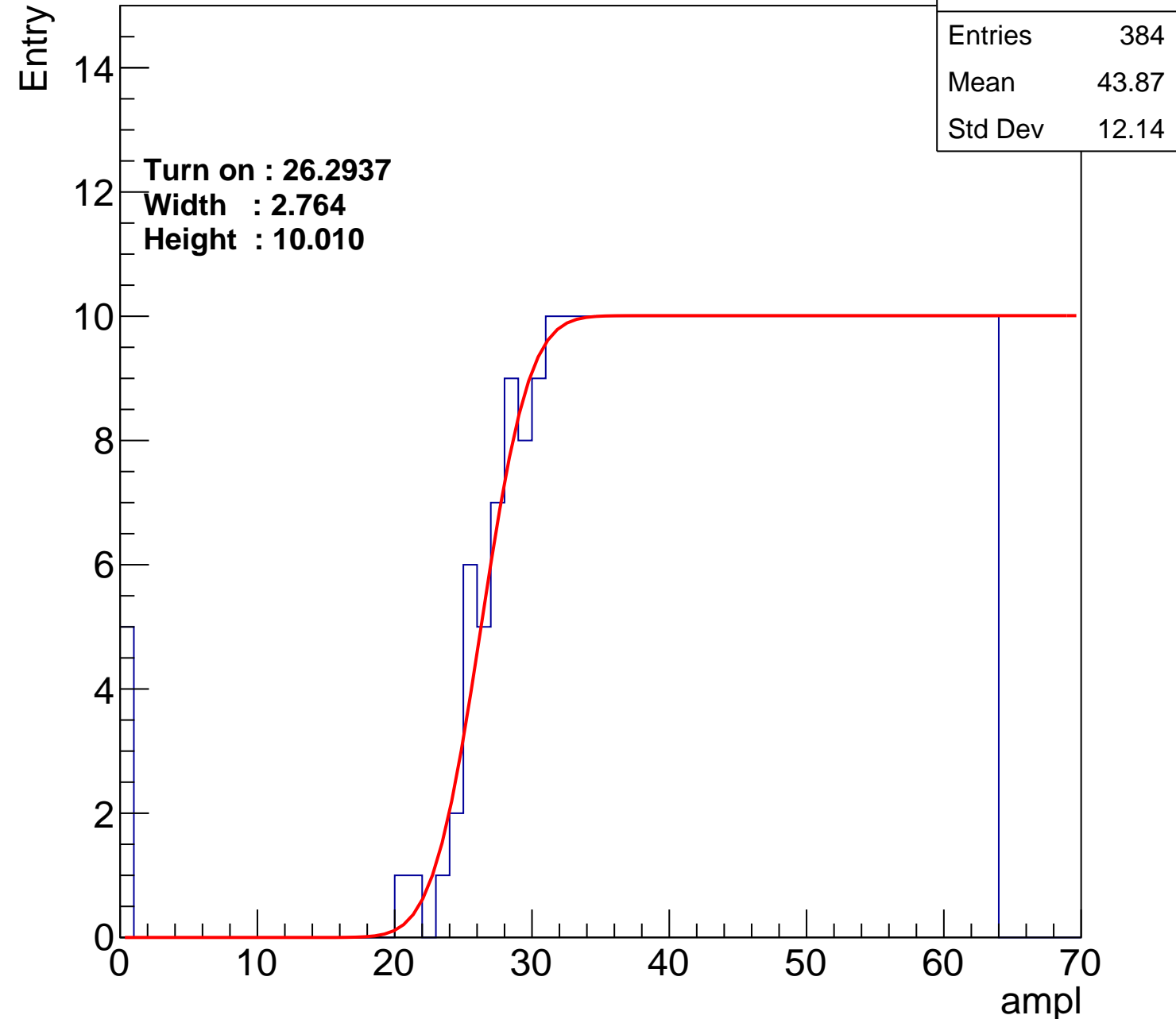
**Width : 2.764**

**Height : 10.010**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch101

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.42
Std Dev	11.47

Turn on : 27.0045

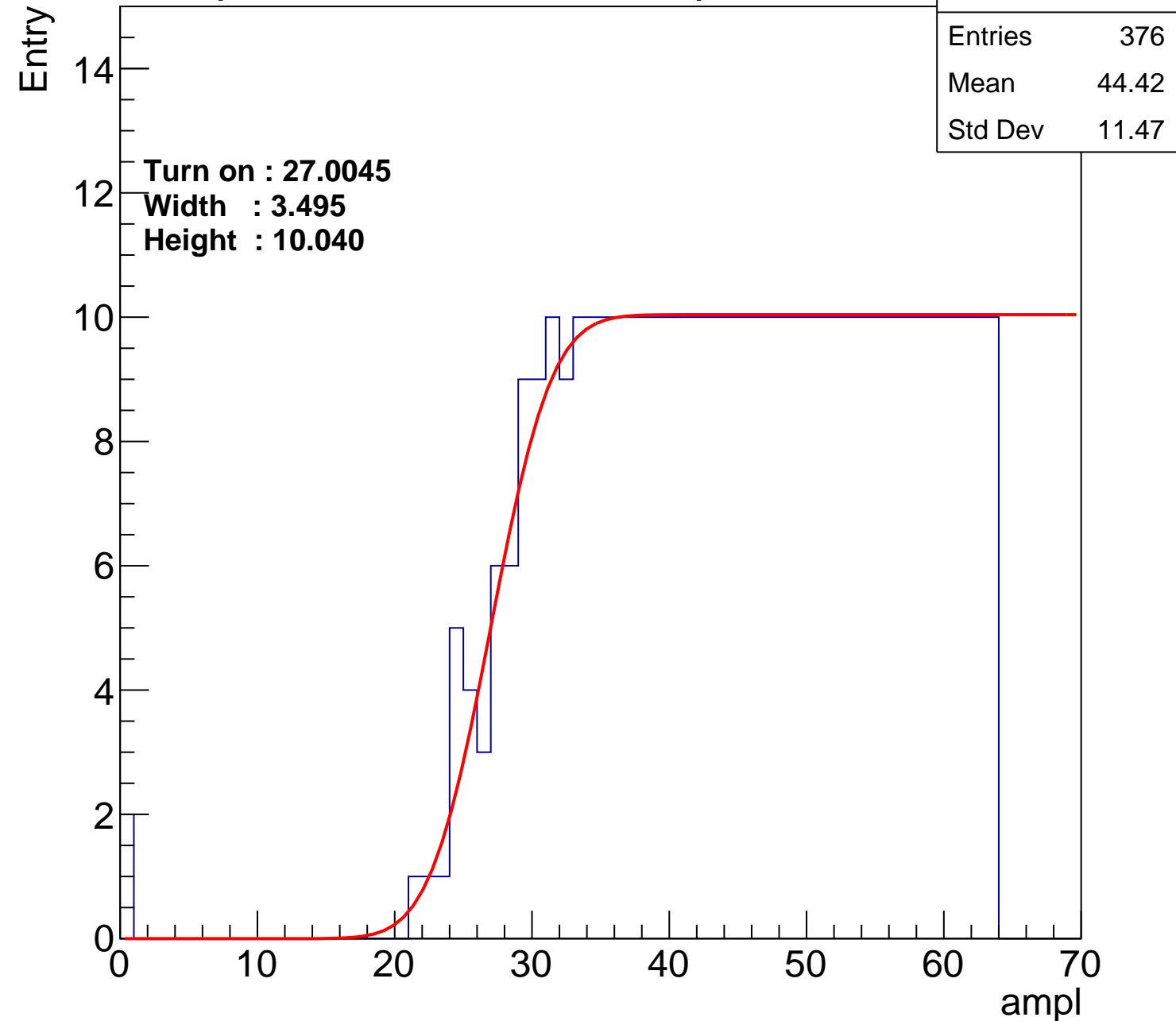
Width : 3.495

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch102

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	400
Mean	43.33
Std Dev	11.96

Turn on : 24.5217

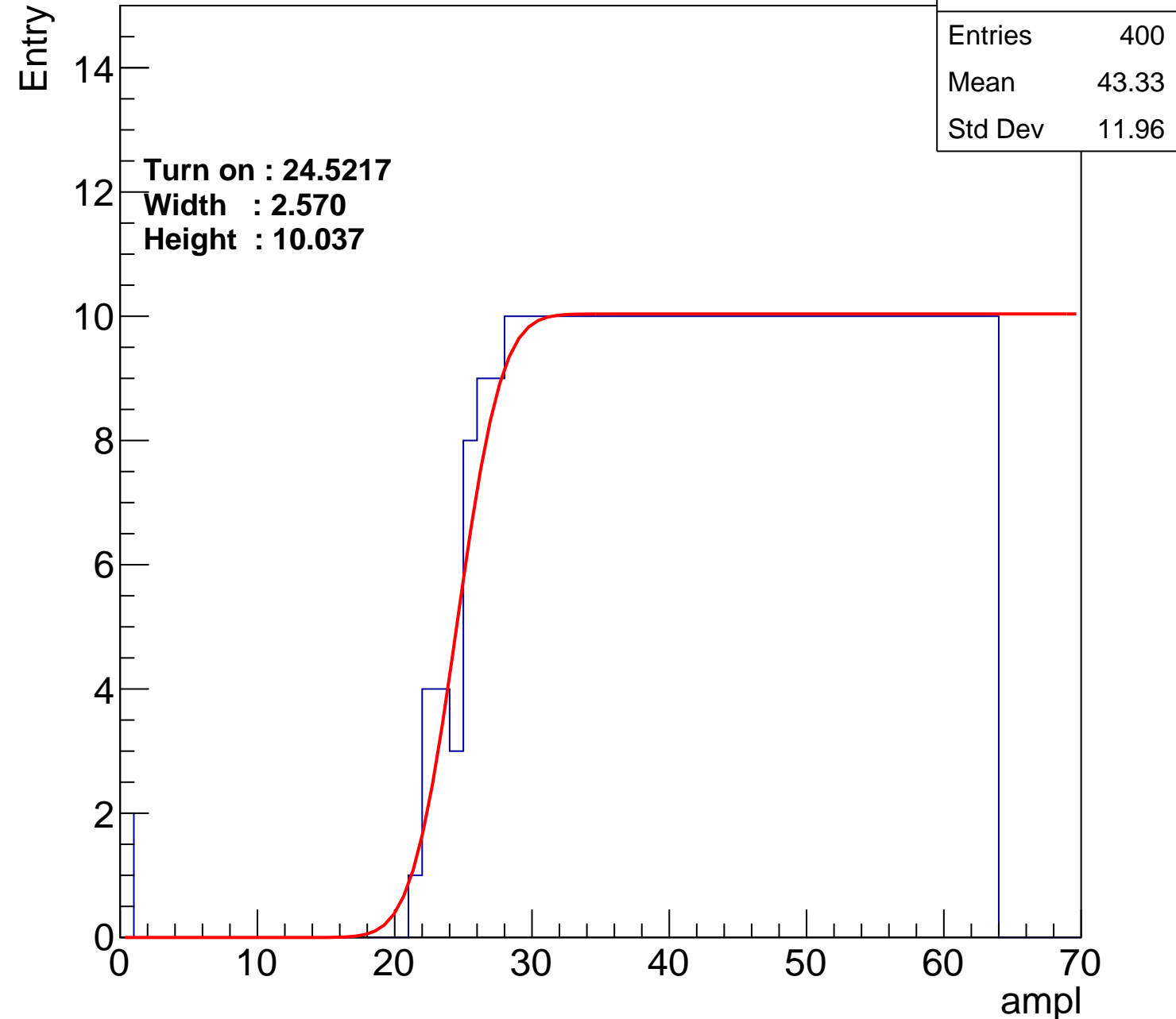
Width : 2.570

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch103

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

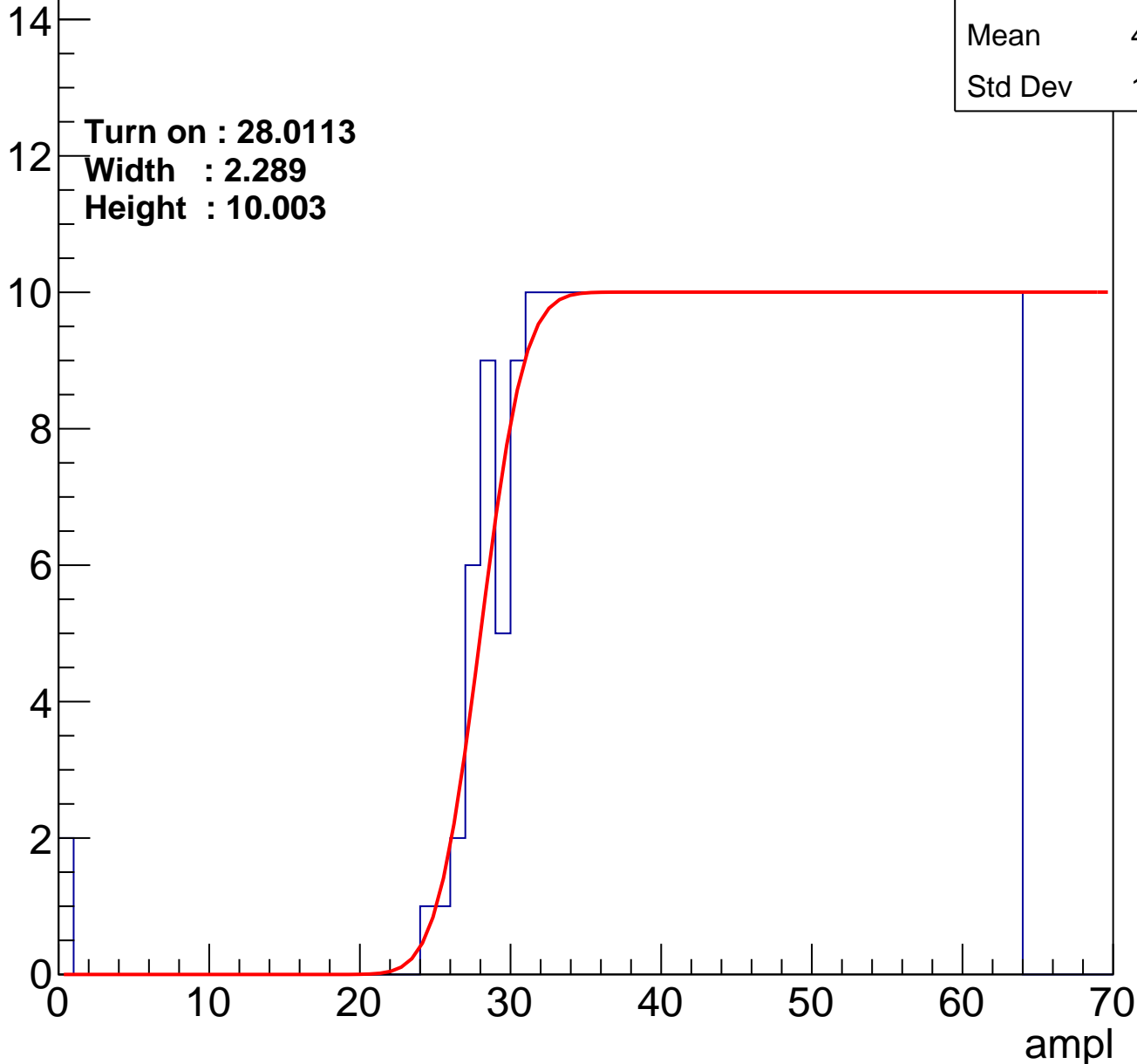
# Entry

Entries	365
Mean	45.04
Std Dev	11.07

**Turn on : 28.0113**

**Width : 2.289**

**Height : 10.003**



# B0L002S, U23-ch104

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	355
Mean	45.35
Std Dev	11.2

**Turn on : 28.7222**

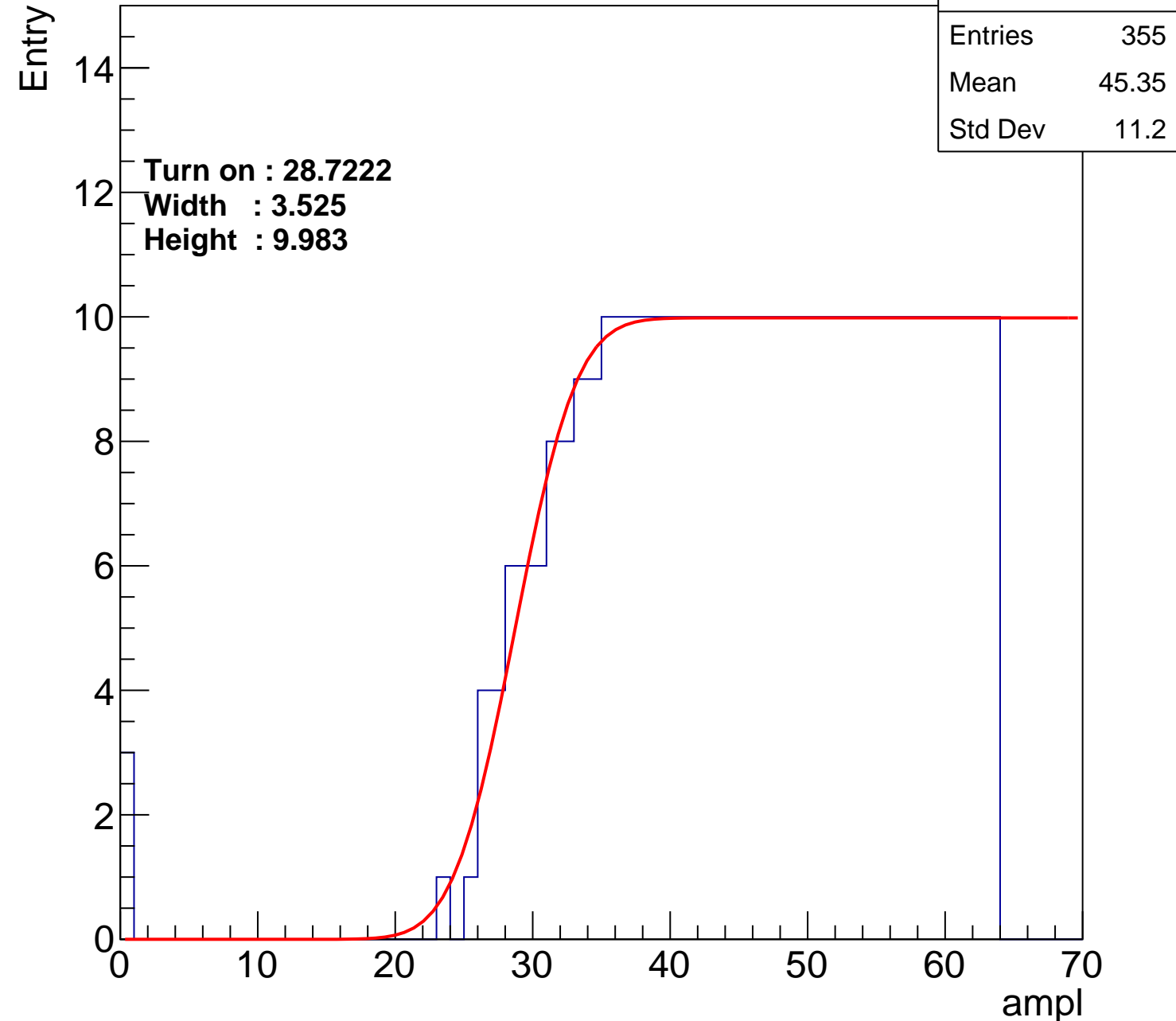
**Width : 3.525**

**Height : 9.983**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch105

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.77
Std Dev	11.7

Turn on : 28.0362

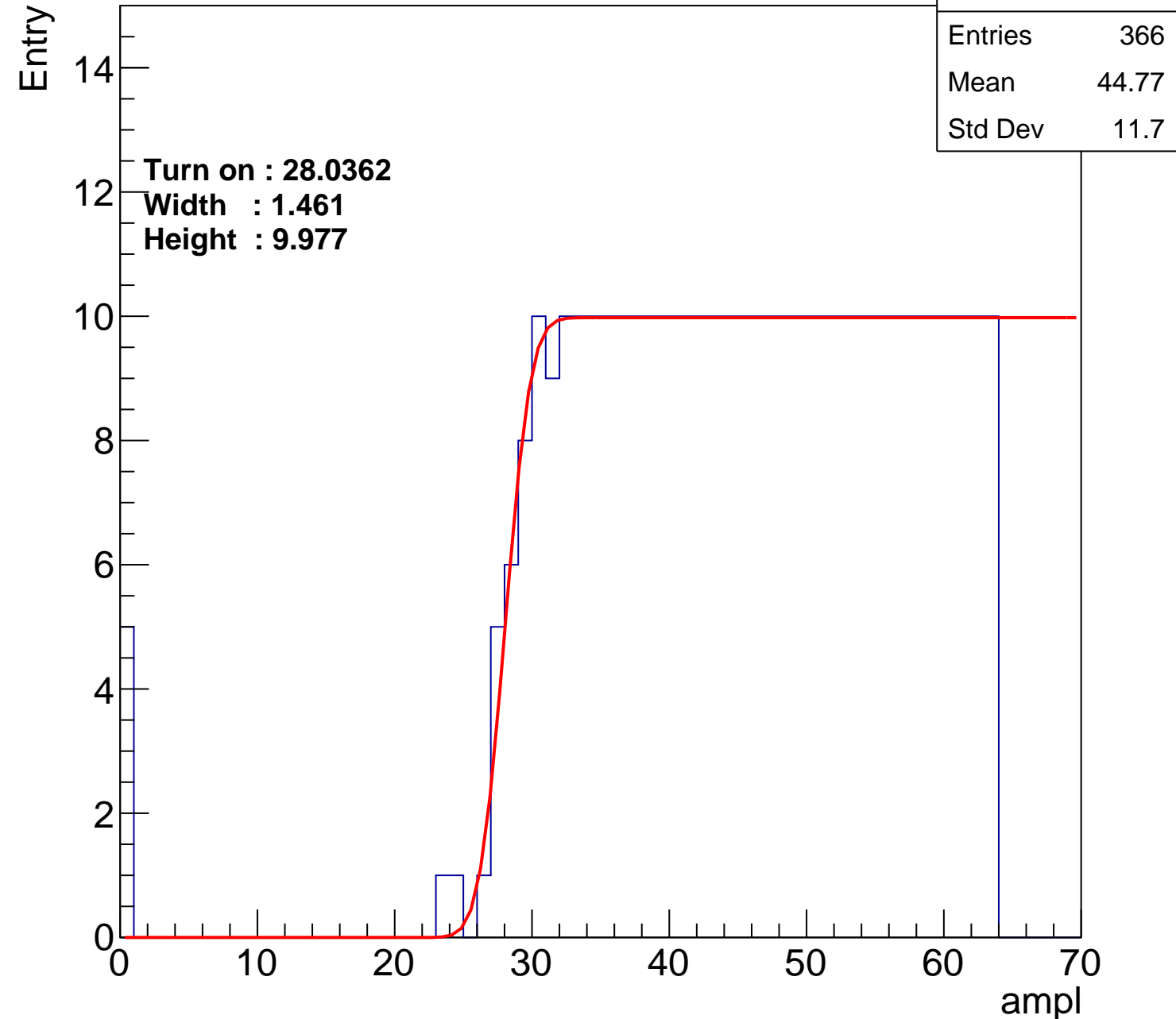
Width : 1.461

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch106

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 27.7071

Width : 3.595

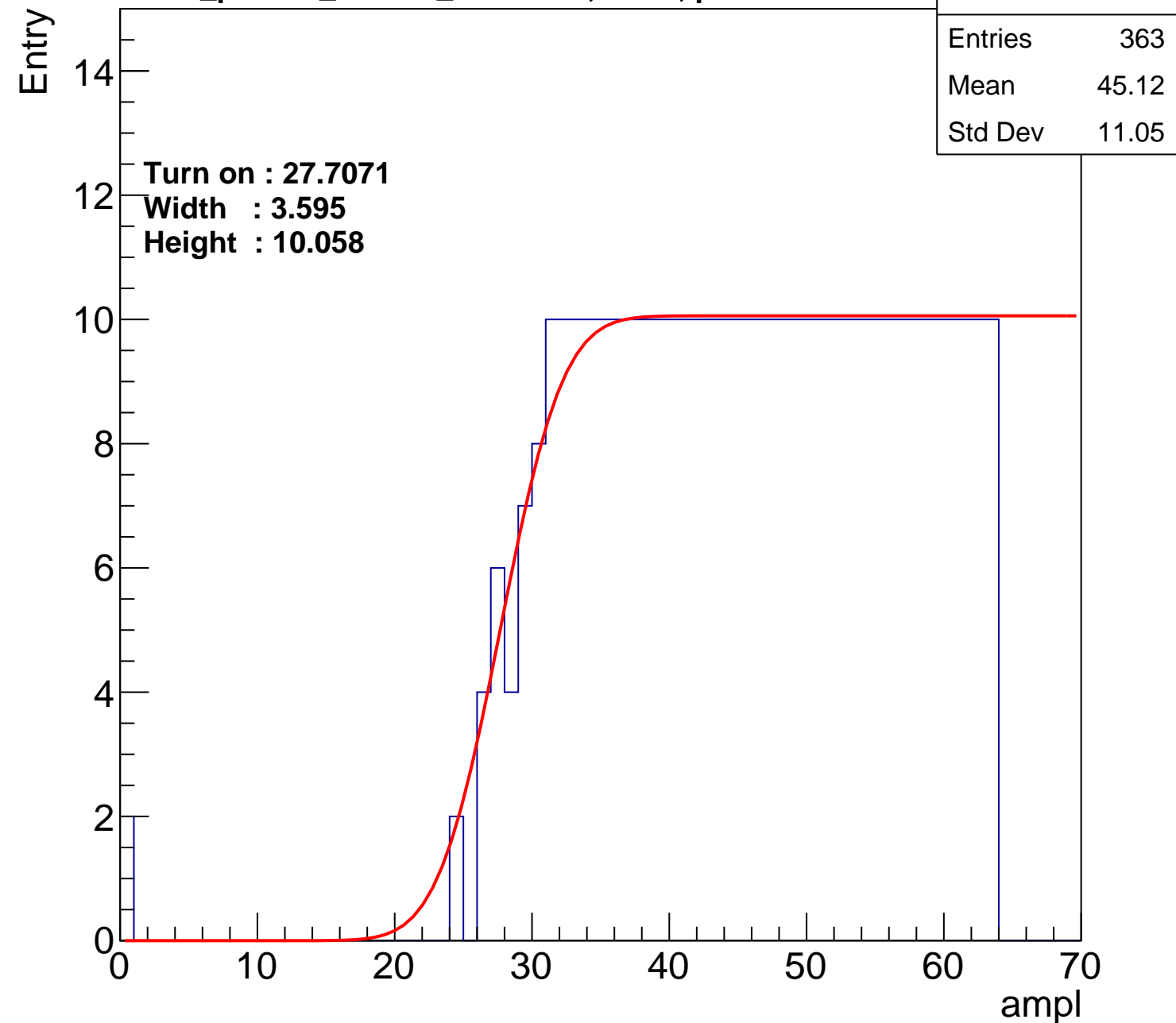
Height : 10.058

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch107

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.63
Std Dev	10.83

Turn on : 29.0271

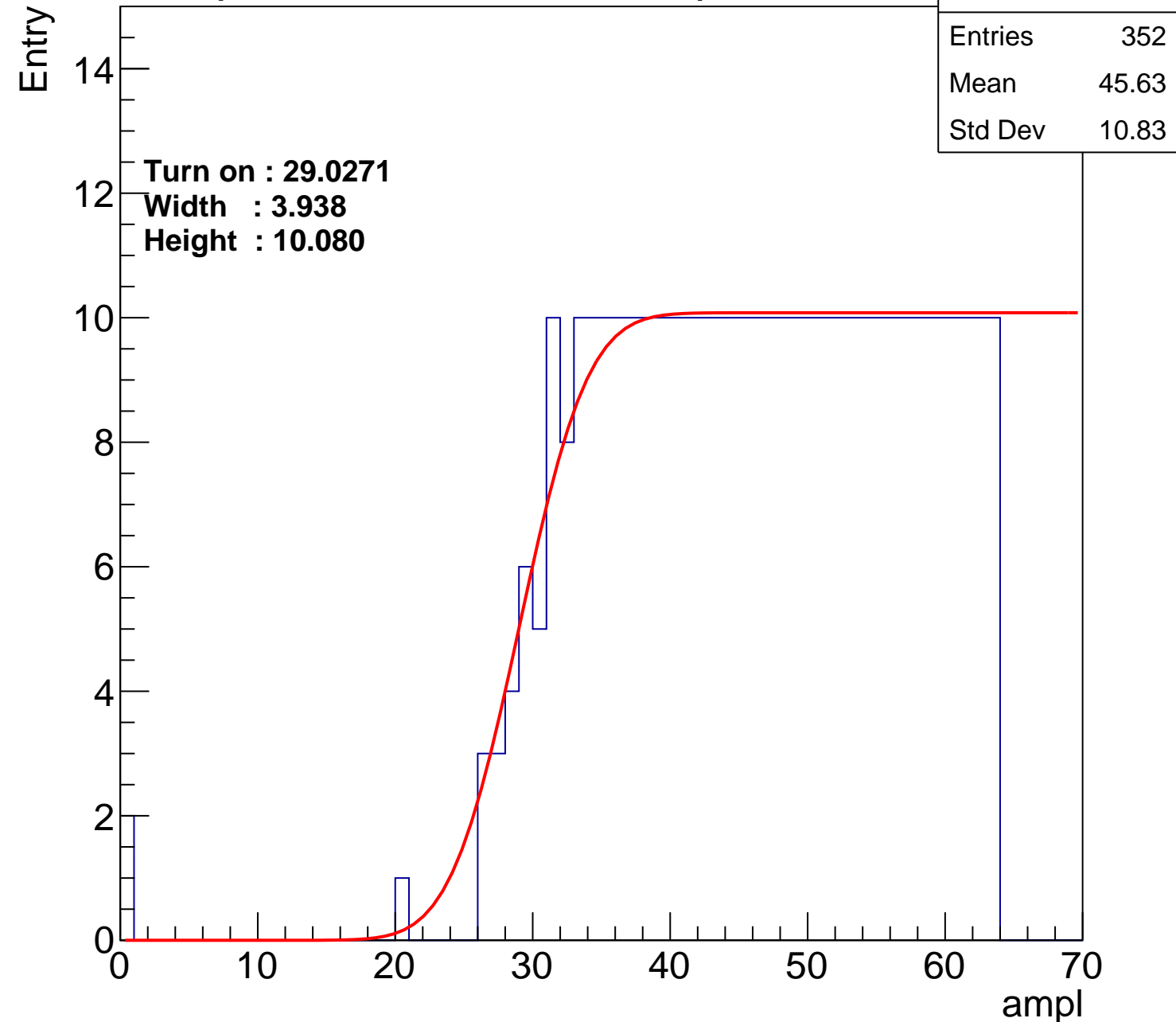
Width : 3.938

Height : 10.080

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch108

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	346
Mean	45.66
Std Dev	11.41

Turn on : 30.9916

Width : 2.123

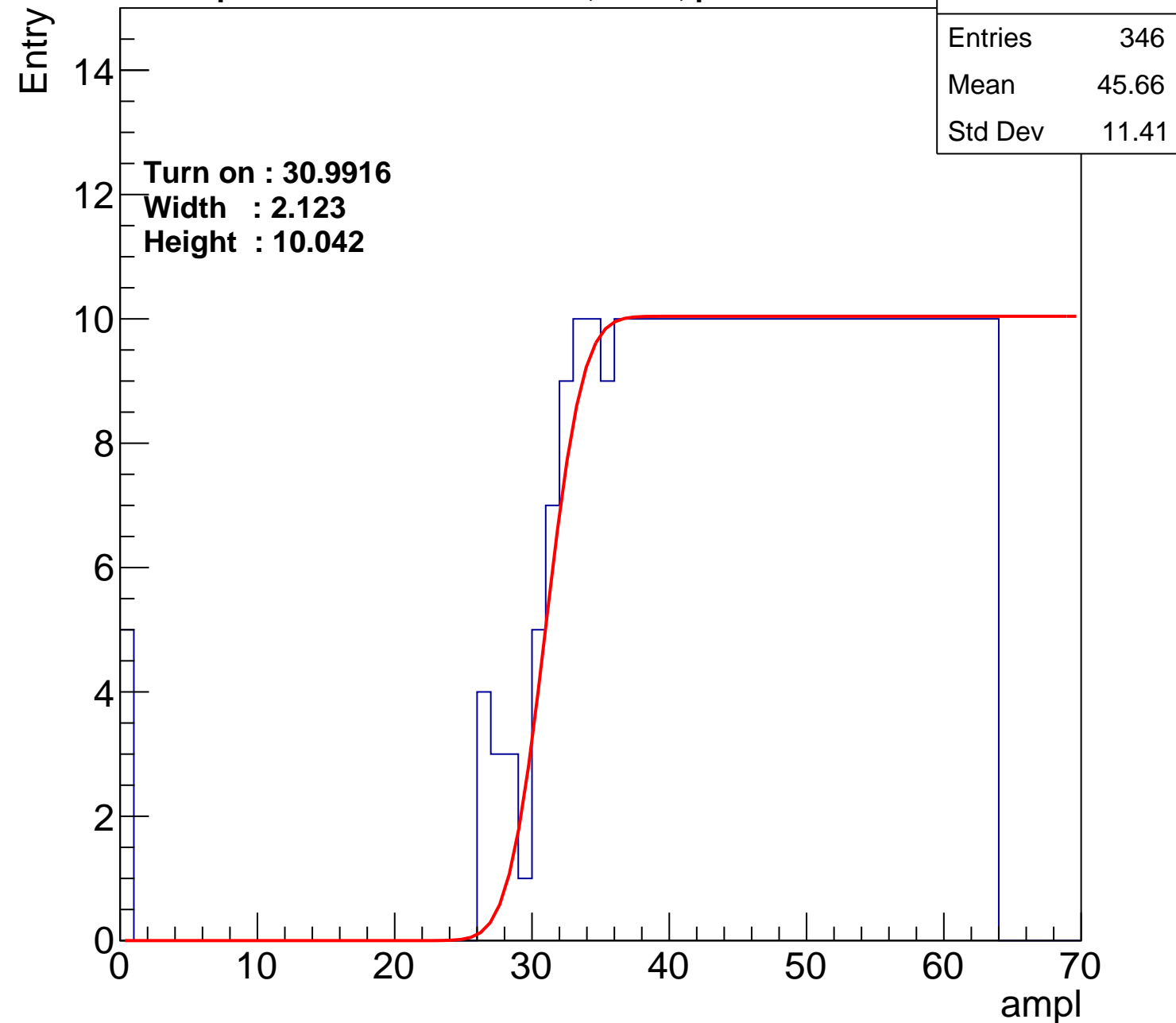
Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L002S, U23-ch109

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	356
Mean	45.46
Std Dev	10.87

**Turn on : 28.8420**

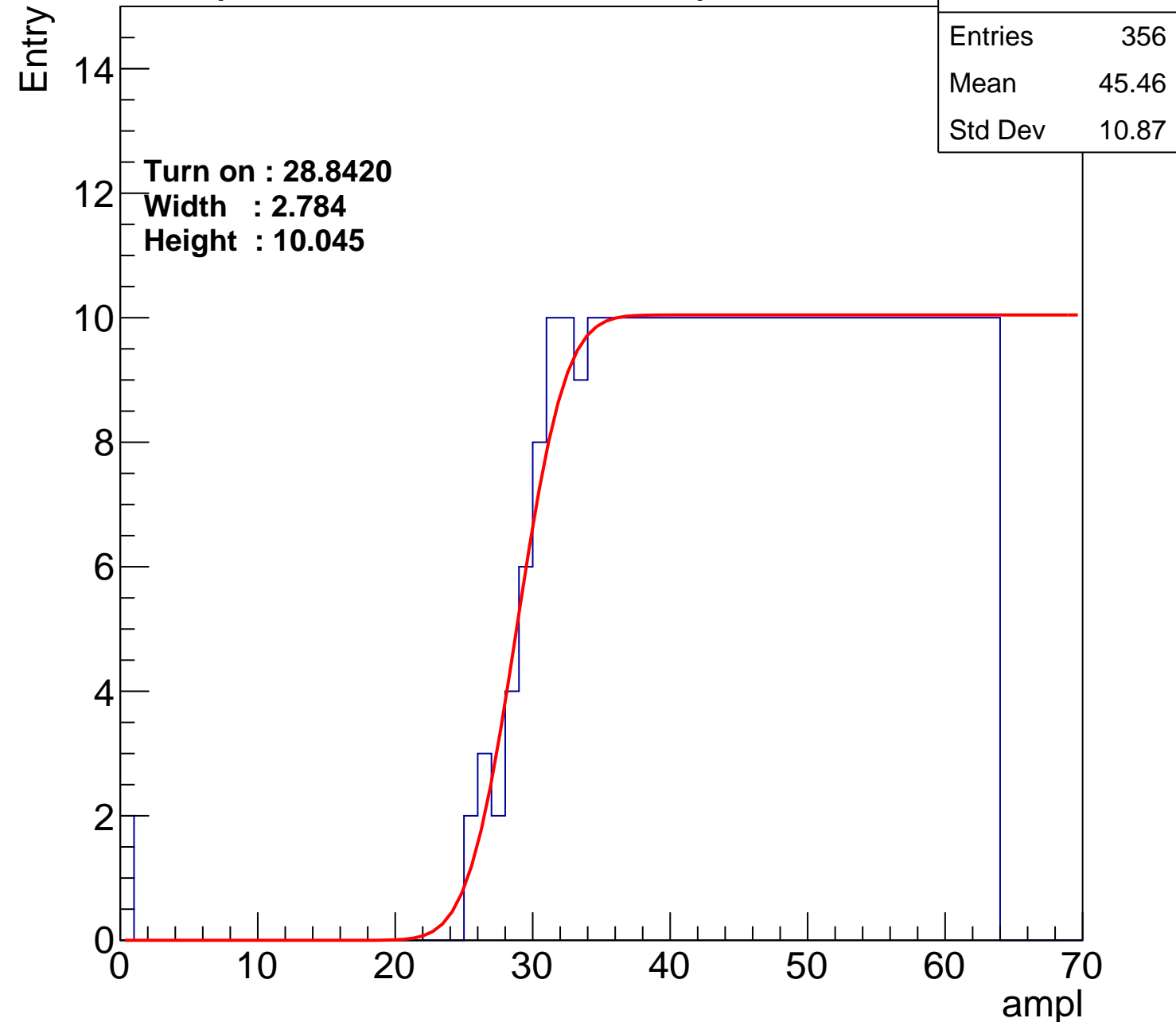
**Width : 2.784**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch110

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.46
Std Dev	11.73

Turn on : 26.9627

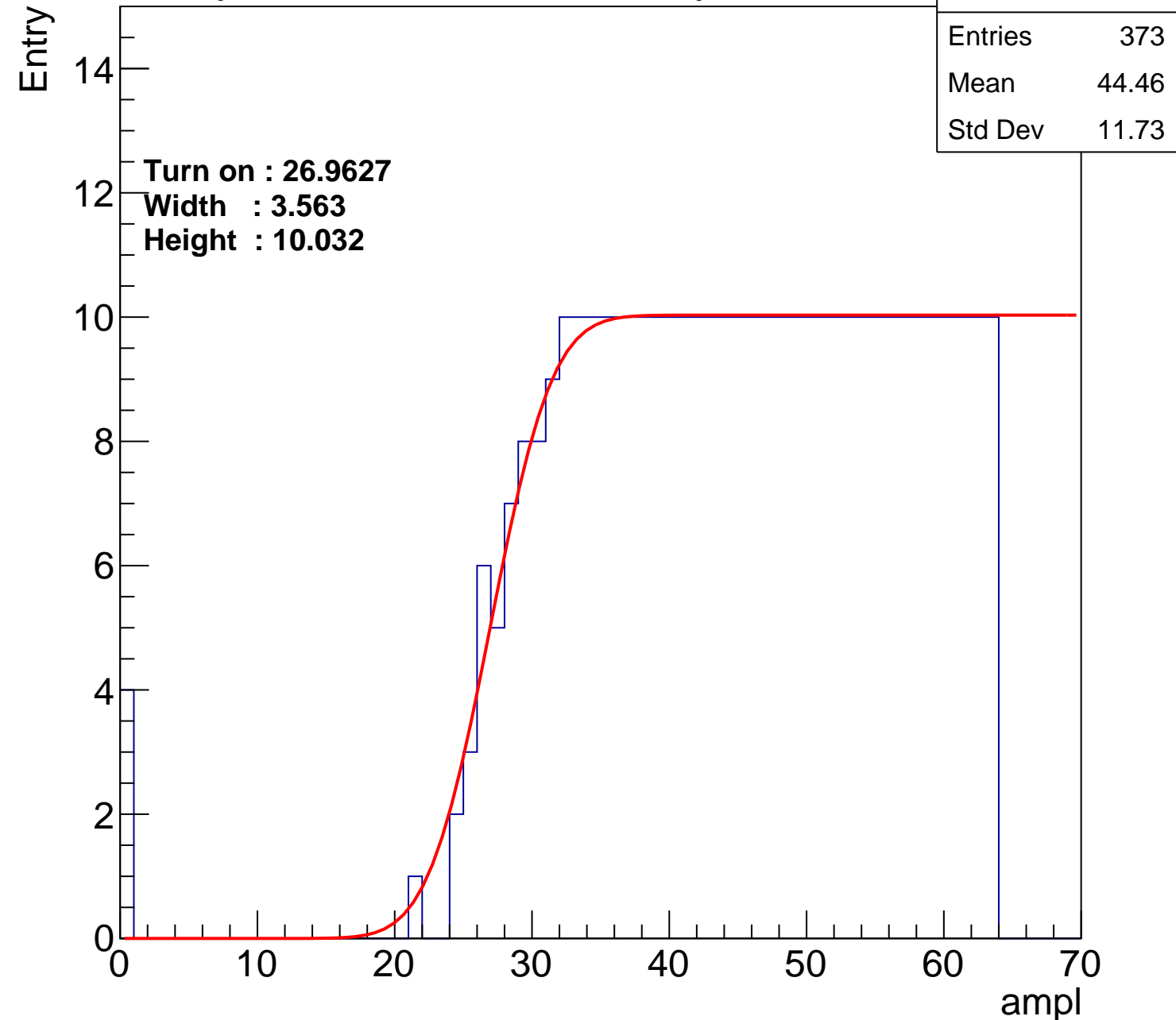
Width : 3.563

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch111

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.07
Std Dev	11.08

Turn on : 28.1724

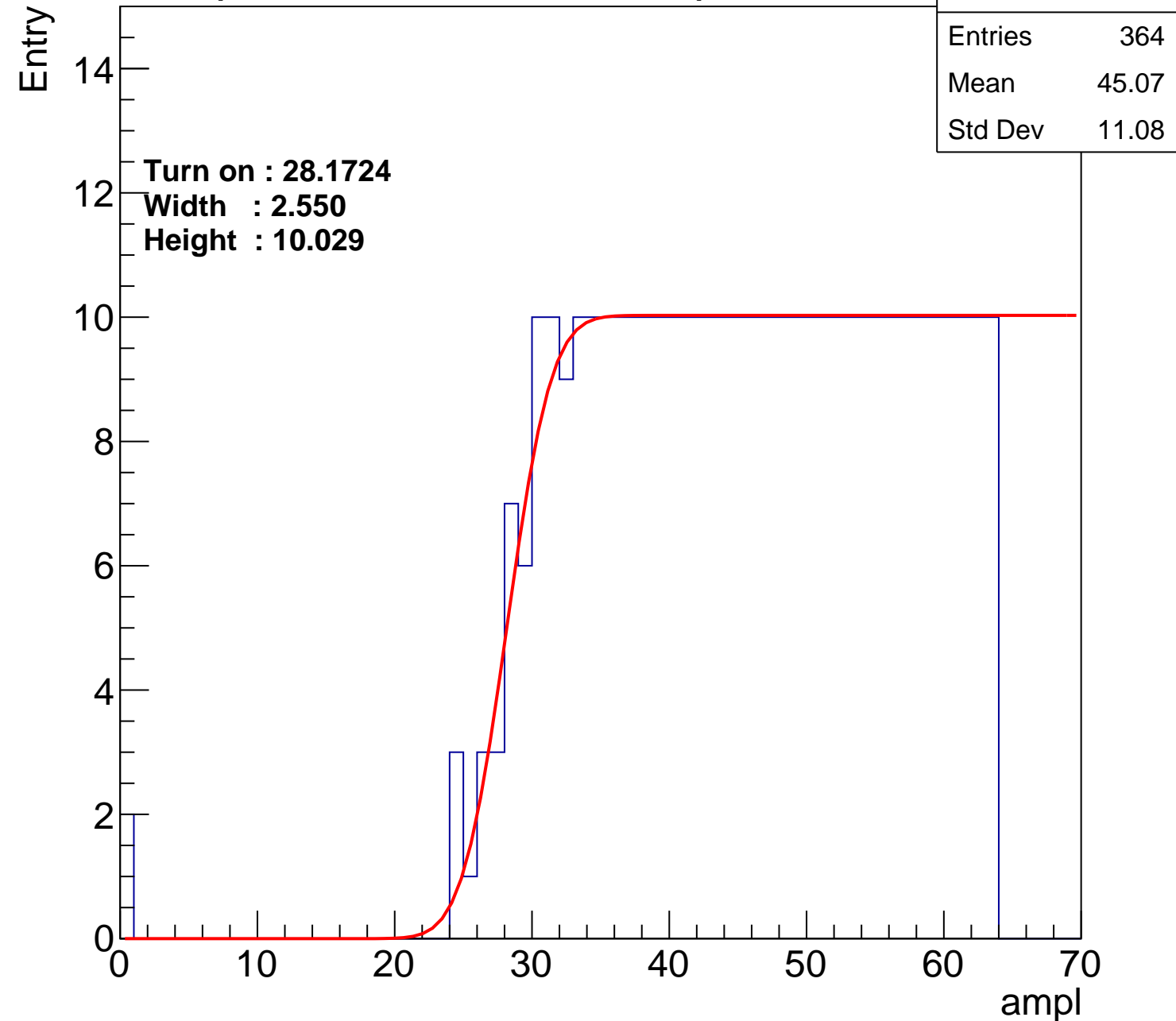
Width : 2.550

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch112

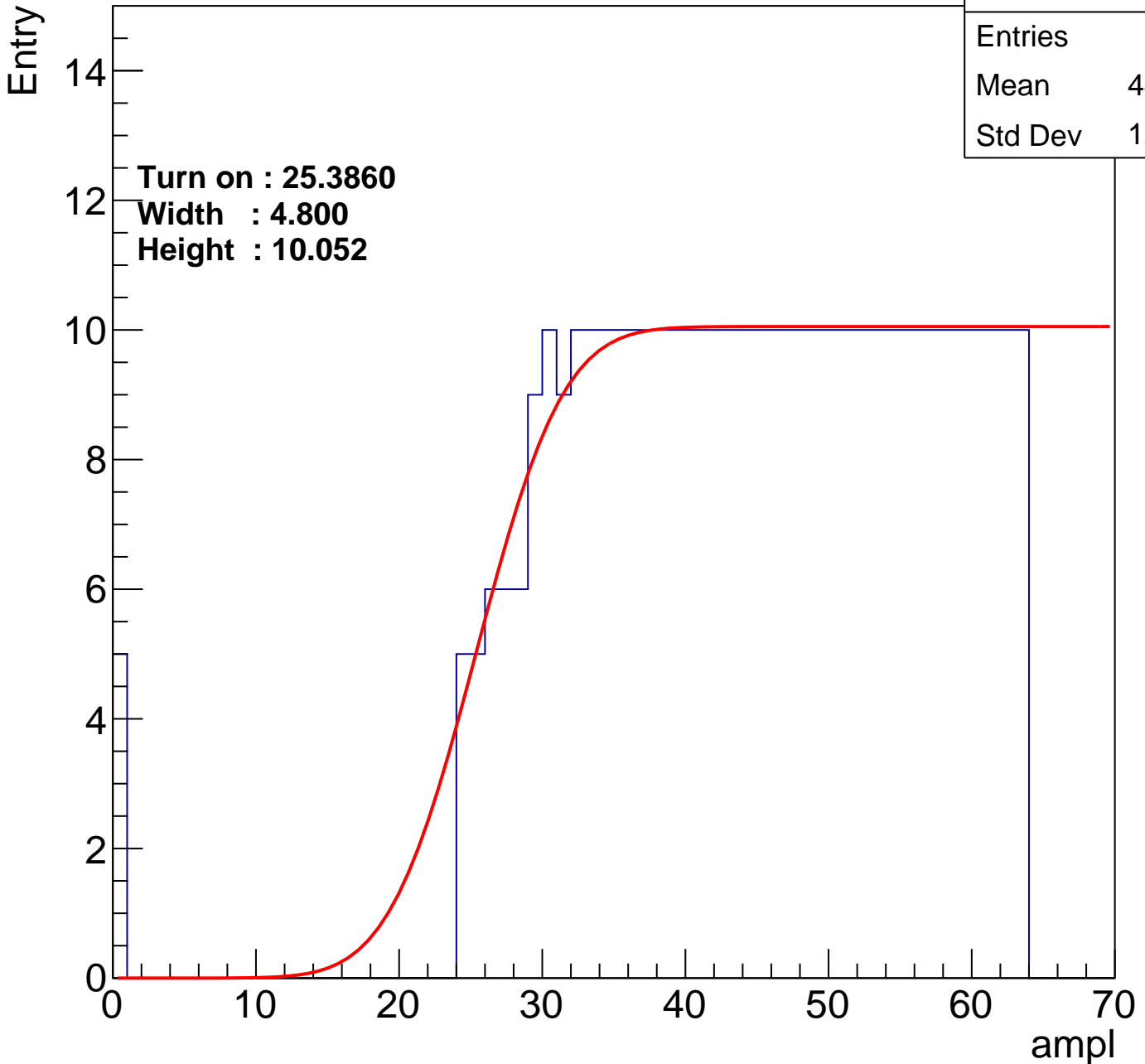
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	381
Mean	44.02
Std Dev	12.06

Turn on : 25.3860

Width : 4.800

Height : 10.052



# B0L002S, U23-ch113

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	345
Mean	45.99
Std Dev	10.62

**Turn on : 29.8944**

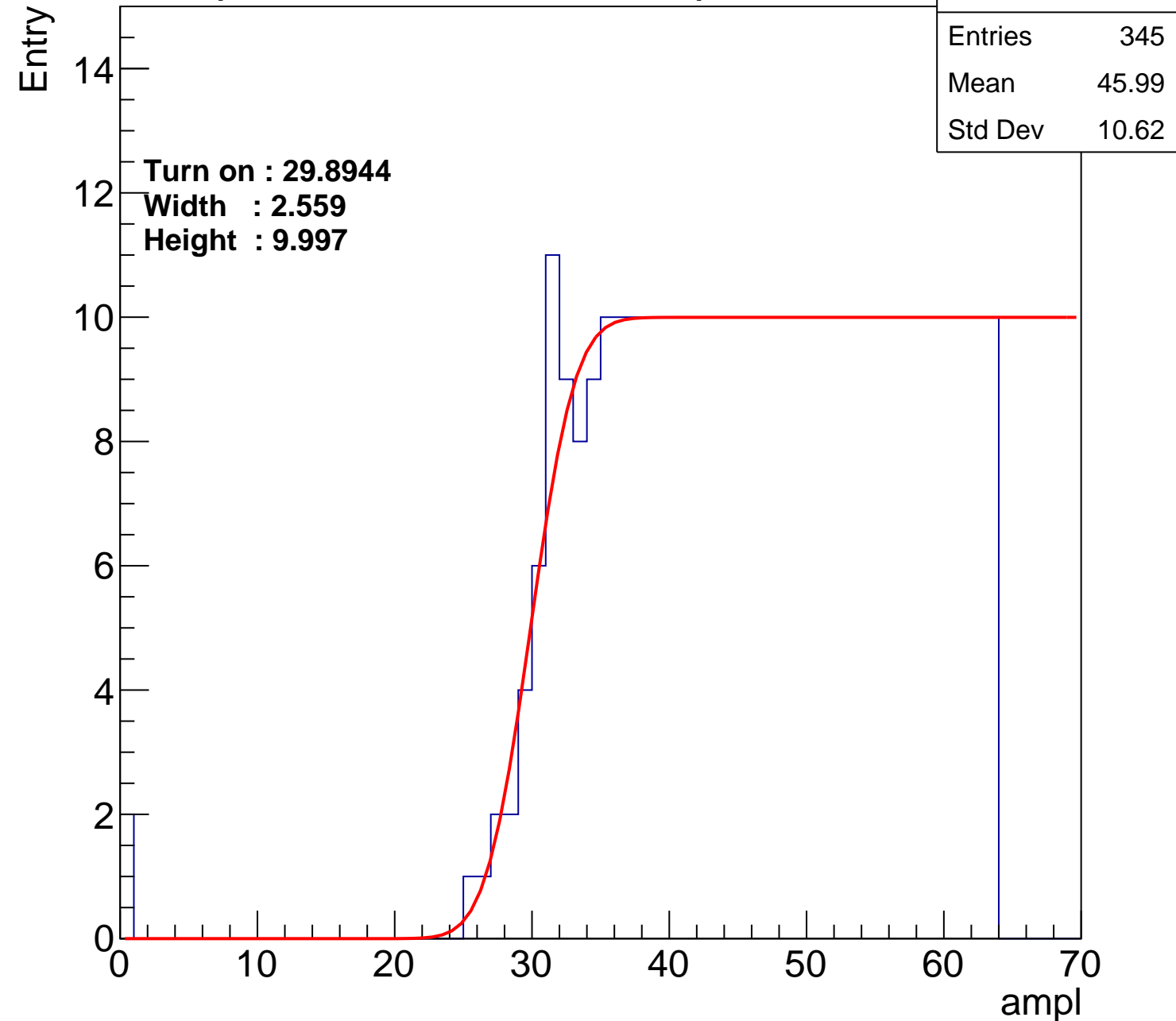
**Width : 2.559**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch114

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 27.4178

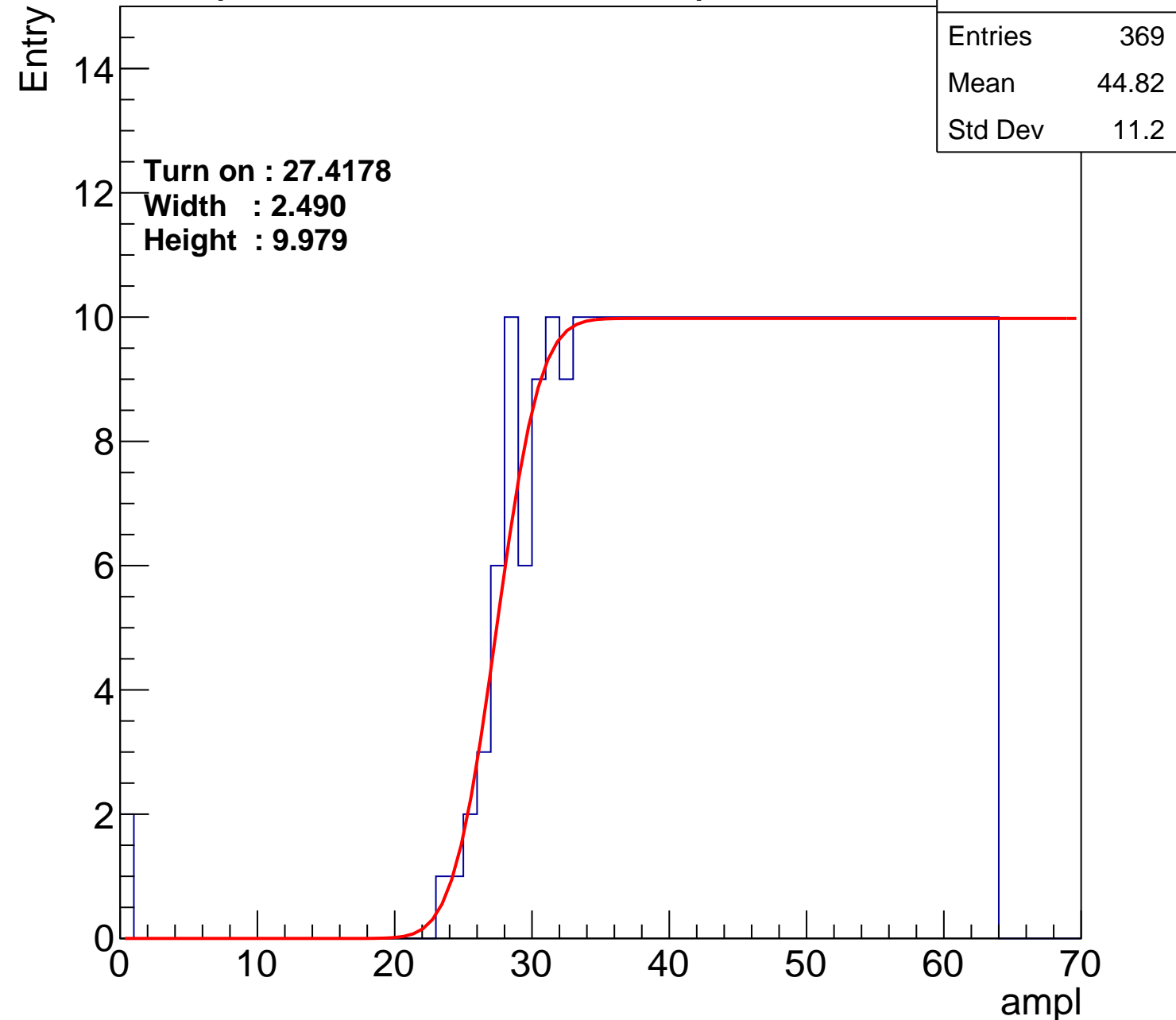
Width : 2.490

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch115

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	355
Mean	45.39
Std Dev	11.14

Turn on : 29.2361

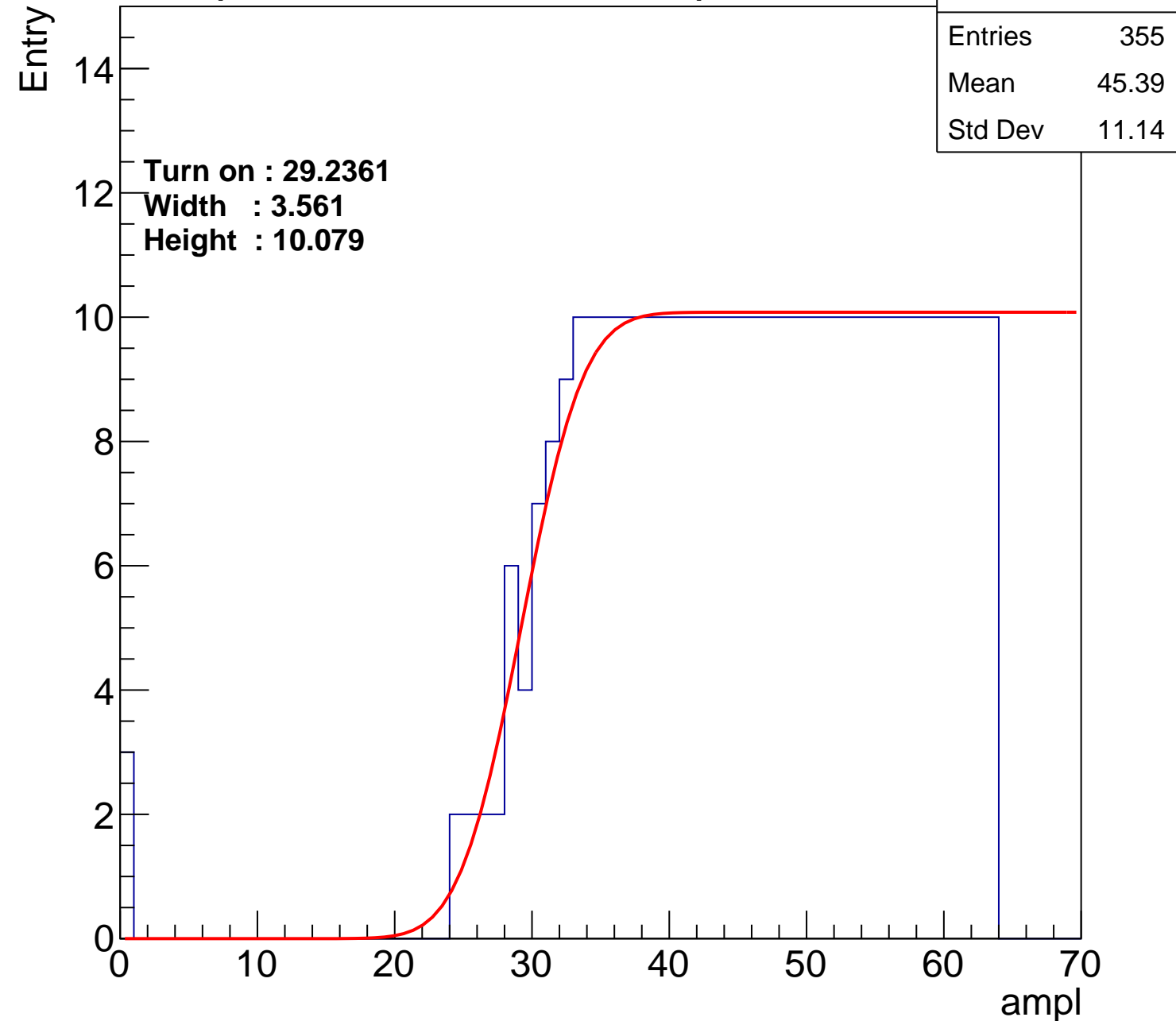
Width : 3.561

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch116

calib\_packv5\_042523\_0143.root, FC#8, port C1

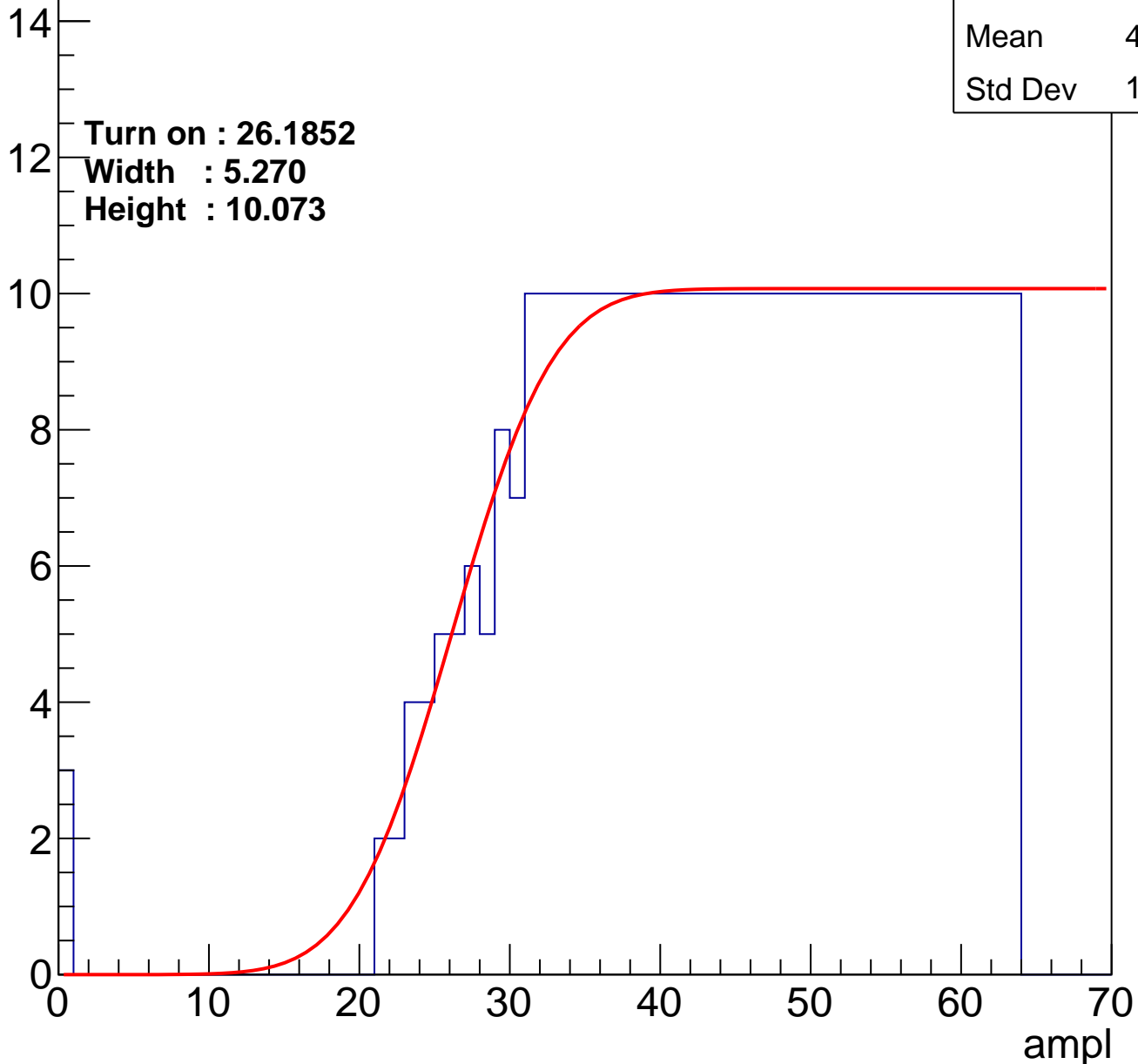
Entries	381
Mean	44.05
Std Dev	11.87

Turn on : 26.1852

Width : 5.270

Height : 10.073

Entry



# B0L002S, U23-ch117

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.76
Std Dev	11.63

Turn on : 28.0325

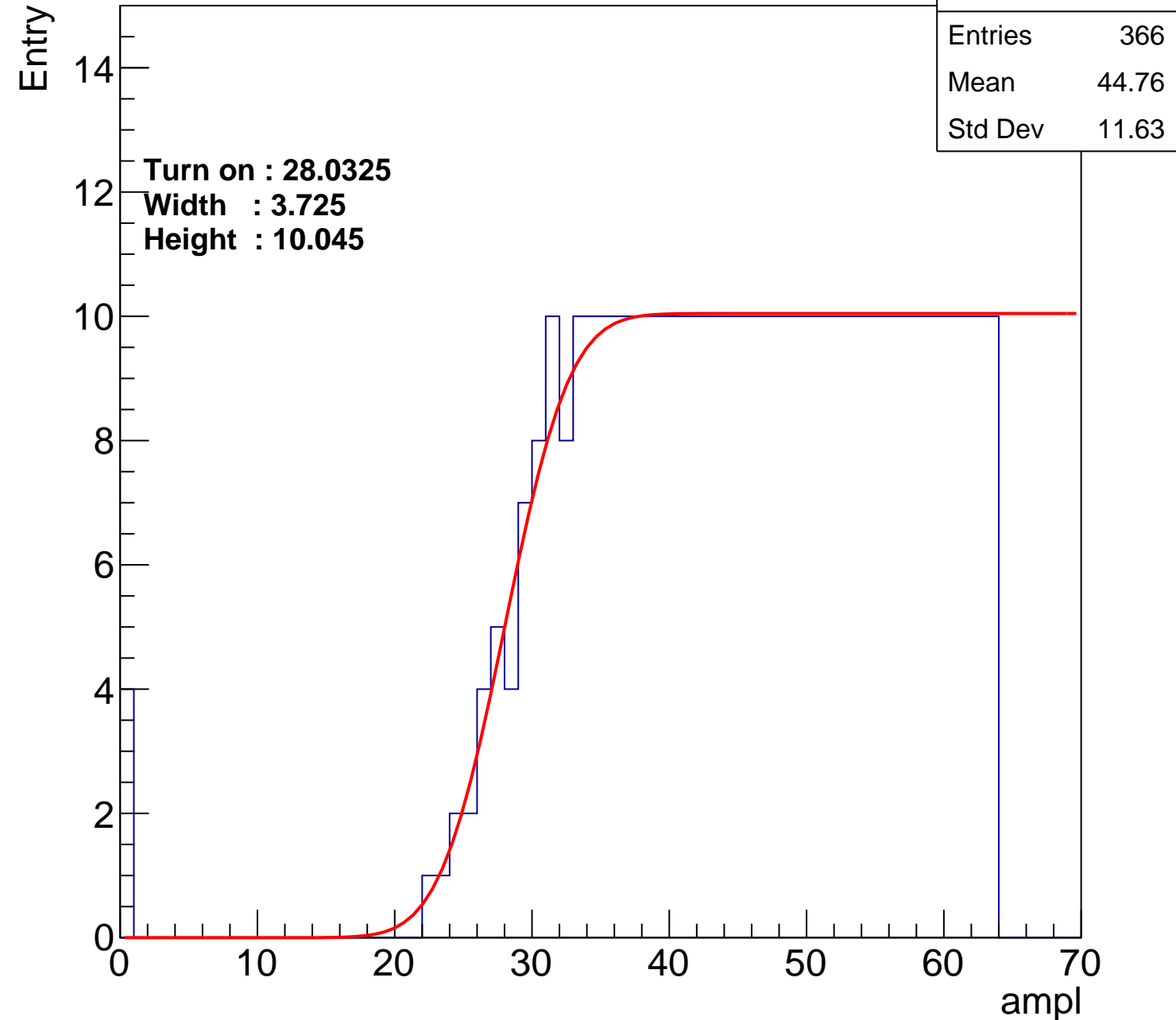
Width : 3.725

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch118

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.82

**Turn on : 28.0965**

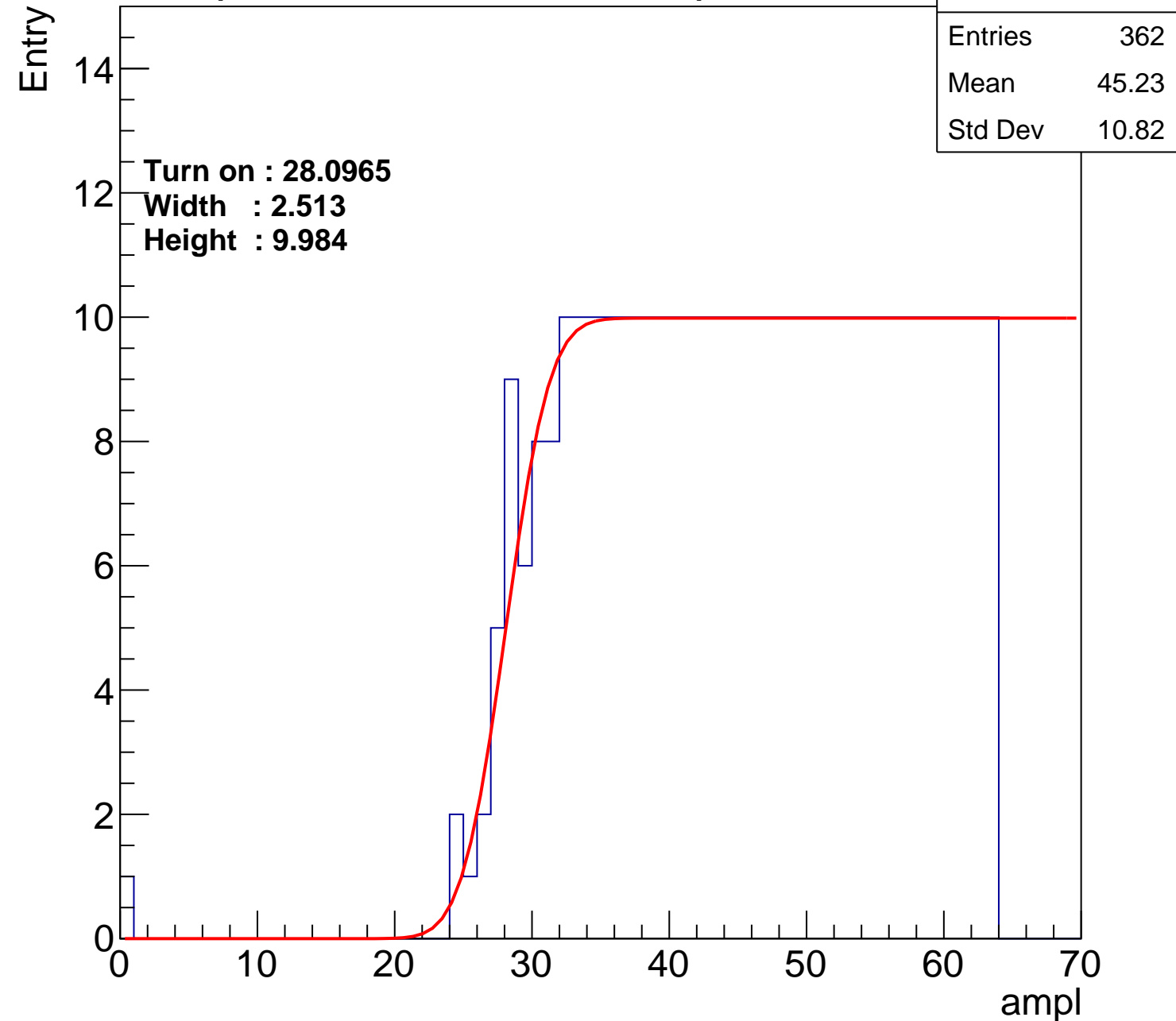
**Width : 2.513**

**Height : 9.984**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch119

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.61
Std Dev	11.51

Turn on : 27.4729

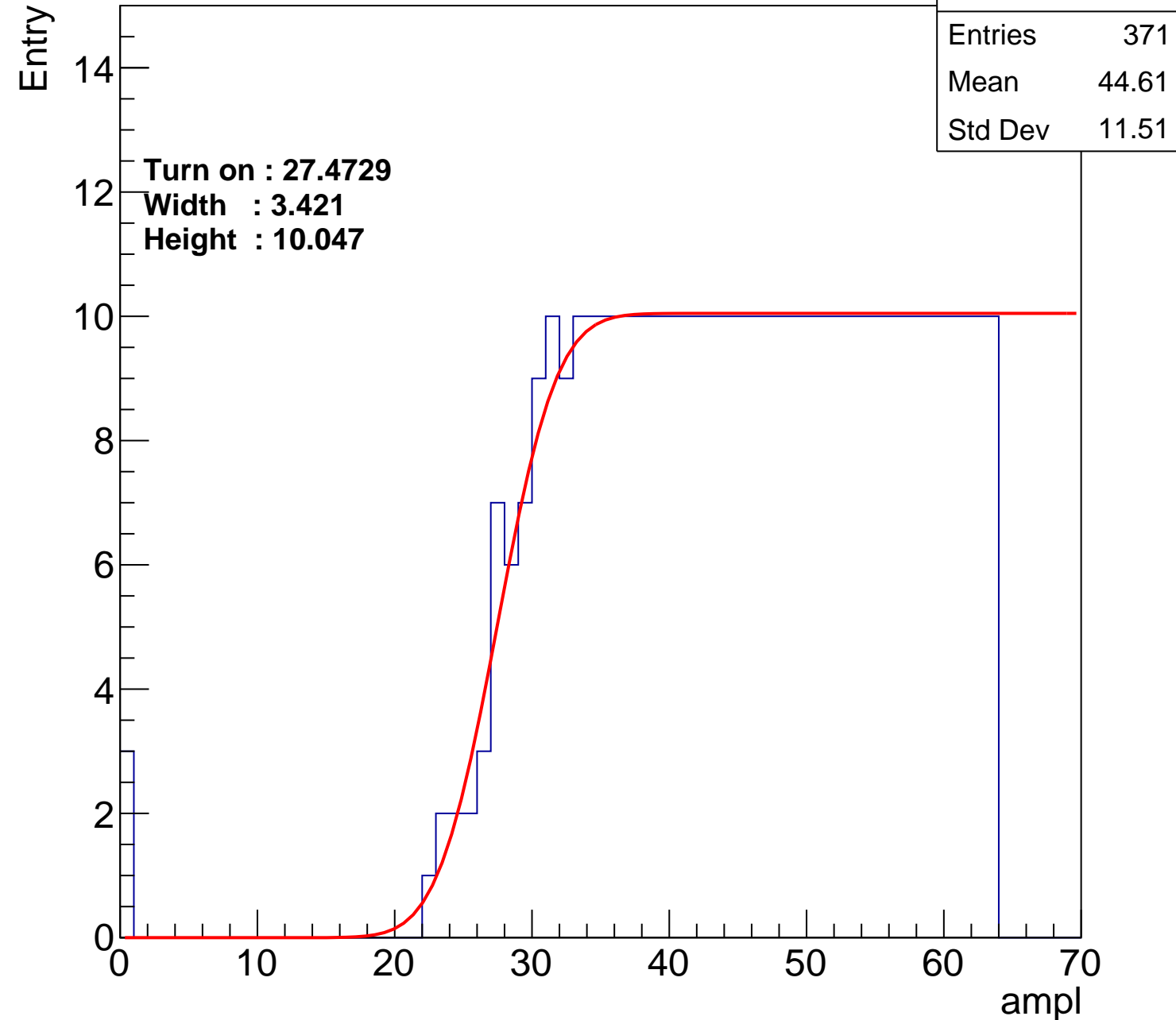
Width : 3.421

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch120

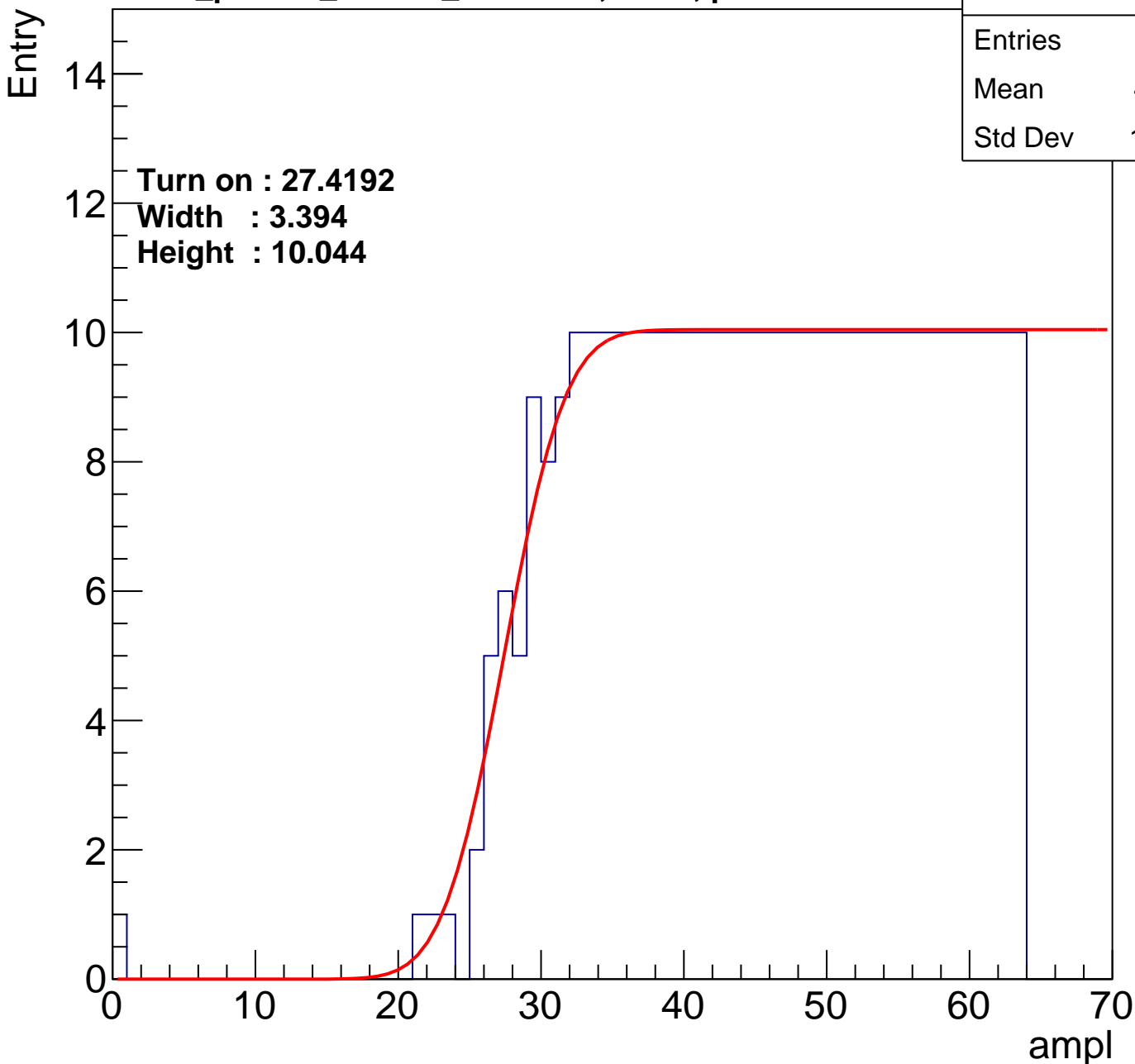
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	368
Mean	44.91
Std Dev	11.03

**Turn on : 27.4192**

**Width : 3.394**

**Height : 10.044**



# B0L002S, U23-ch121

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.08
Std Dev	11.41

**Turn on : 28.5917**

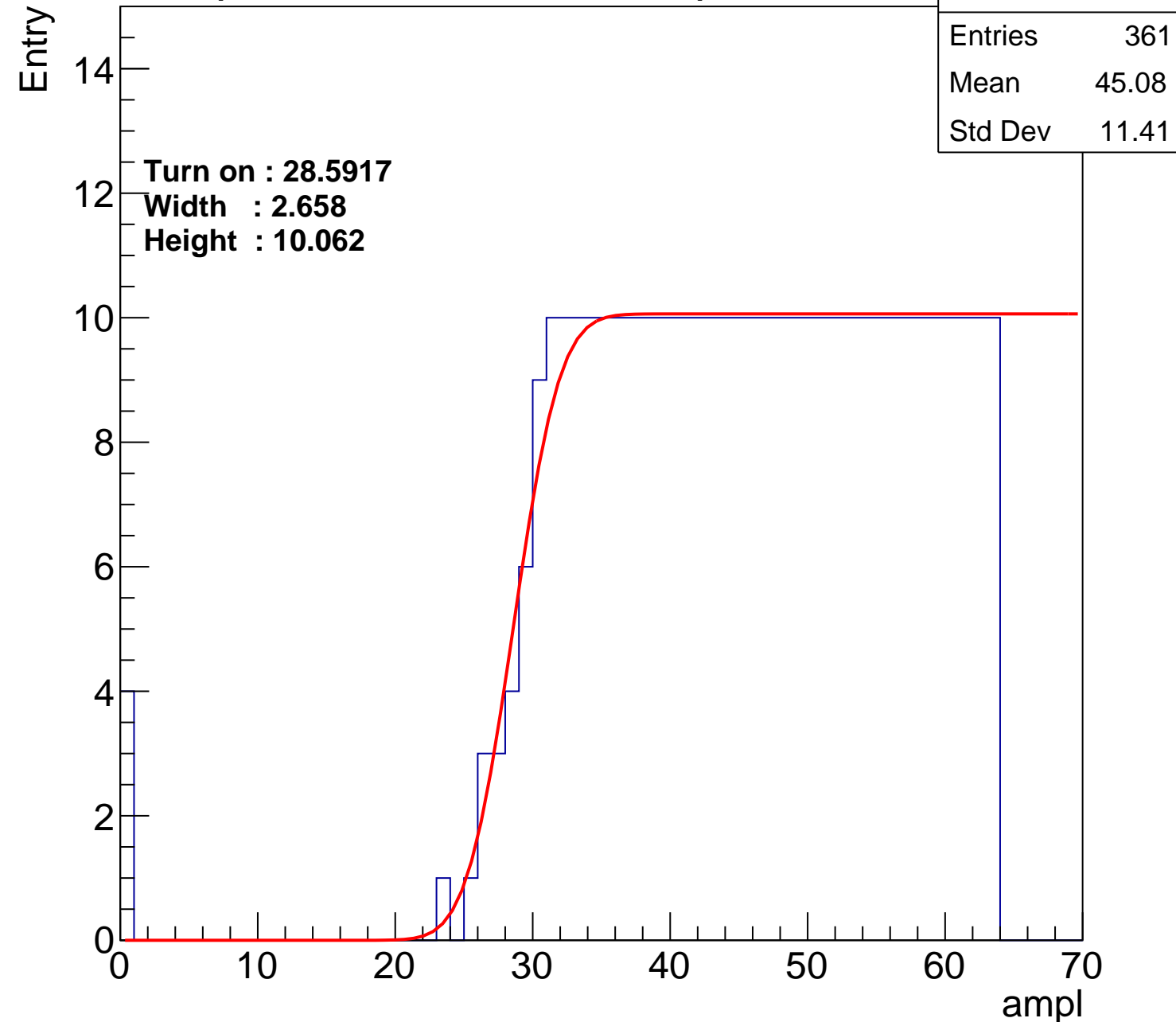
**Width : 2.658**

**Height : 10.062**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch122

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.2
Std Dev	11.03

Turn on : 28.6163

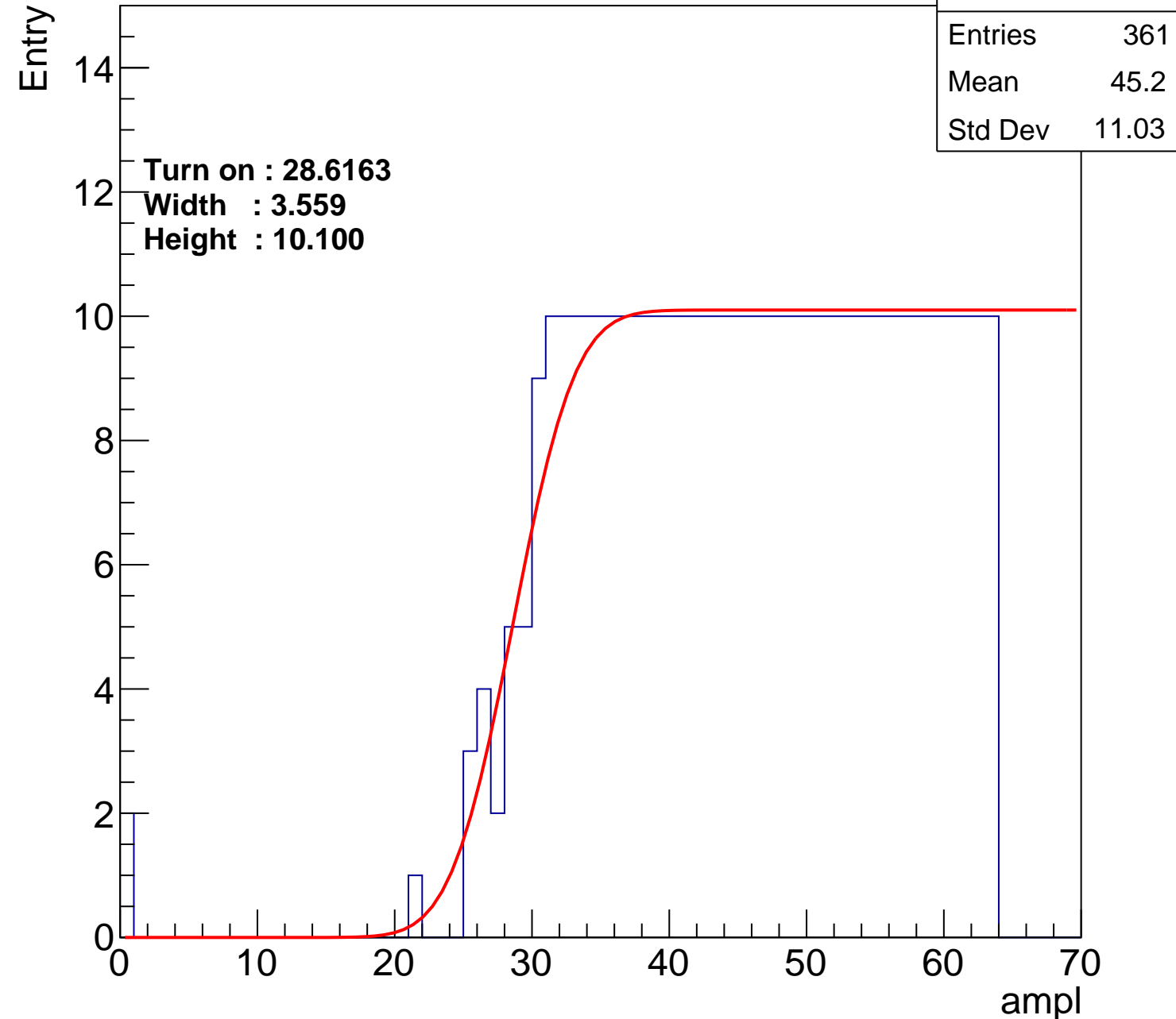
Width : 3.559

Height : 10.100

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch123

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.74
Std Dev	10.59

**Turn on : 29.3386**

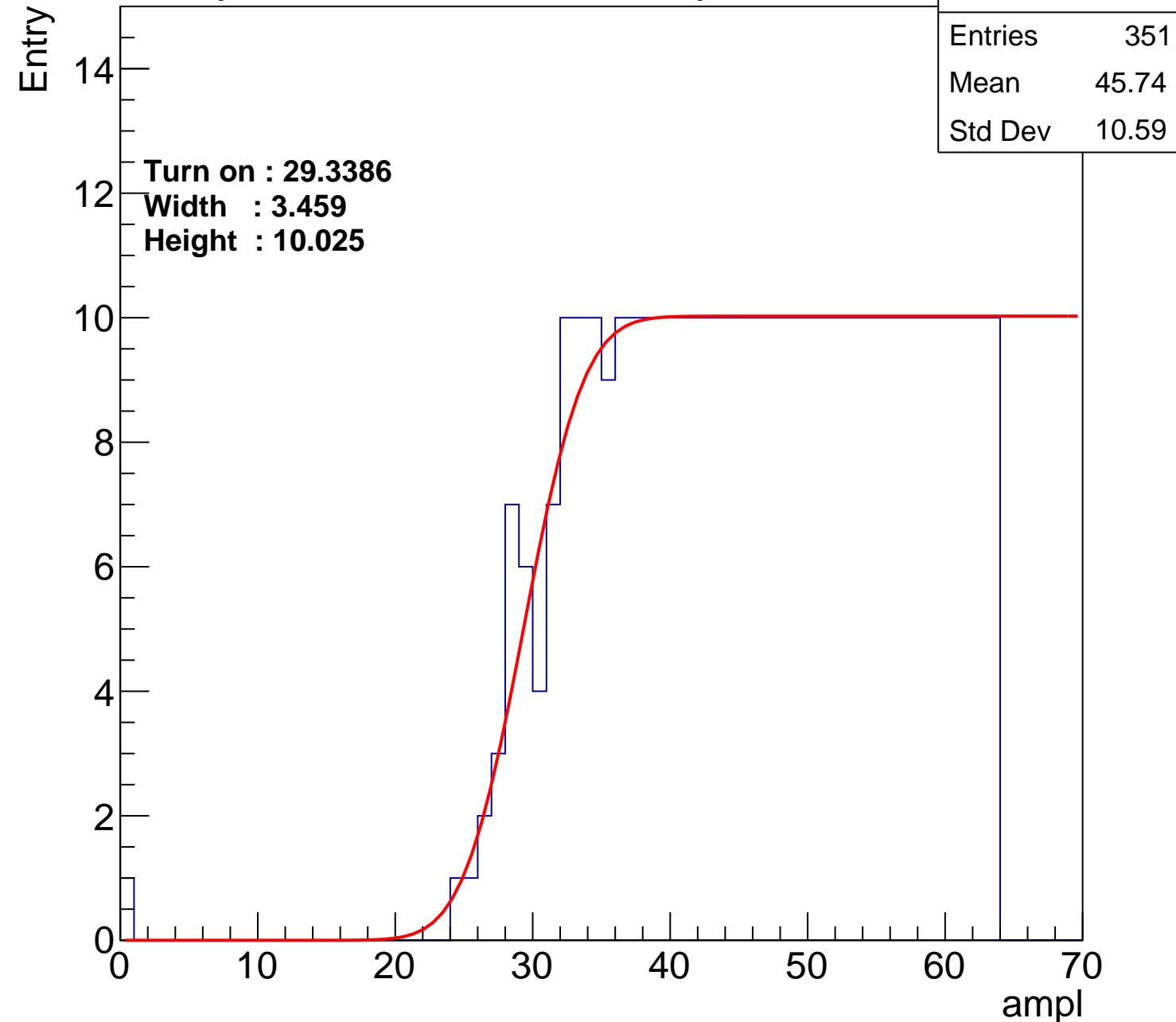
**Width : 3.459**

**Height : 10.025**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch124

calib\_packv5\_042523\_0143.root, FC#8, port C1

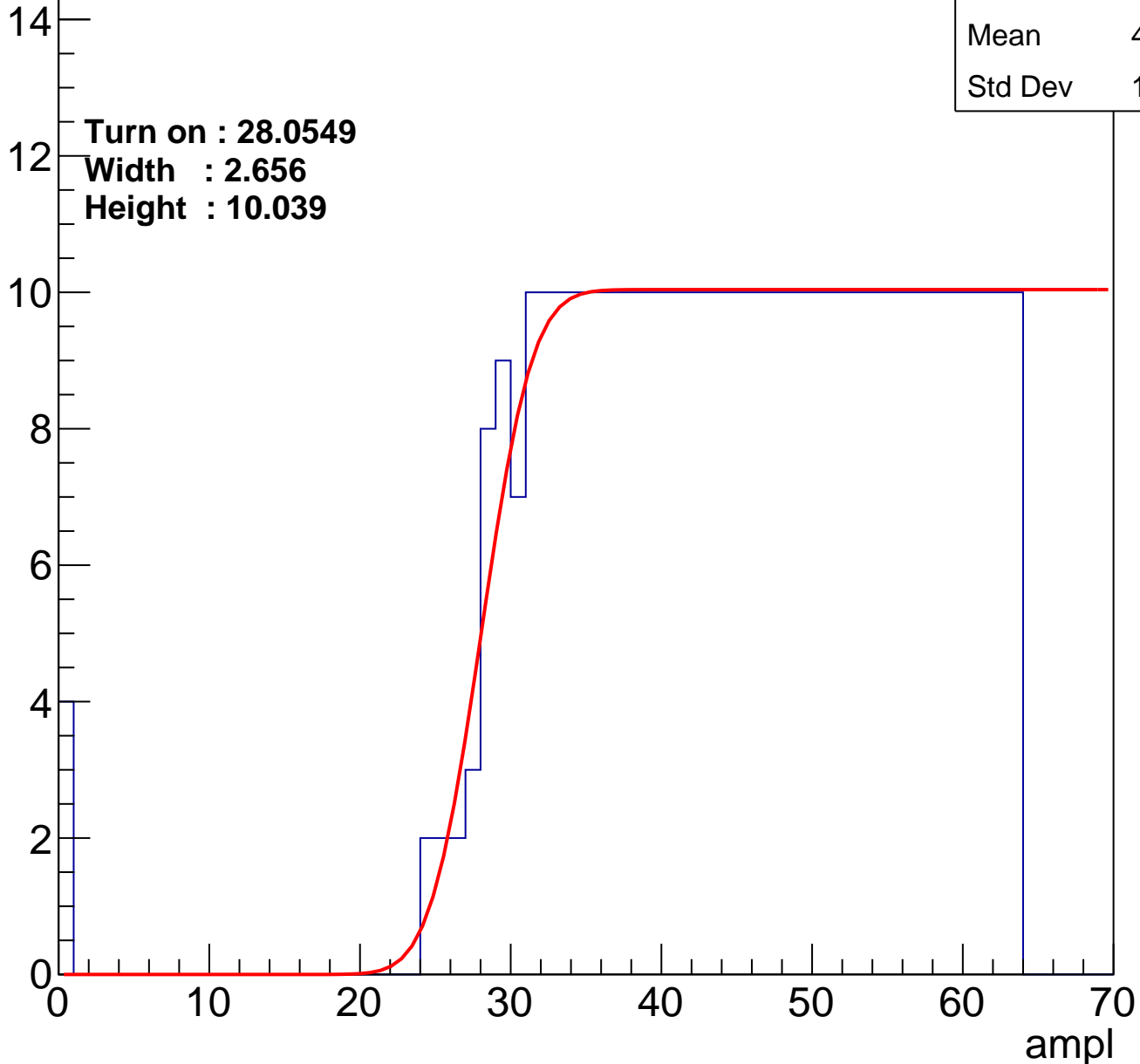
Entries	367
Mean	44.78
Std Dev	11.54

Turn on : 28.0549

Width : 2.656

Height : 10.039

Entry



# B0L002S, U23-ch125

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.16
Std Dev	10.89

Turn on : 28.2825

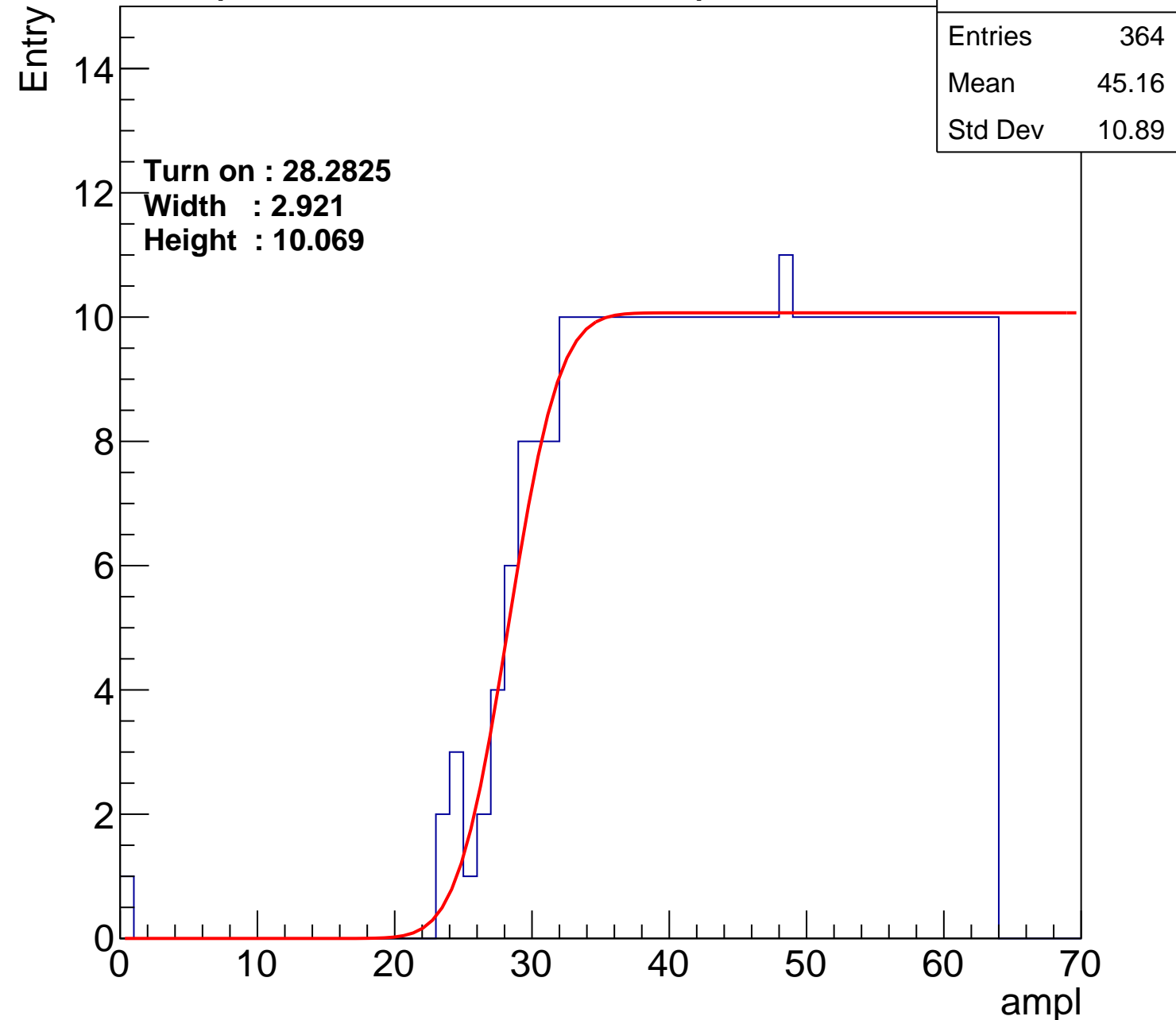
Width : 2.921

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch126

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.72
Std Dev	11.32

Turn on : 27.4033

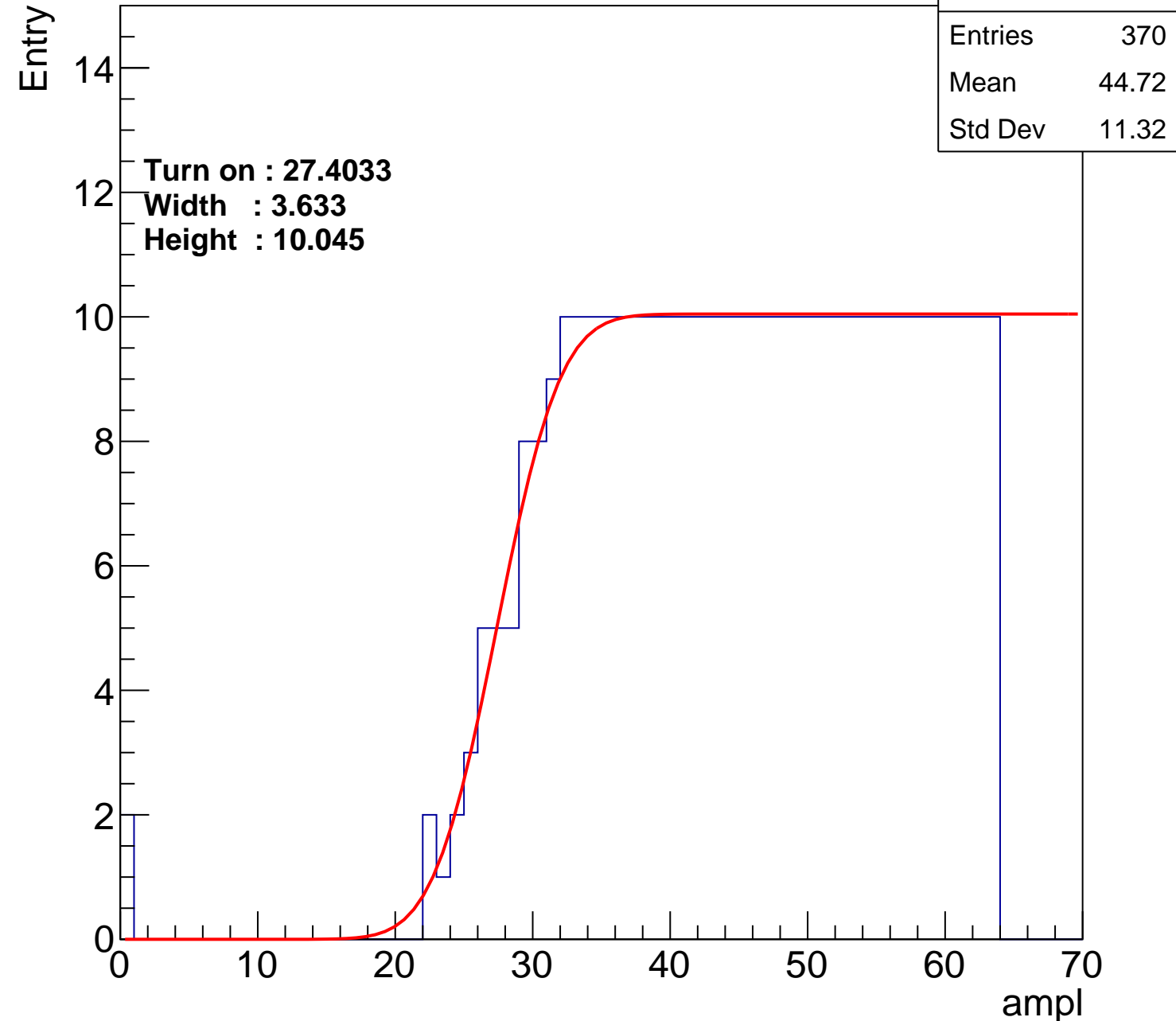
Width : 3.633

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U23-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 27.9727

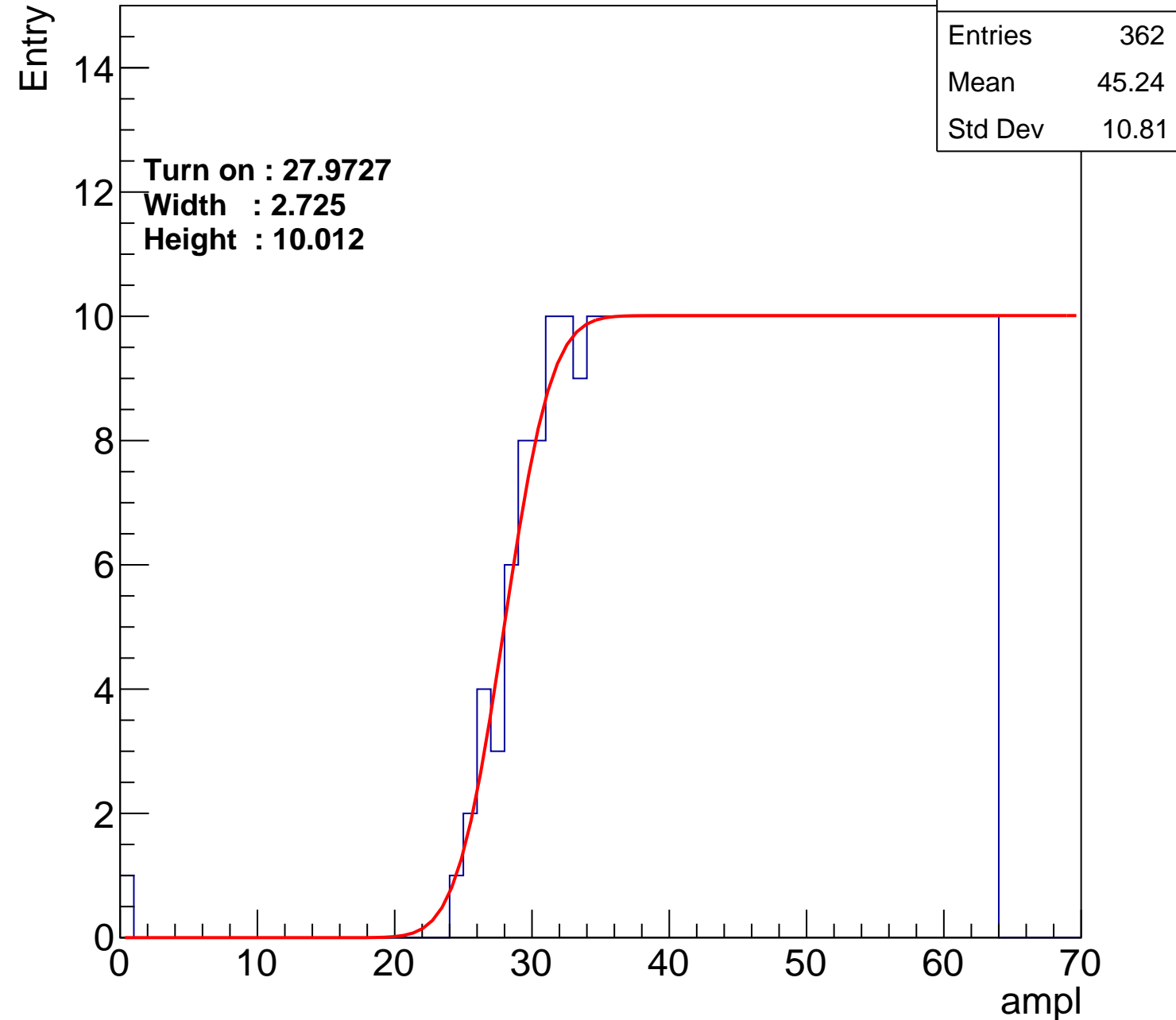
Width : 2.725

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U23-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.24
Std Dev	10.81

**Turn on : 27.9727**

**Width : 2.725**

**Height : 10.012**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

