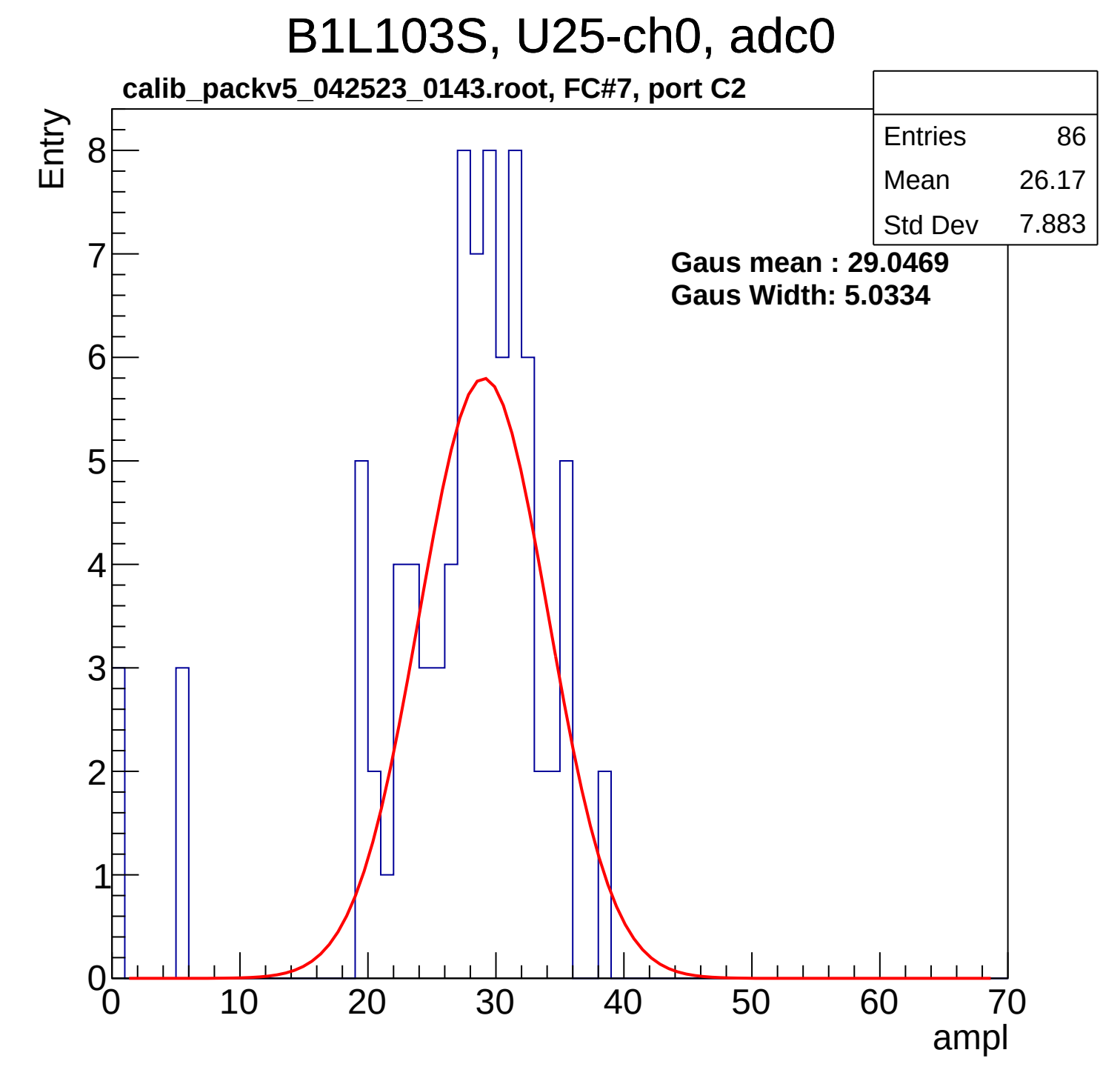
B1L103S, U25-ch0, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 26.17
Std Dev 7.883
Gaus mean : 29.0469
Gaus Width: 5.0334
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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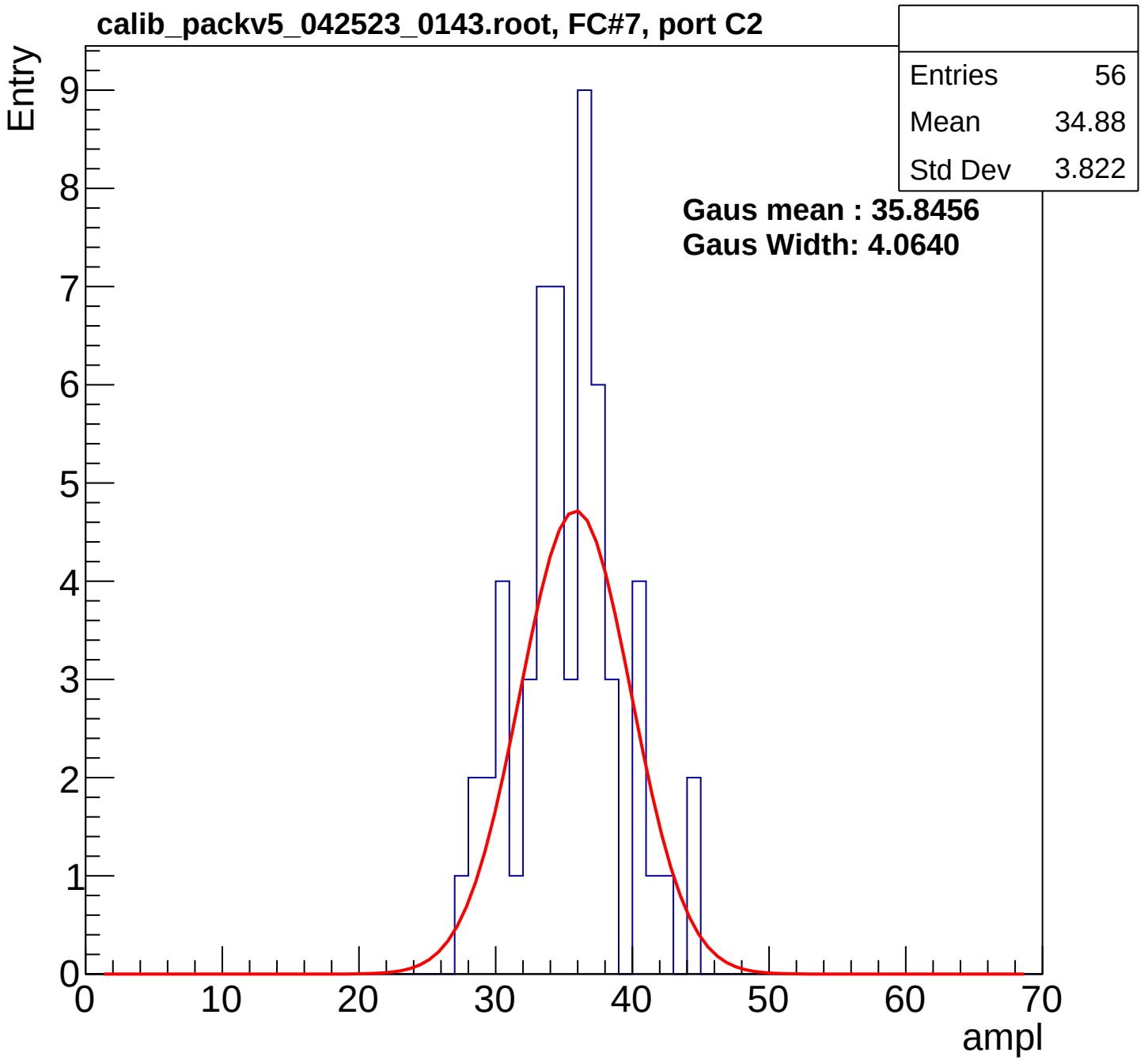
B1L103S, U25-ch0, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 34.88
Std Dev 3.822
Gaus mean : 35.8456
Gaus Width: 4.0640
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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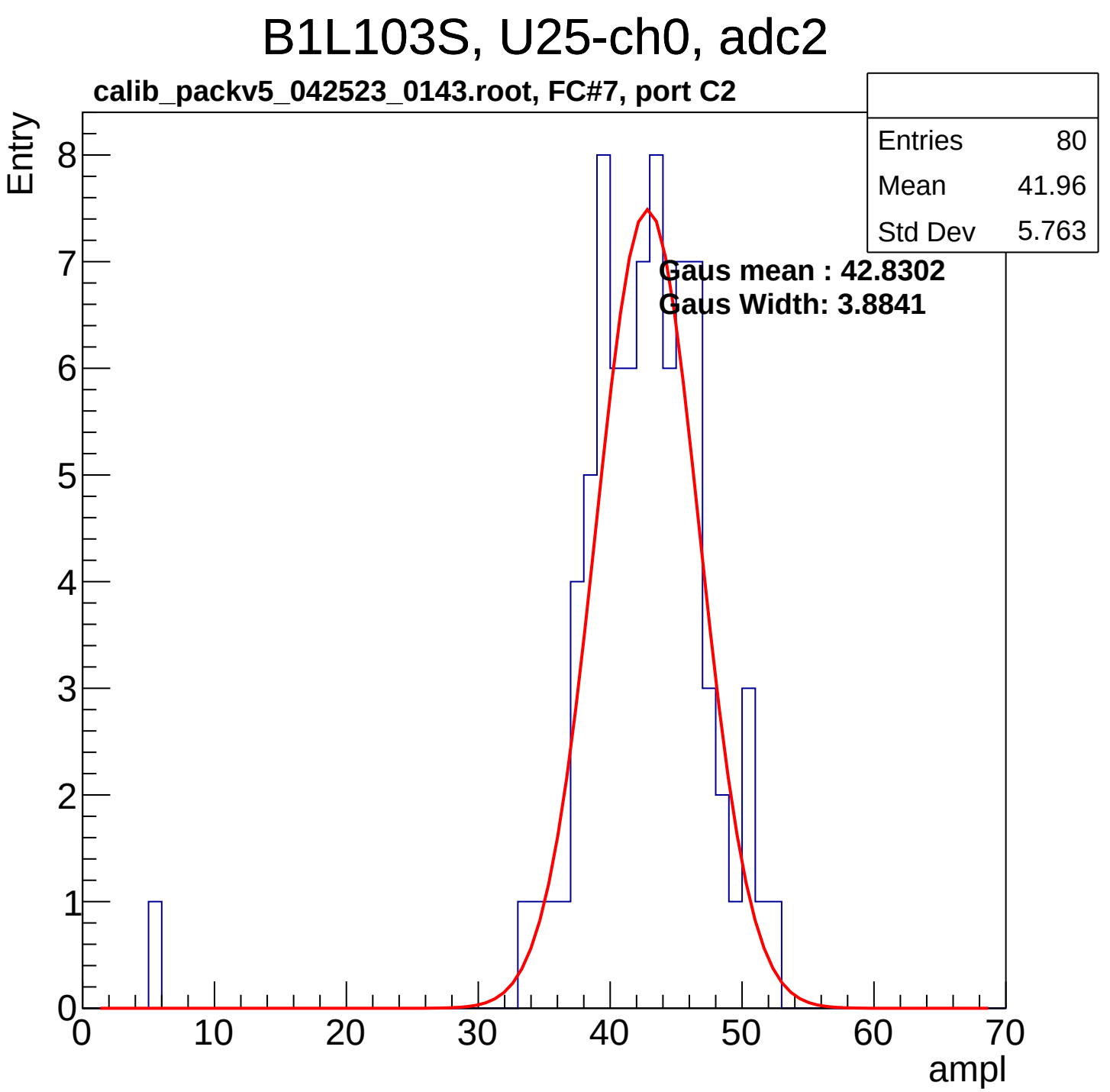
B1L103S, U25-ch0, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 41.96
Std Dev 5.763
Gaus mean : 42.8302
Gaus Width: 3.8841
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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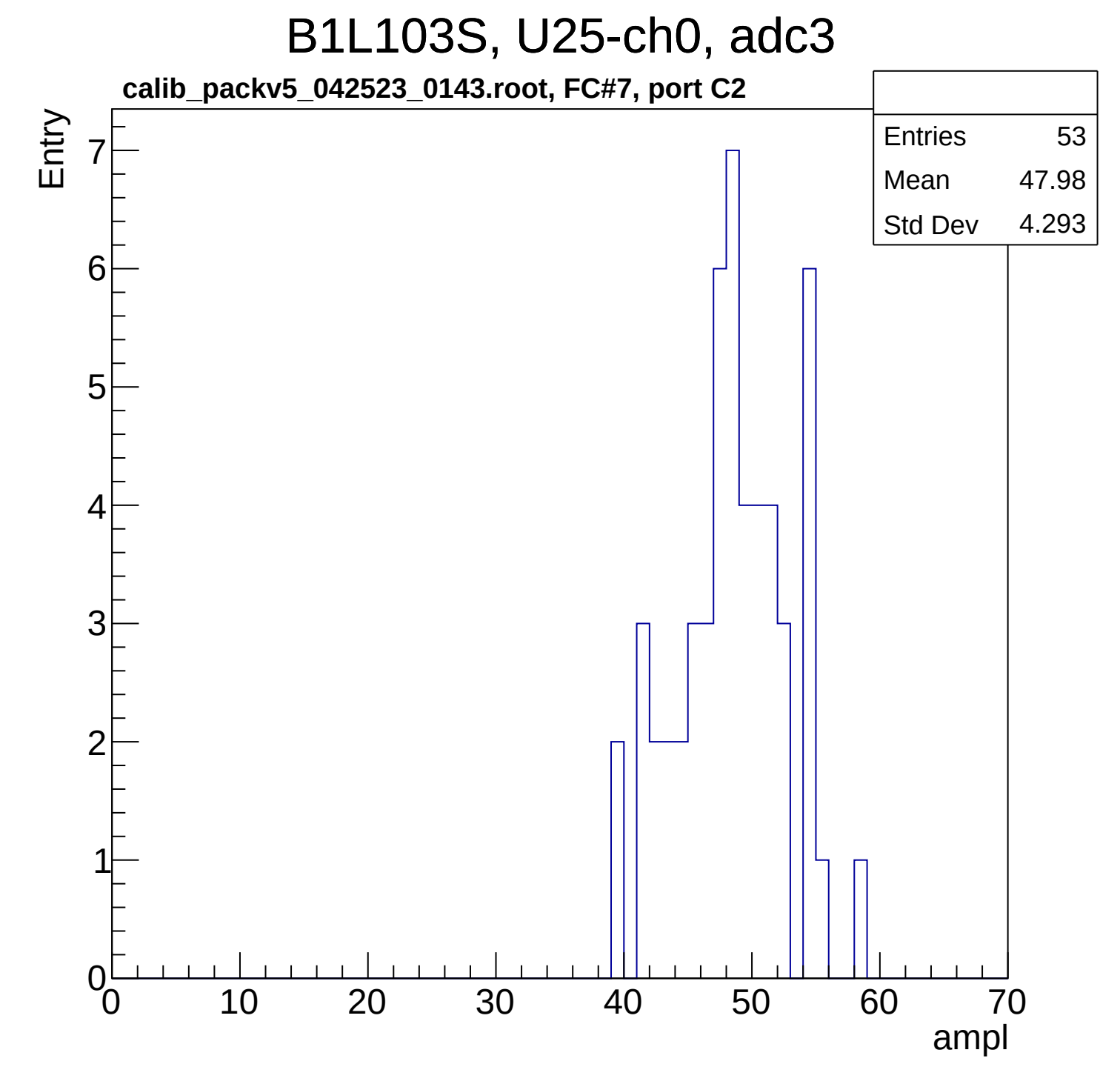
B1L103S, U25-ch0, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 47.98
Std Dev 4.293
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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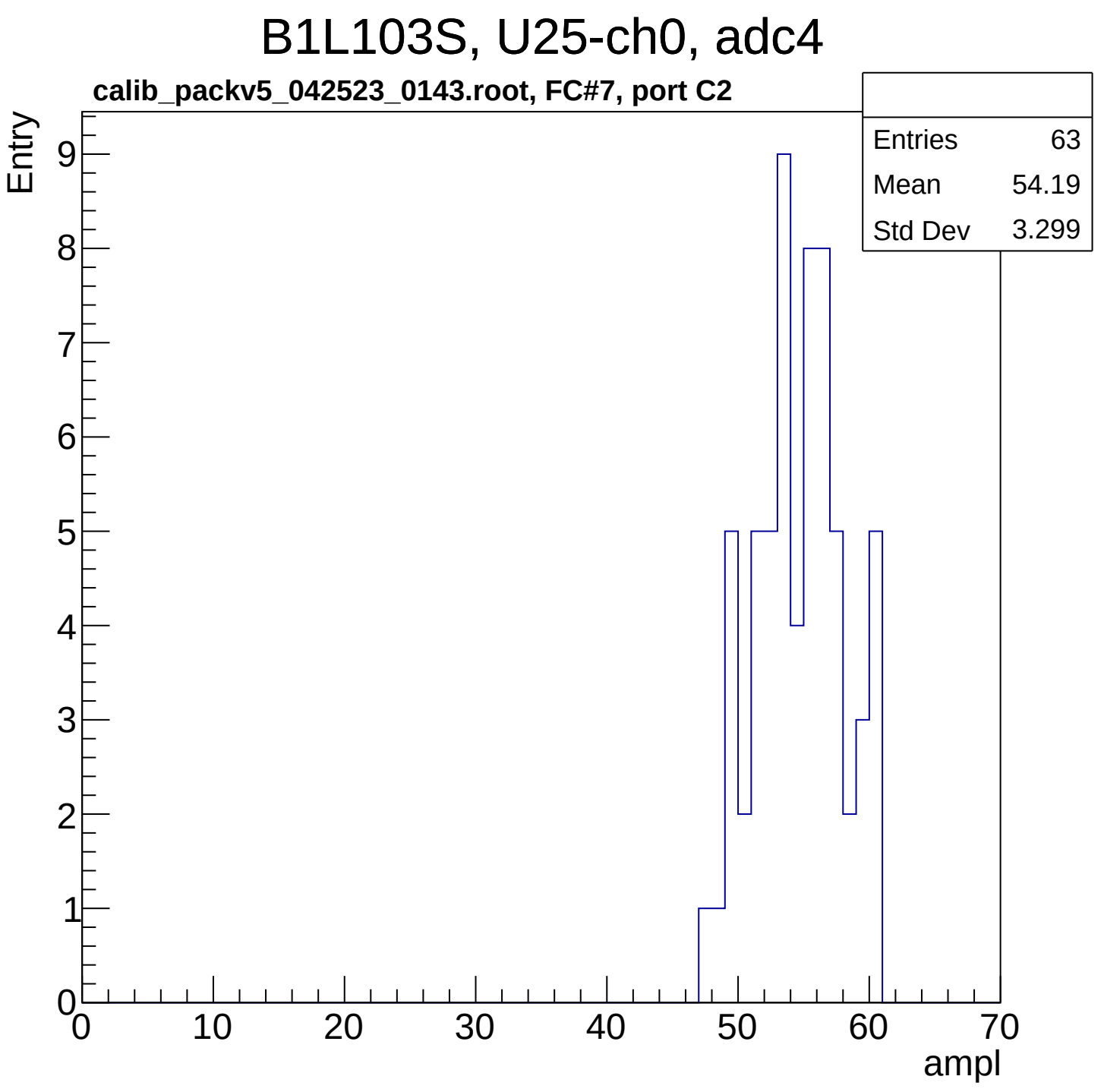
B1L103S, U25-ch0, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 54.19
Std Dev 3.299
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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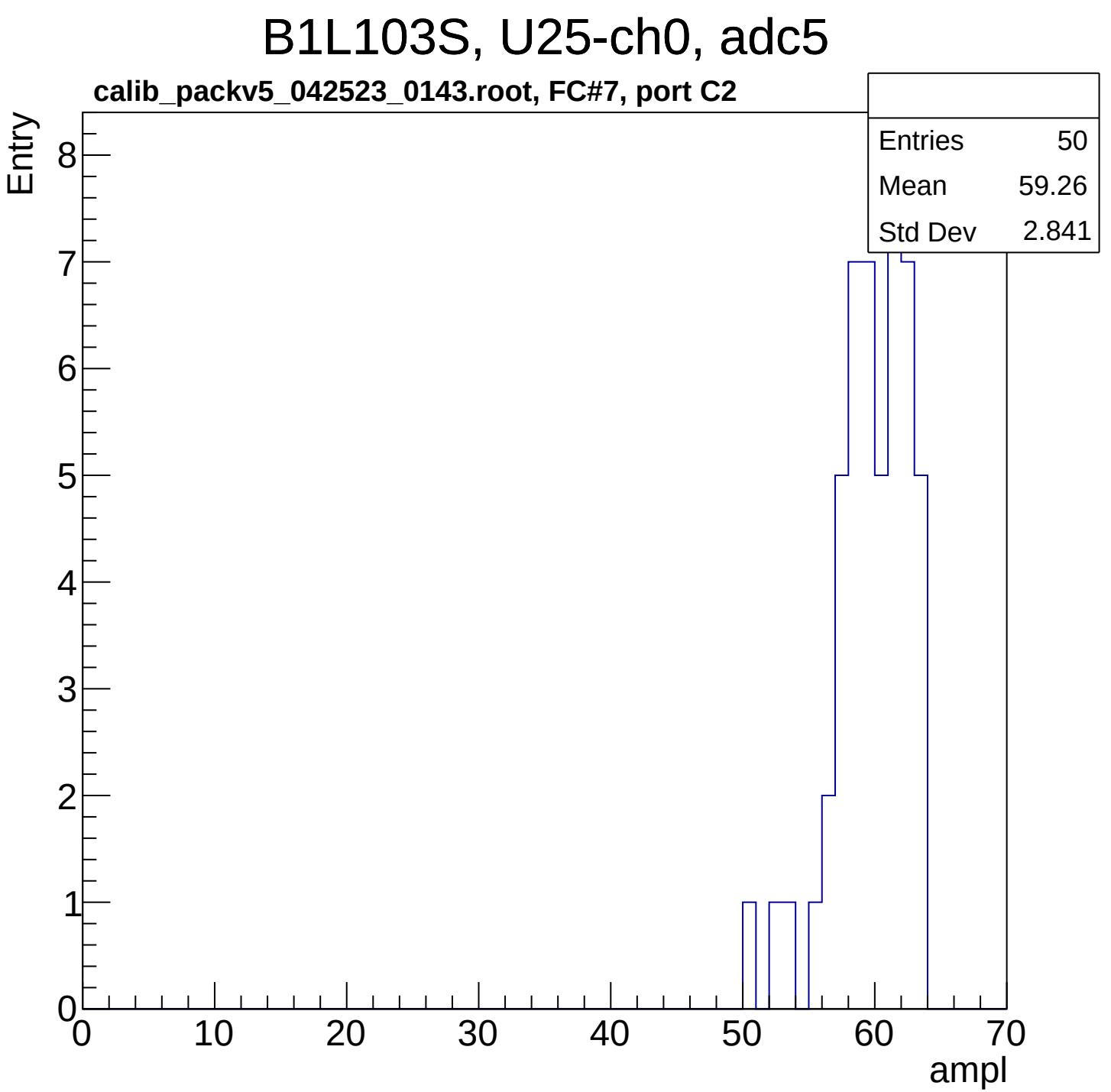
B1L103S, U25-ch0, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.26
Std Dev 2.841
Entry
ampl
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2
3
4
5
6
7
8
0
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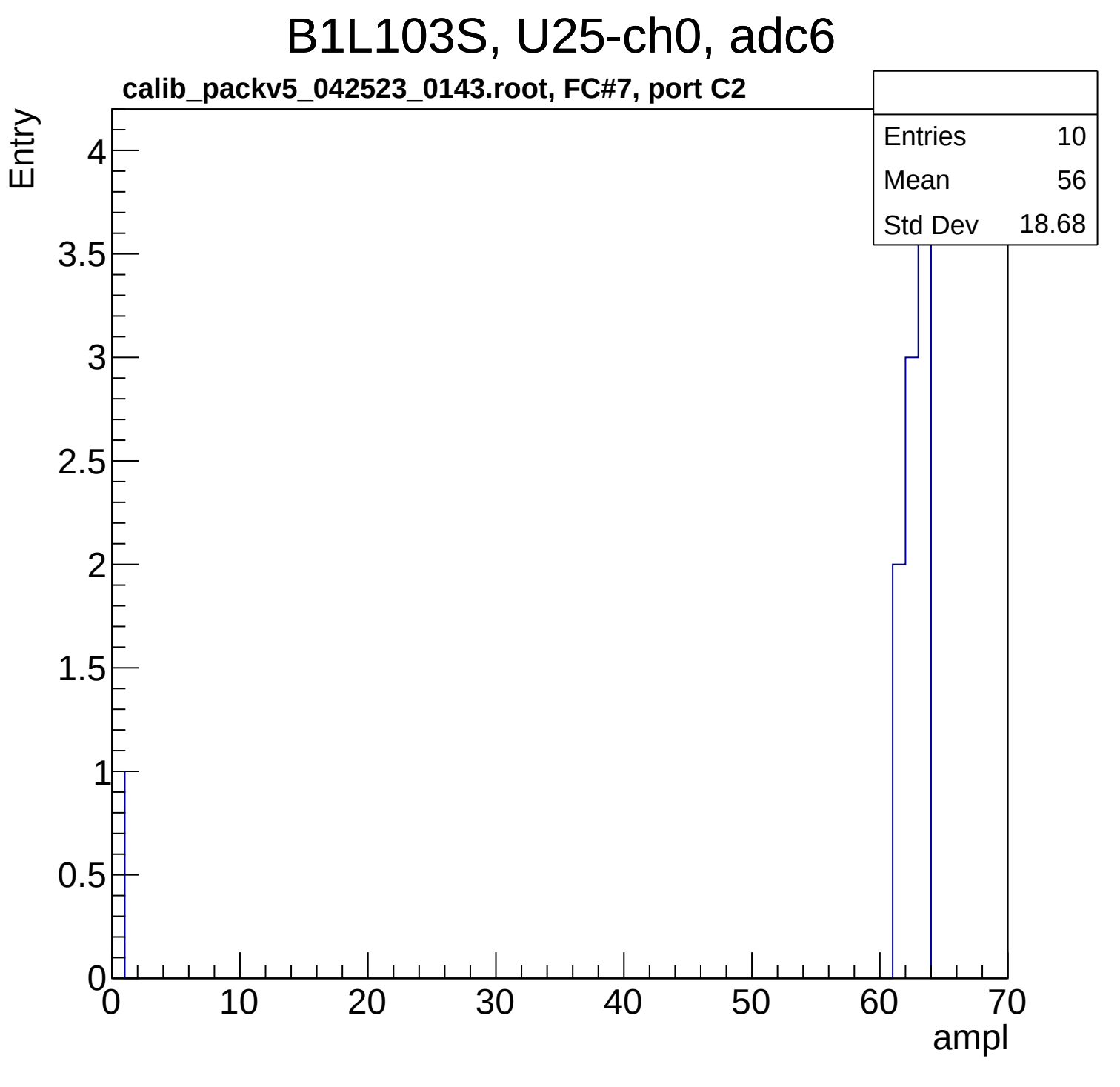
B1L103S, U25-ch0, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56
Std Dev 18.68
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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50
60
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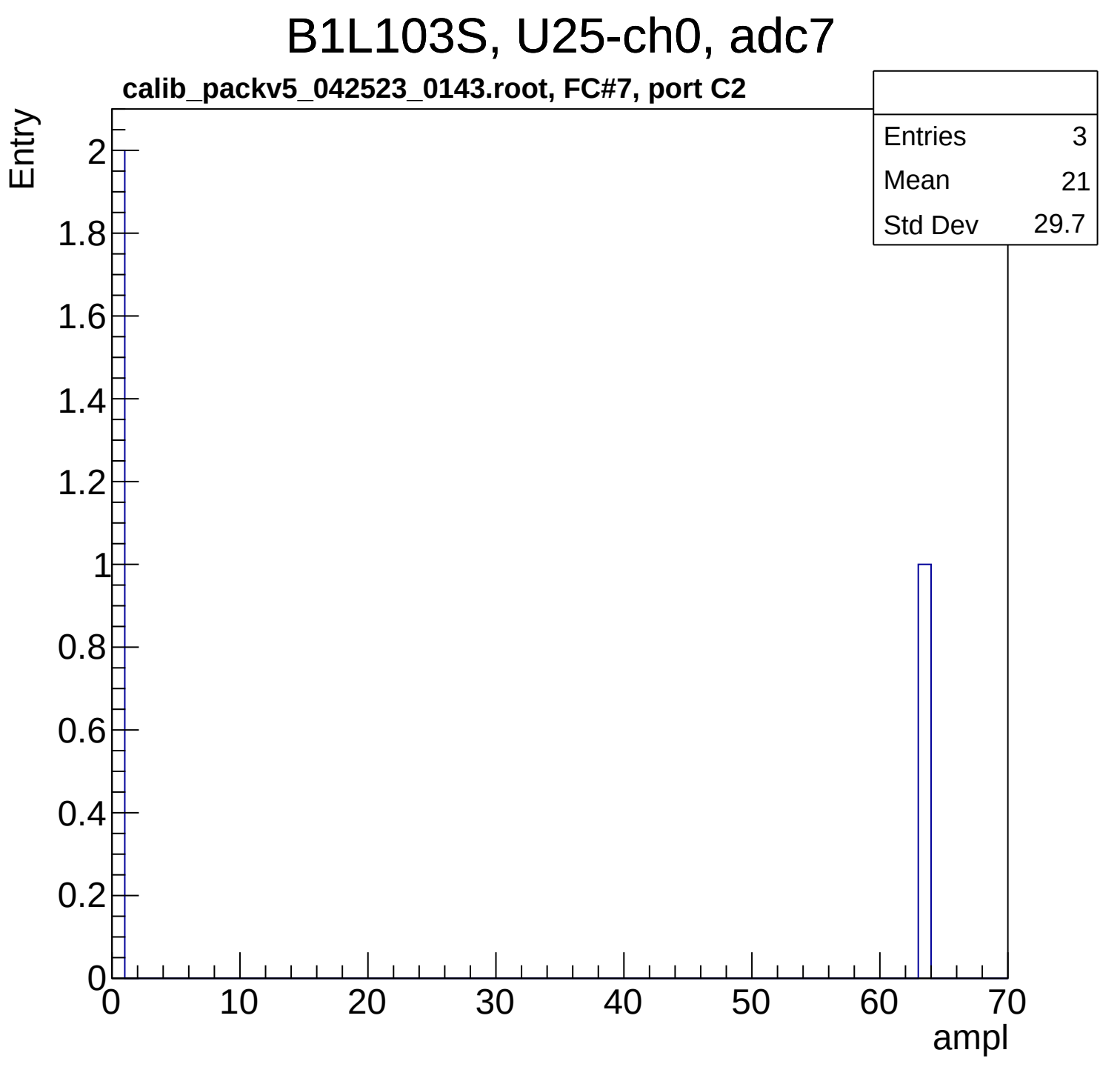
B1L103S, U25-ch0, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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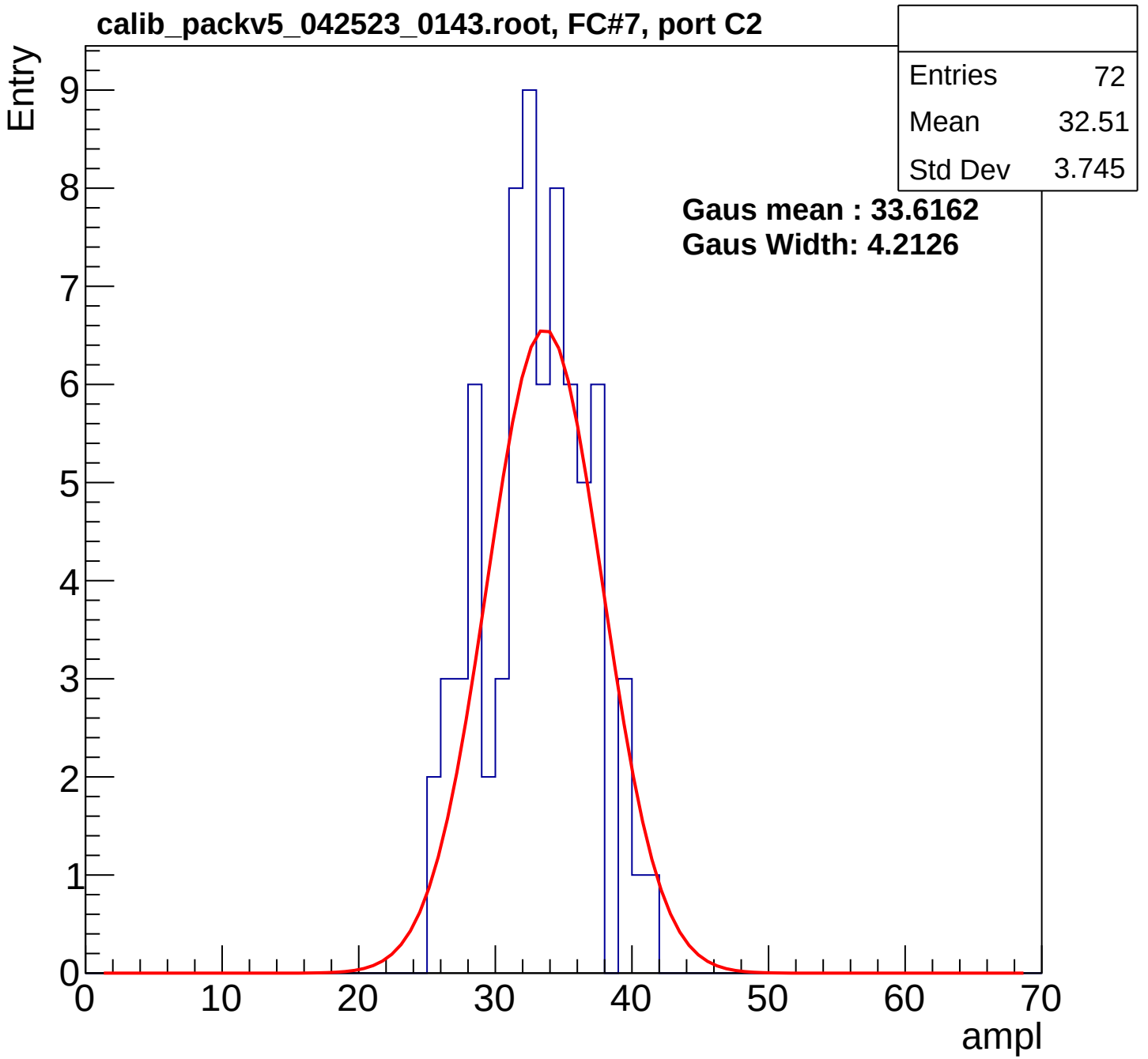
B1L103S, U25-ch1, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 32.51
Std Dev 3.745
Gaus mean : 33.6162
Gaus Width: 4.2126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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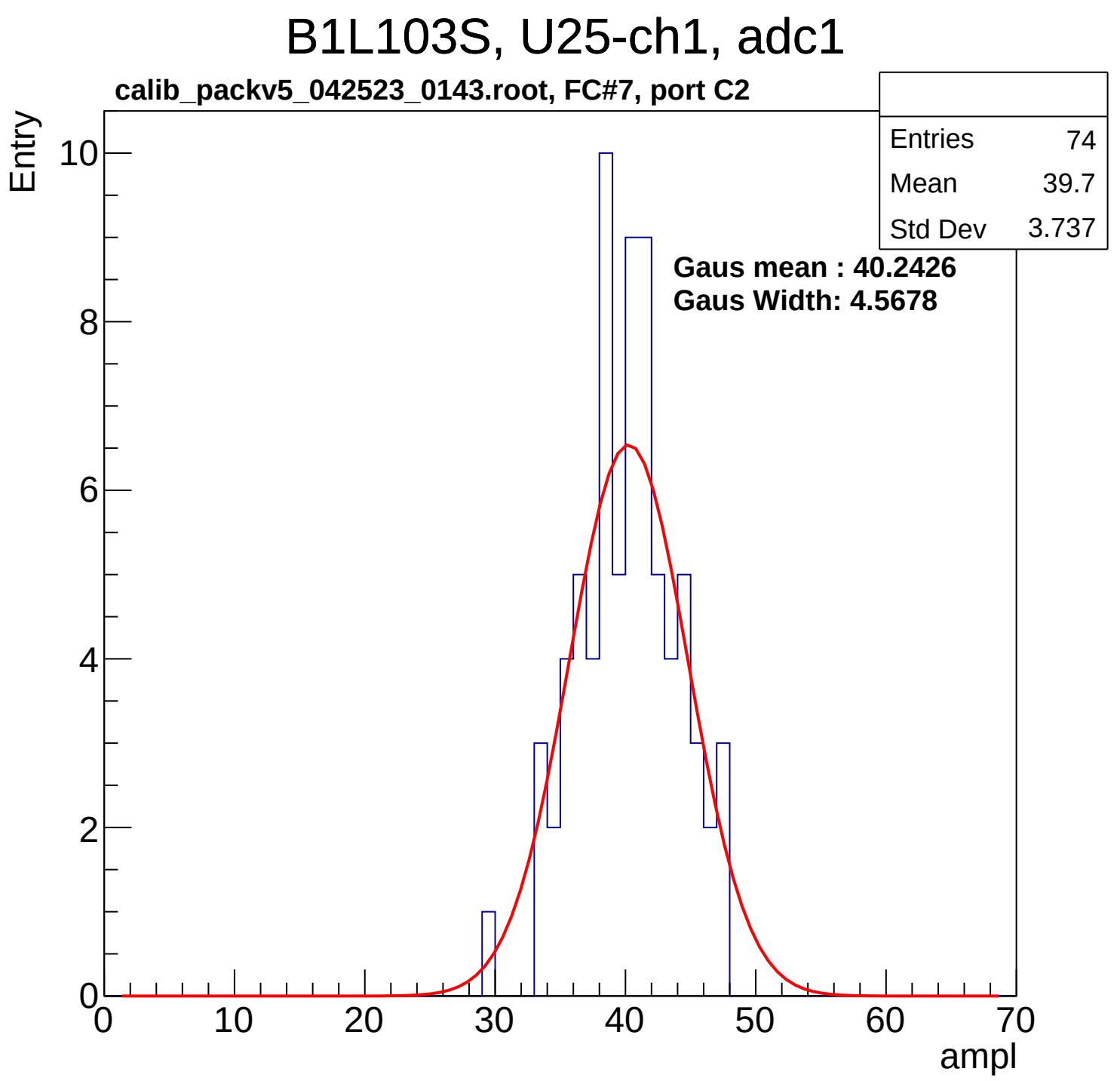
B1L103S, U25-ch1, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 39.7
Std Dev 3.737
Gaus mean : 40.2426
Gaus Width: 4.5678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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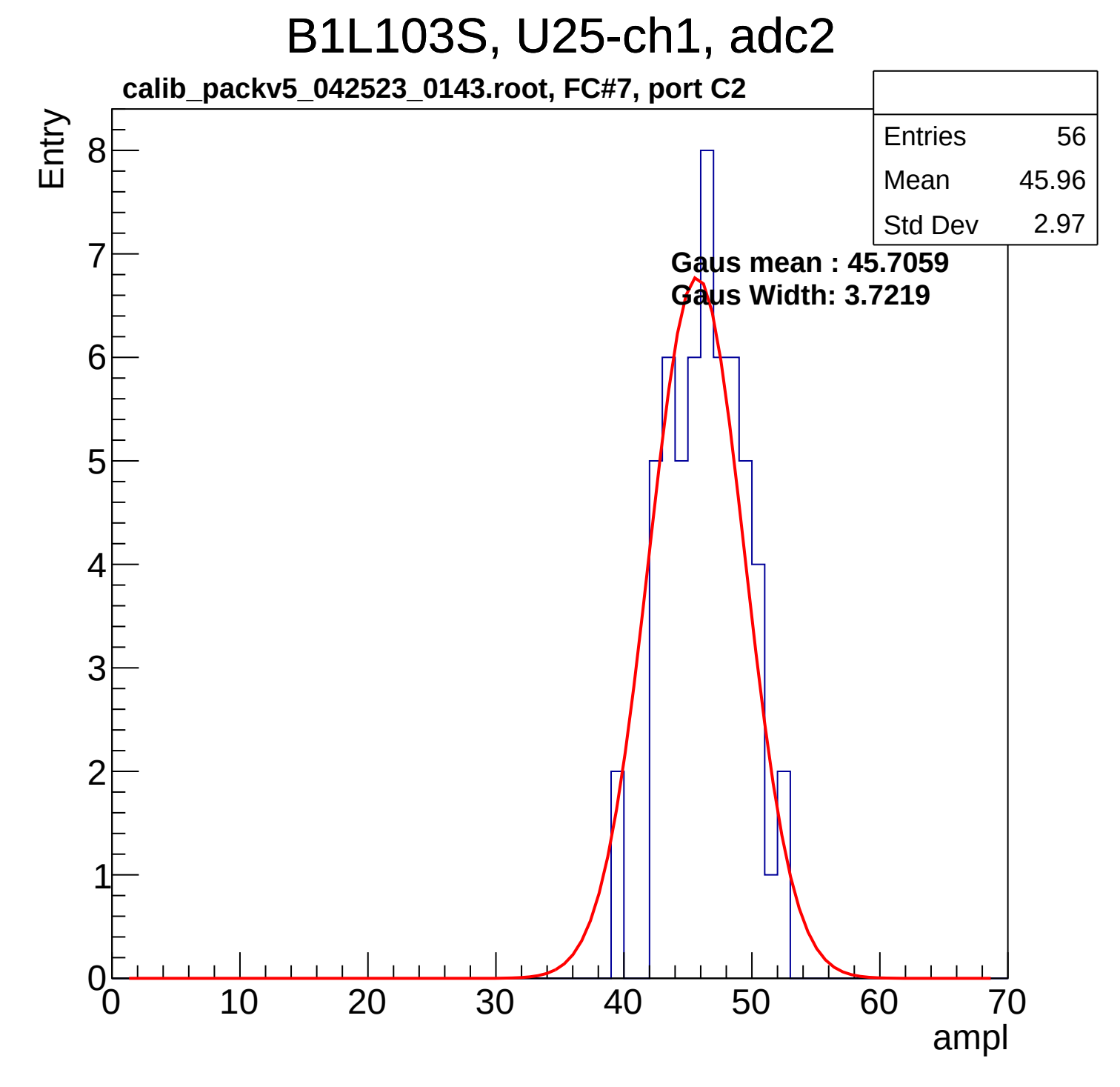
B1L103S, U25-ch1, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 45.96
Std Dev 2.97
Gaus mean : 45.7059
Gaus Width: 3.7219
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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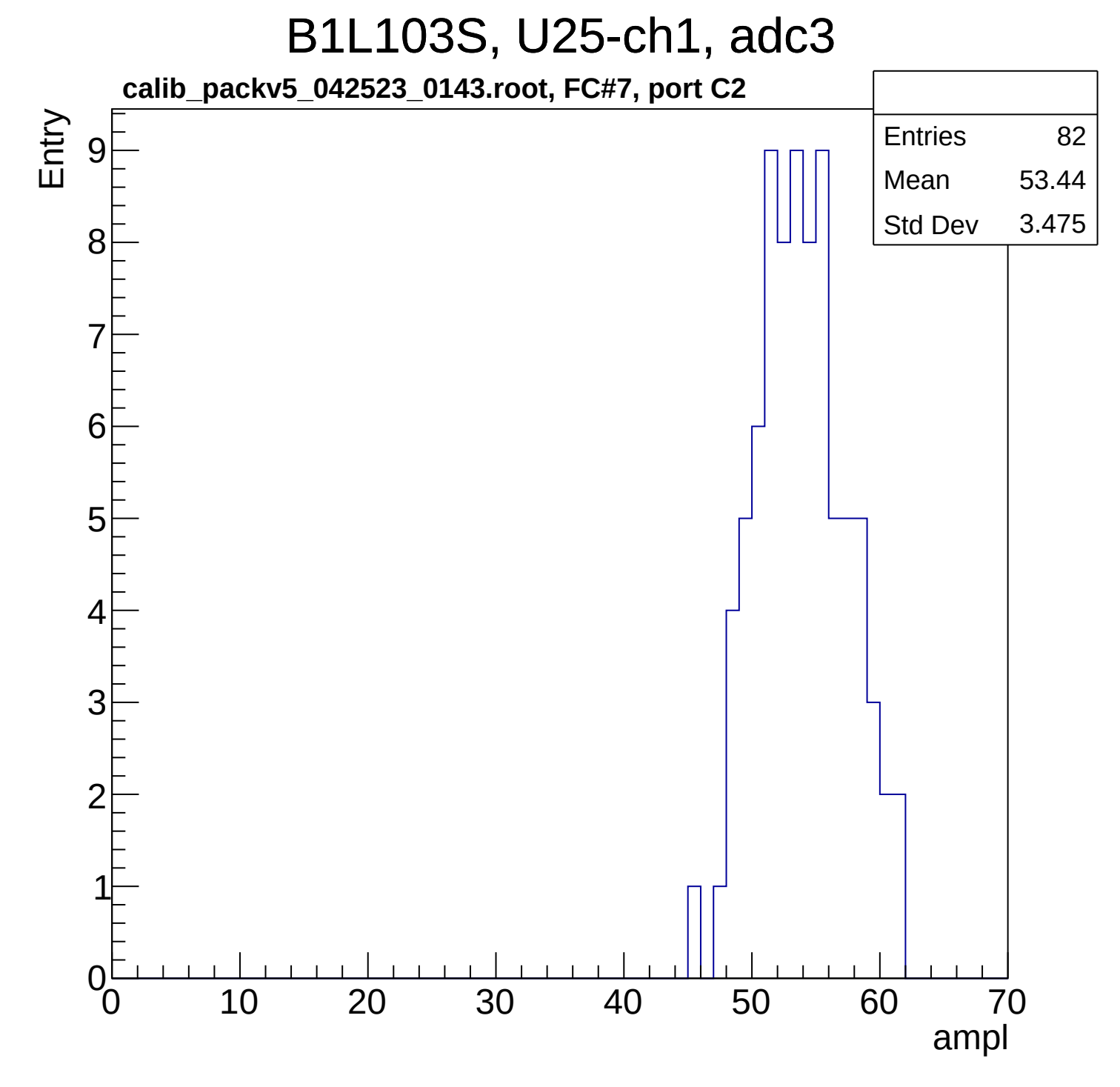
B1L103S, U25-ch1, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 53.44
Std Dev 3.475
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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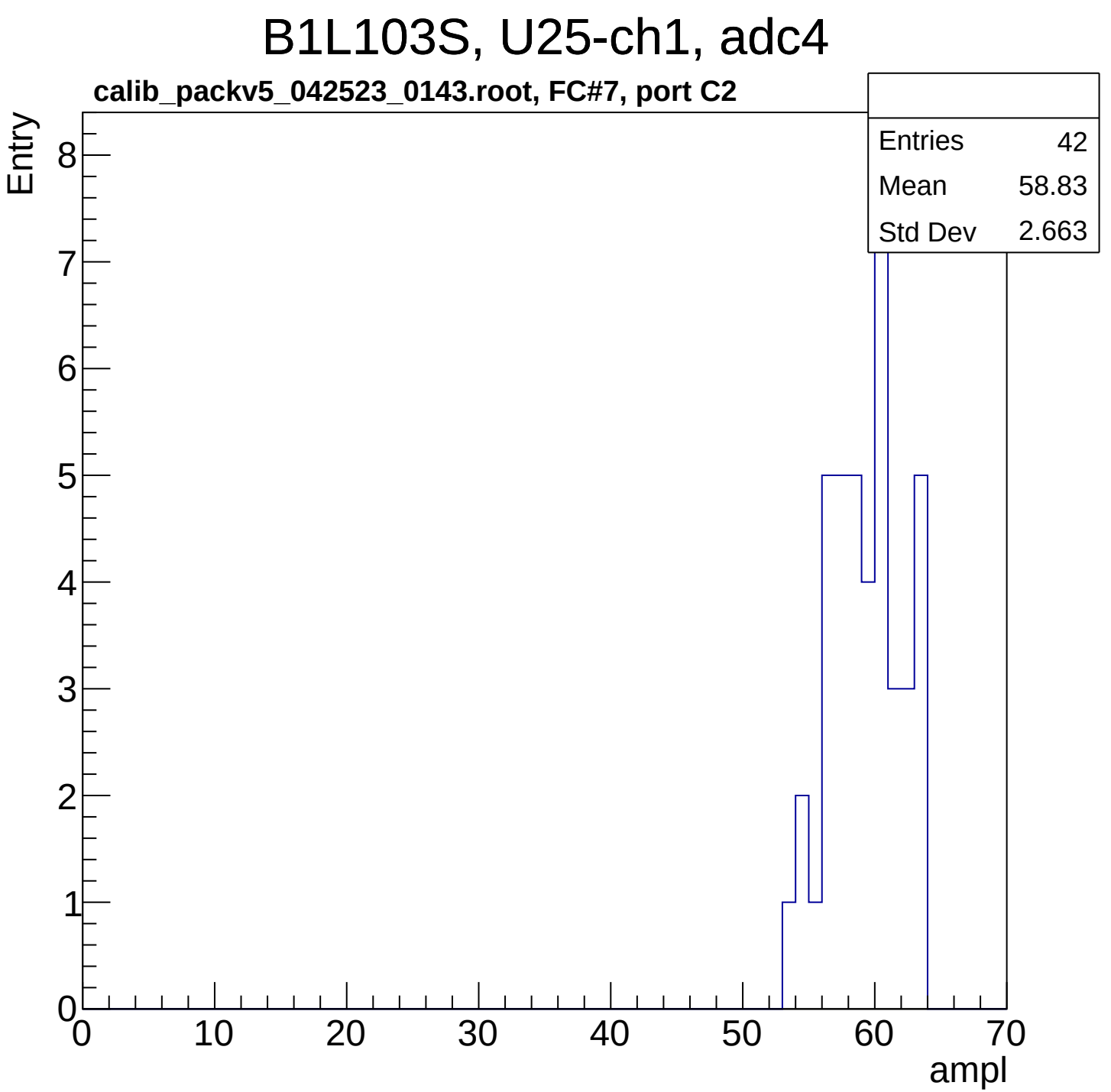
B1L103S, U25-ch1, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.83
Std Dev 2.663
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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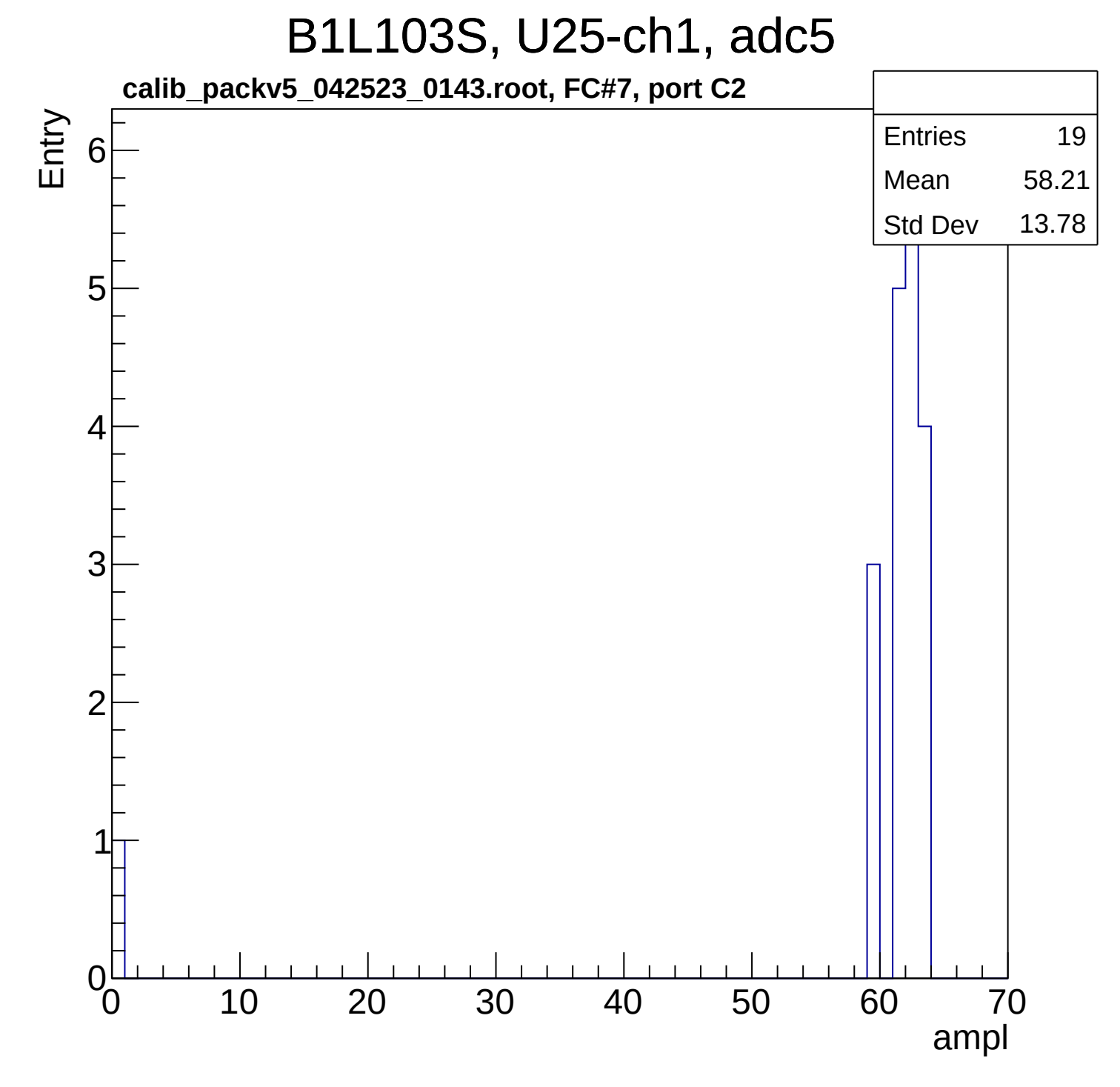
B1L103S, U25-ch1, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 58.21
Std Dev 13.78
Entry
ampl
0
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2
3
4
5
6
0
10
20
30
40
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60
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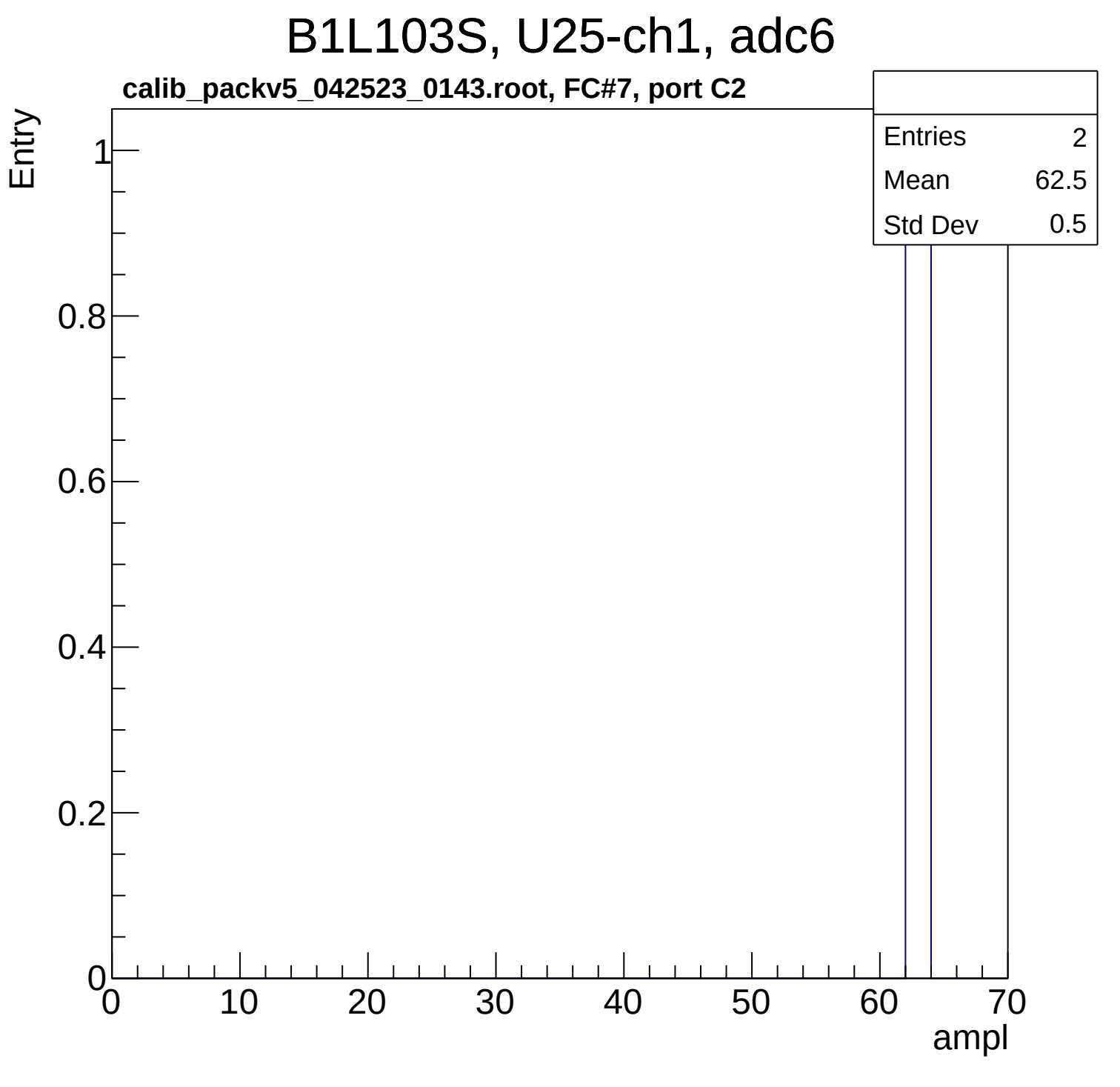
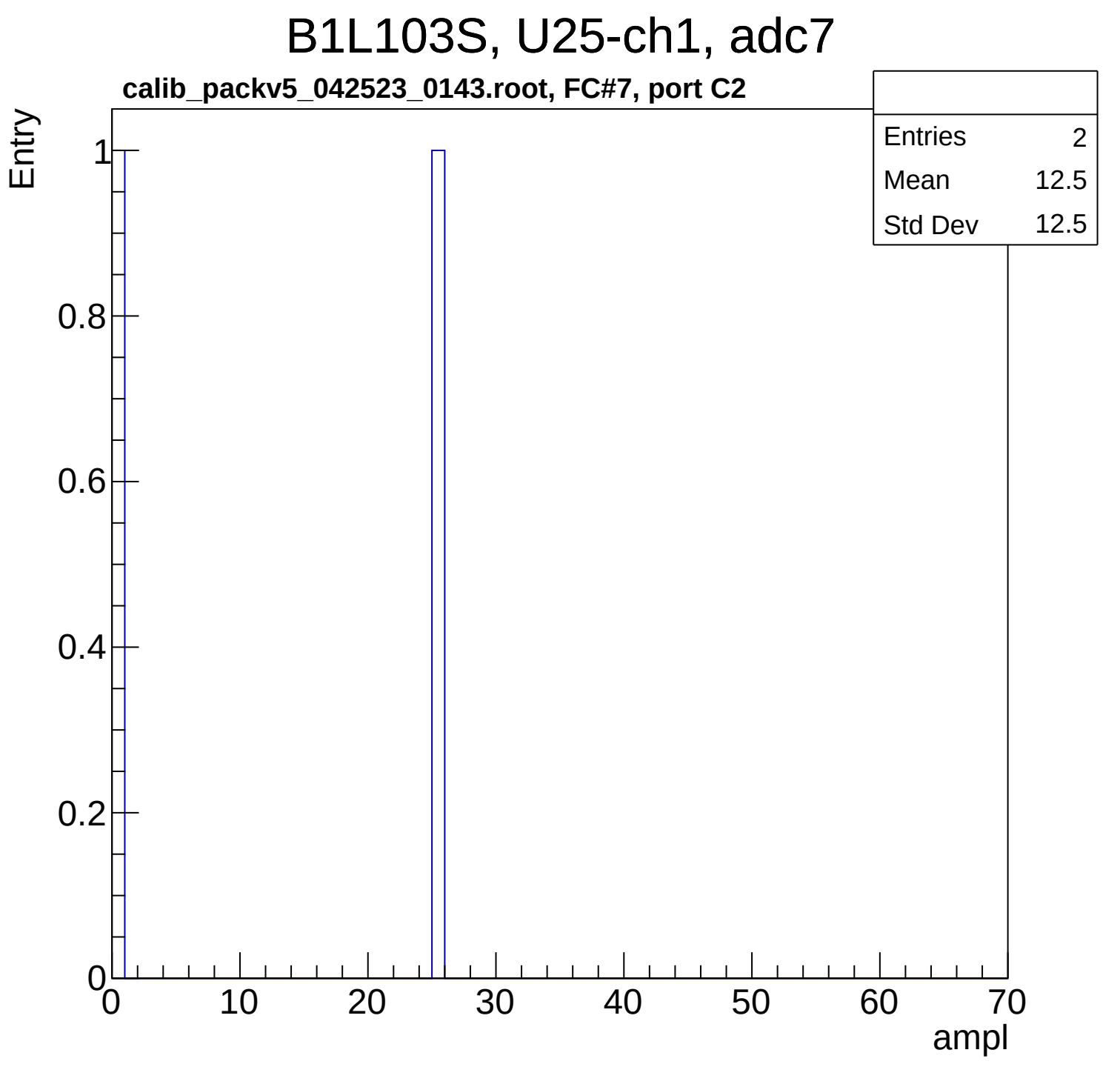
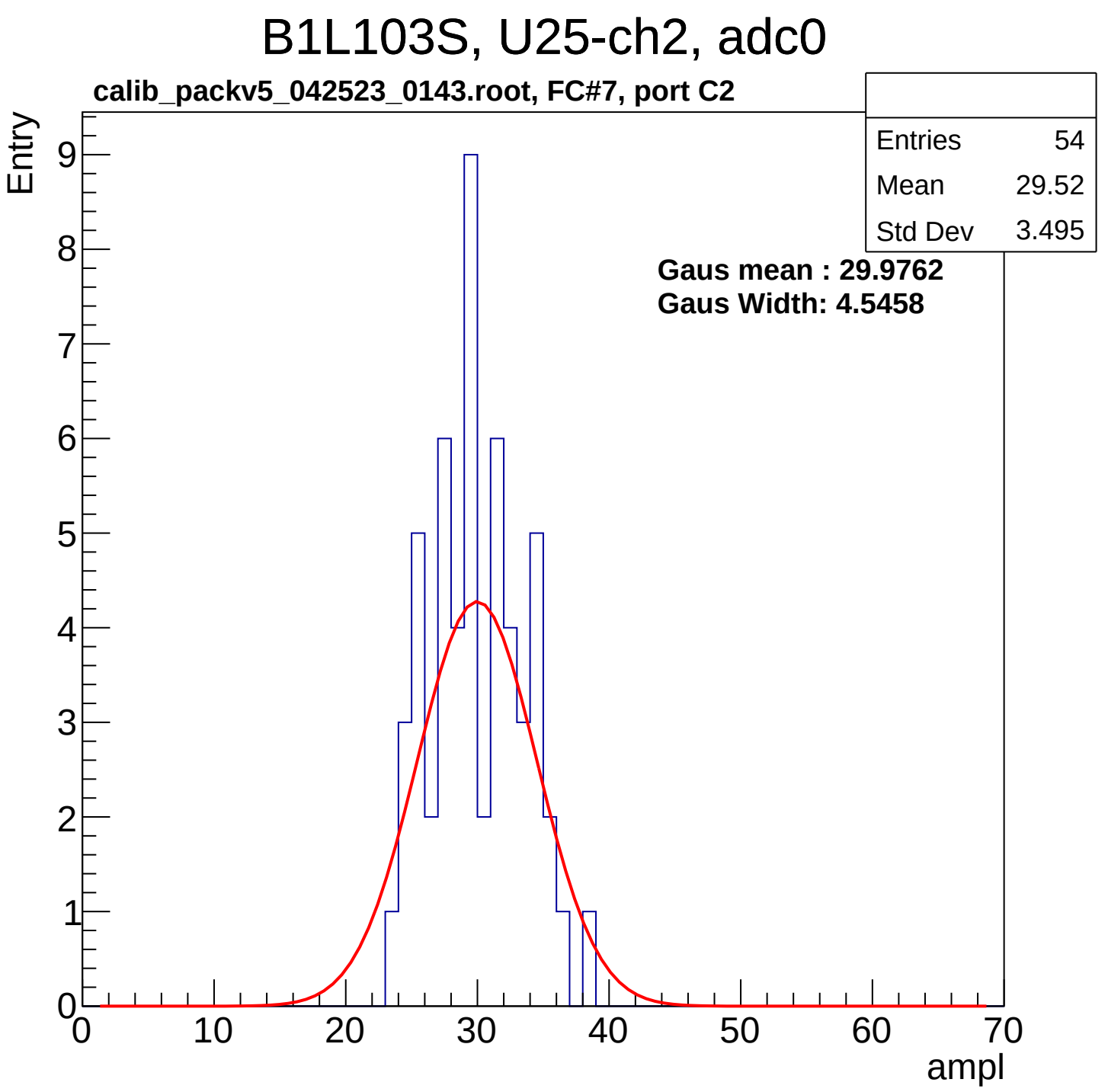
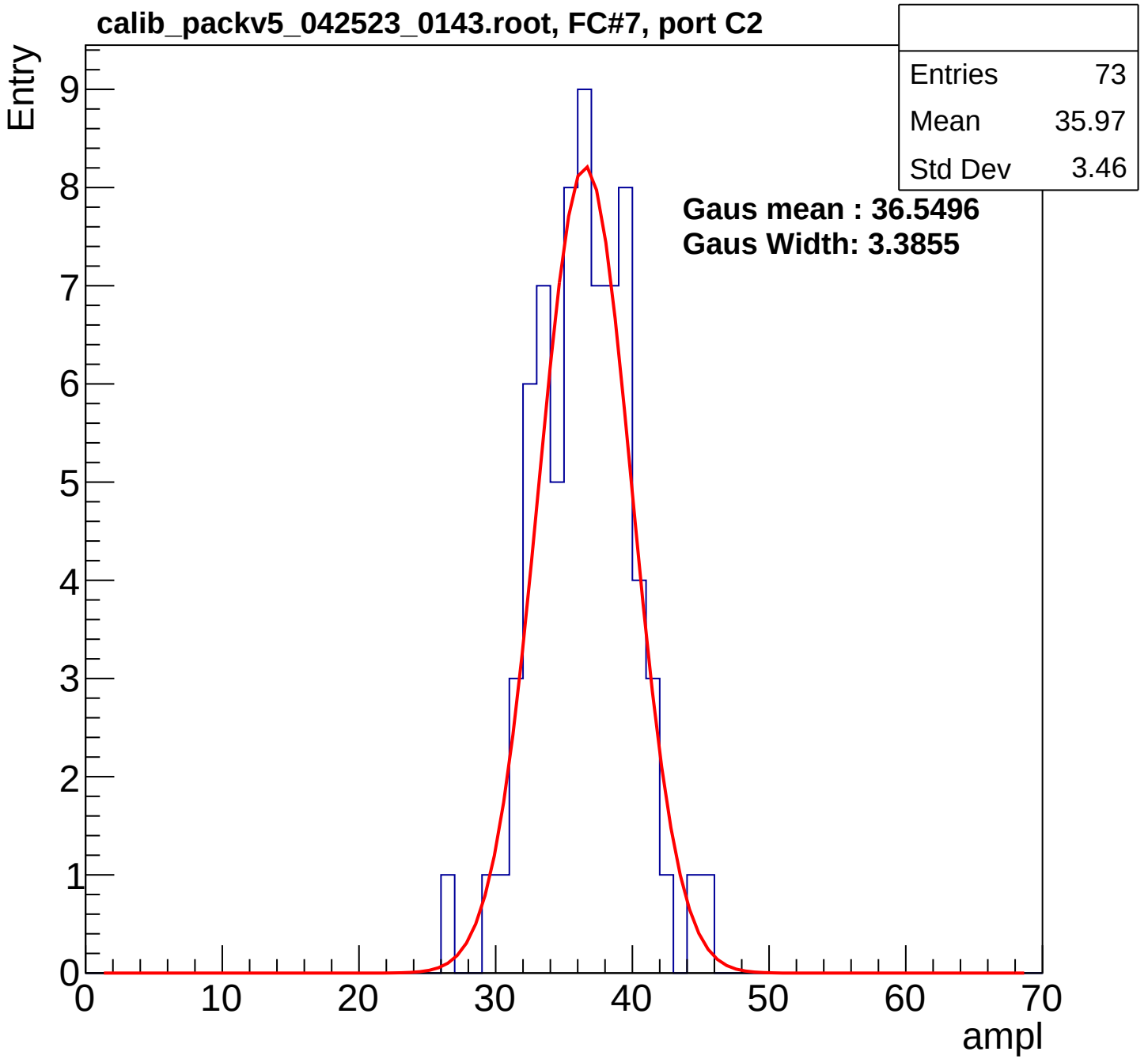

B1L103S, U25-ch1, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch1, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch2, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 29.52
Std Dev 3.495
Gaus mean : 29.9762
Gaus Width: 4.5458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch2, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 35.97
Std Dev 3.46
Gaus mean : 36.5496
Gaus Width: 3.3855
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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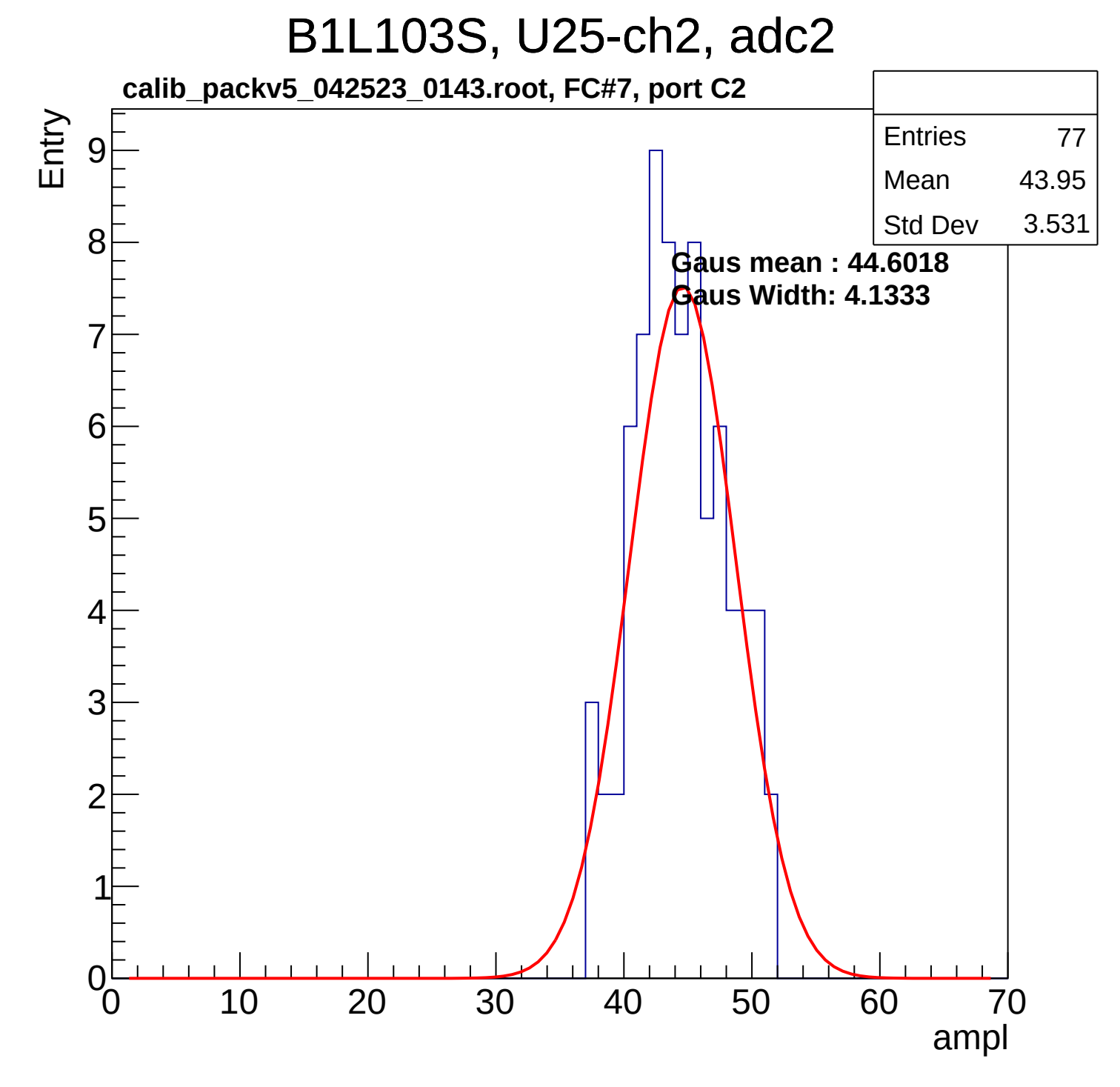
B1L103S, U25-ch2, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 43.95
Std Dev 3.531
Gaus mean : 44.6018
Gaus Width: 4.1333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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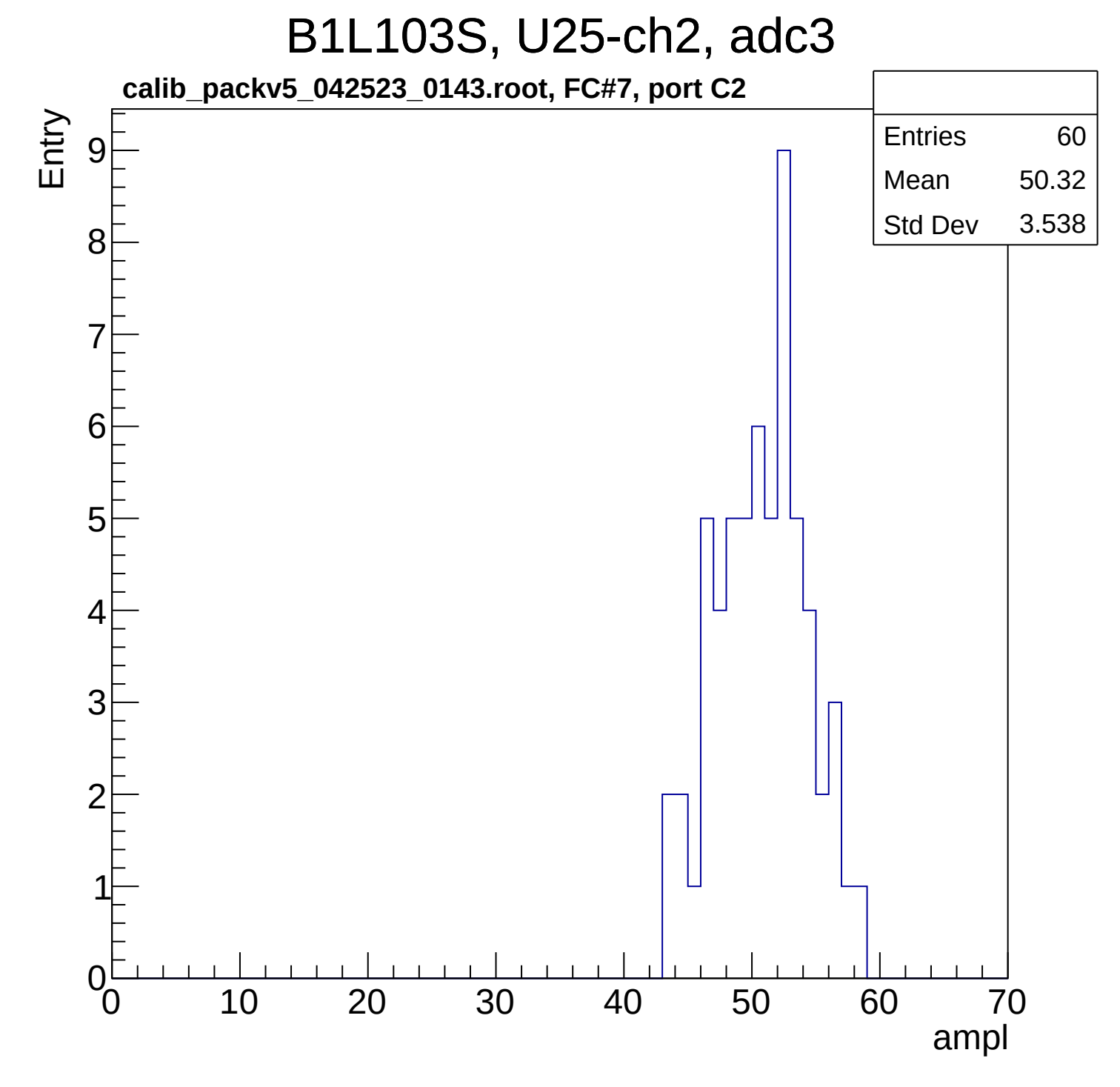
B1L103S, U25-ch2, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.32
Std Dev 3.538
Entry
ampl
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40
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60
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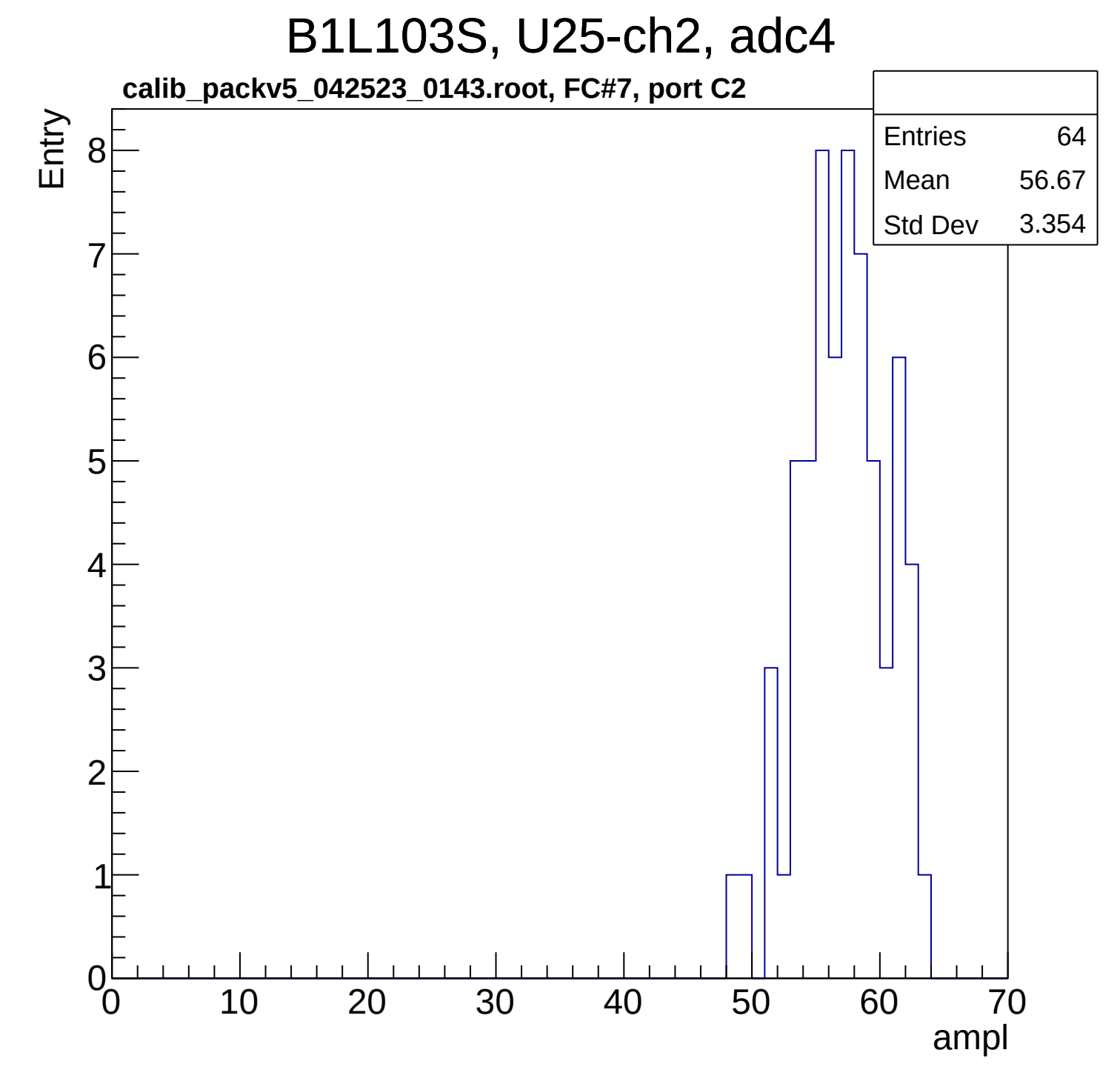
B1L103S, U25-ch2, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 56.67
Std Dev 3.354
Entry
ampl
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0
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60
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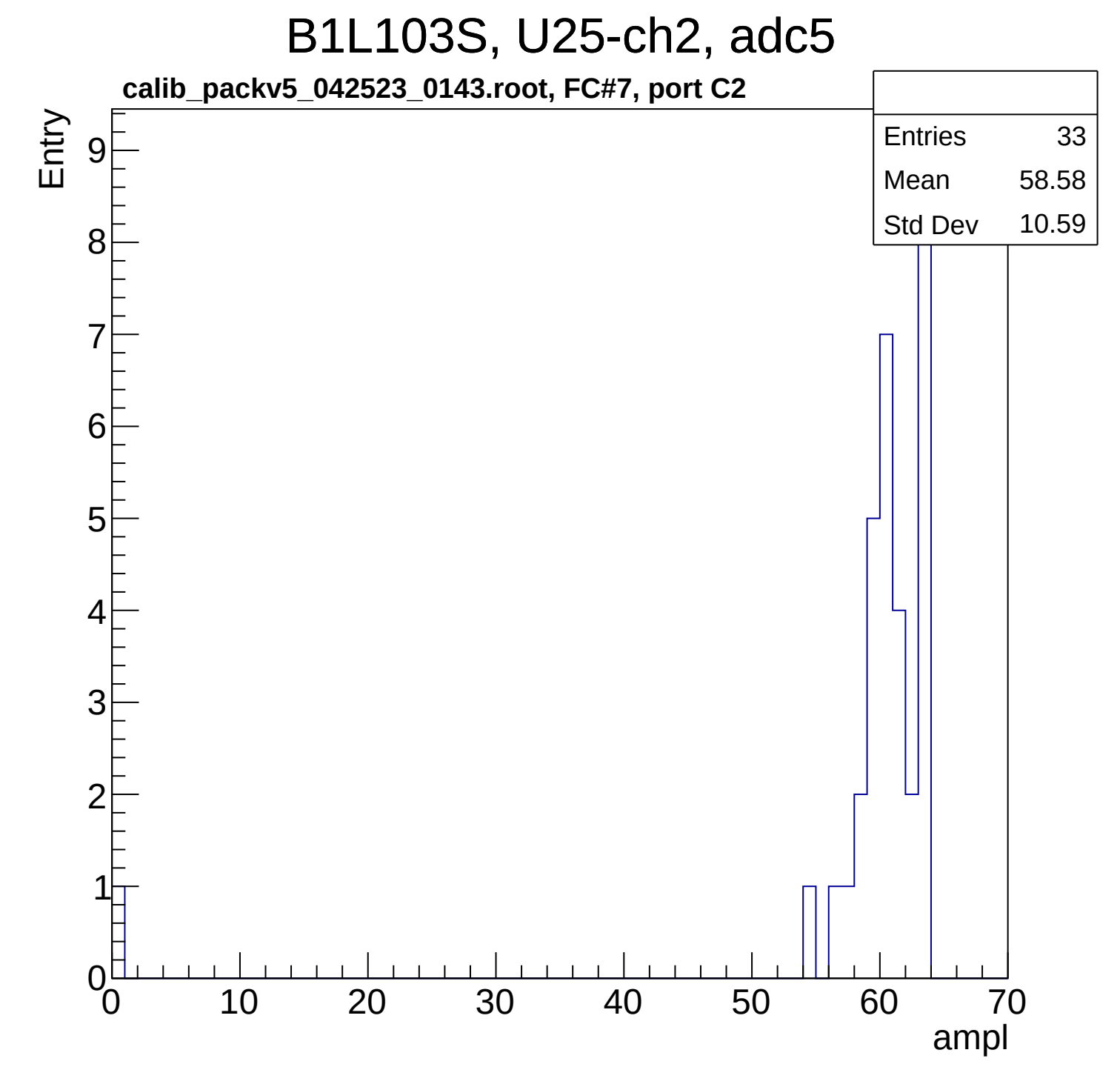
B1L103S, U25-ch2, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 33
Mean 58.58
Std Dev 10.59
Entry
ampl
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3
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7
8
9
0
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30
40
50
60
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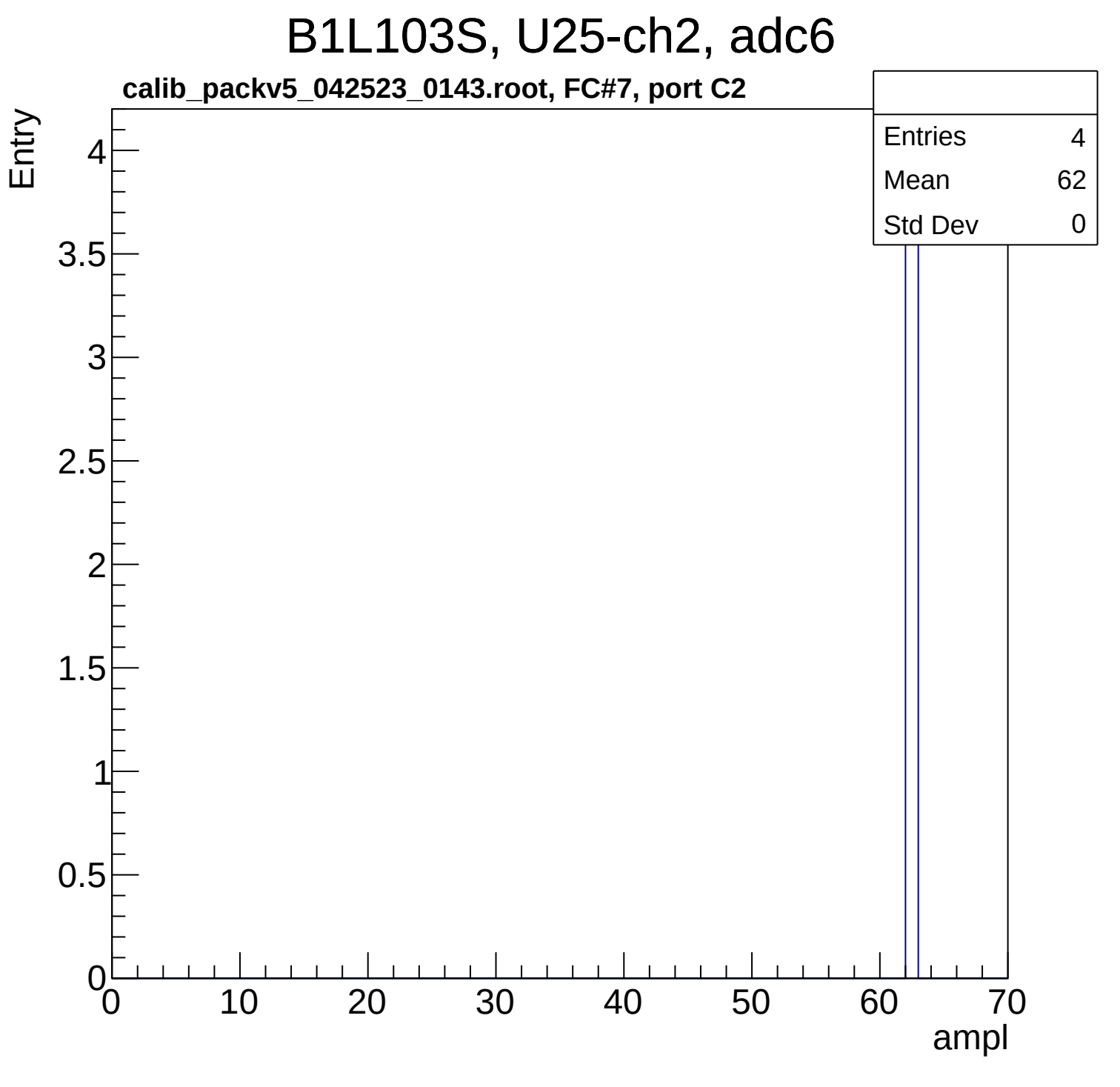
B1L103S, U25-ch2, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62
Std Dev 0
Entry
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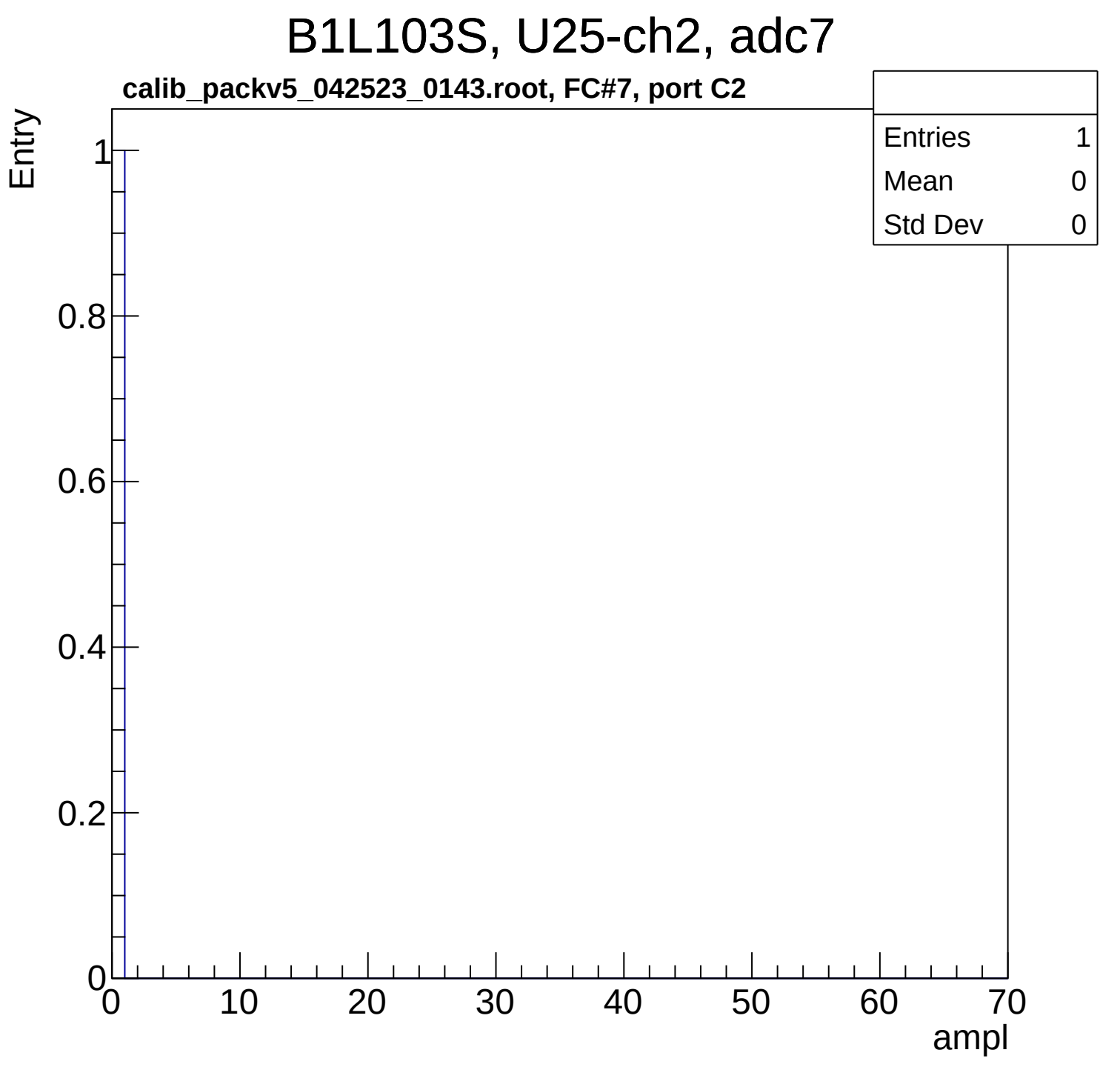
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch2, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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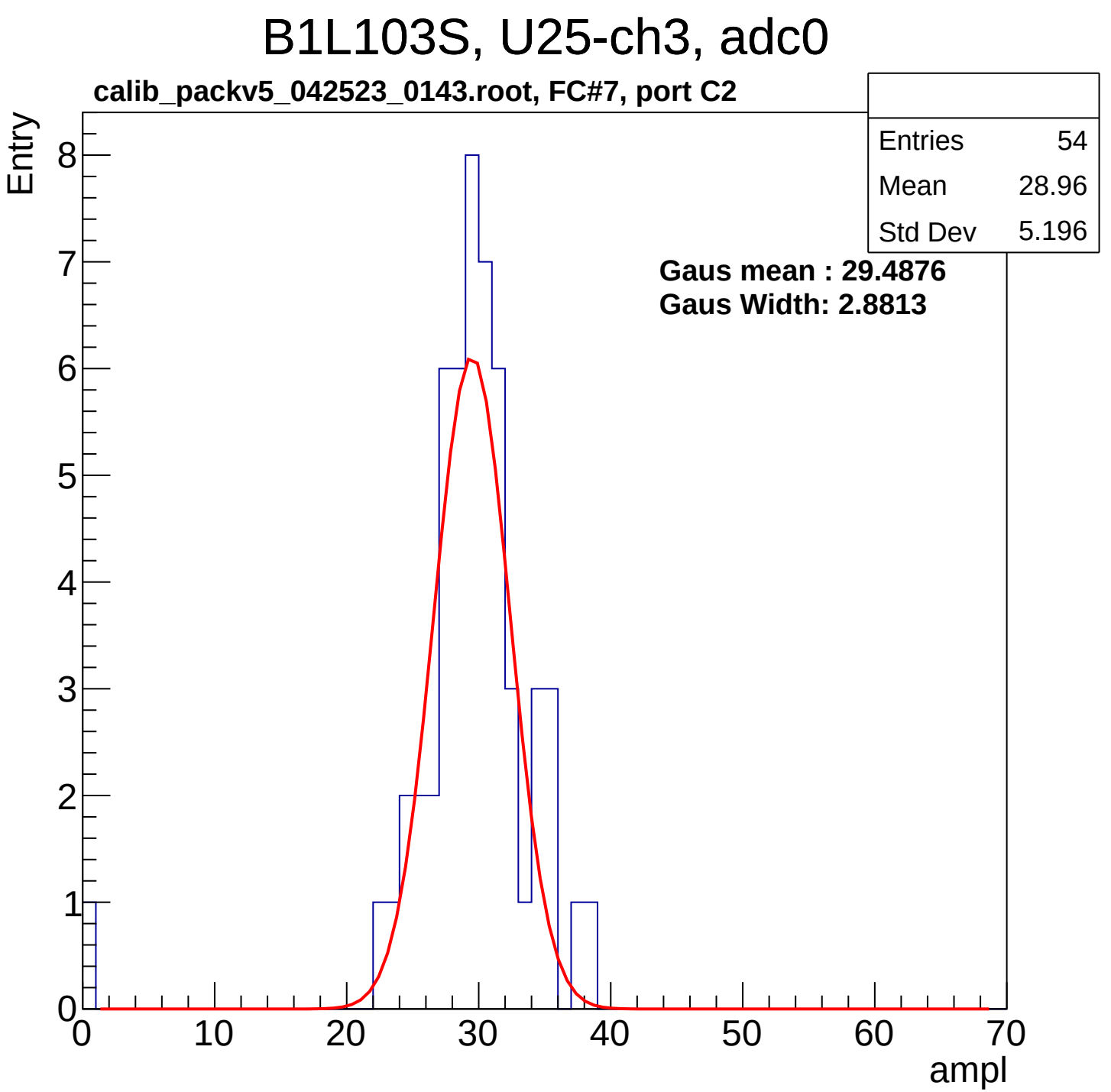
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch3, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 28.96
Std Dev 5.196
Gaus mean : 29.4876
Gaus Width: 2.8813
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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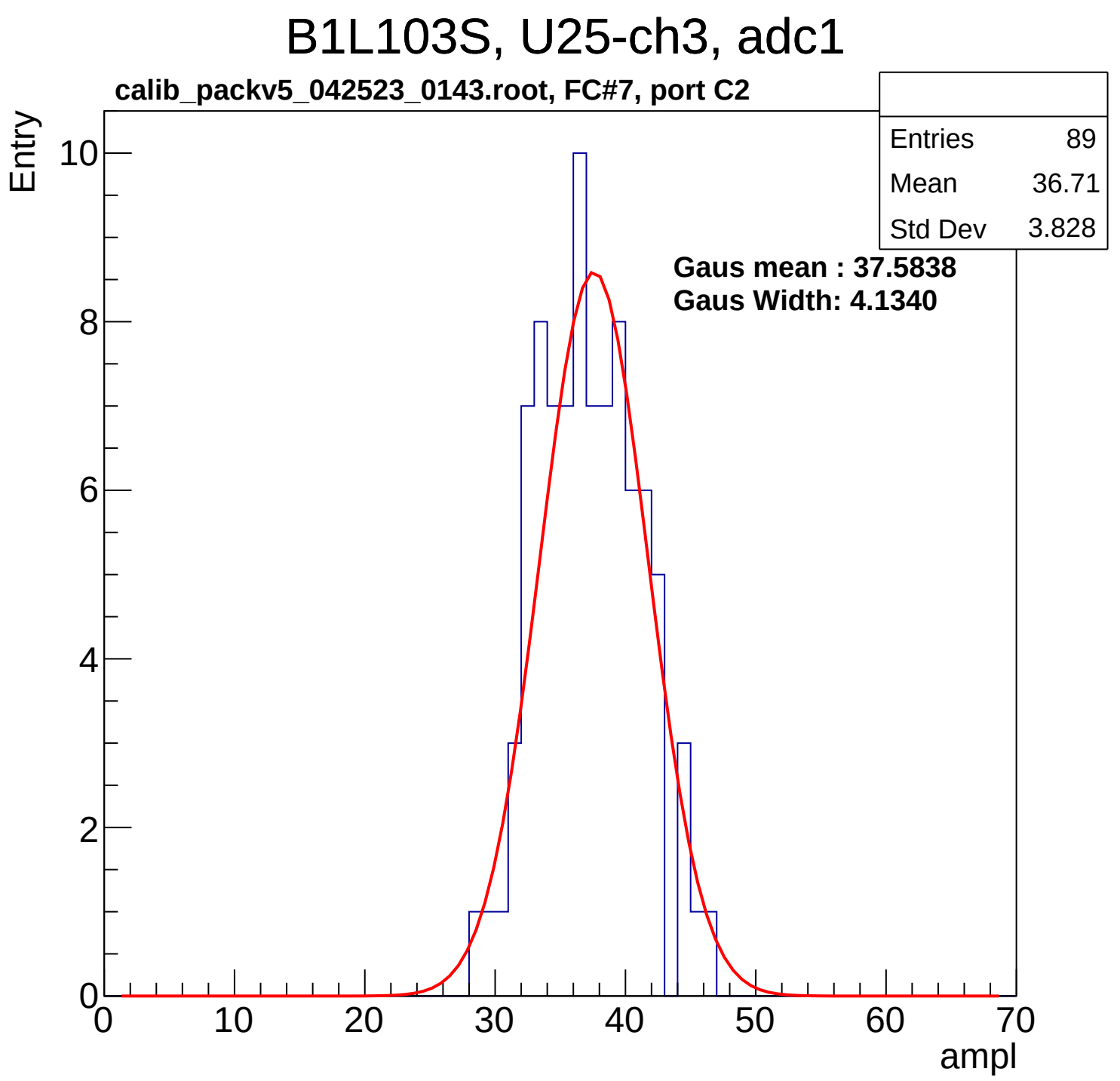
60
70


B1L103S, U25-ch3, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 89
Mean 36.71
Std Dev 3.828
Gaus mean : 37.5838
Gaus Width: 4.1340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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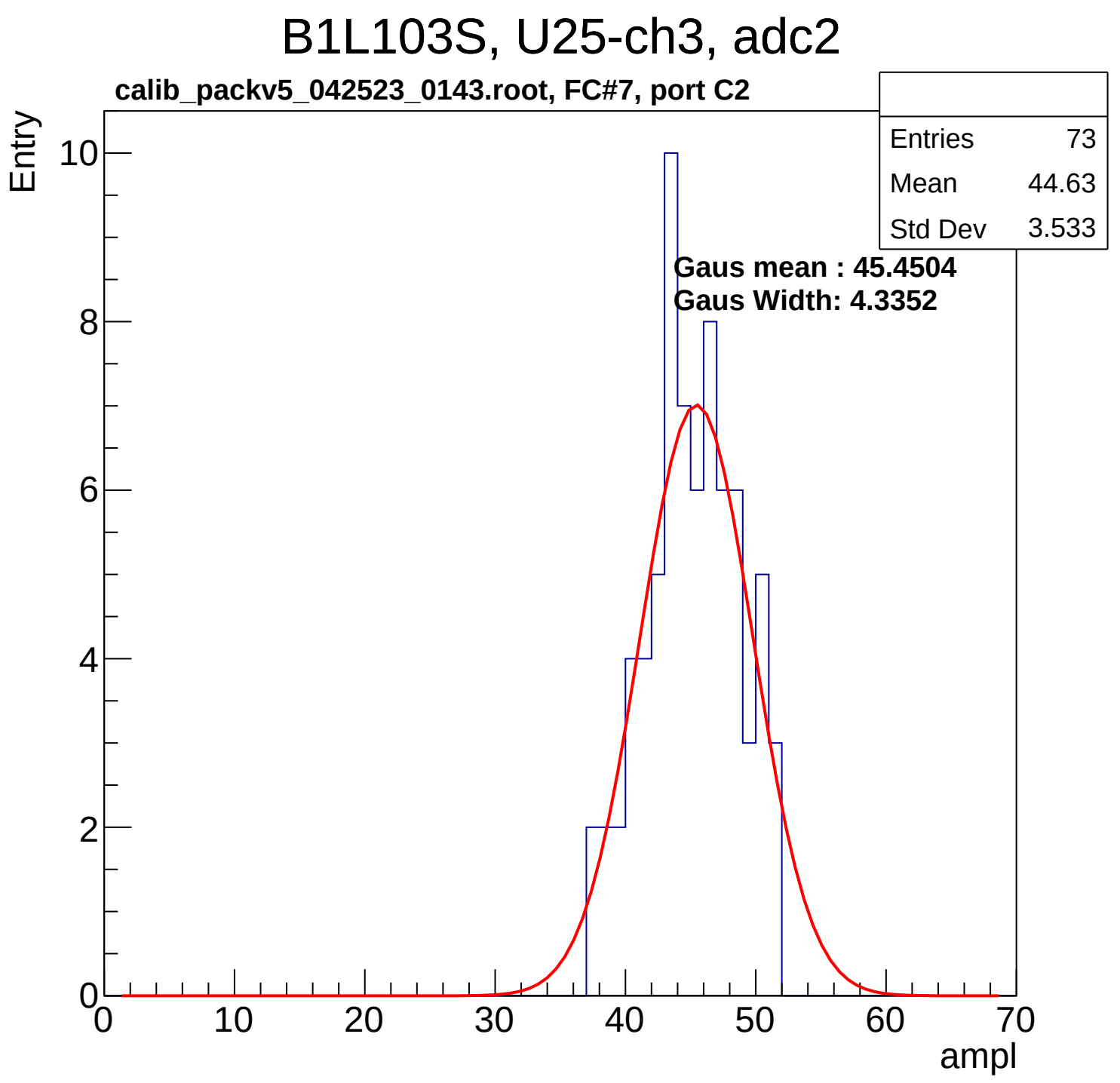
B1L103S, U25-ch3, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 44.63
Std Dev 3.533
Gaus mean : 45.4504
Gaus Width: 4.3352
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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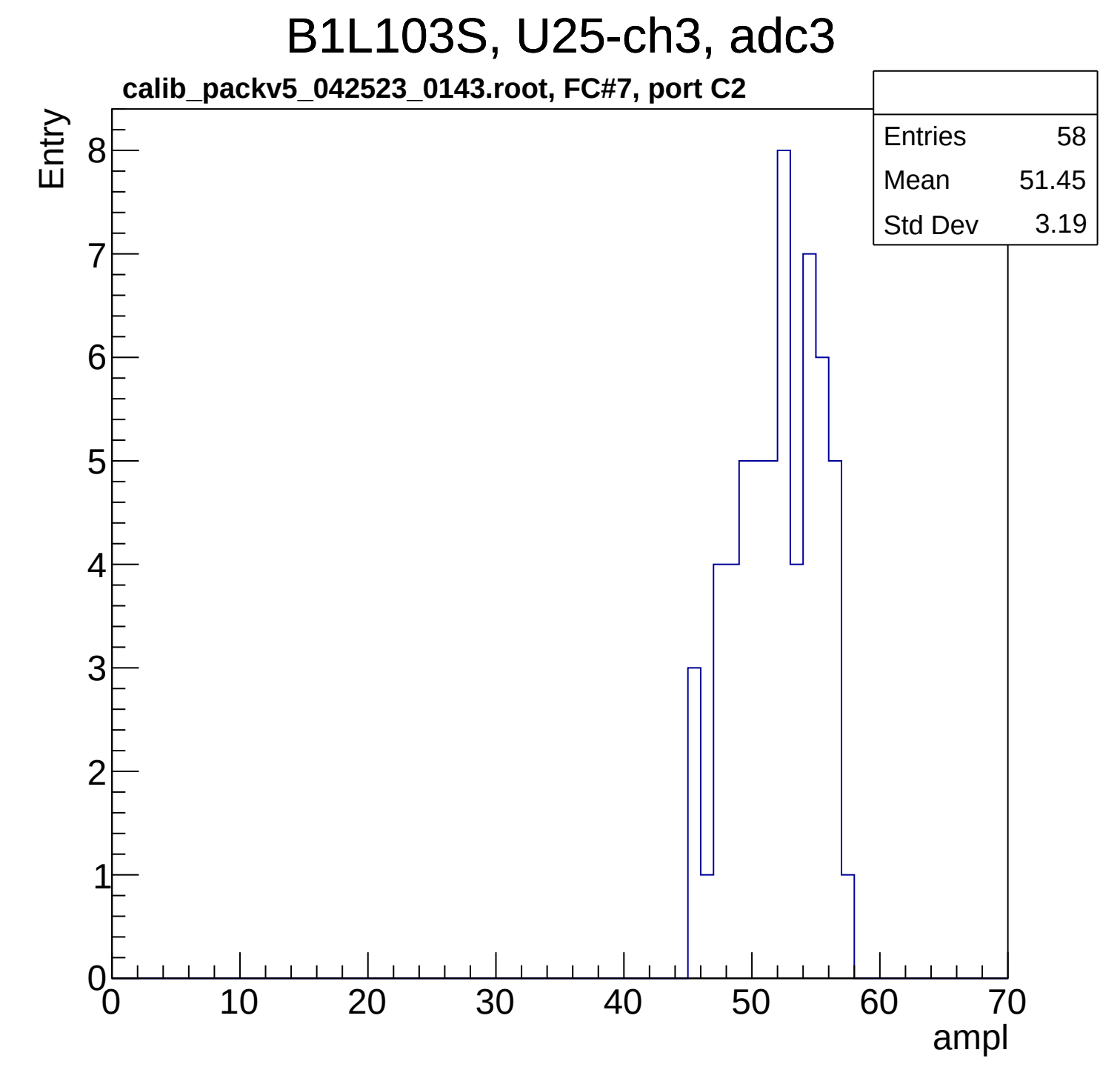
B1L103S, U25-ch3, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 51.45
Std Dev 3.19
Entry
ampl
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3
4
5
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7
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40
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60
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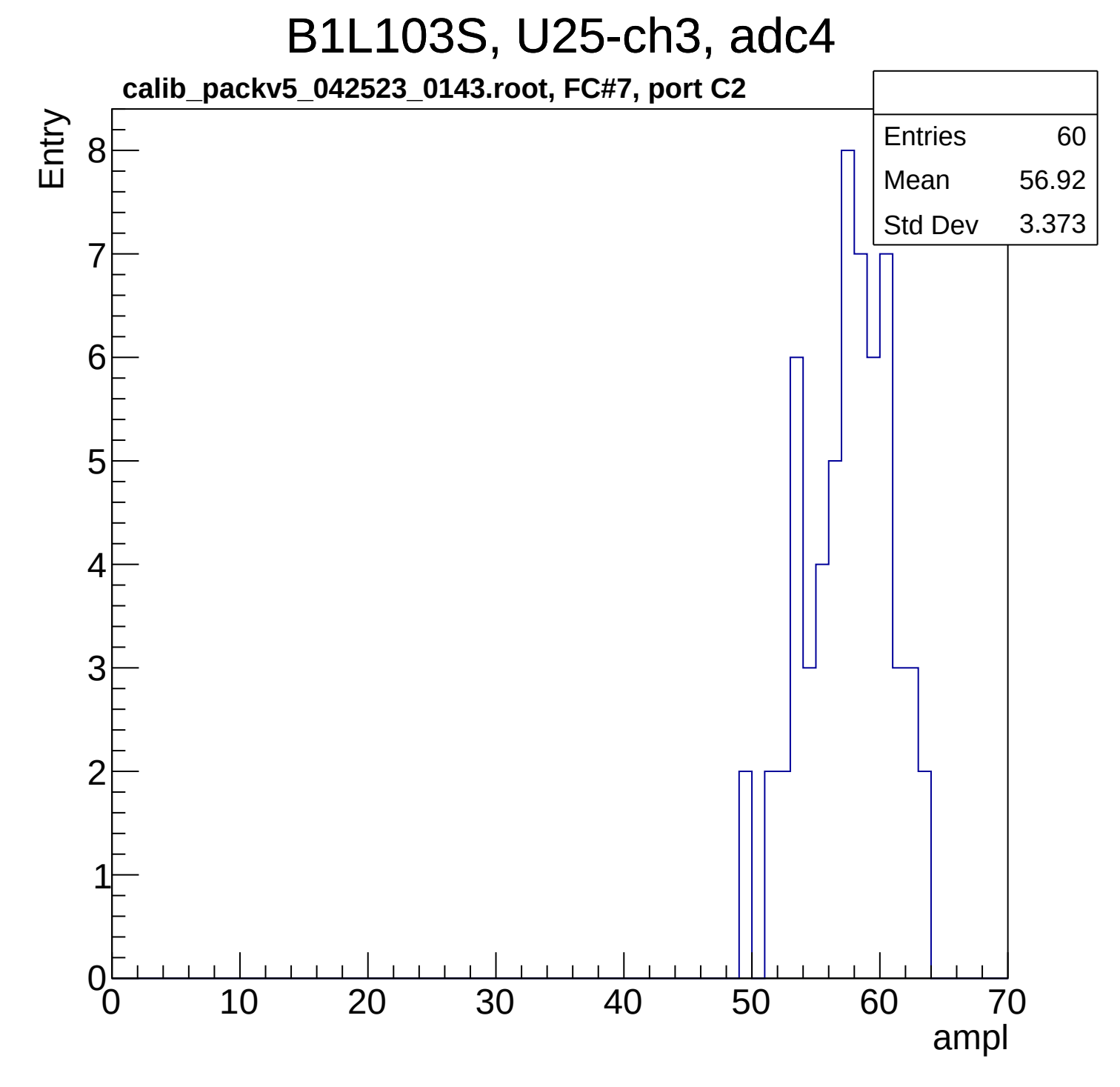
B1L103S, U25-ch3, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 56.92
Std Dev 3.373
Entry
ampl
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1
2
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4
5
6
7
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0
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40
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60
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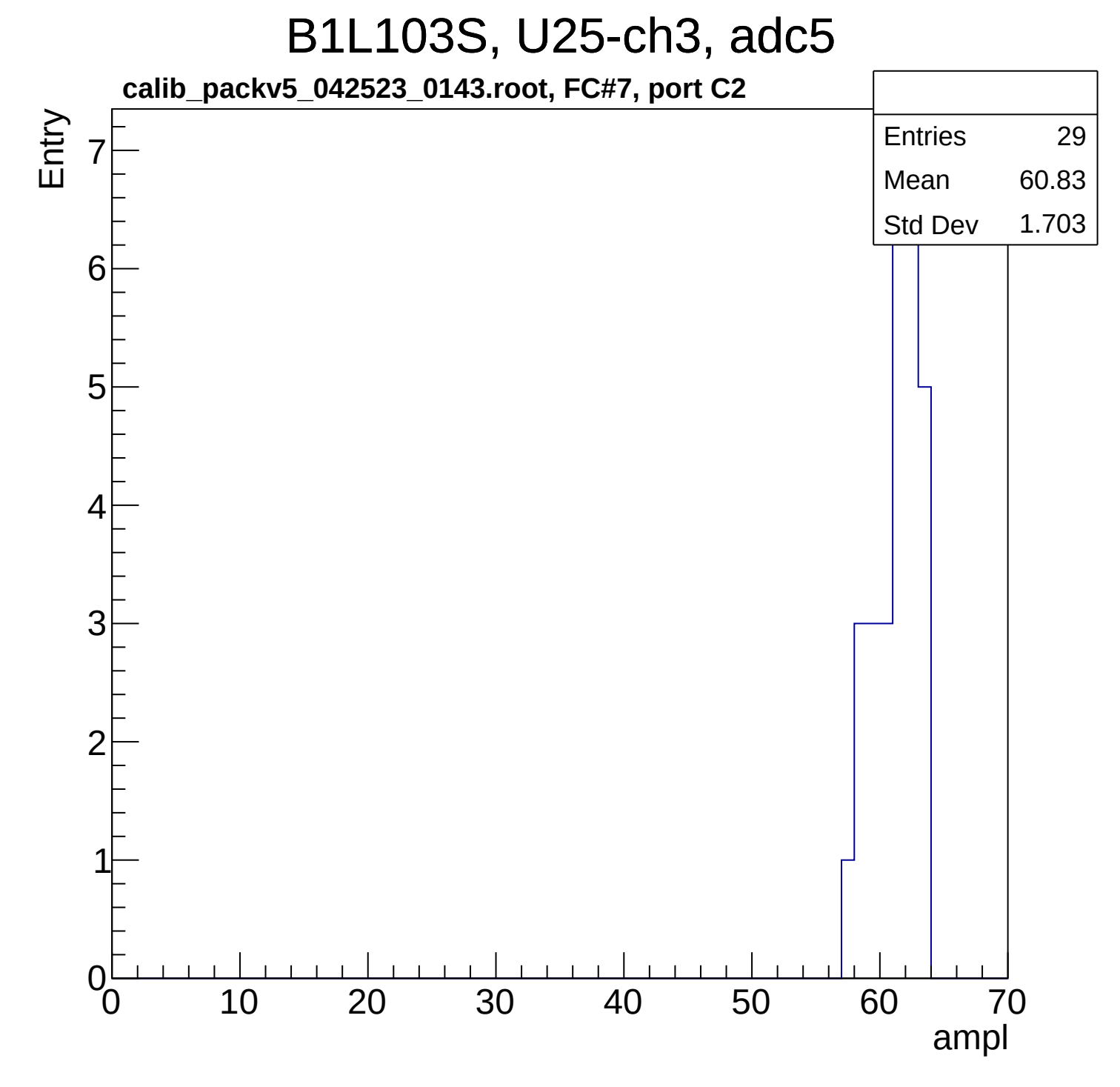
B1L103S, U25-ch3, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 29
Mean 60.83
Std Dev 1.703
Entry
ampl
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3
4
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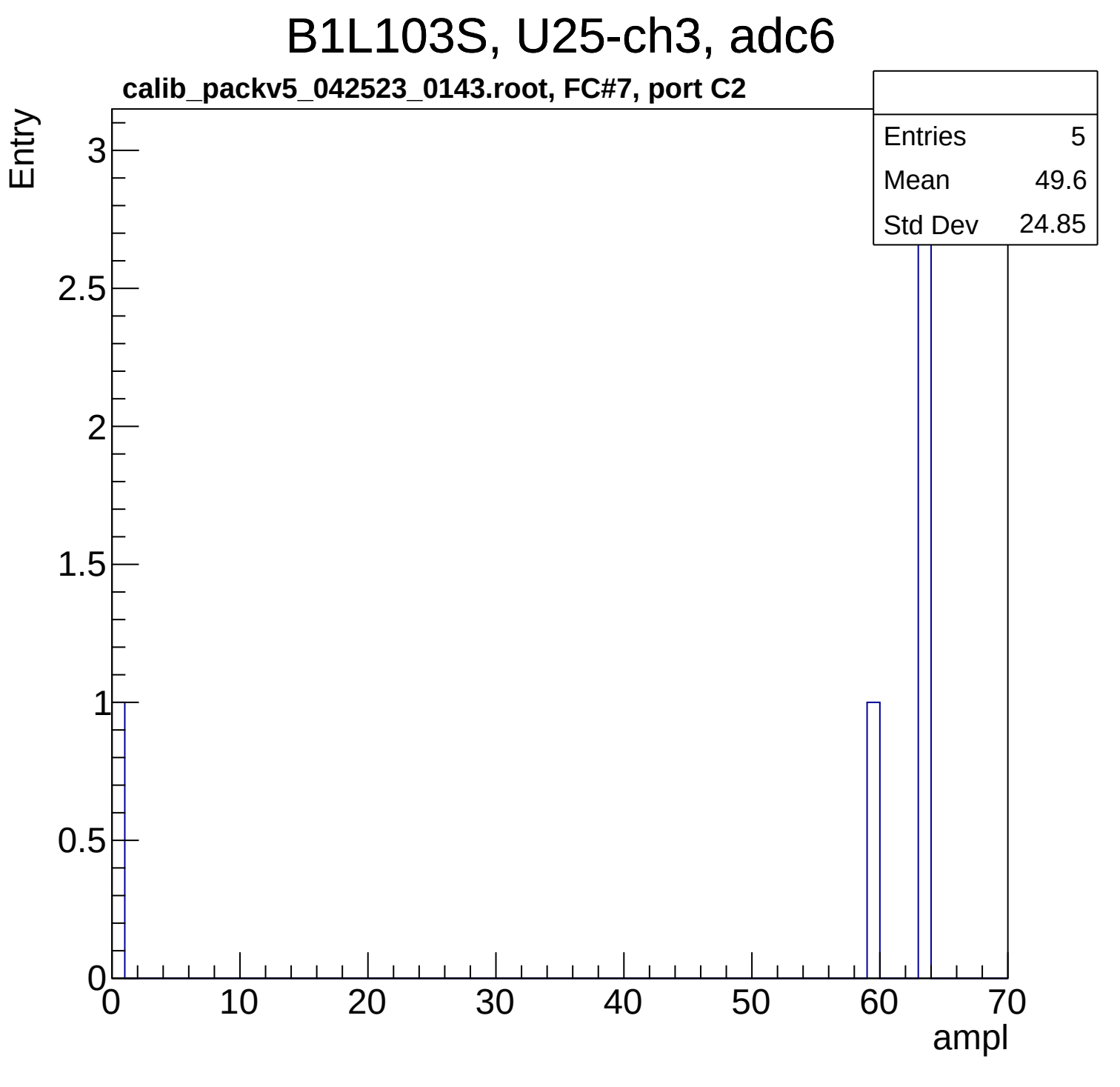
0
10
20
30
40
50
60
70


B1L103S, U25-ch3, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.6
Std Dev 24.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
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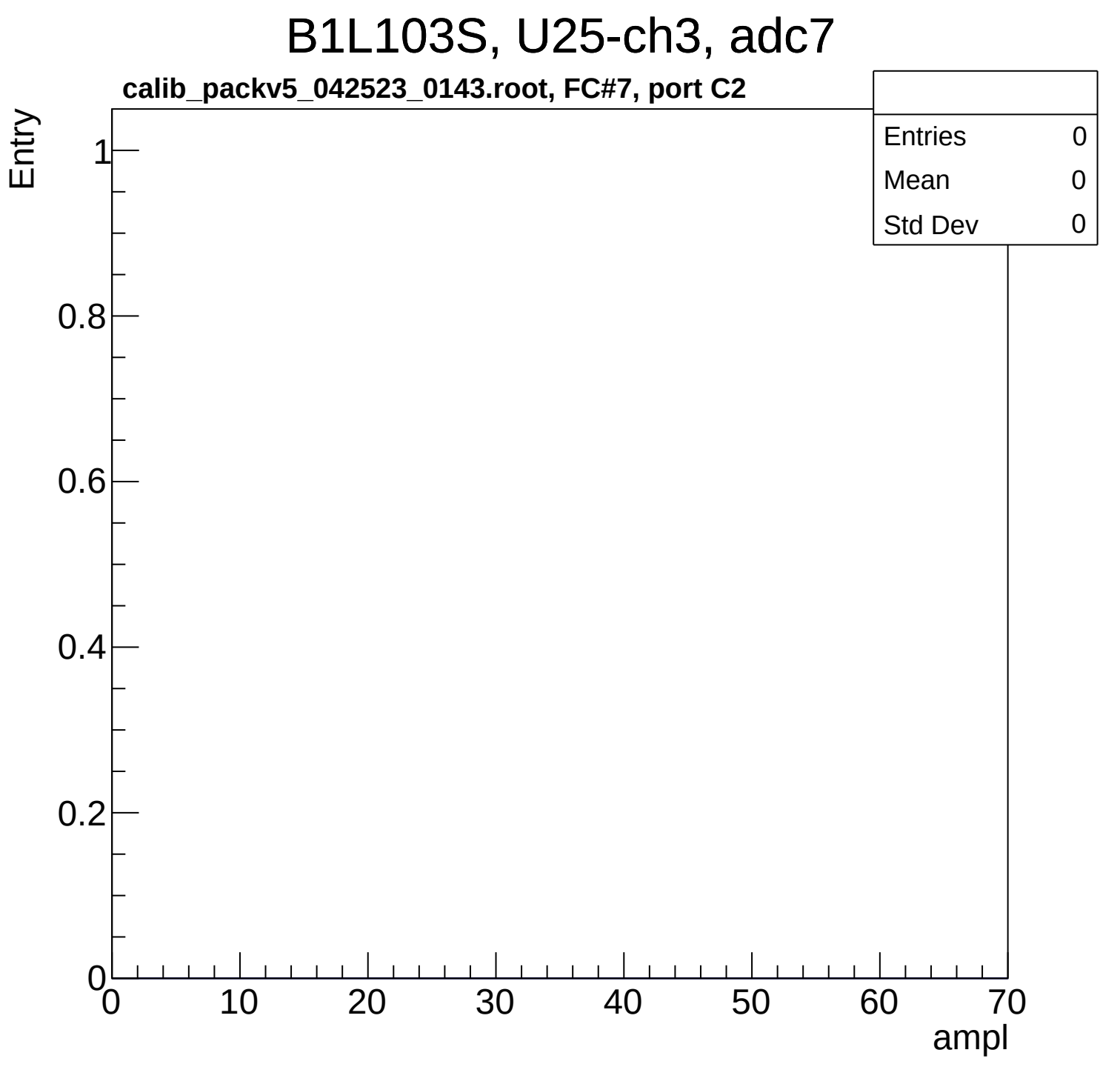
B1L103S, U25-ch3, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
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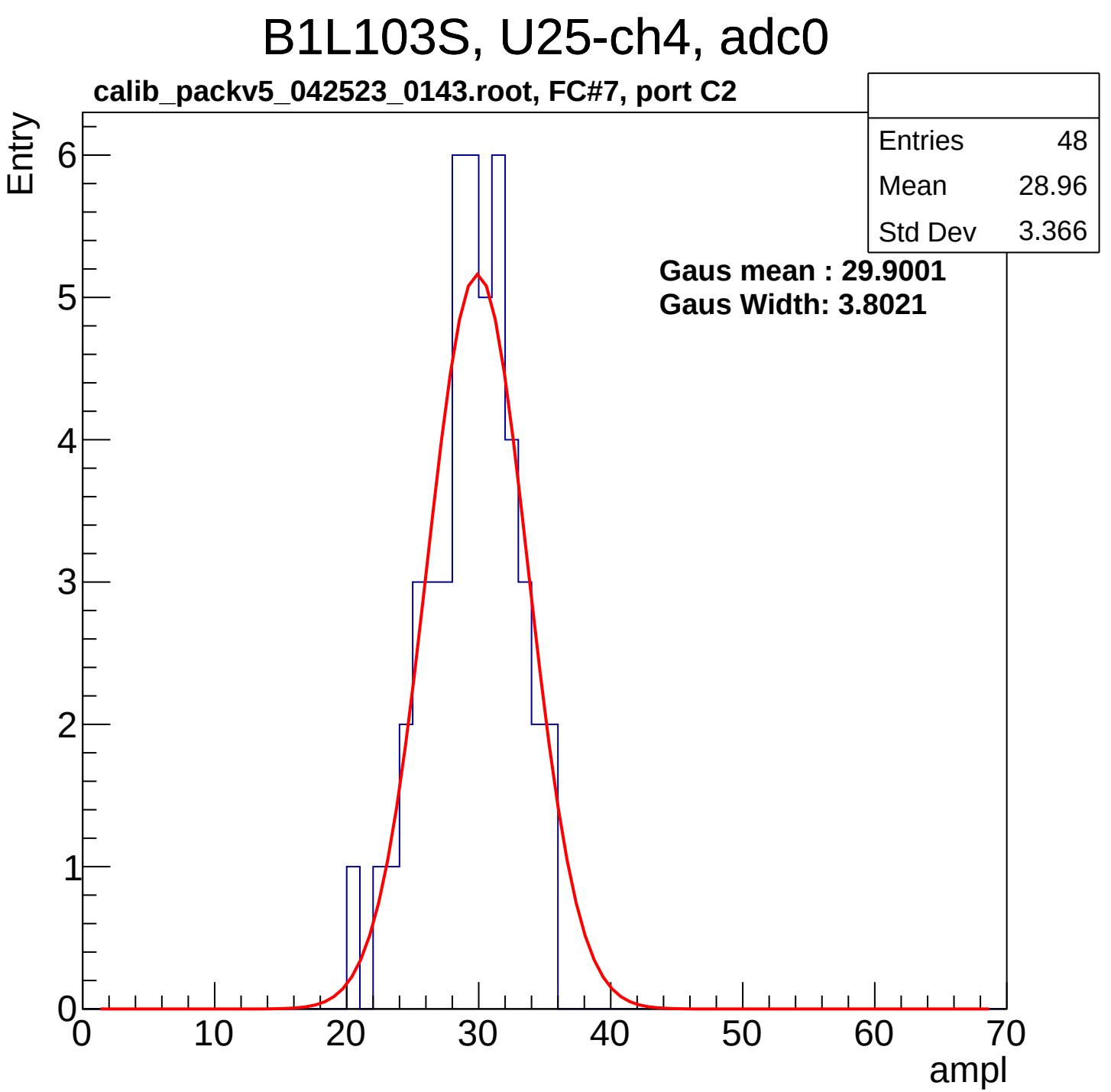
ampl


B1L103S, U25-ch4, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 28.96
Std Dev 3.366
Gaus mean : 29.9001
Gaus Width: 3.8021
Entry
ampl
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1
2
3
4
5
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0
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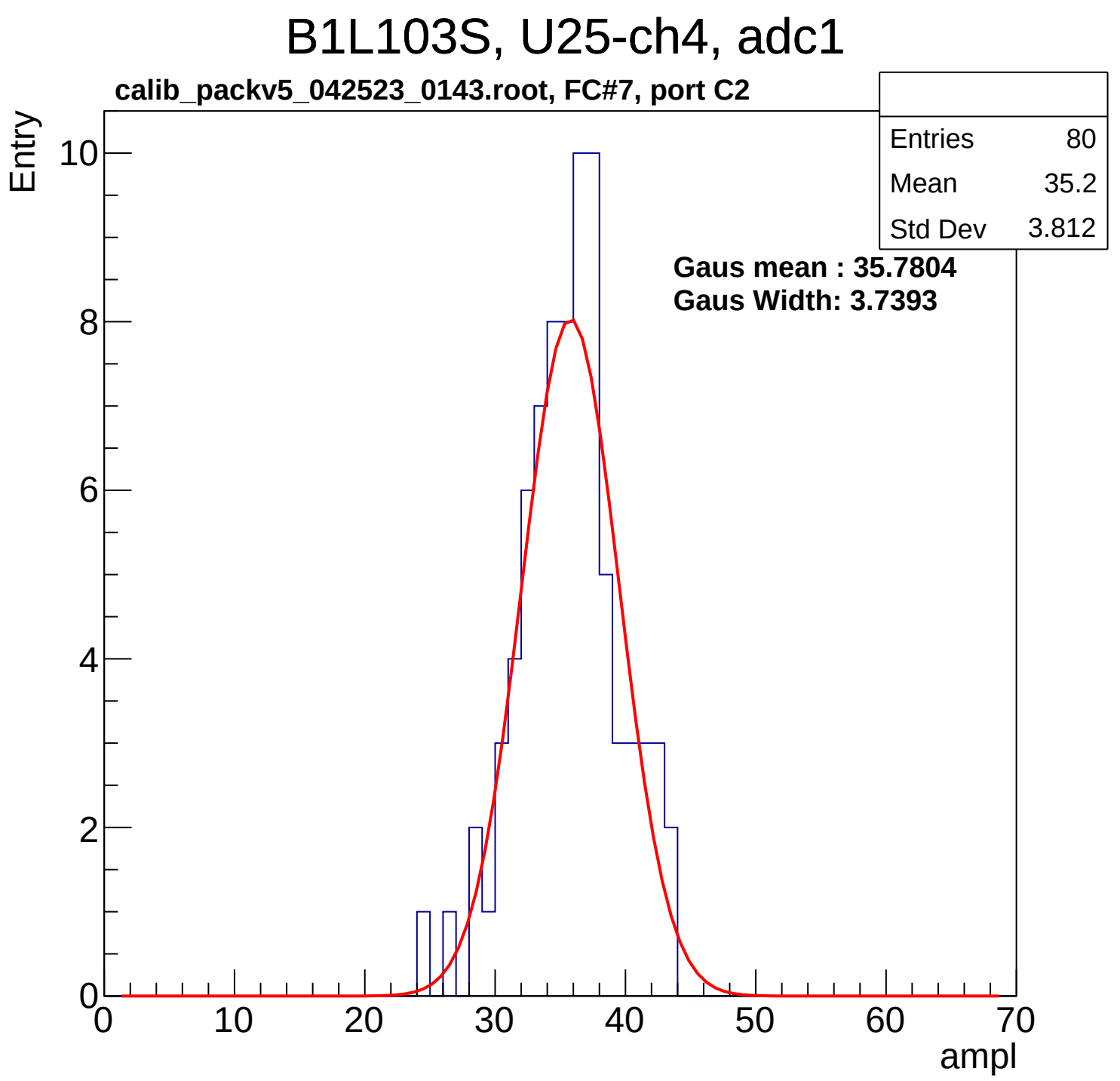
B1L103S, U25-ch4, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 35.2
Std Dev 3.812
Gaus mean : 35.7804
Gaus Width: 3.7393
Entry
ampl
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2
4
6
8
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0
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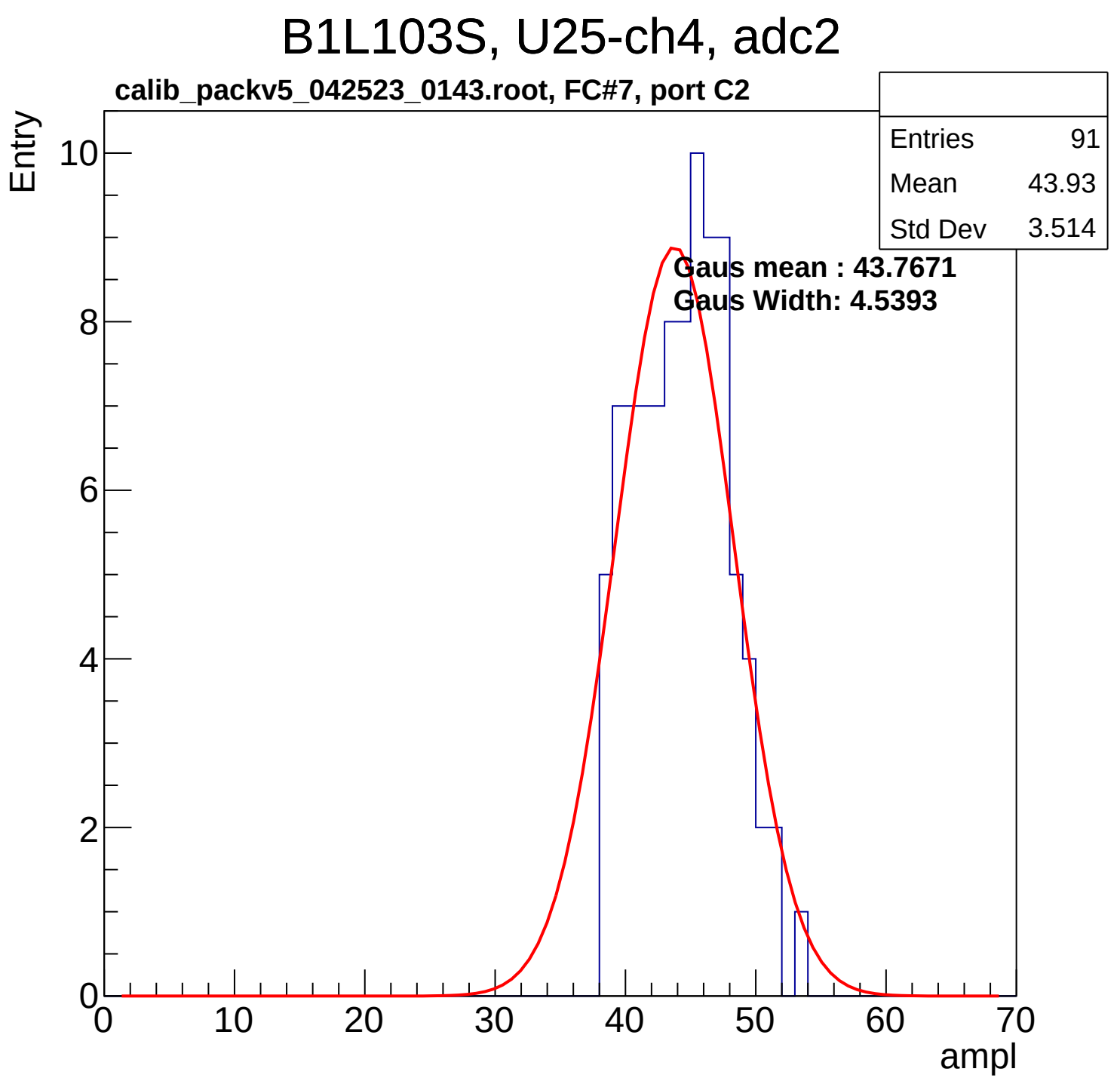
B1L103S, U25-ch4, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 43.93
Std Dev 3.514
Gaus mean : 43.7671
Gaus Width: 4.5393
Entry
ampl
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0
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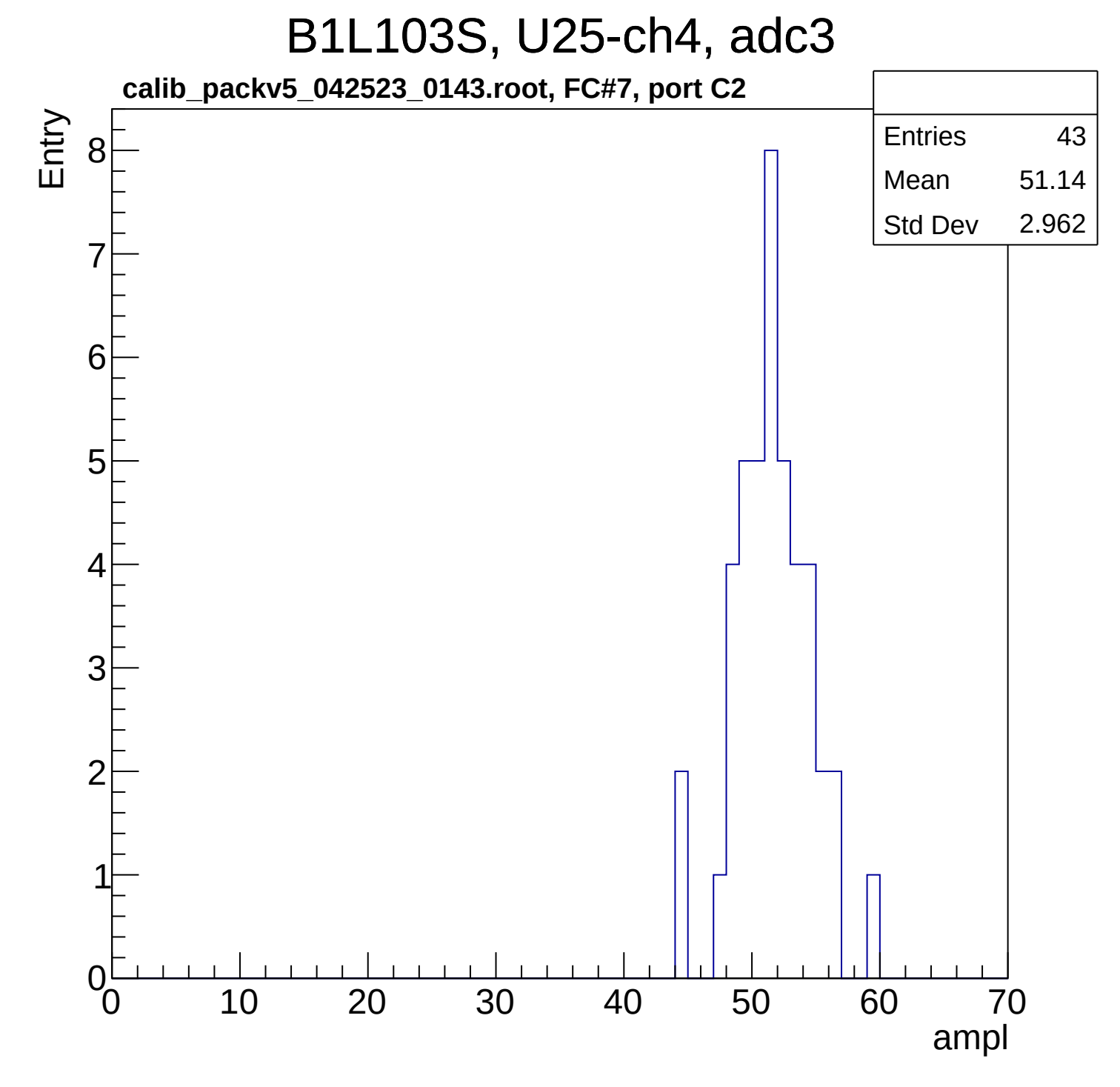
B1L103S, U25-ch4, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 51.14
Std Dev 2.962
Entry
ampl
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0
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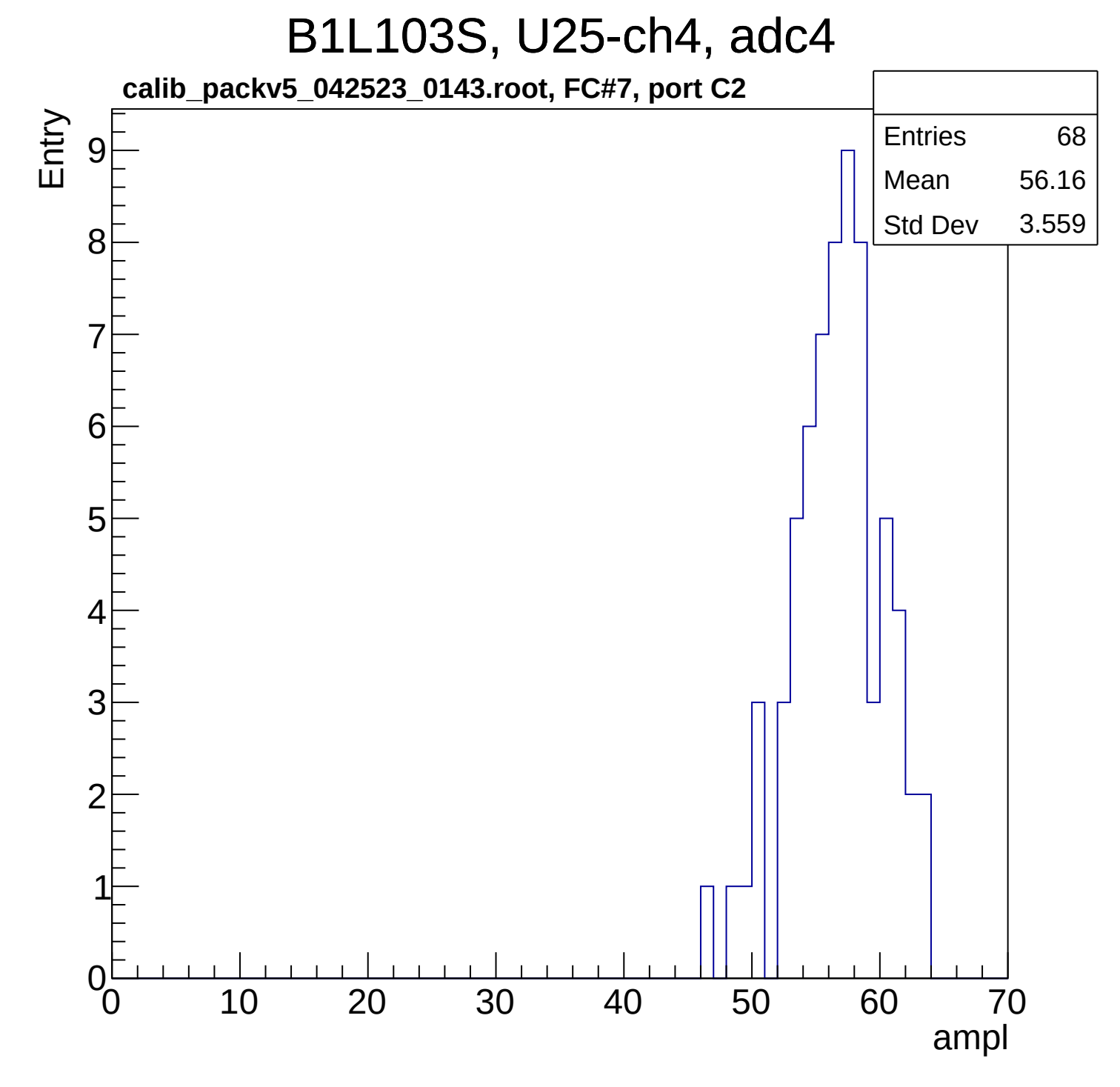
B1L103S, U25-ch4, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 56.16
Std Dev 3.559
Entry
ampl
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2
3
4
5
6
7
8
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0
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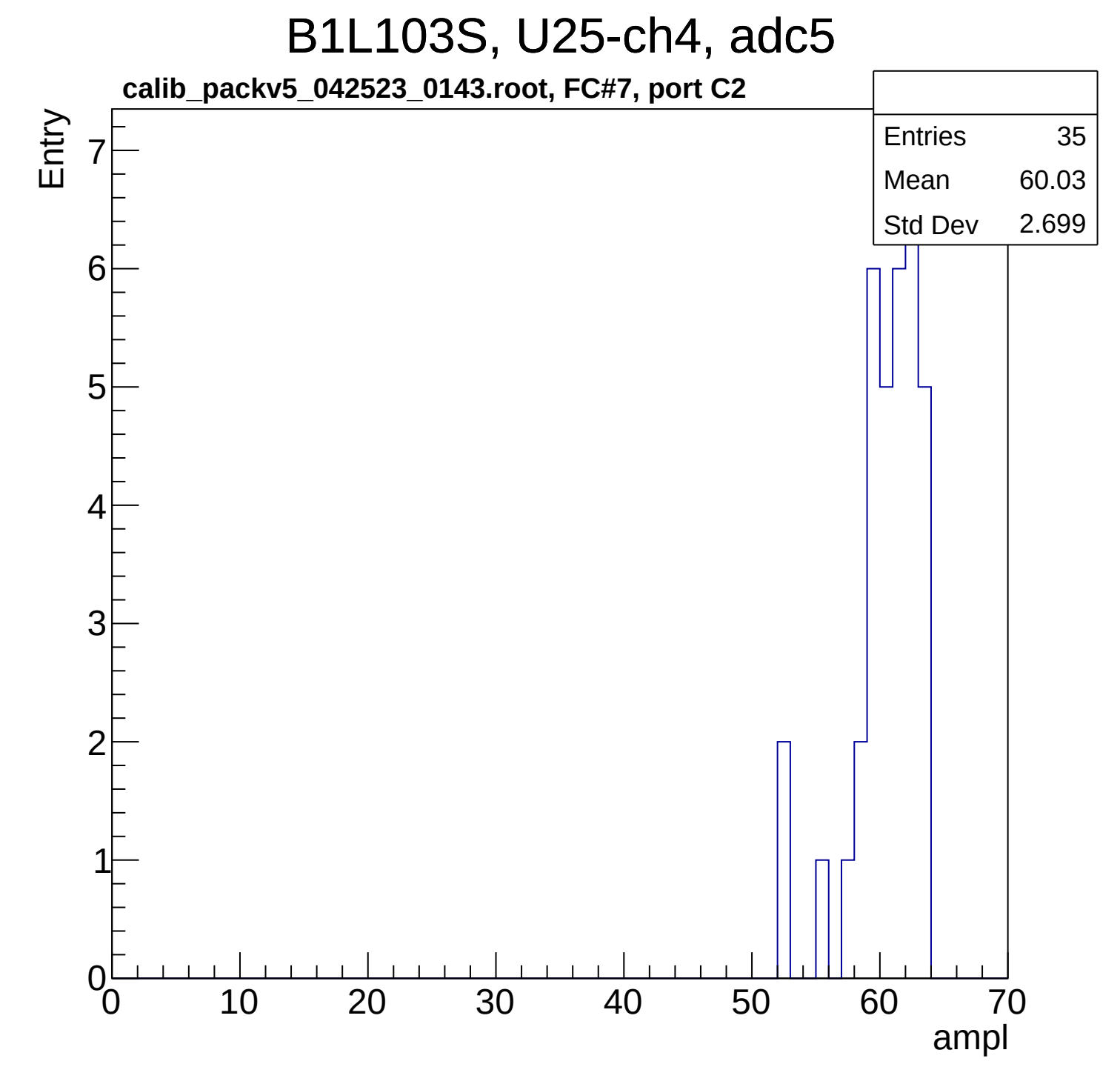
B1L103S, U25-ch4, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 60.03
Std Dev 2.699
Entry
ampl
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1
2
3
4
5
6
7
0
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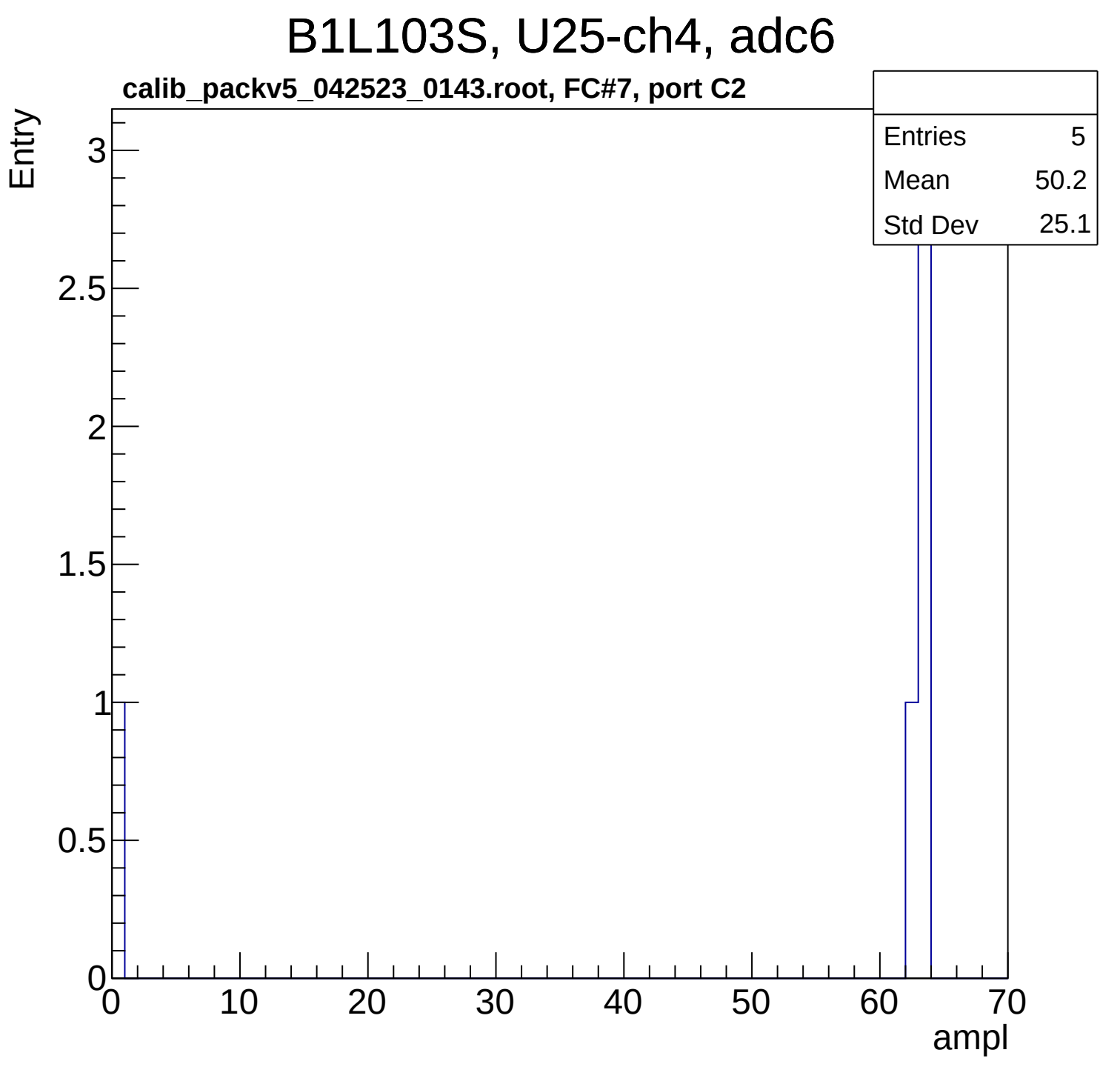
B1L103S, U25-ch4, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 50.2
Std Dev 25.1
Entry
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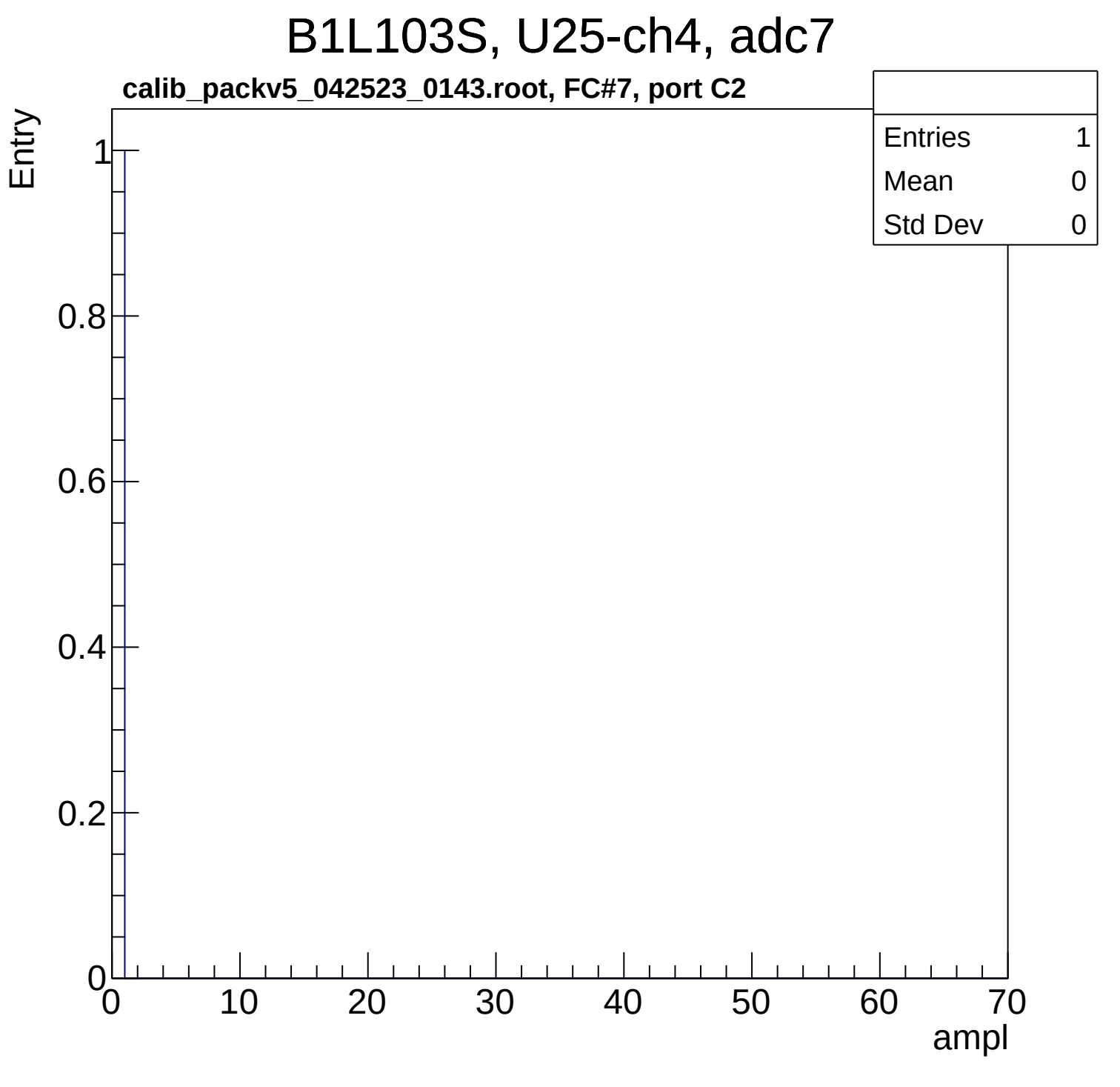
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch4, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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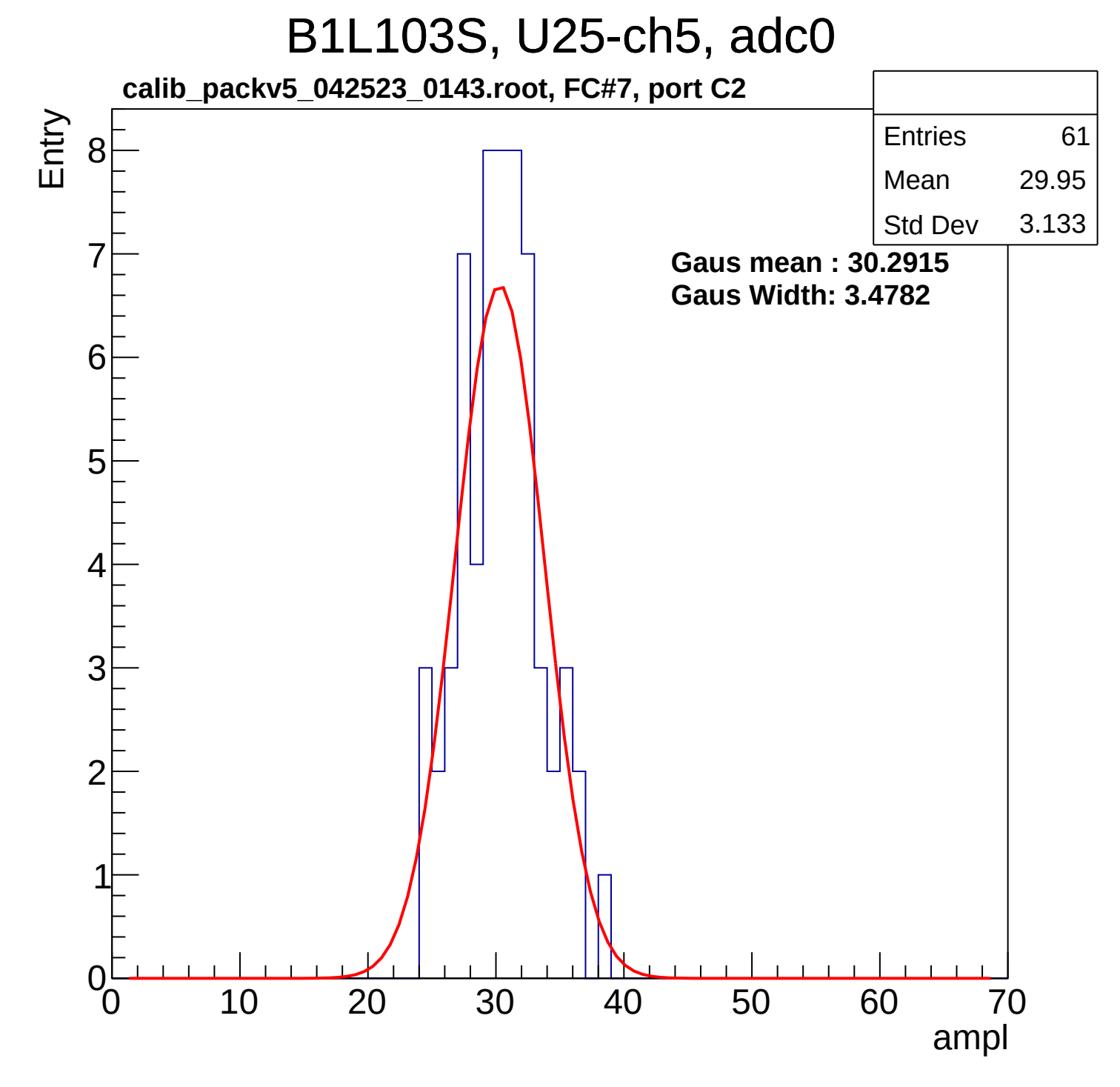
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch5, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 29.95
Std Dev 3.133
Gaus mean : 30.2915
Gaus Width: 3.4782
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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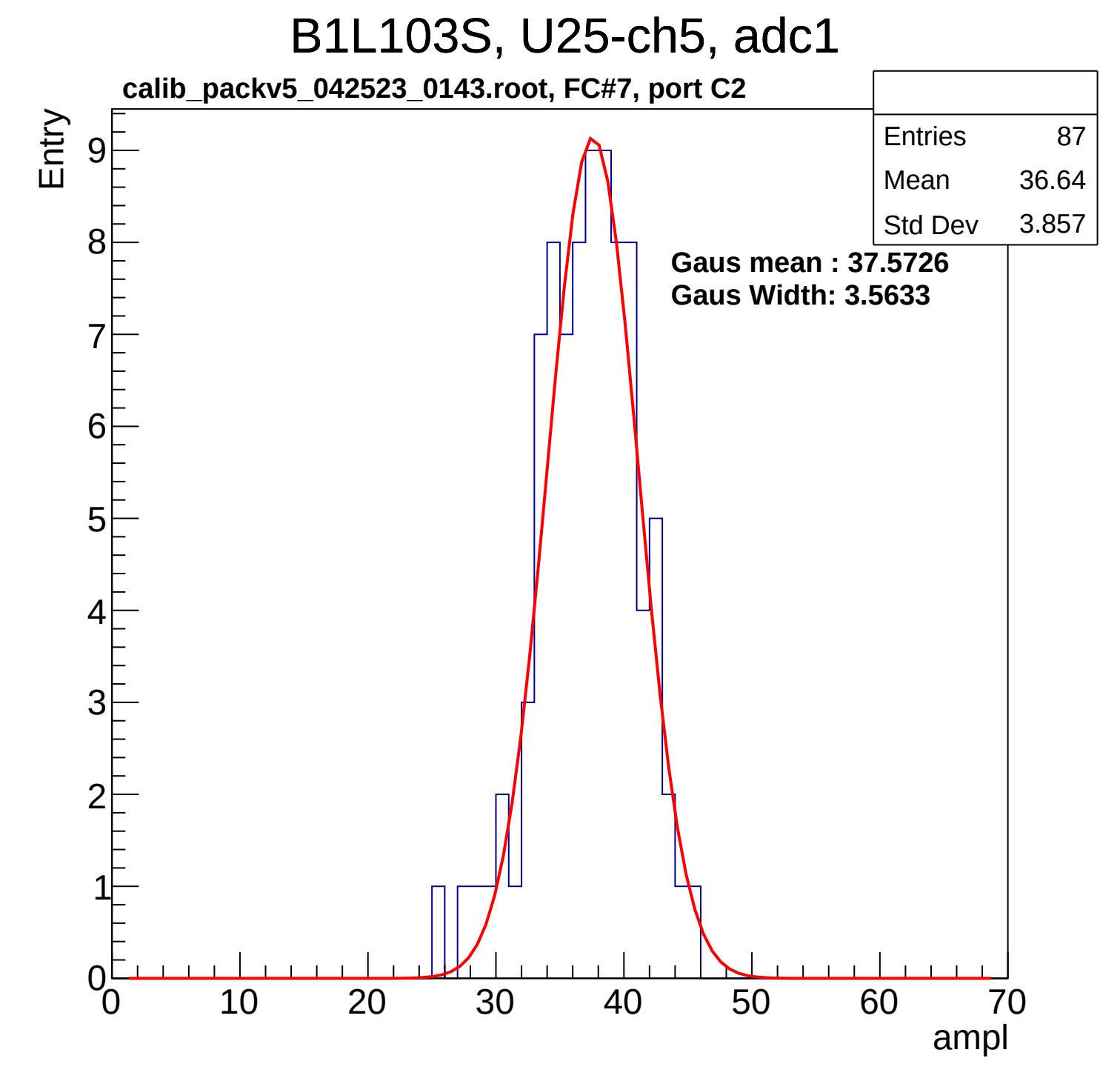
B1L103S, U25-ch5, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 36.64
Std Dev 3.857
Gaus mean : 37.5726
Gaus Width: 3.5633
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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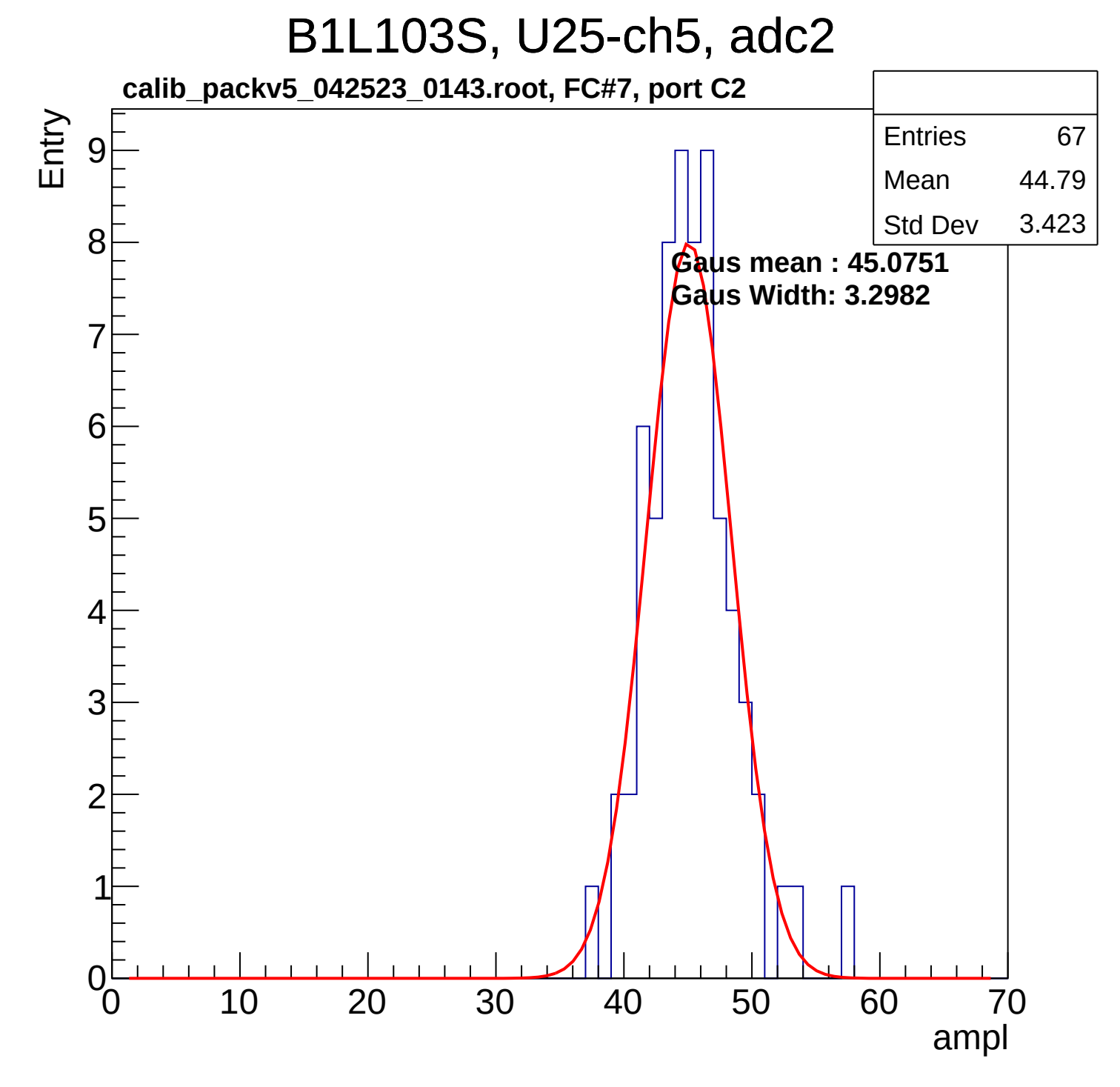
B1L103S, U25-ch5, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 44.79
Std Dev 3.423
Gaus mean : 45.0751
Gaus Width: 3.2982
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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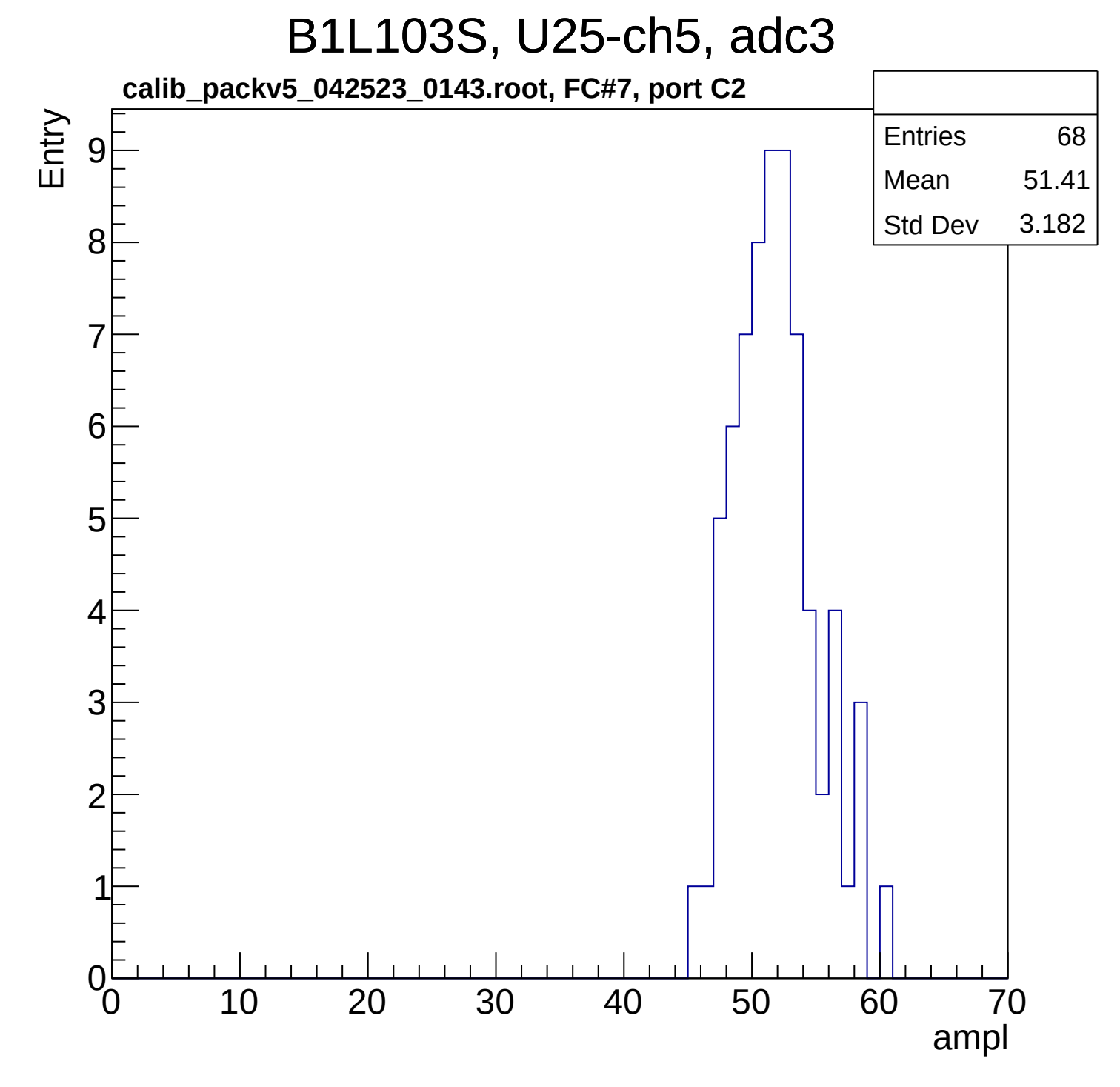
B1L103S, U25-ch5, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 51.41
Std Dev 3.182
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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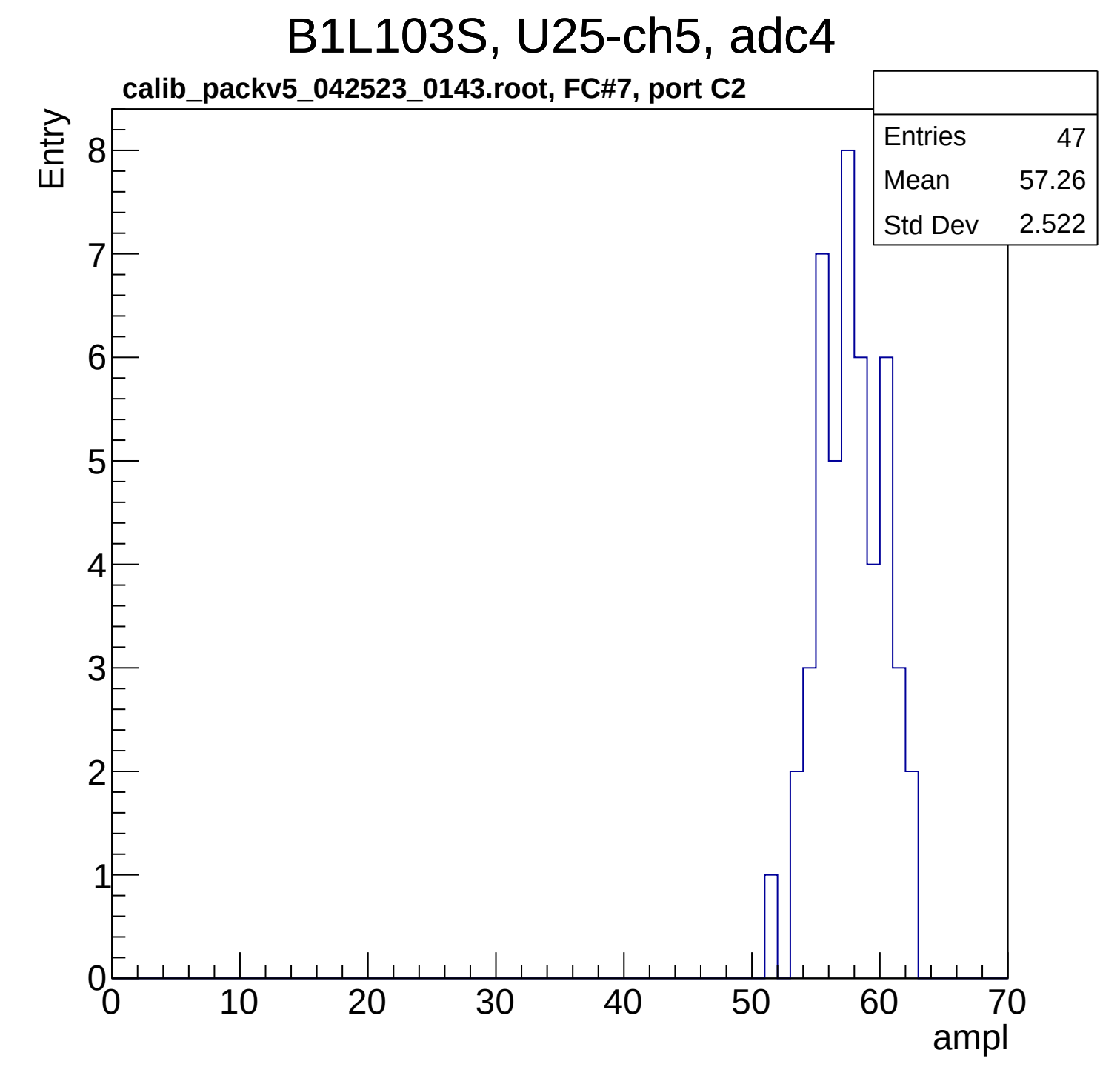
B1L103S, U25-ch5, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.26
Std Dev 2.522
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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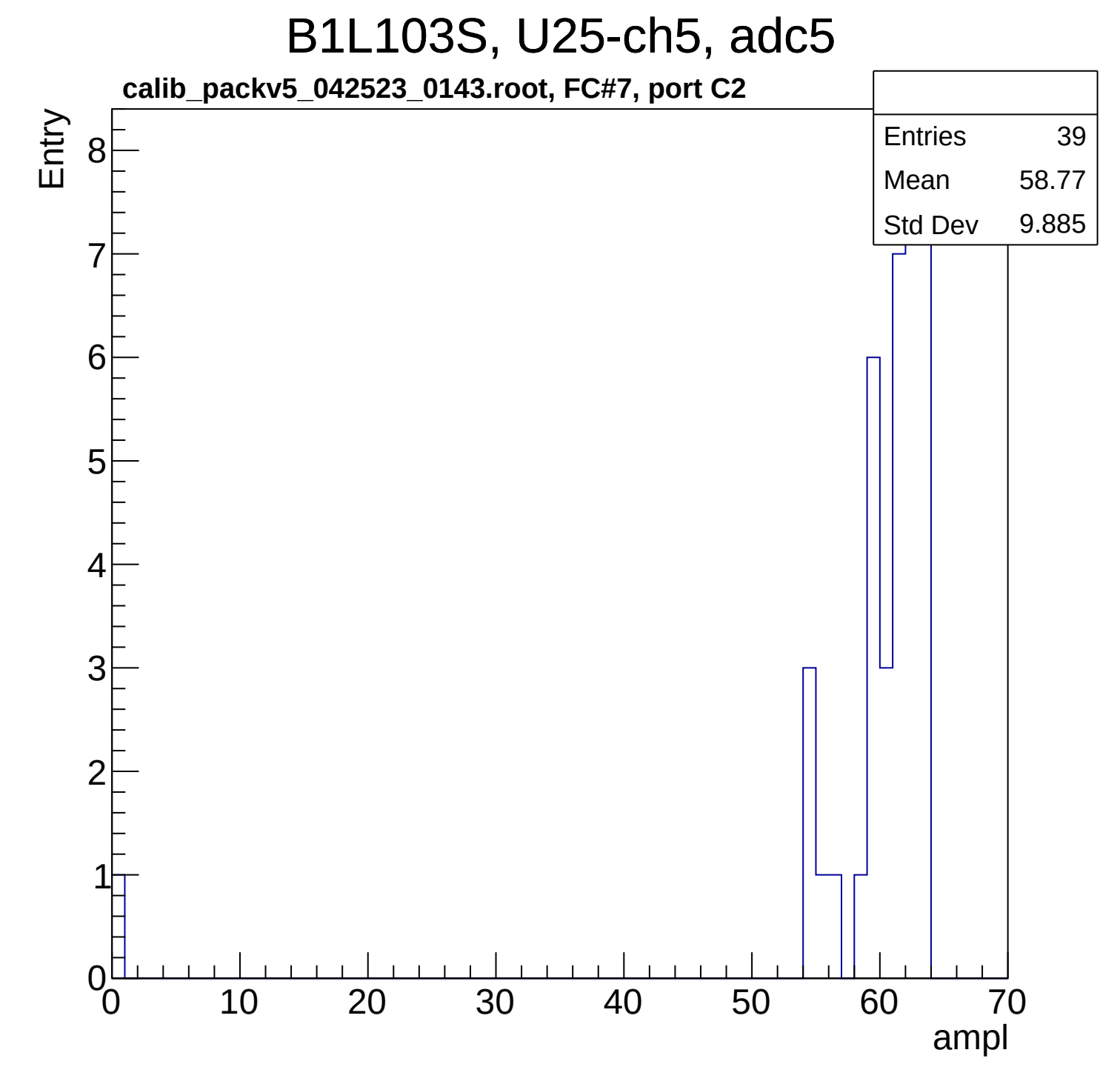
B1L103S, U25-ch5, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.77
Std Dev 9.885
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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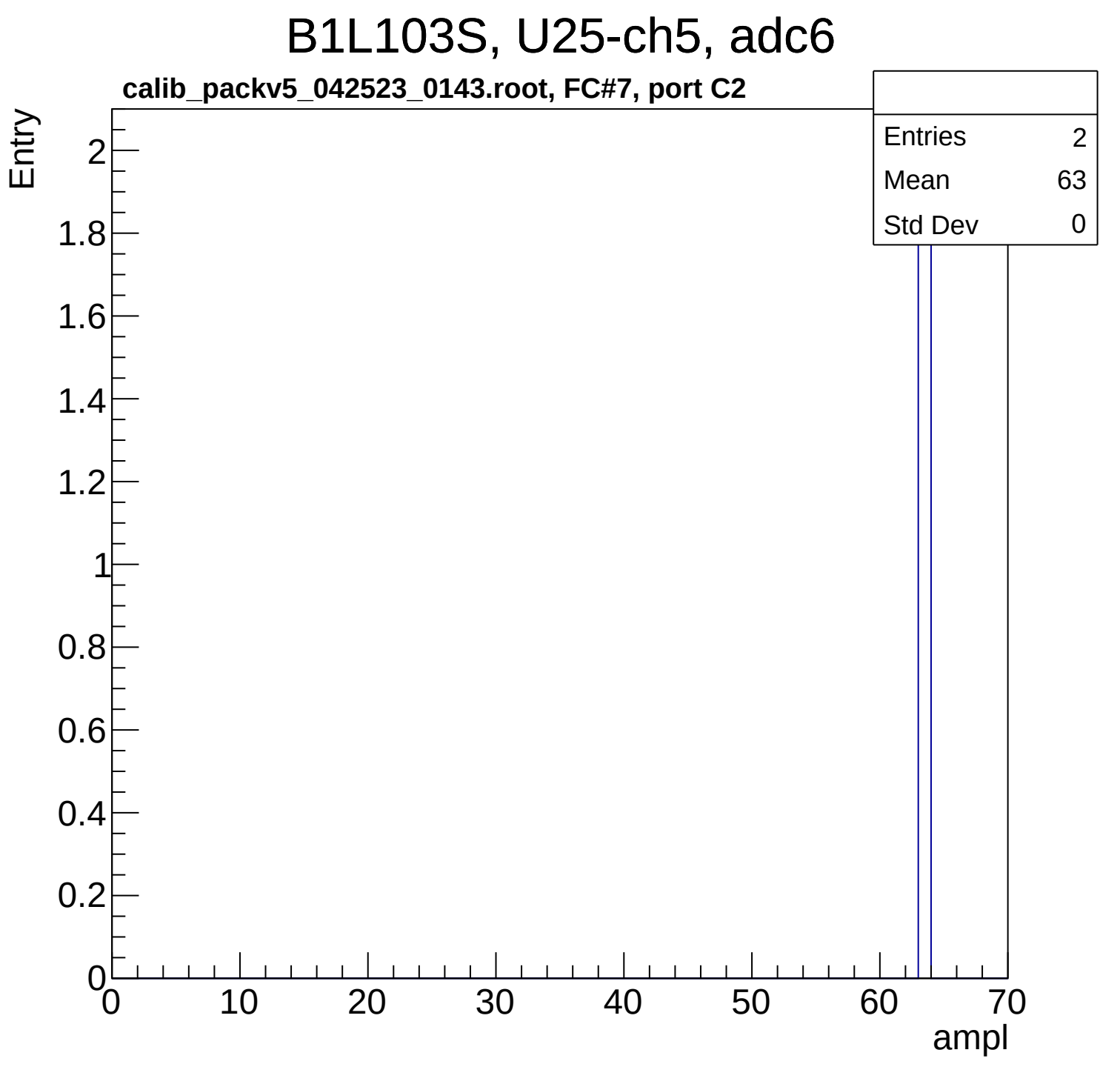
B1L103S, U25-ch5, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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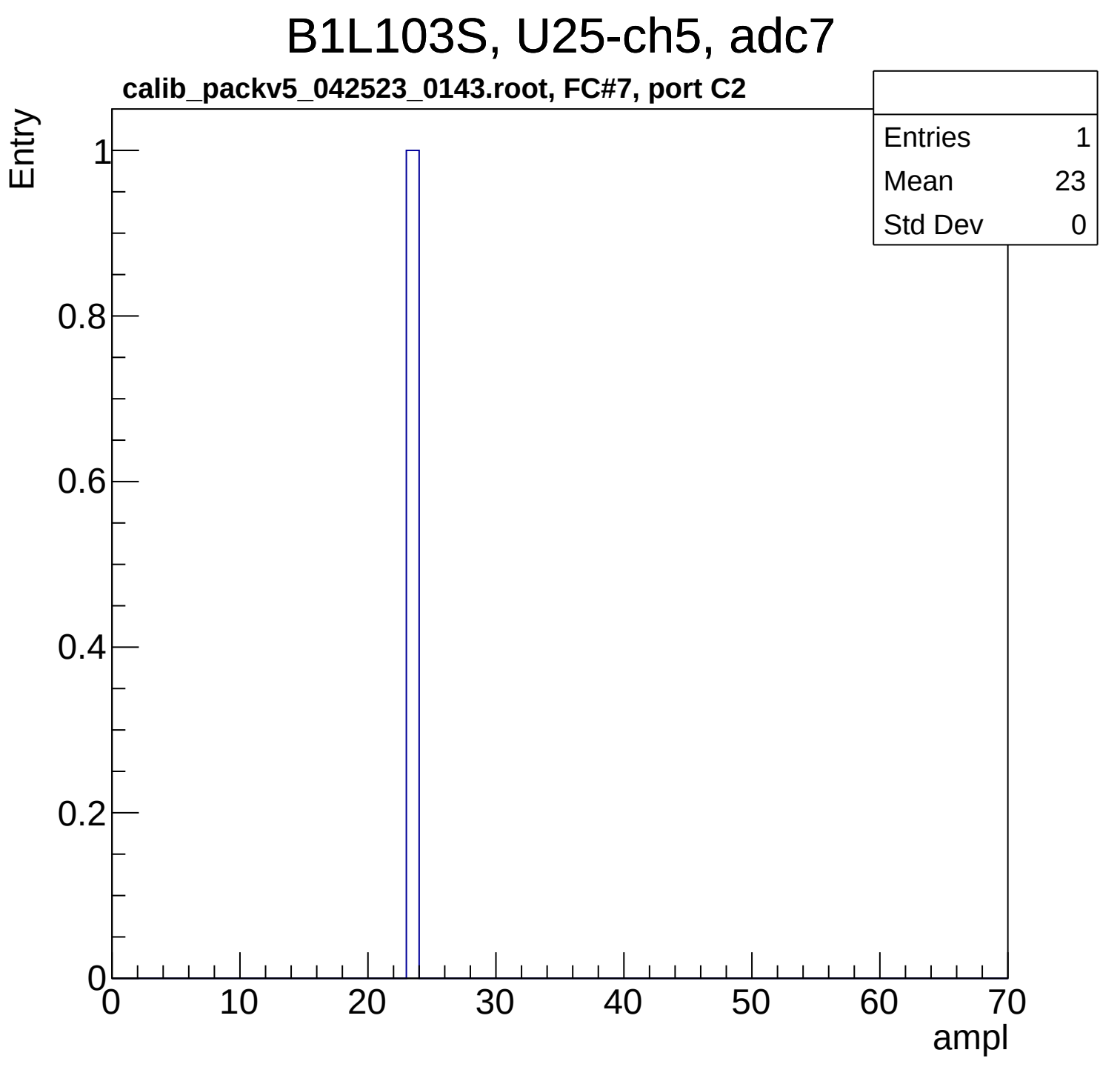
B1L103S, U25-ch5, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
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10
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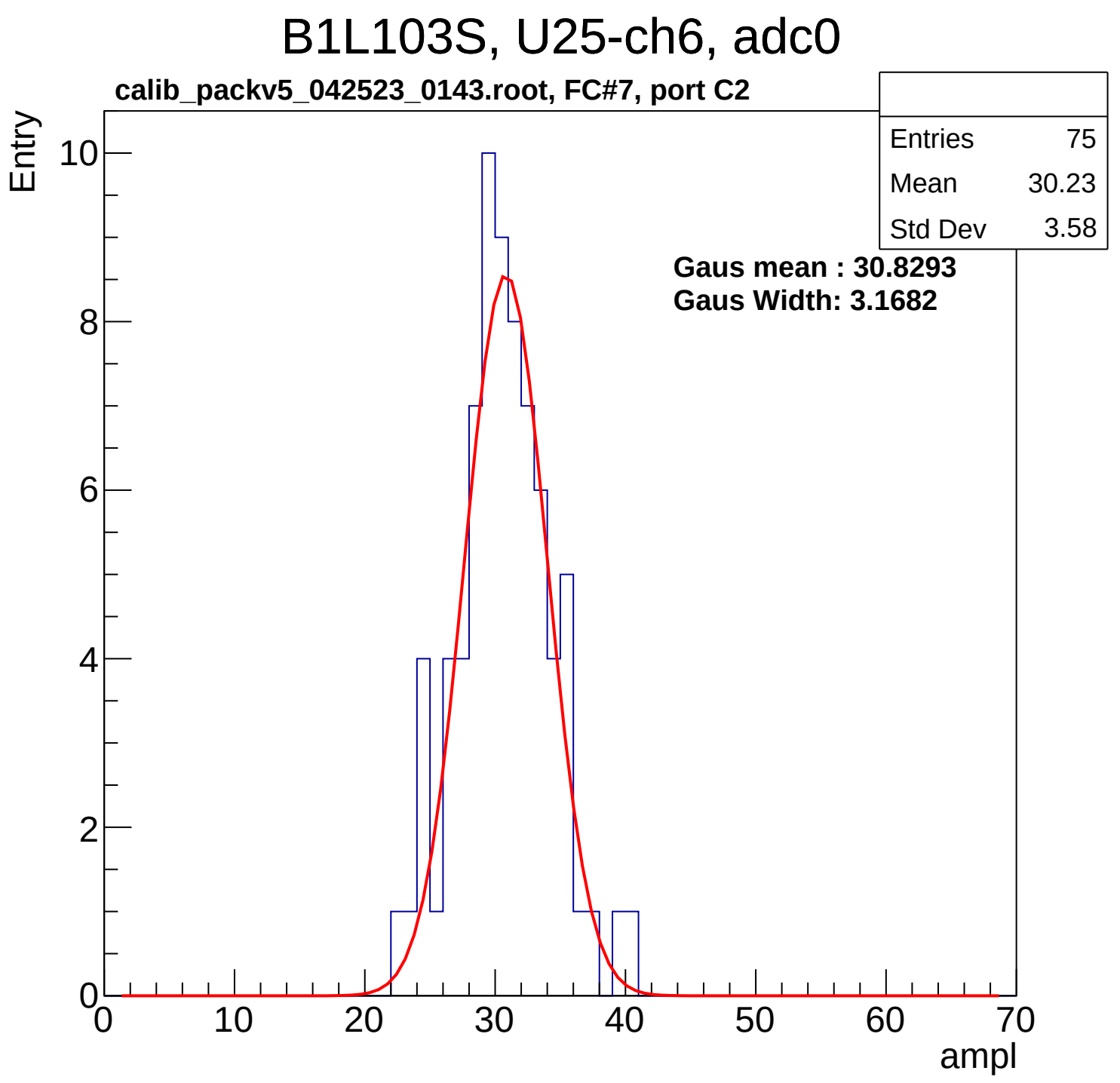
40
50
60
70
ampl


B1L103S, U25-ch6, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.23
Std Dev 3.58
Gaus mean : 30.8293
Gaus Width: 3.1682
Entry
ampl
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6
8
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0
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20
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40
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60
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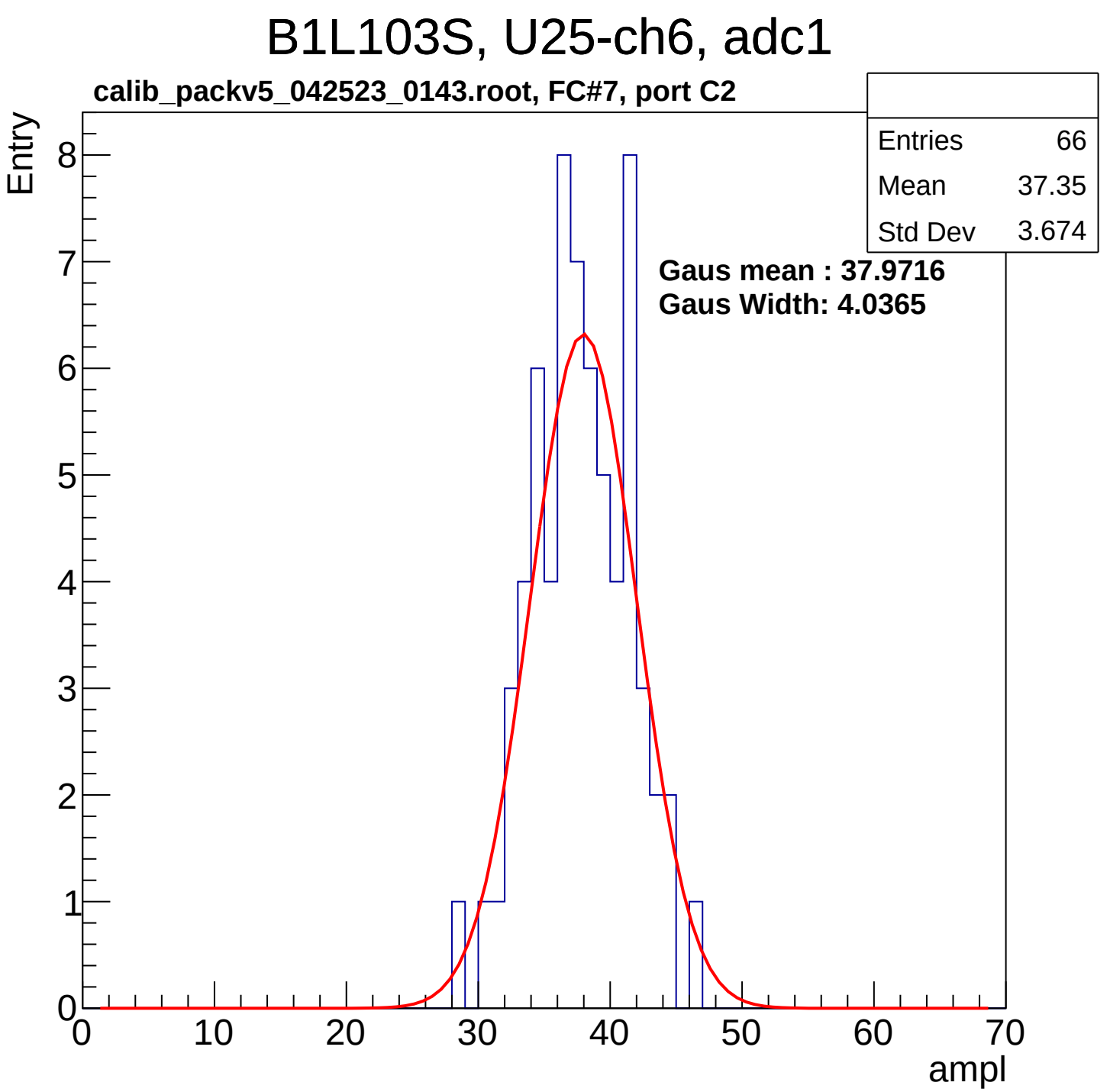
B1L103S, U25-ch6, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 37.35
Std Dev 3.674
Gaus mean : 37.9716
Gaus Width: 4.0365
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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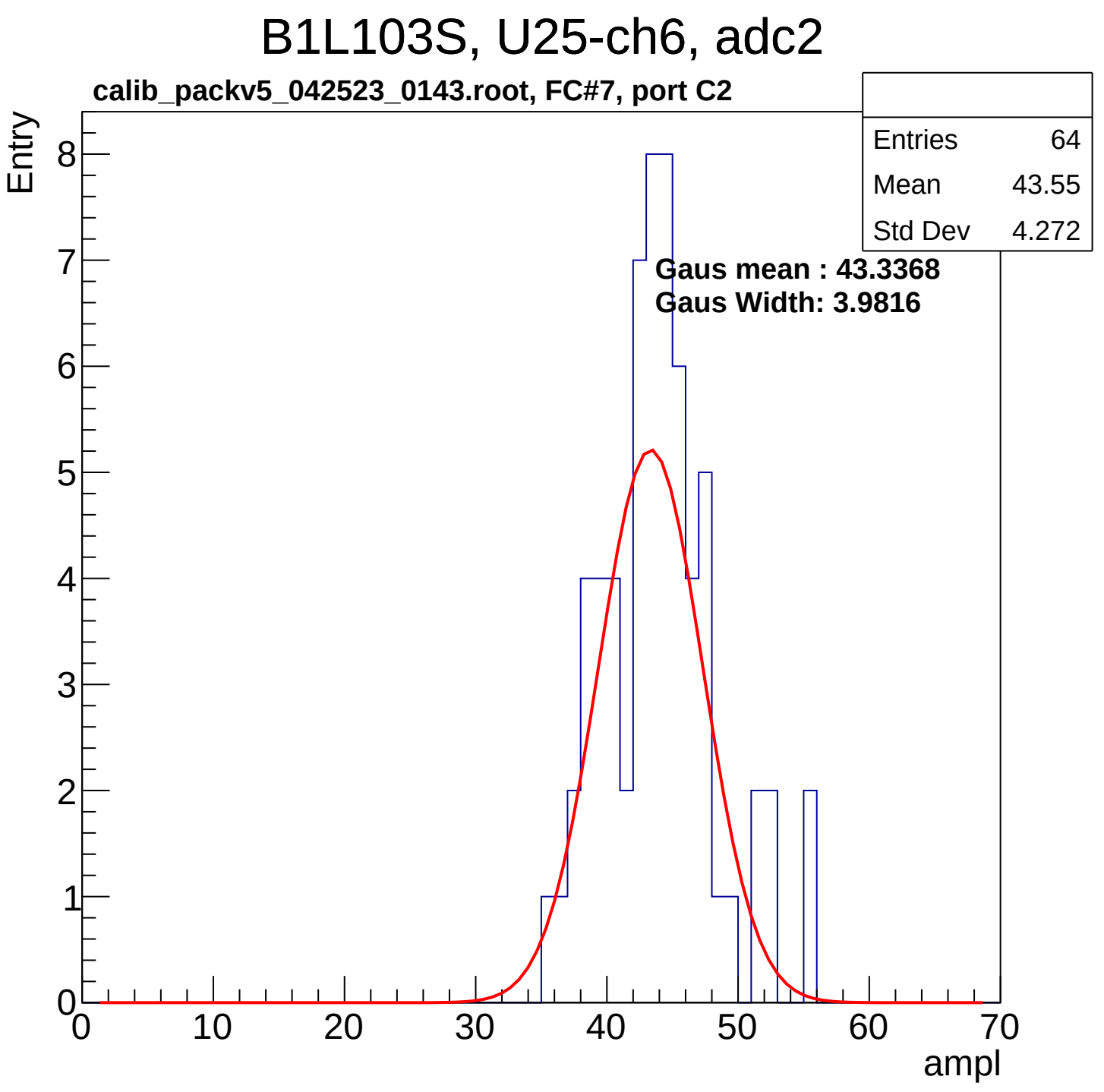
B1L103S, U25-ch6, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 43.55
Std Dev 4.272
Gaus mean : 43.3368
Gaus Width: 3.9816
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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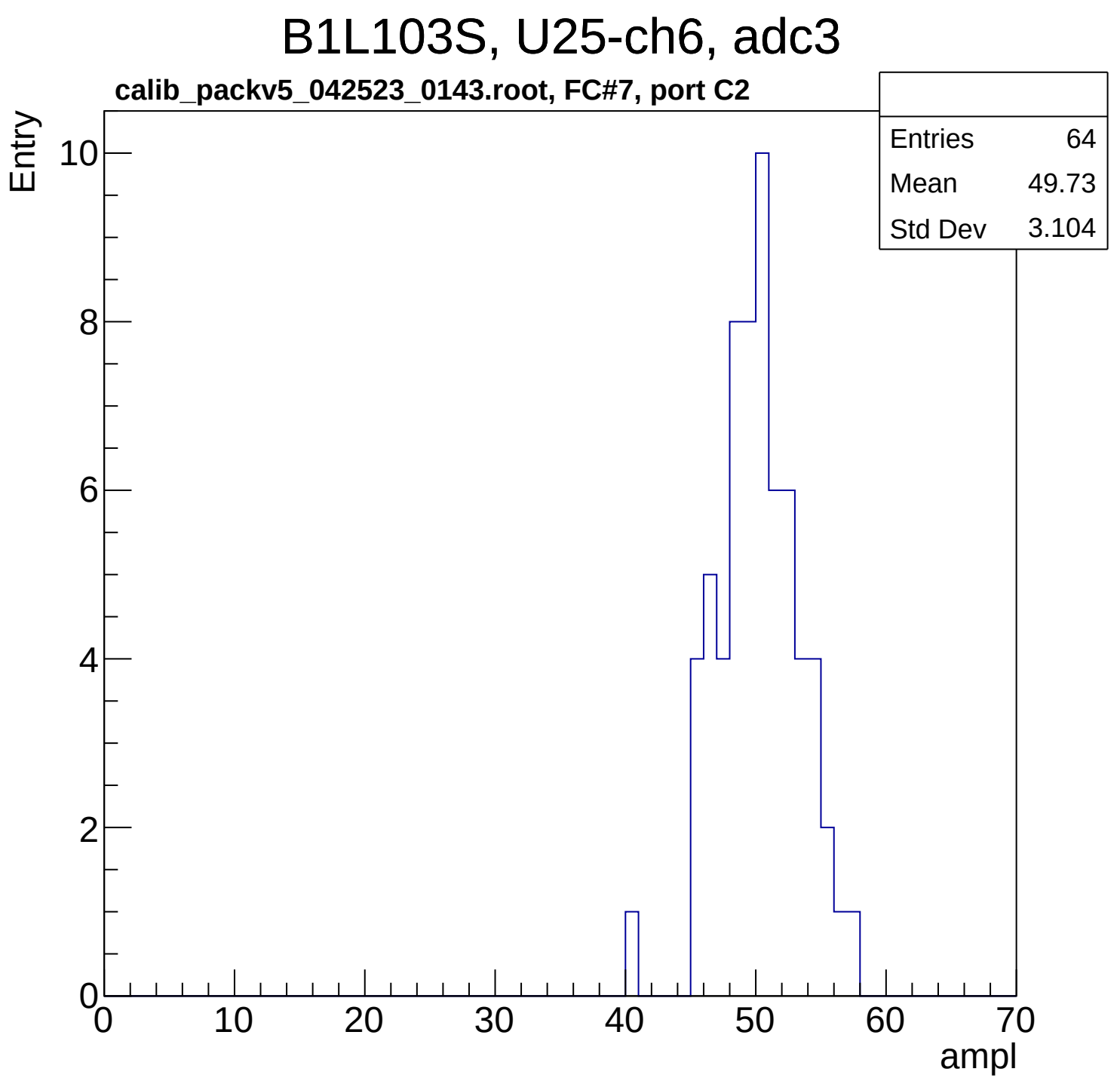
B1L103S, U25-ch6, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 49.73
Std Dev 3.104
Entry
ampl
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0
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20
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40
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60
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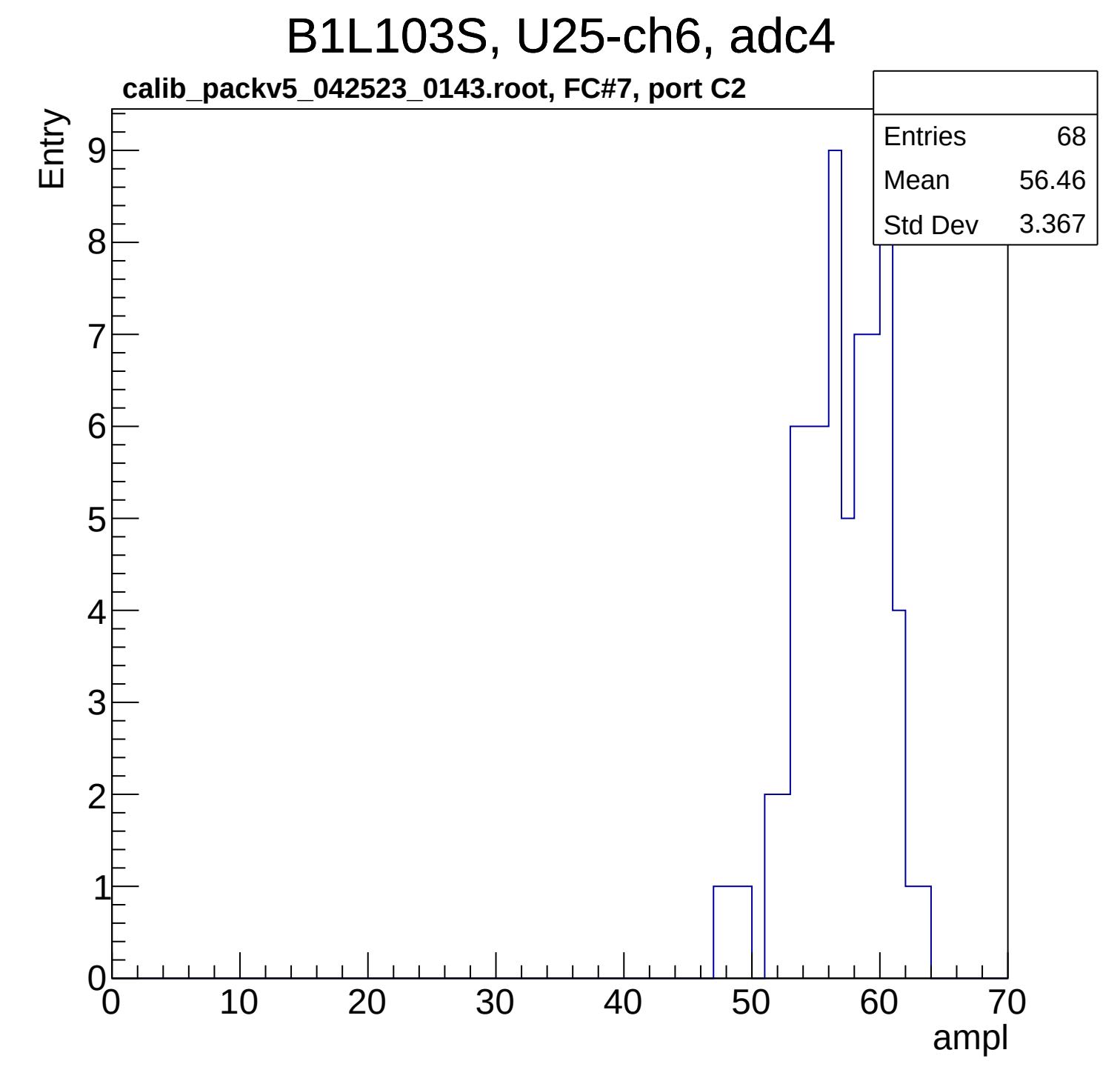
B1L103S, U25-ch6, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 56.46
Std Dev 3.367
Entry
ampl
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3
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6
7
8
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0
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60
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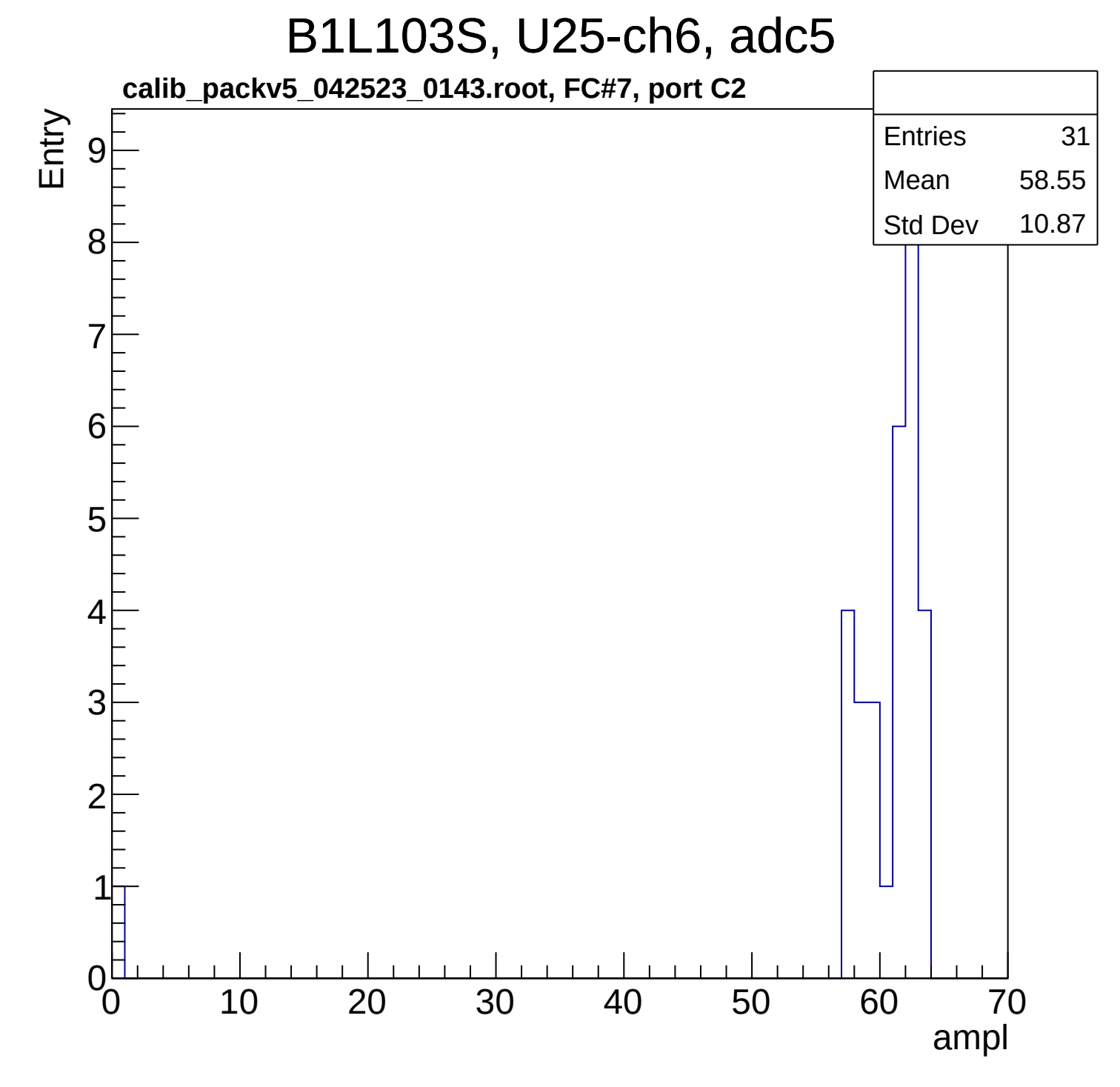
B1L103S, U25-ch6, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 58.55
Std Dev 10.87
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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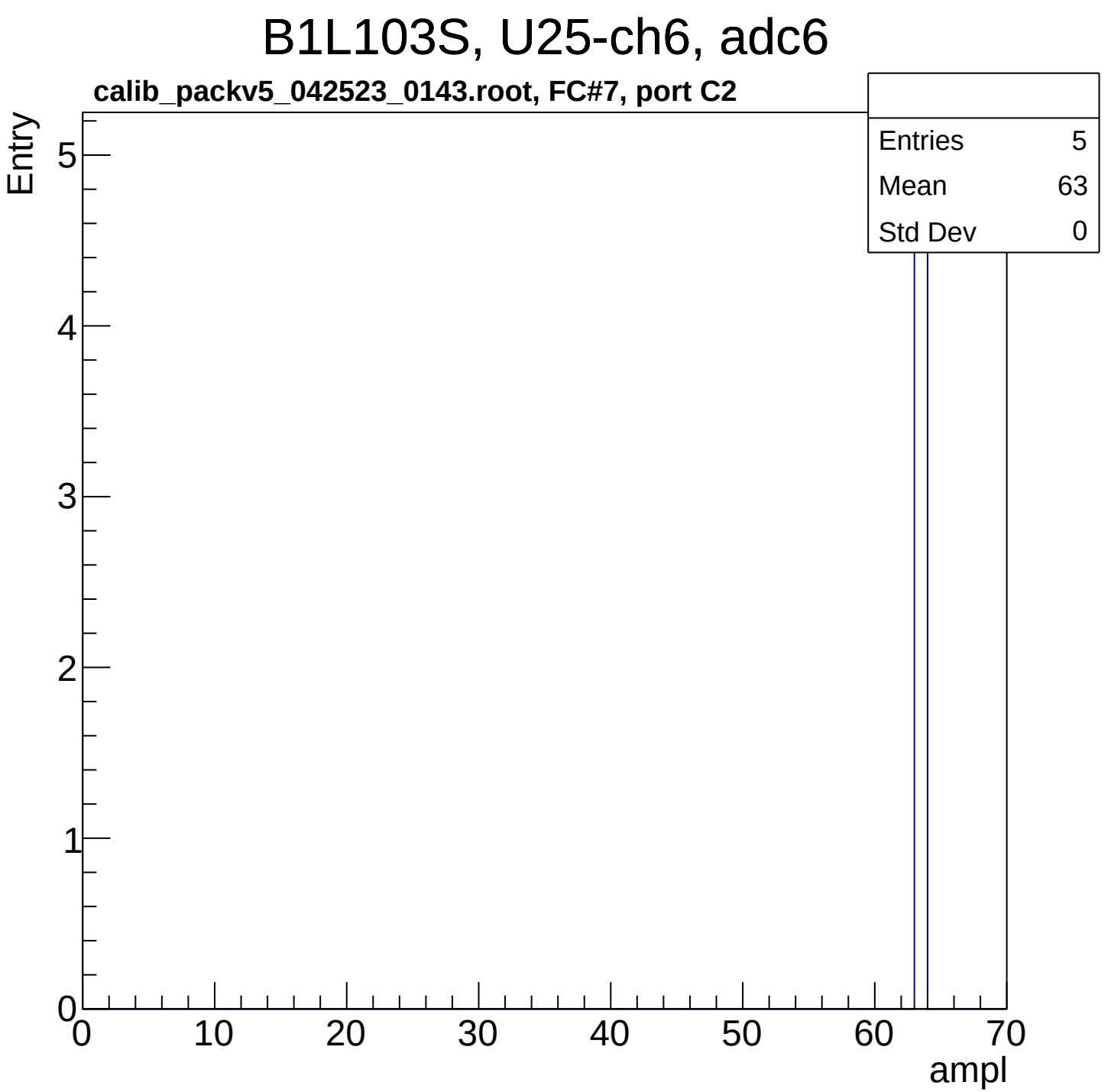
B1L103S, U25-ch6, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 63
Std Dev 0
Entry
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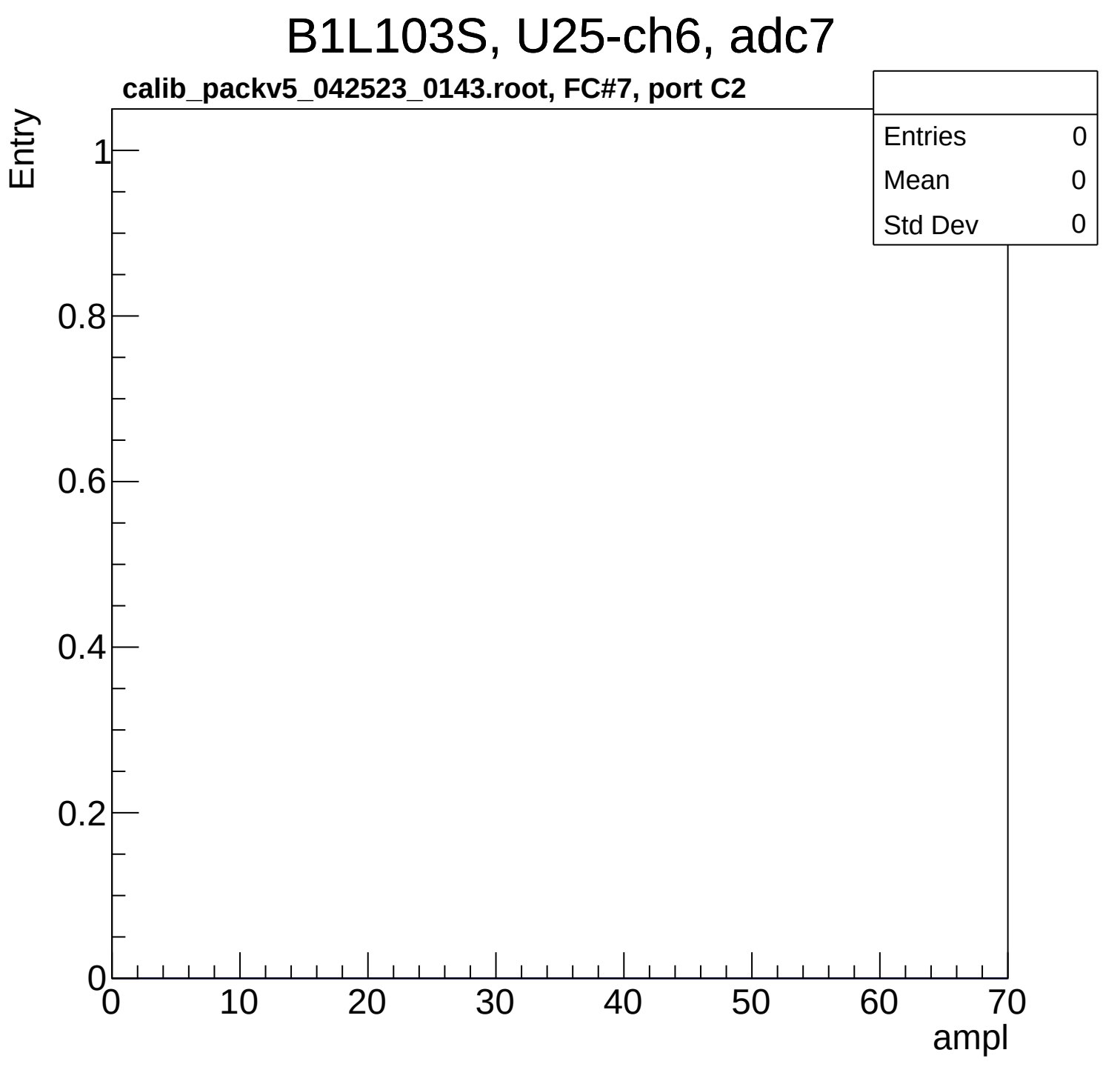
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch6, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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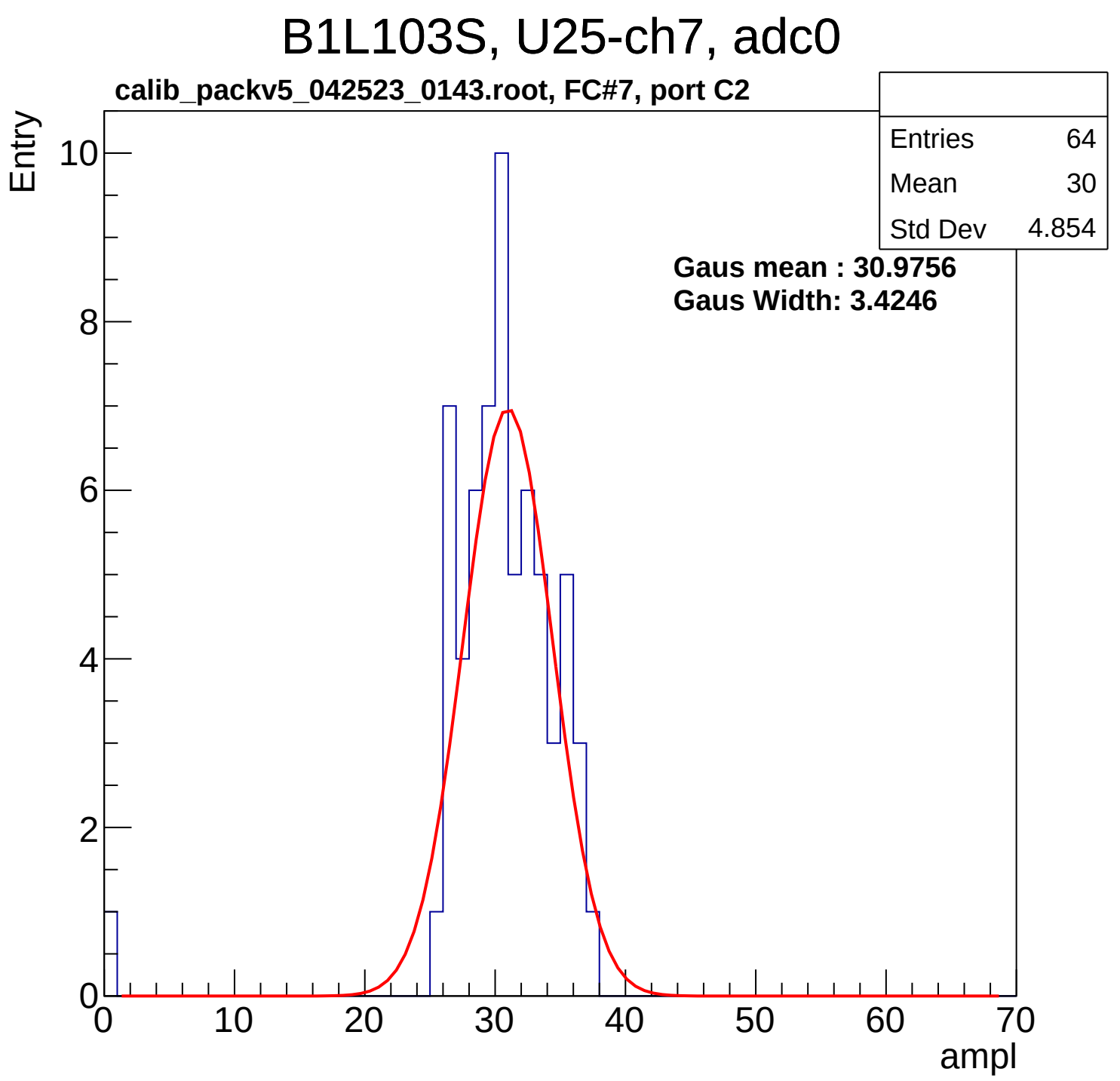
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch7, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 30
Std Dev 4.854
Gaus mean : 30.9756
Gaus Width: 3.4246
Entry
ampl
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6
8
10
0
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20
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40
50
60
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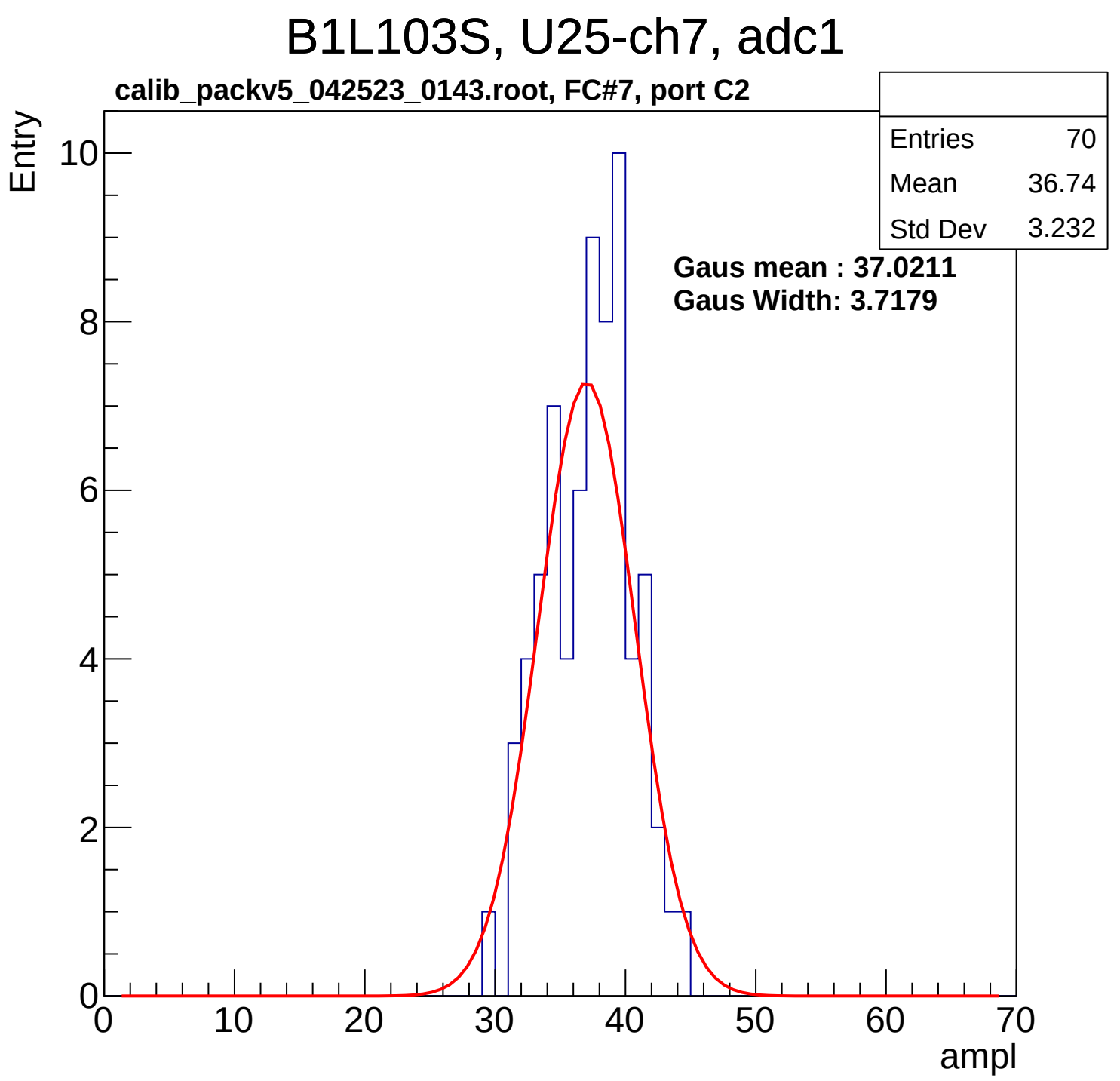
B1L103S, U25-ch7, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 36.74
Std Dev 3.232
Gaus mean : 37.0211
Gaus Width: 3.7179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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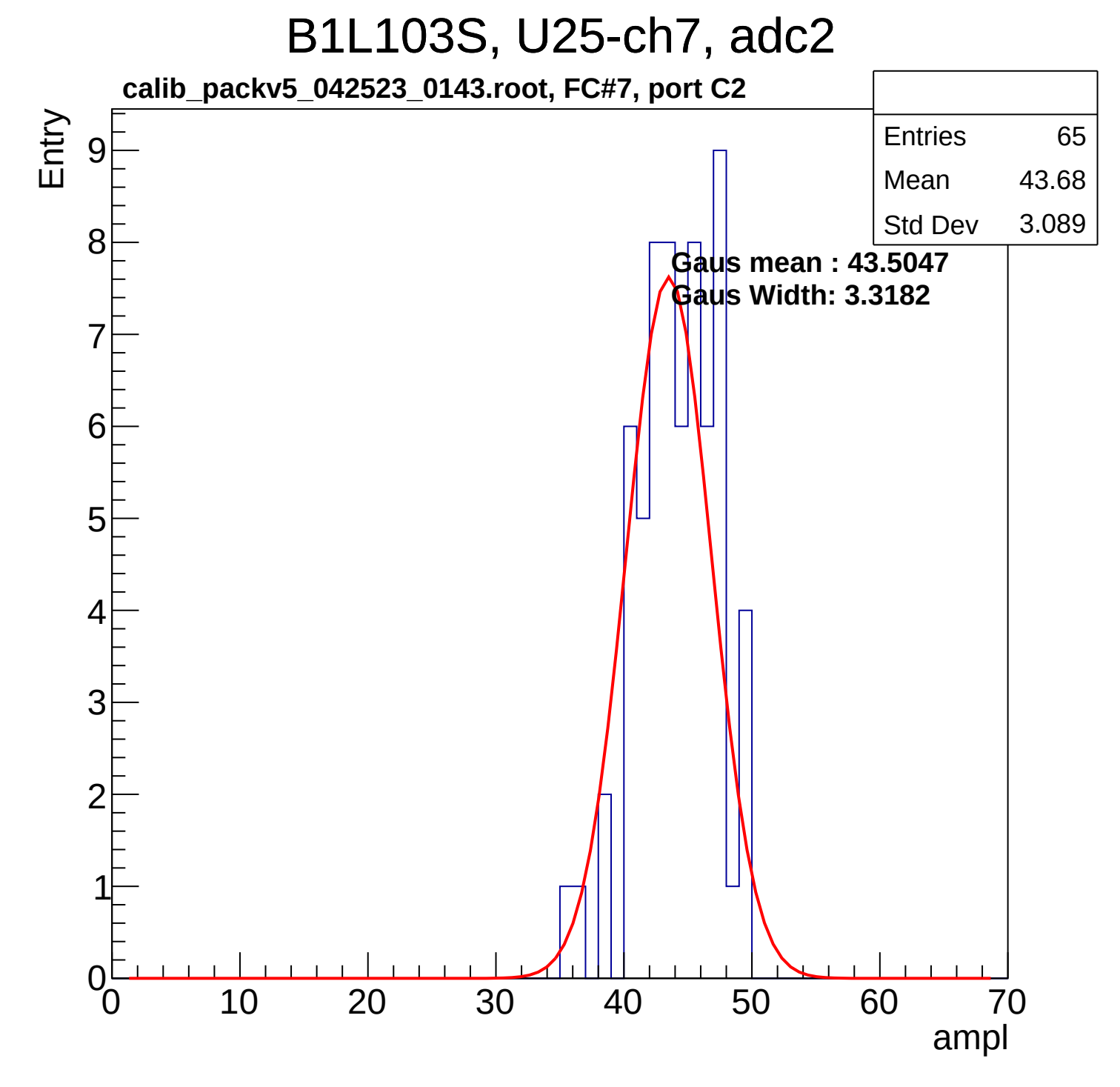
B1L103S, U25-ch7, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 43.68
Std Dev 3.089
Gaus mean : 43.5047
Gaus Width: 3.3182
Entry
ampl
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10
20
30
40
50
60
70
1
2
3
4
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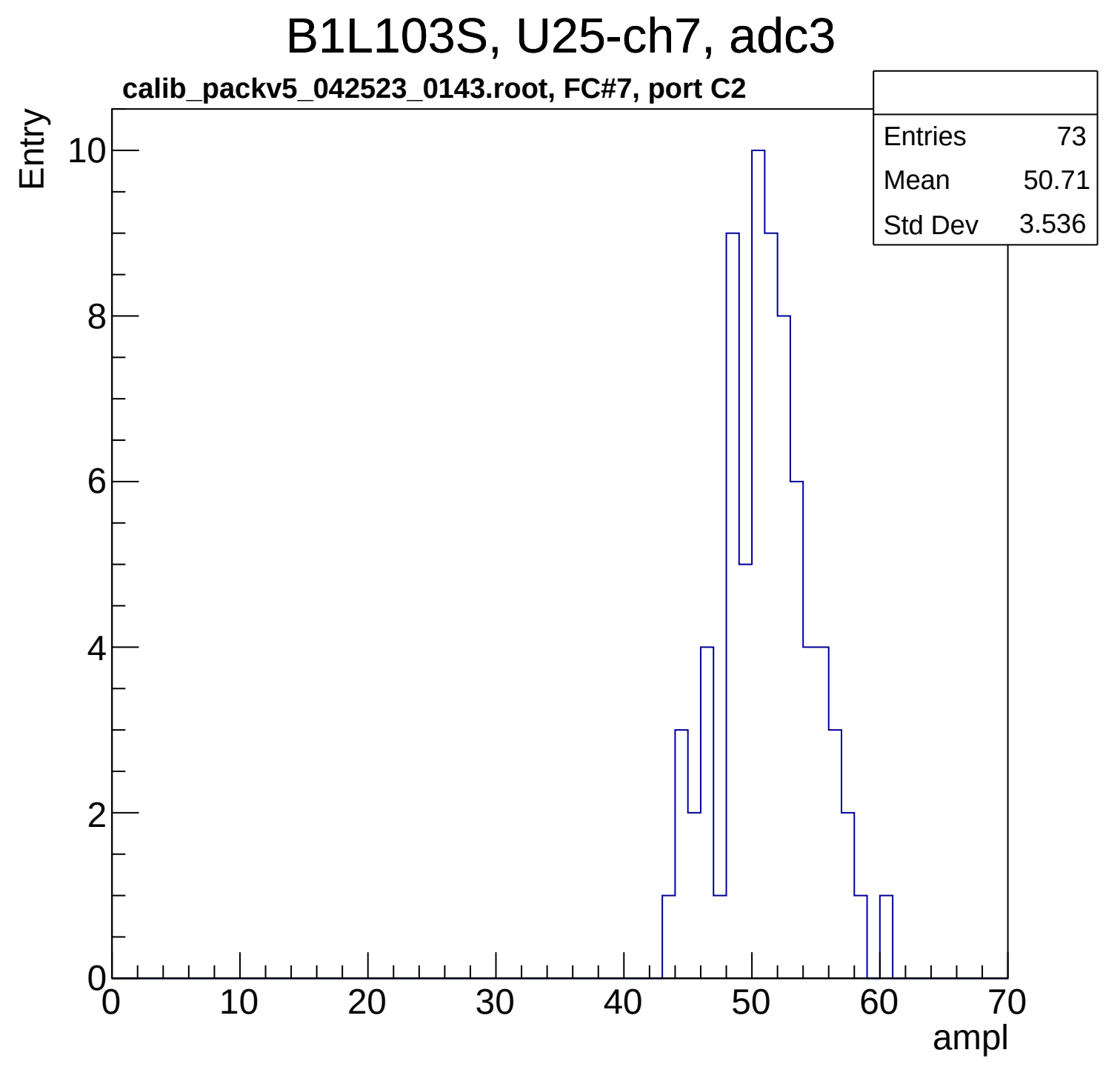
6
7
8
9


B1L103S, U25-ch7, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50.71
Std Dev 3.536
Entry
ampl
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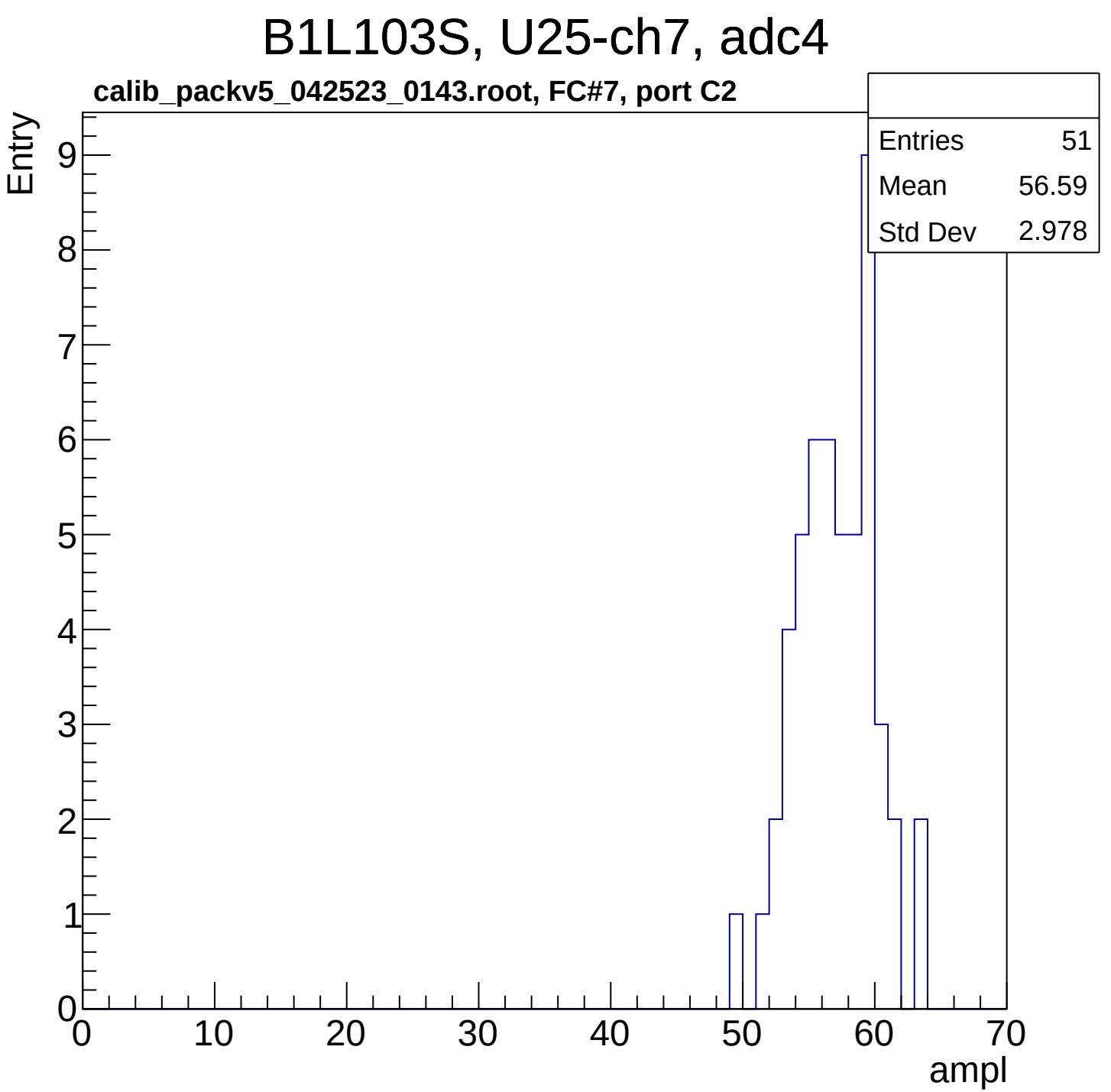
B1L103S, U25-ch7, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 56.59
Std Dev 2.978
Entry
ampl
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5
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7
8
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0
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40
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60
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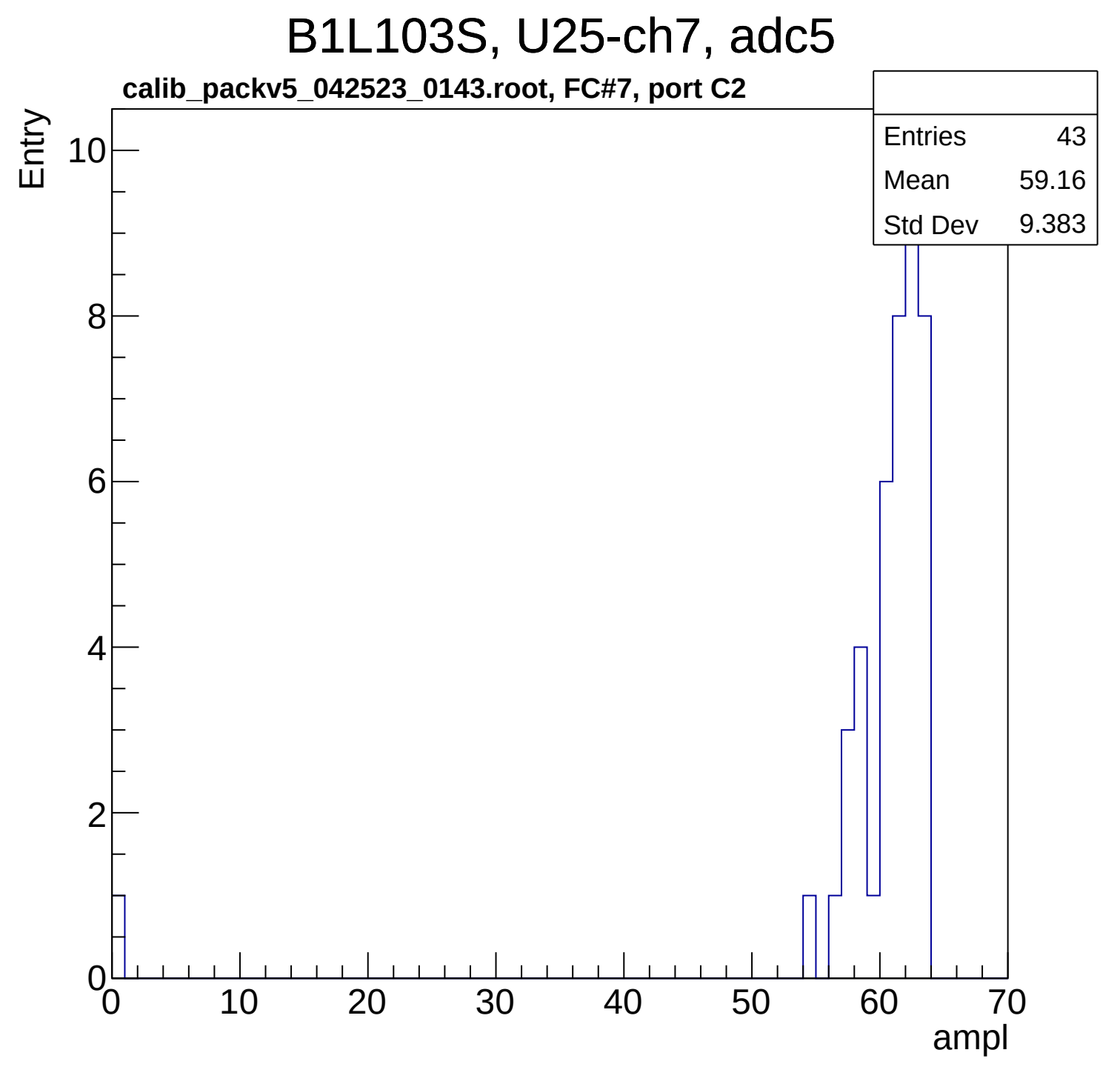
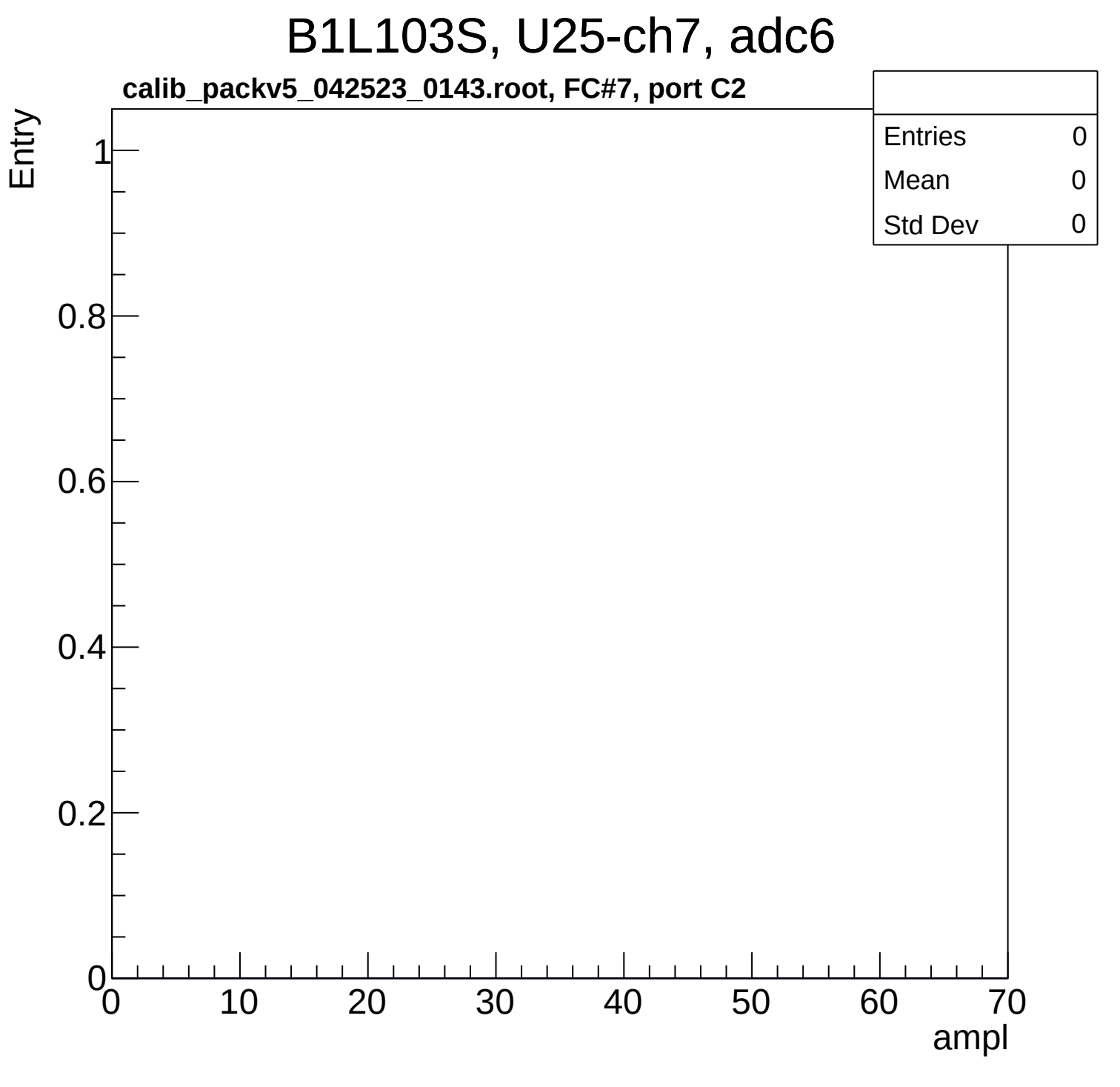

B1L103S, U25-ch7, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.16
Std Dev 9.383
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch7, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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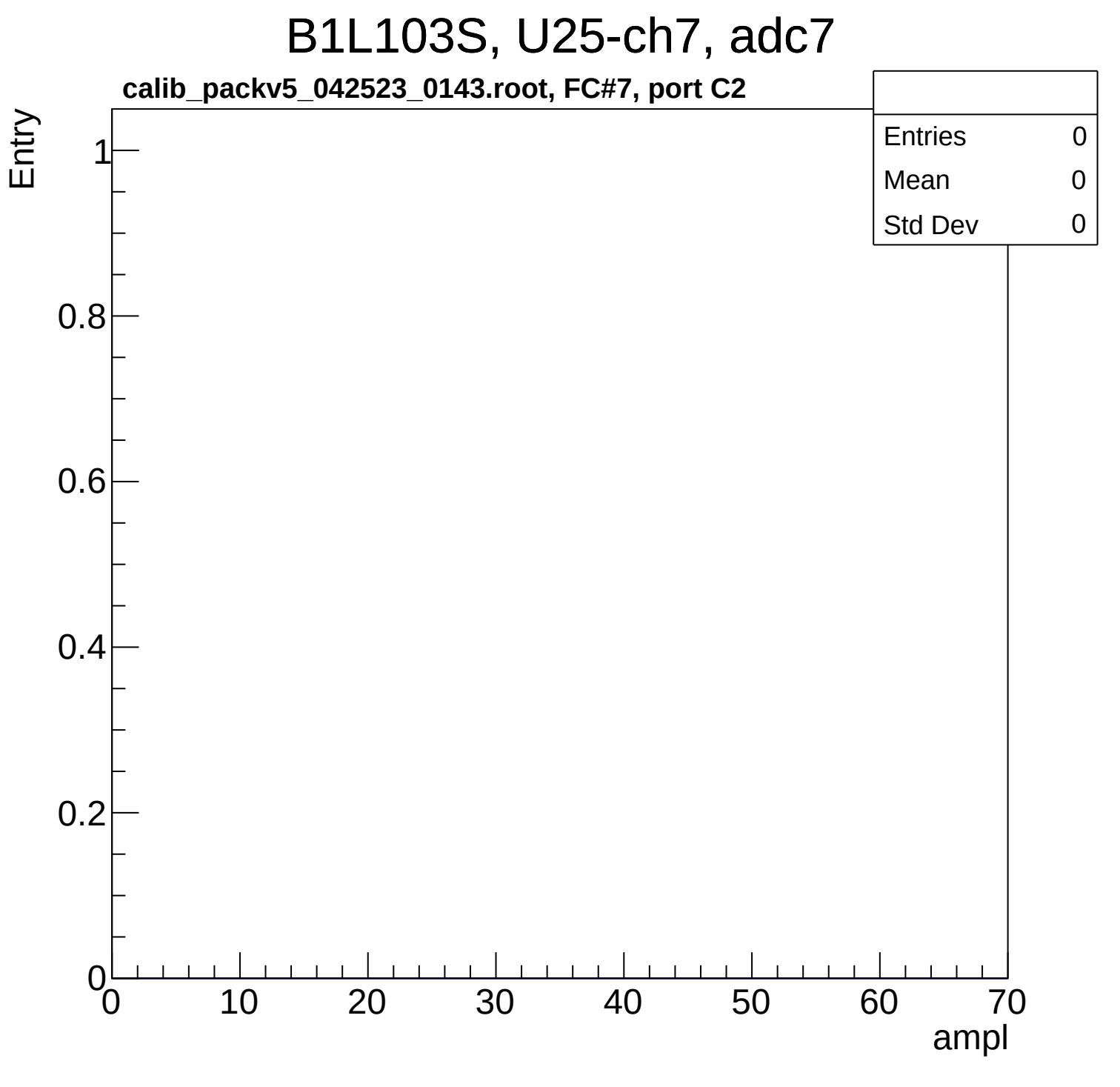
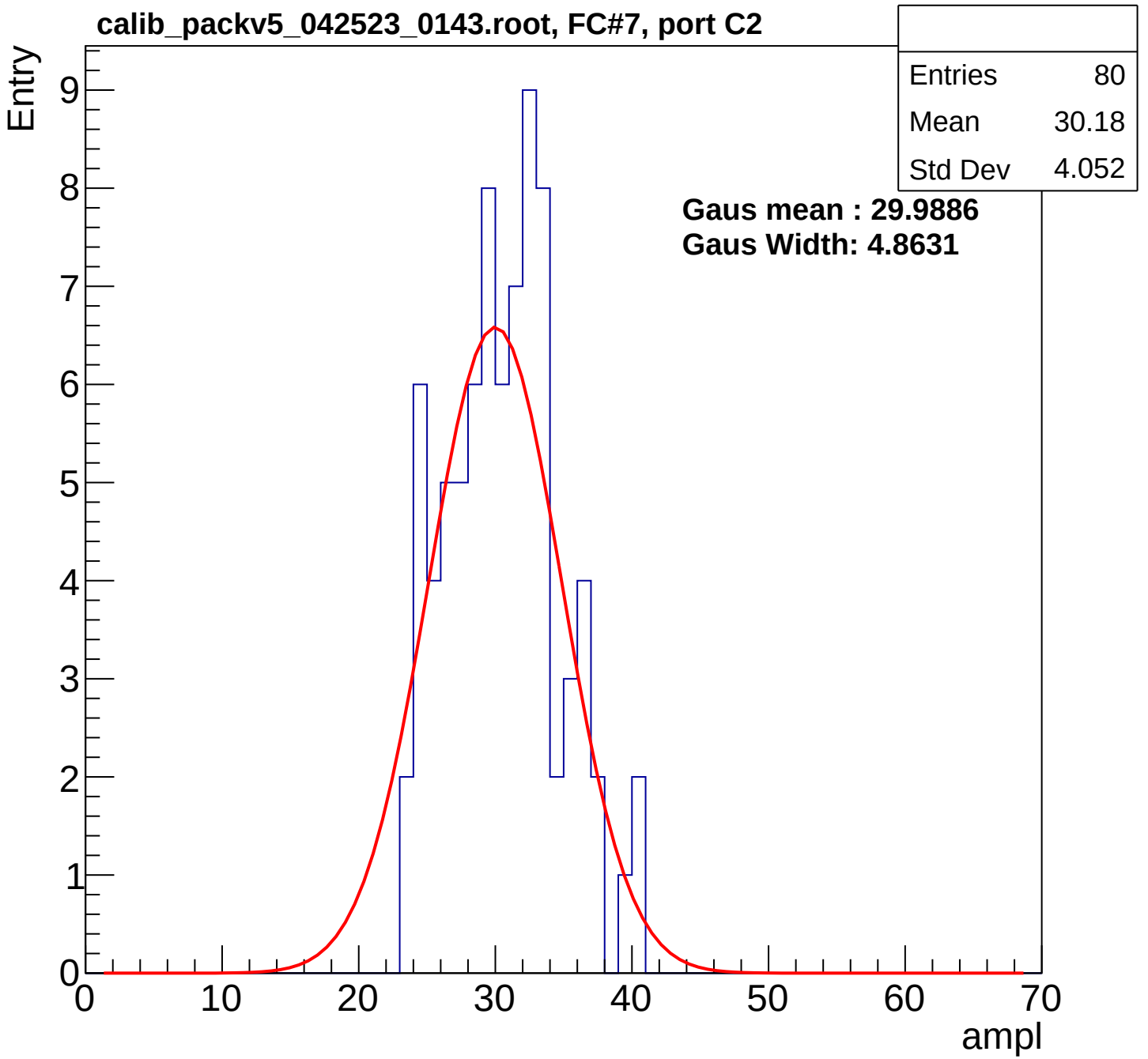
40
50
60
70
ampl


B1L103S, U25-ch7, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch8, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 30.18
Std Dev 4.052
Gaus mean : 29.9886
Gaus Width: 4.8631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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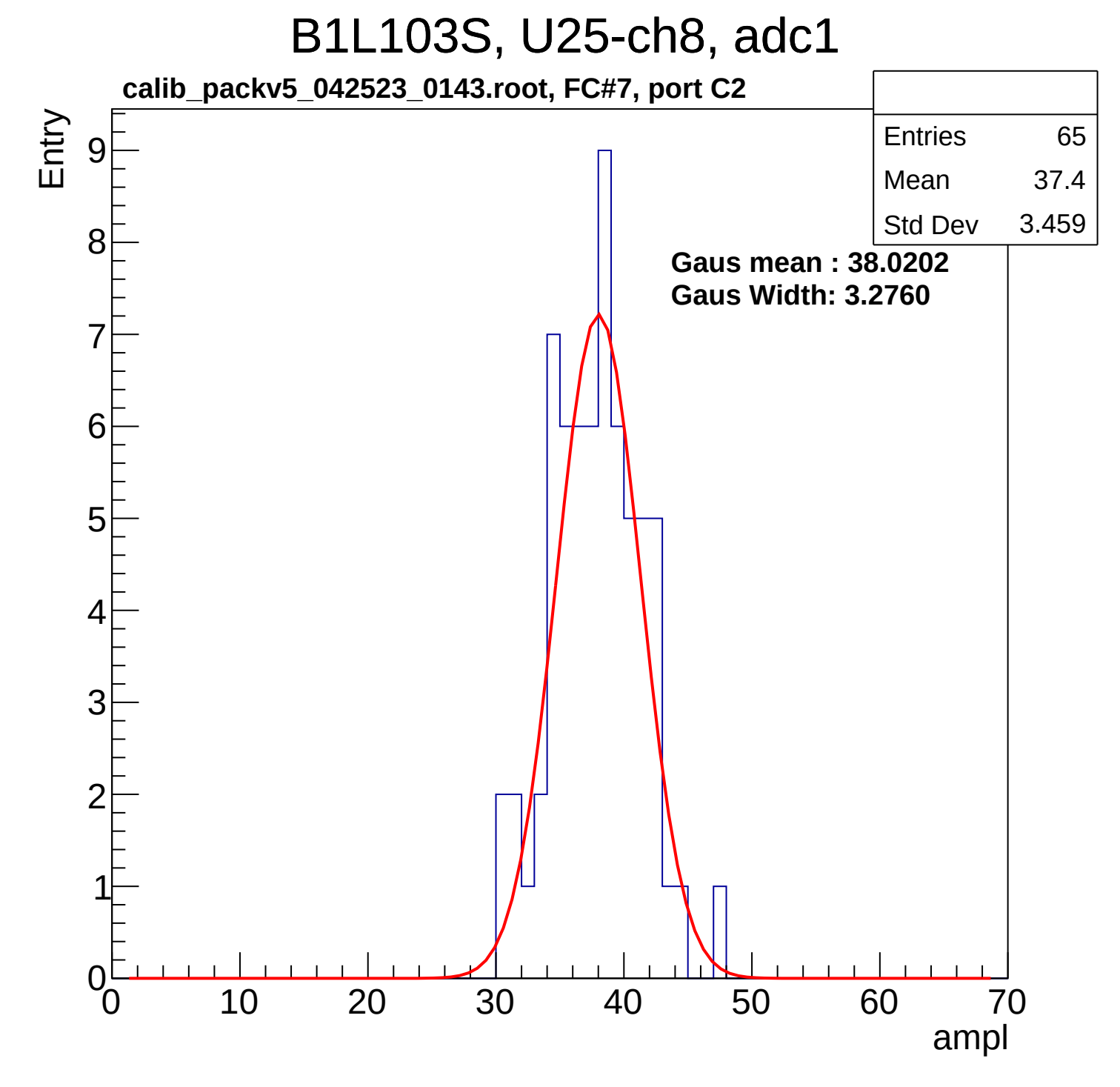
B1L103S, U25-ch8, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 37.4
Std Dev 3.459
Gaus mean : 38.0202
Gaus Width: 3.2760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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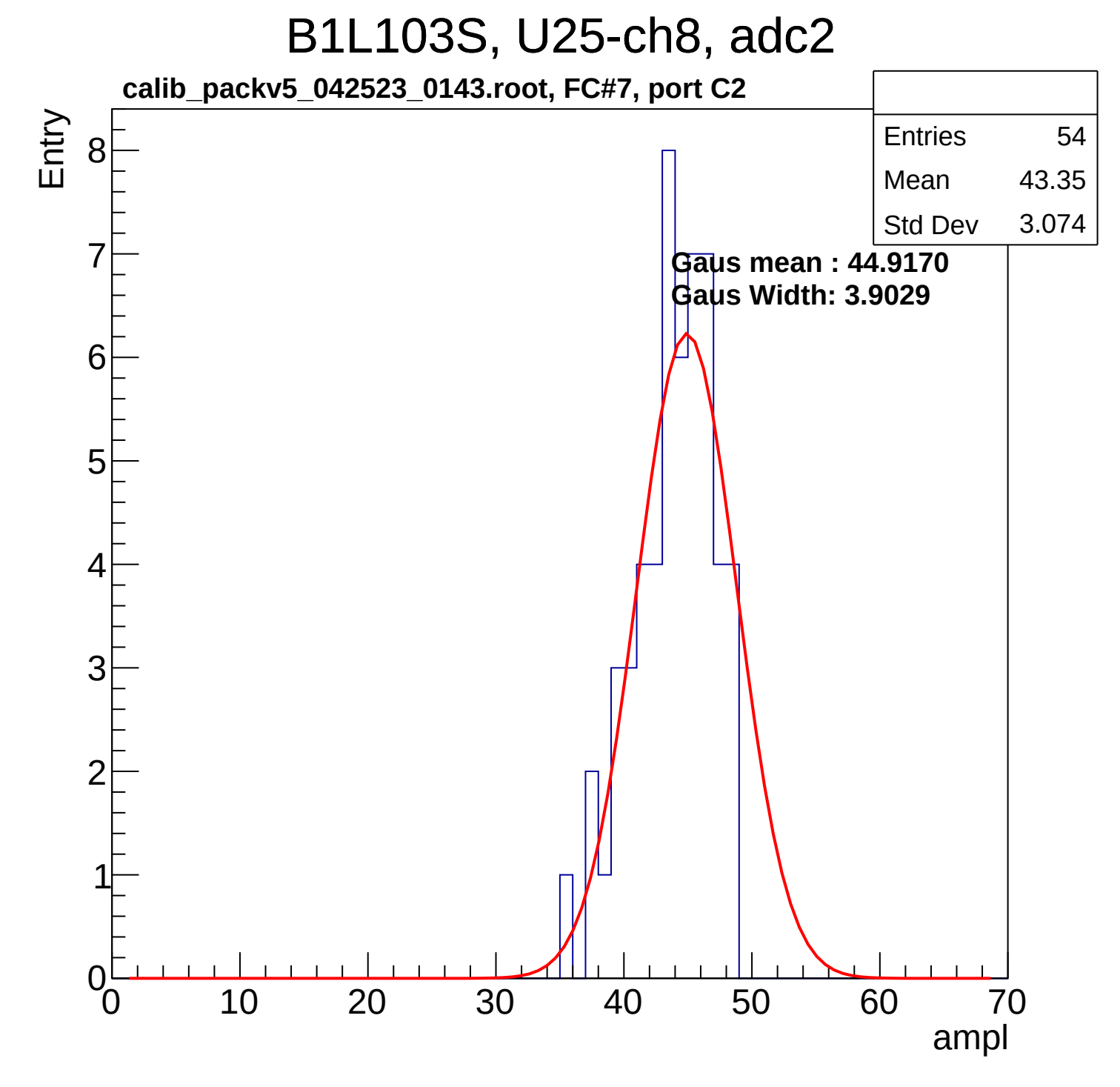
B1L103S, U25-ch8, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 43.35
Std Dev 3.074
Gaus mean : 44.9170
Gaus Width: 3.9029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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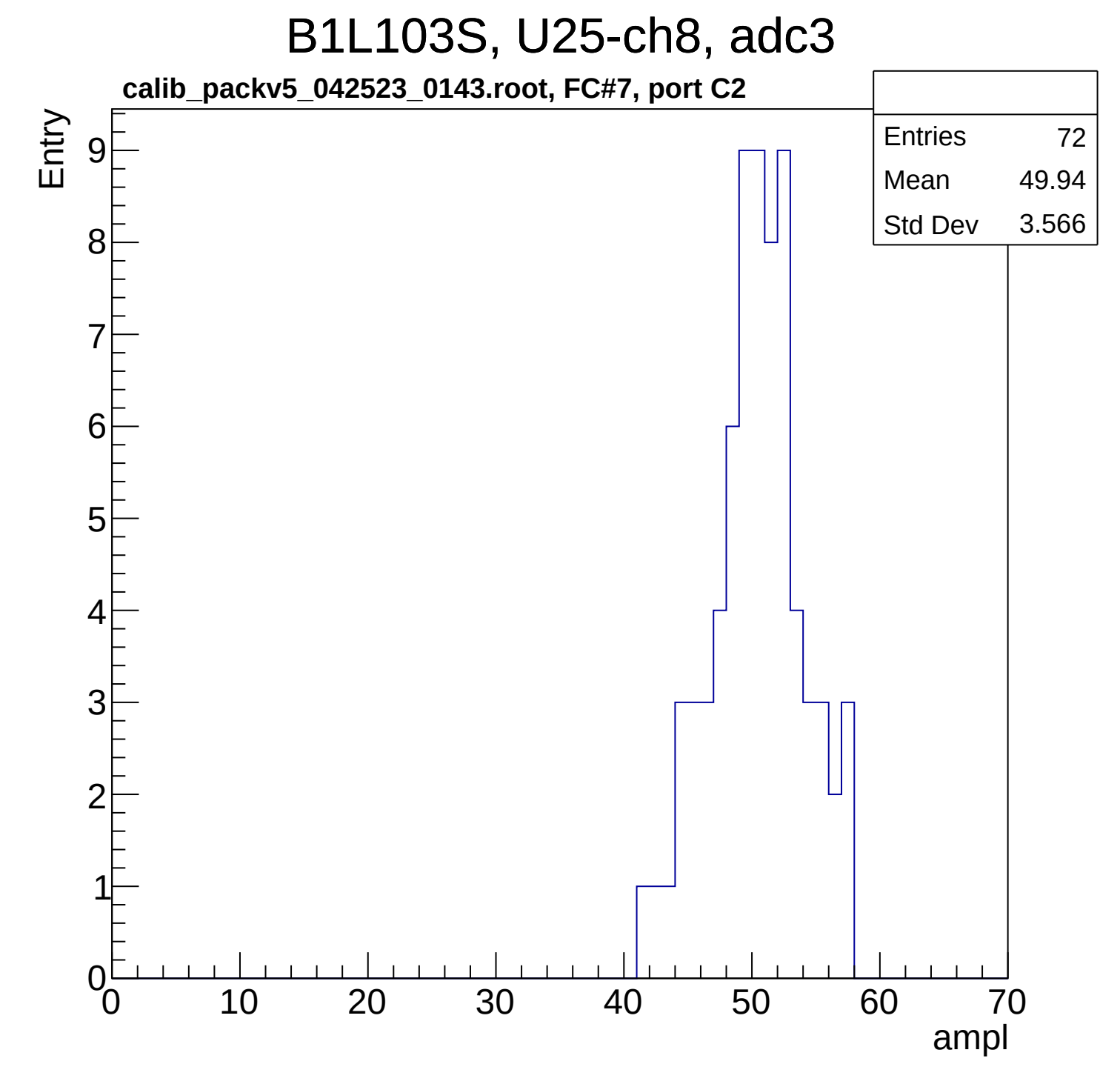
B1L103S, U25-ch8, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.94
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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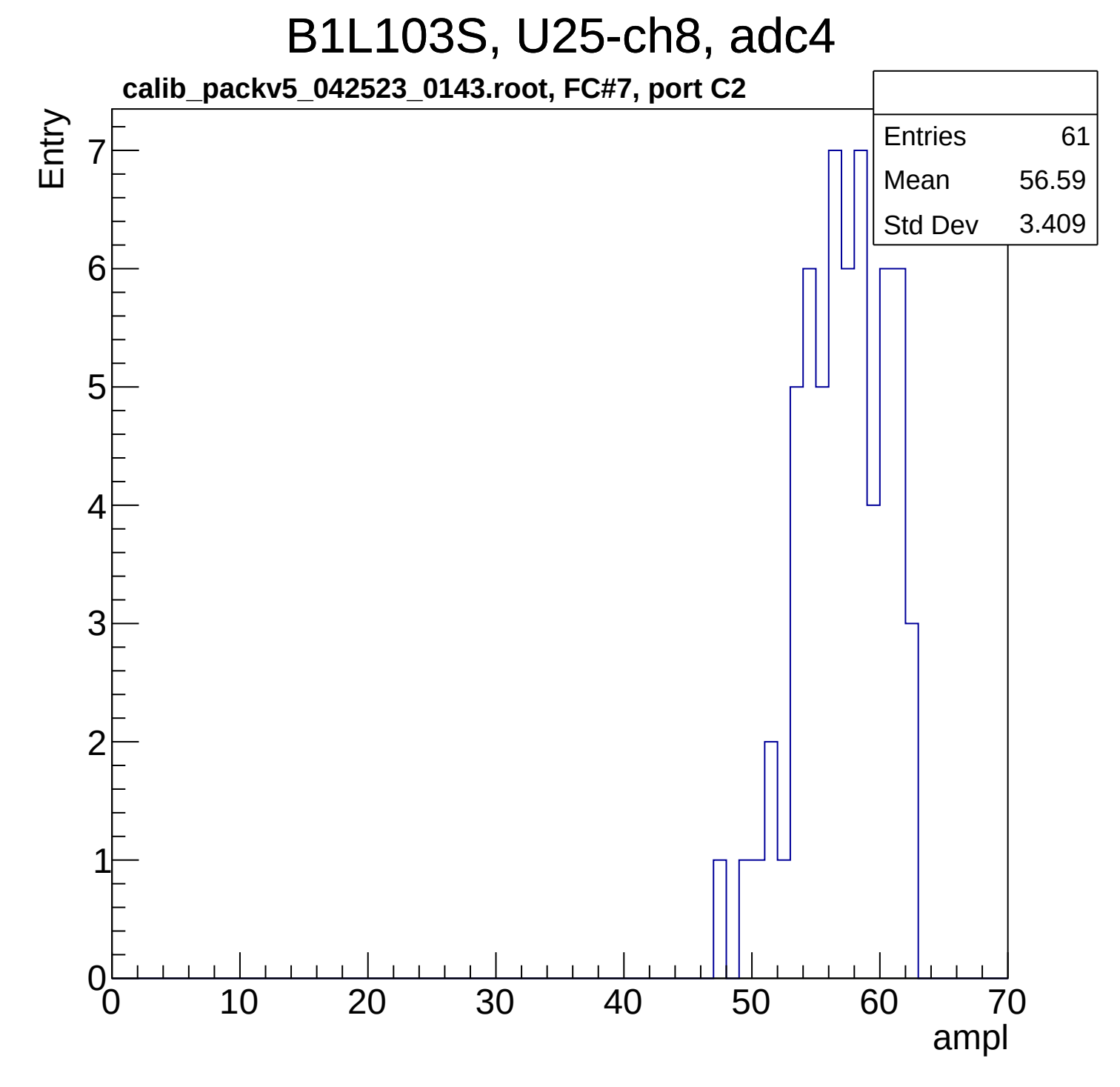
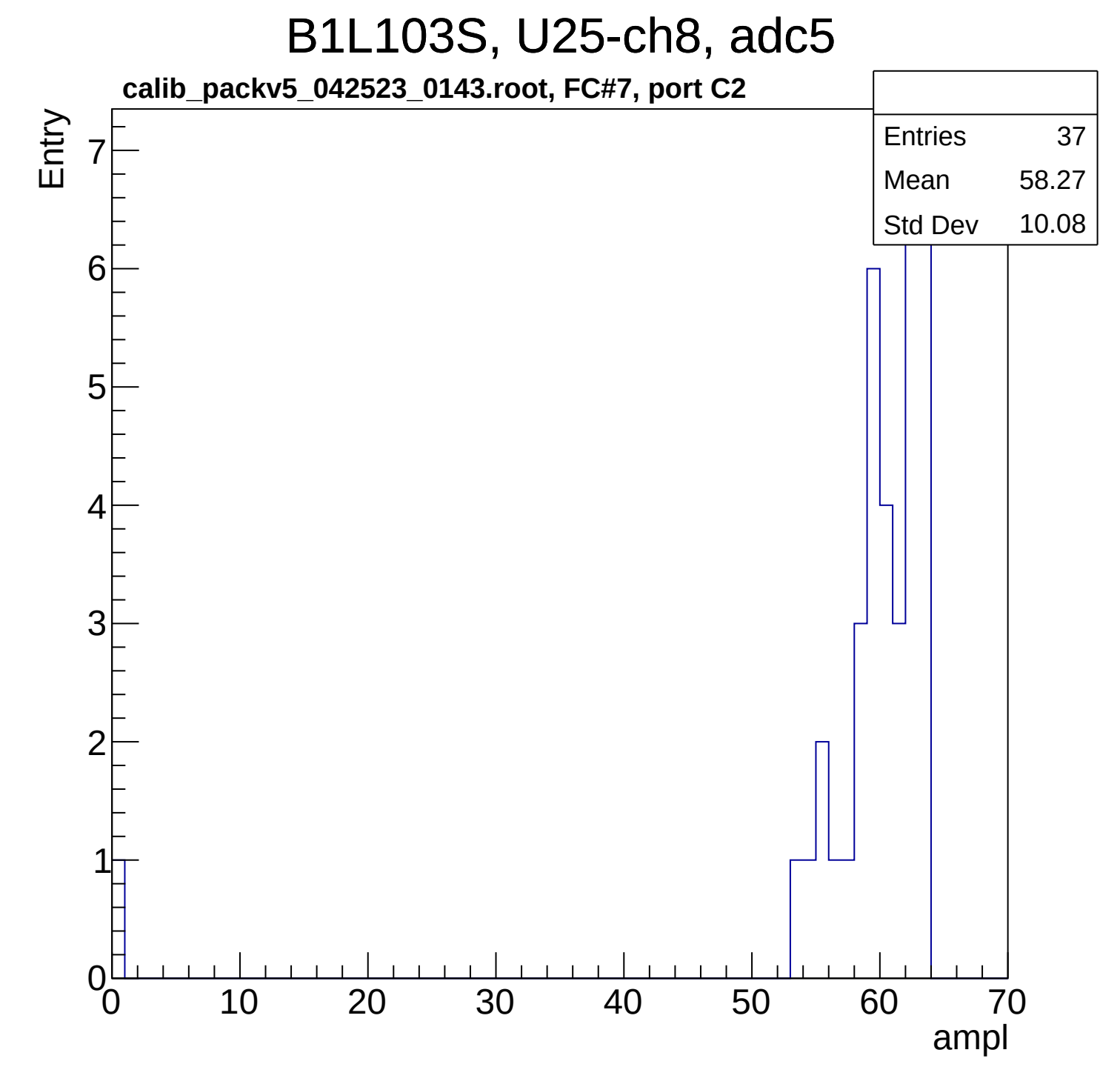

B1L103S, U25-ch8, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 56.59
Std Dev 3.409
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch8, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 58.27
Std Dev 10.08
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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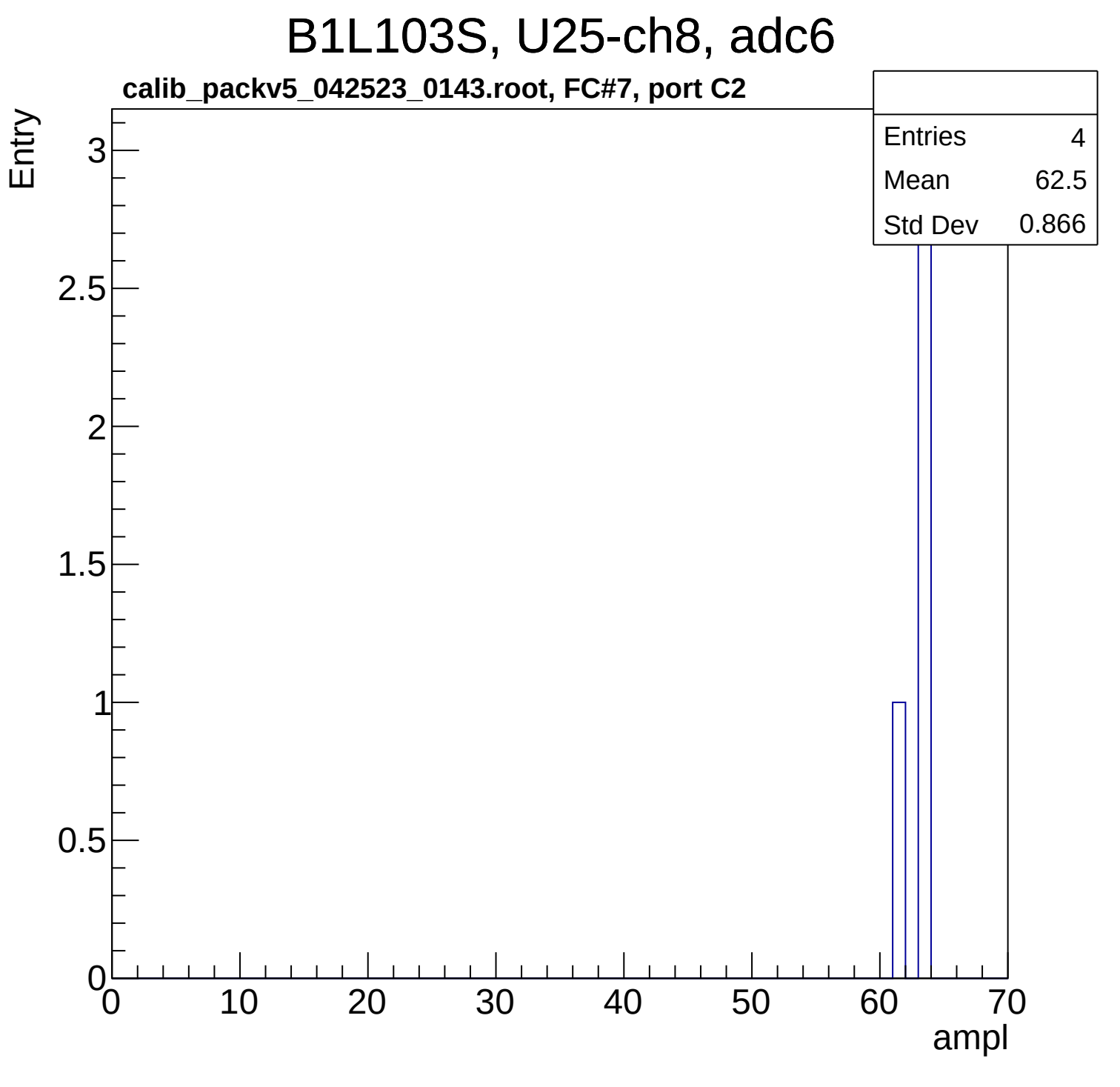
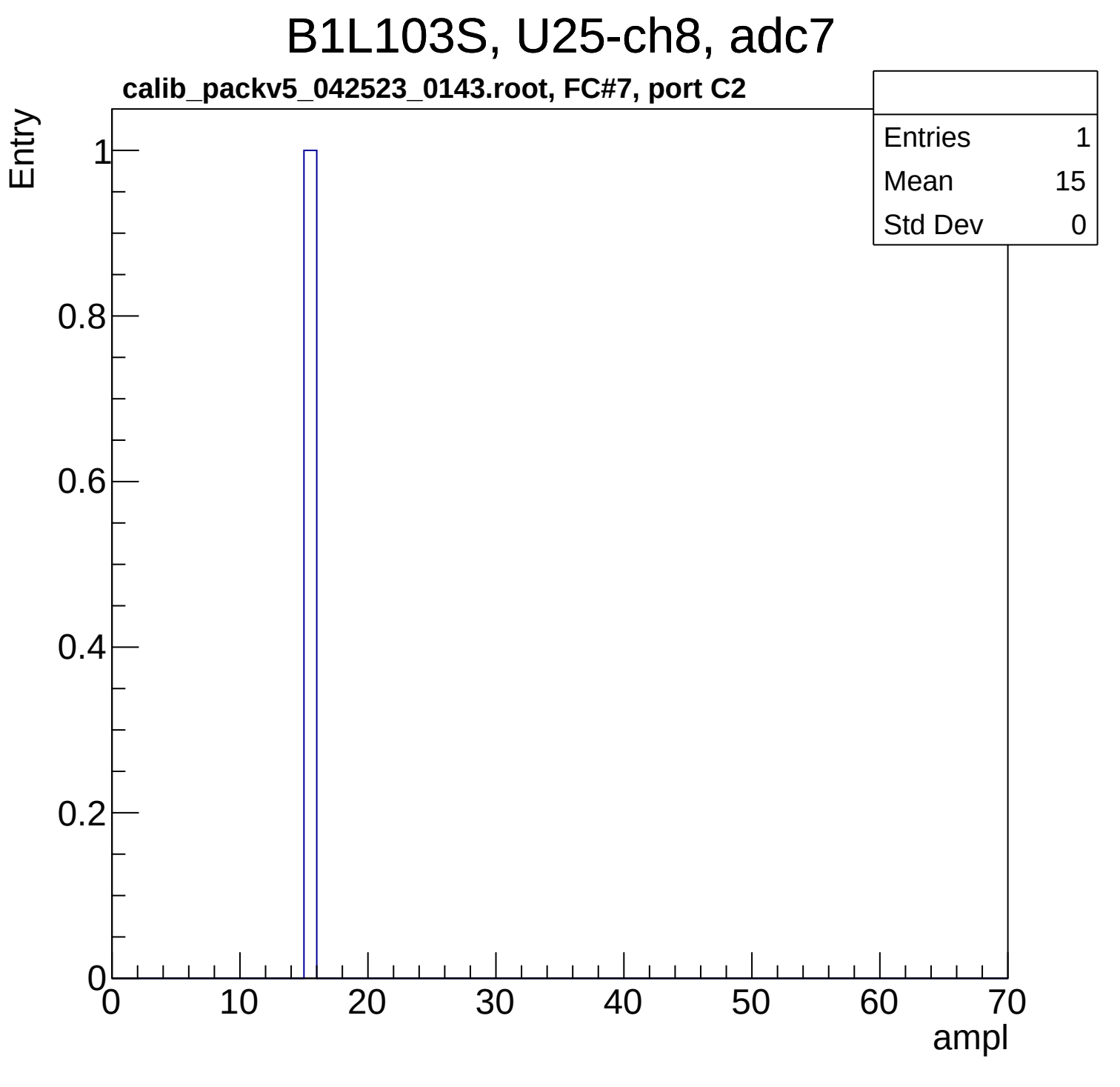

B1L103S, U25-ch8, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch8, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 15
Std Dev 0
Entry
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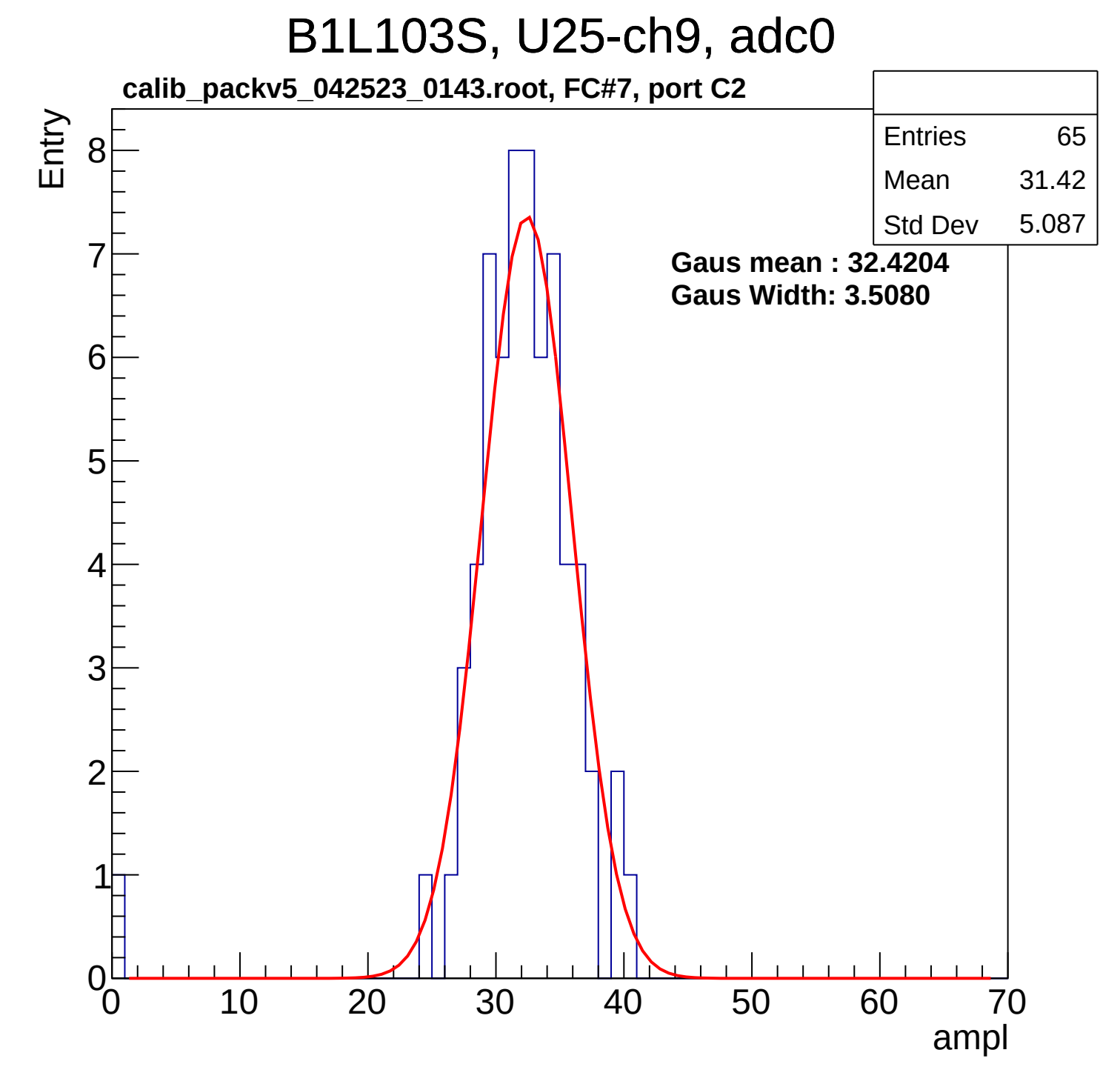
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch9, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 31.42
Std Dev 5.087
Gaus mean : 32.4204
Gaus Width: 3.5080
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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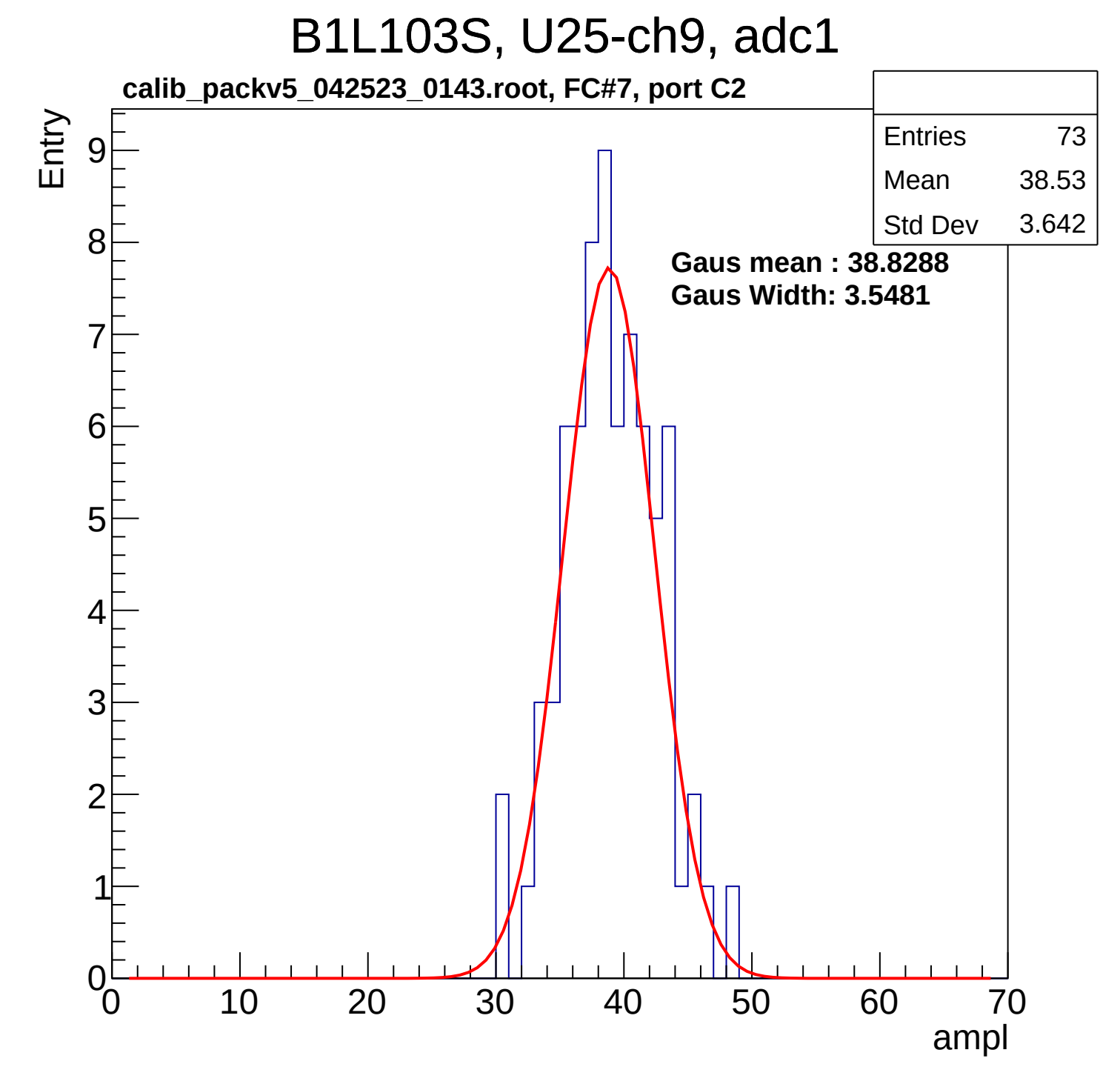
B1L103S, U25-ch9, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 38.53
Std Dev 3.642
Gaus mean : 38.8288
Gaus Width: 3.5481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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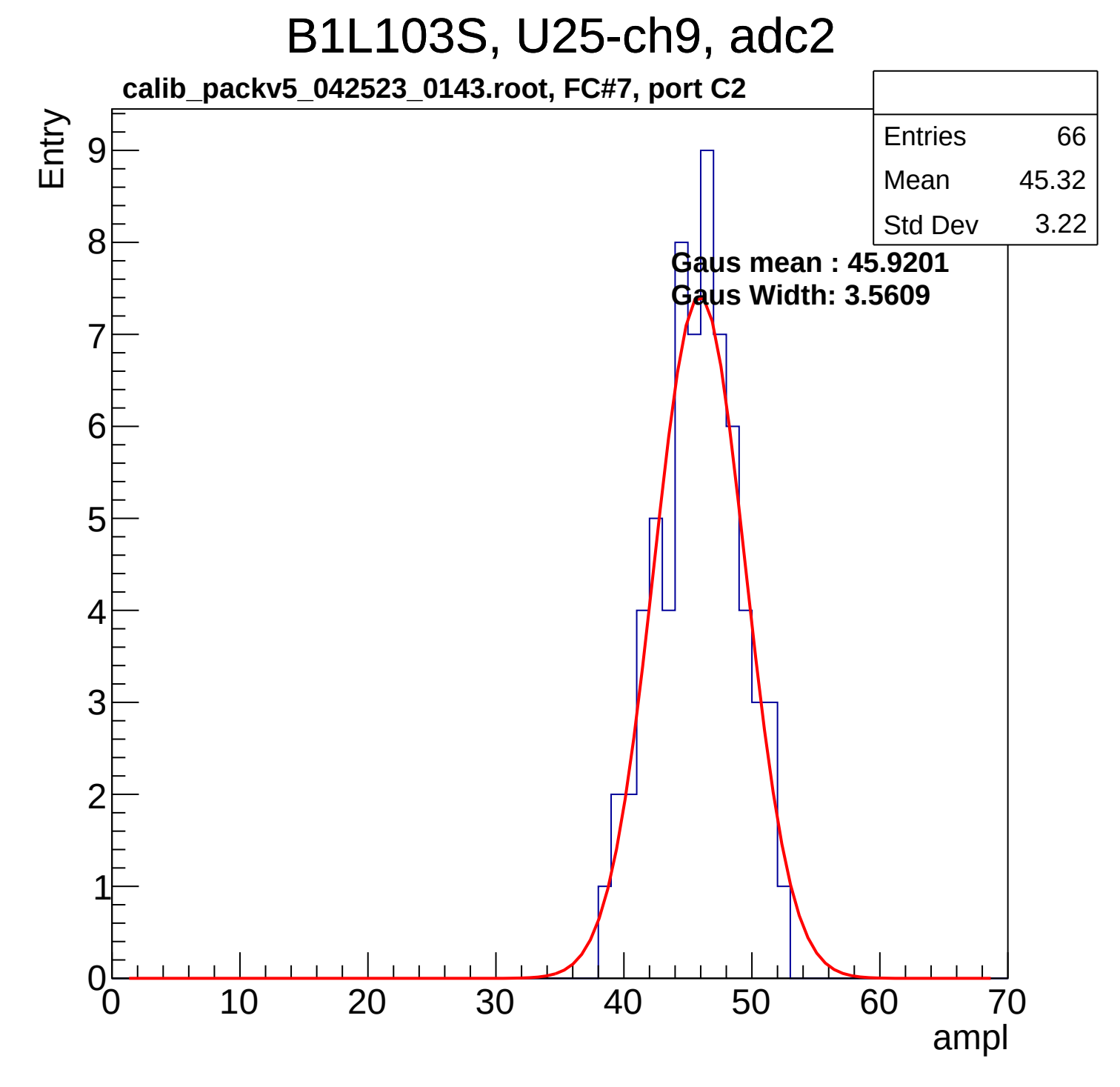
B1L103S, U25-ch9, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 45.32
Std Dev 3.22
Gaus mean : 45.9201
Gaus Width: 3.5609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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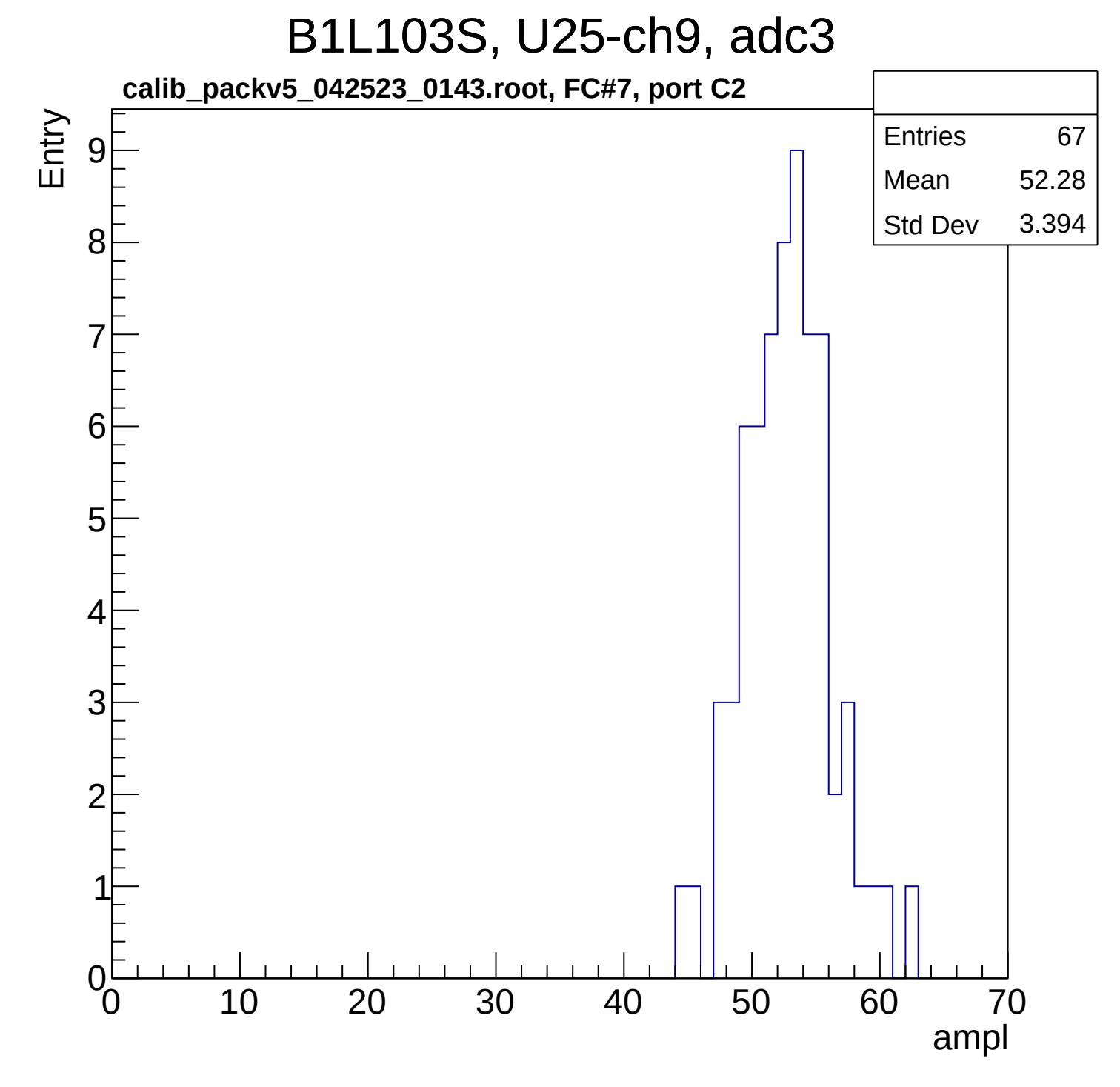
B1L103S, U25-ch9, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 52.28
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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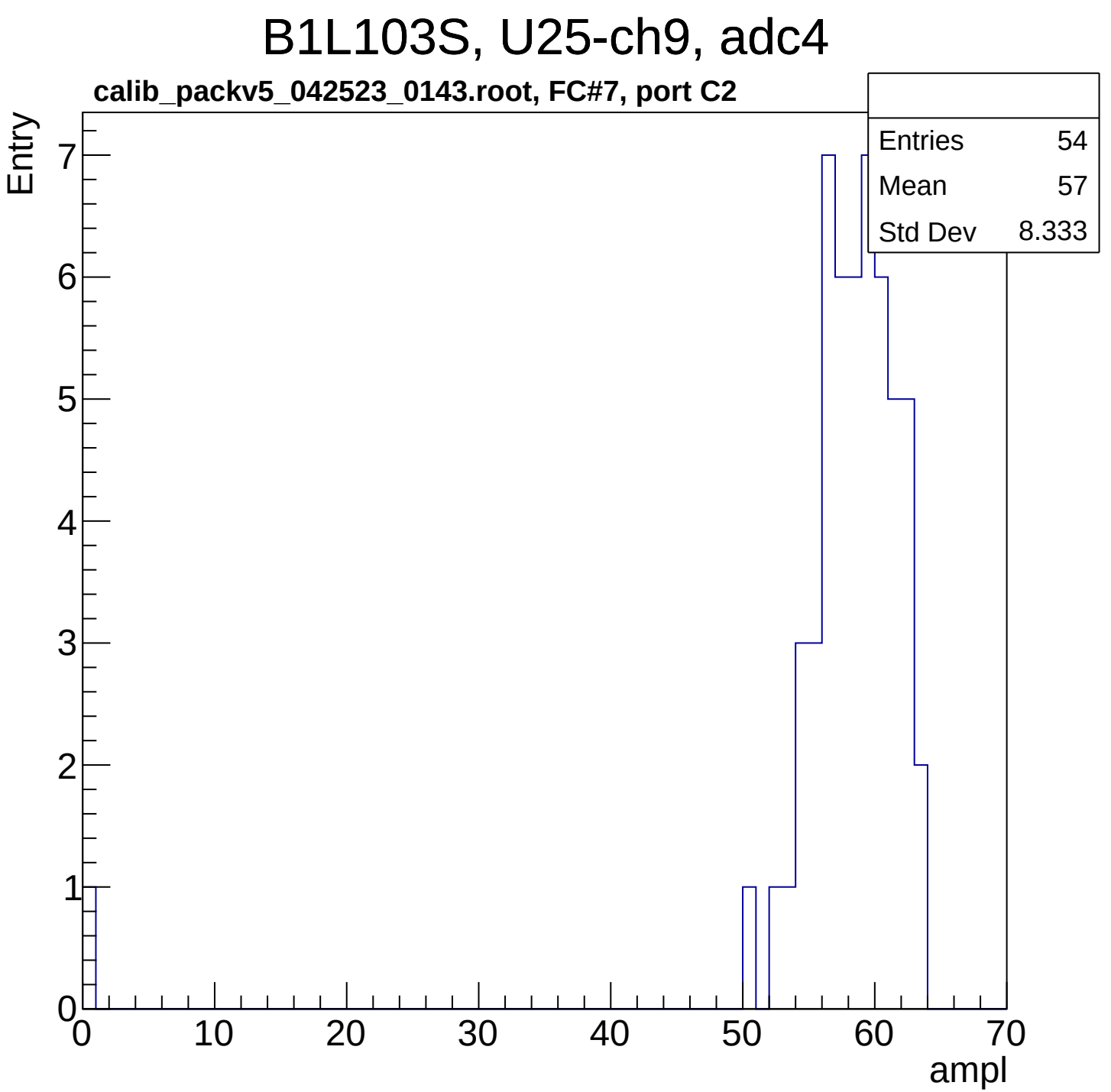
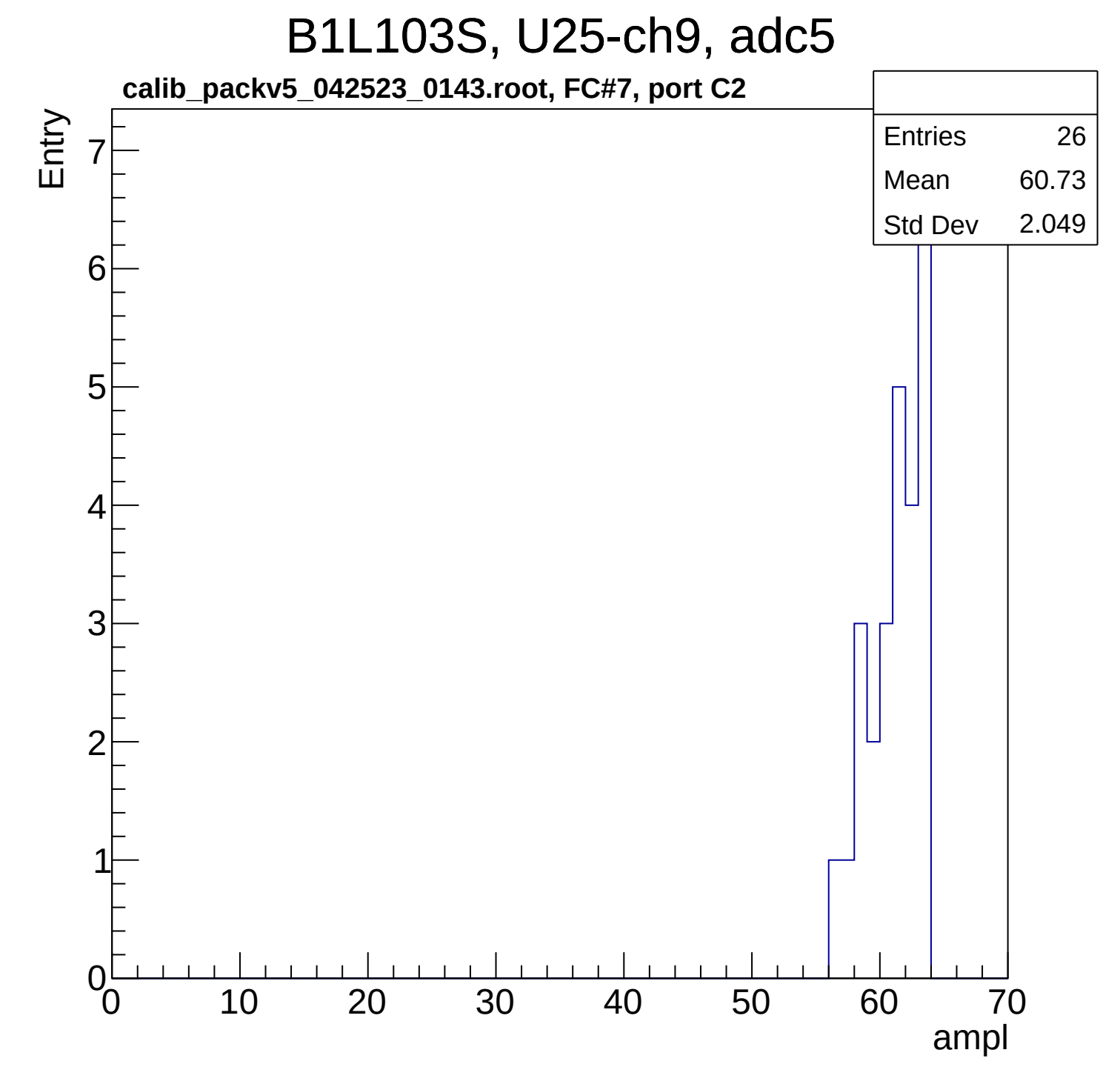
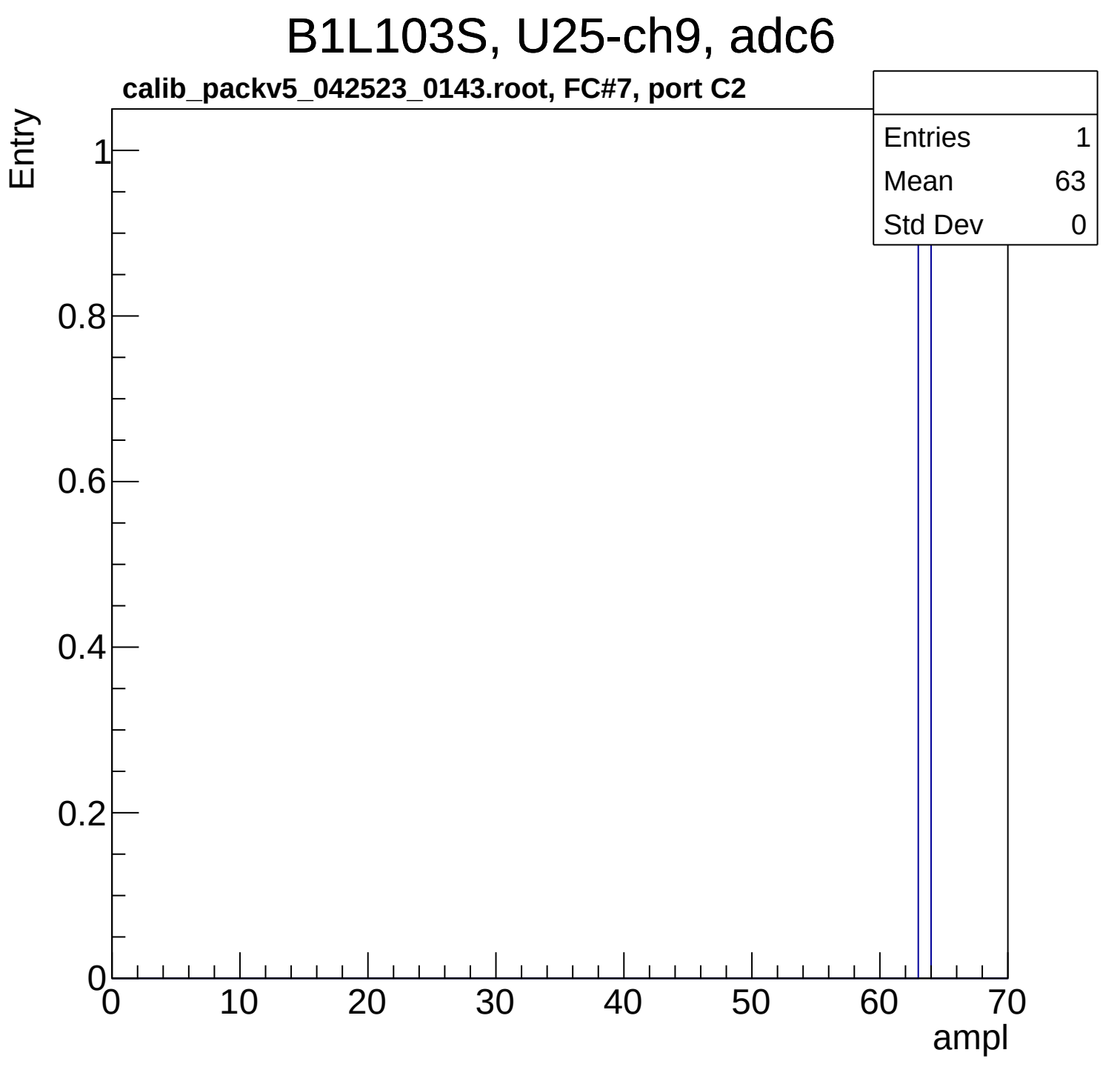

B1L103S, U25-ch9, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57
Std Dev 8.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch9, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.73
Std Dev 2.049
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch9, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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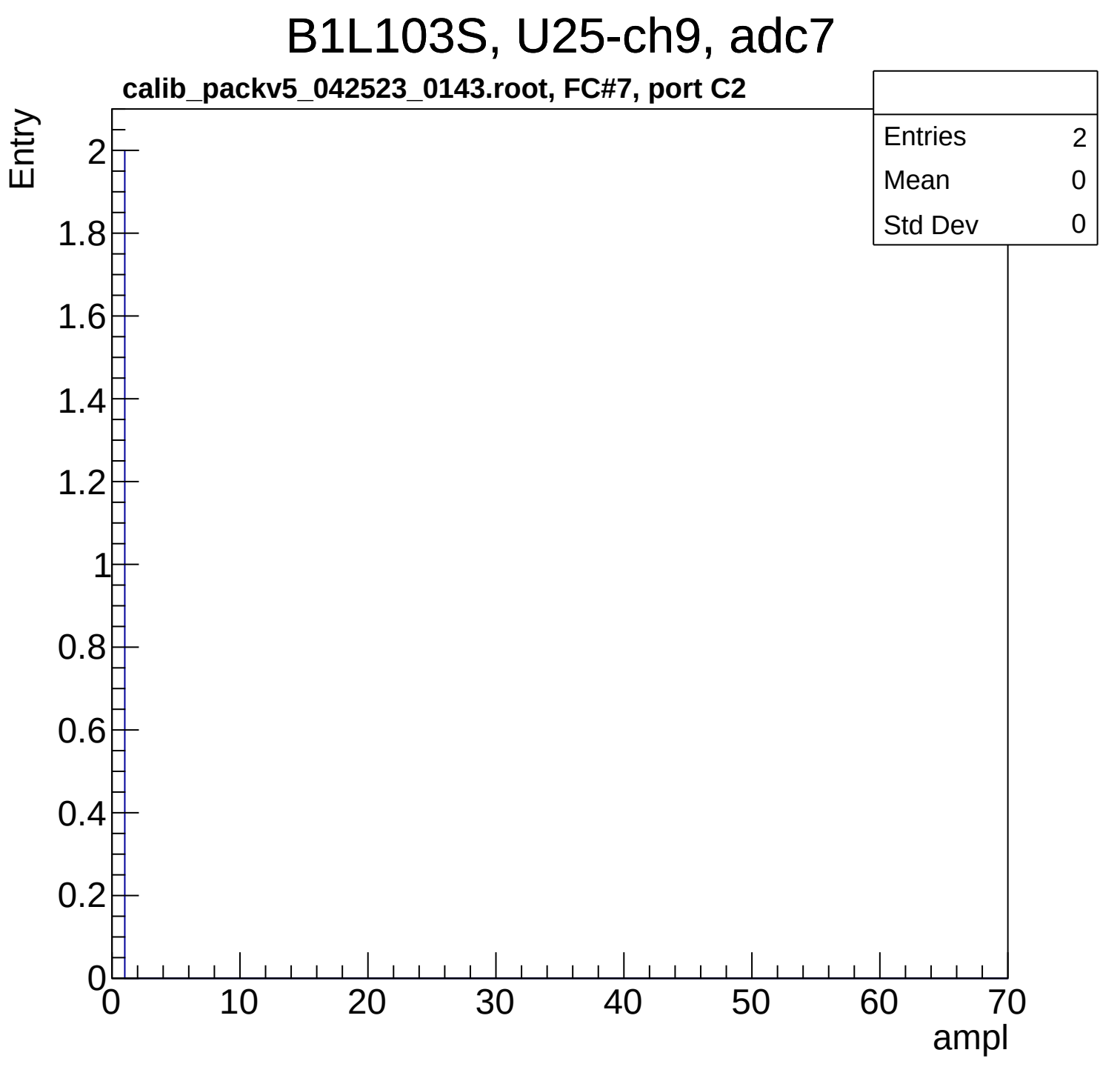
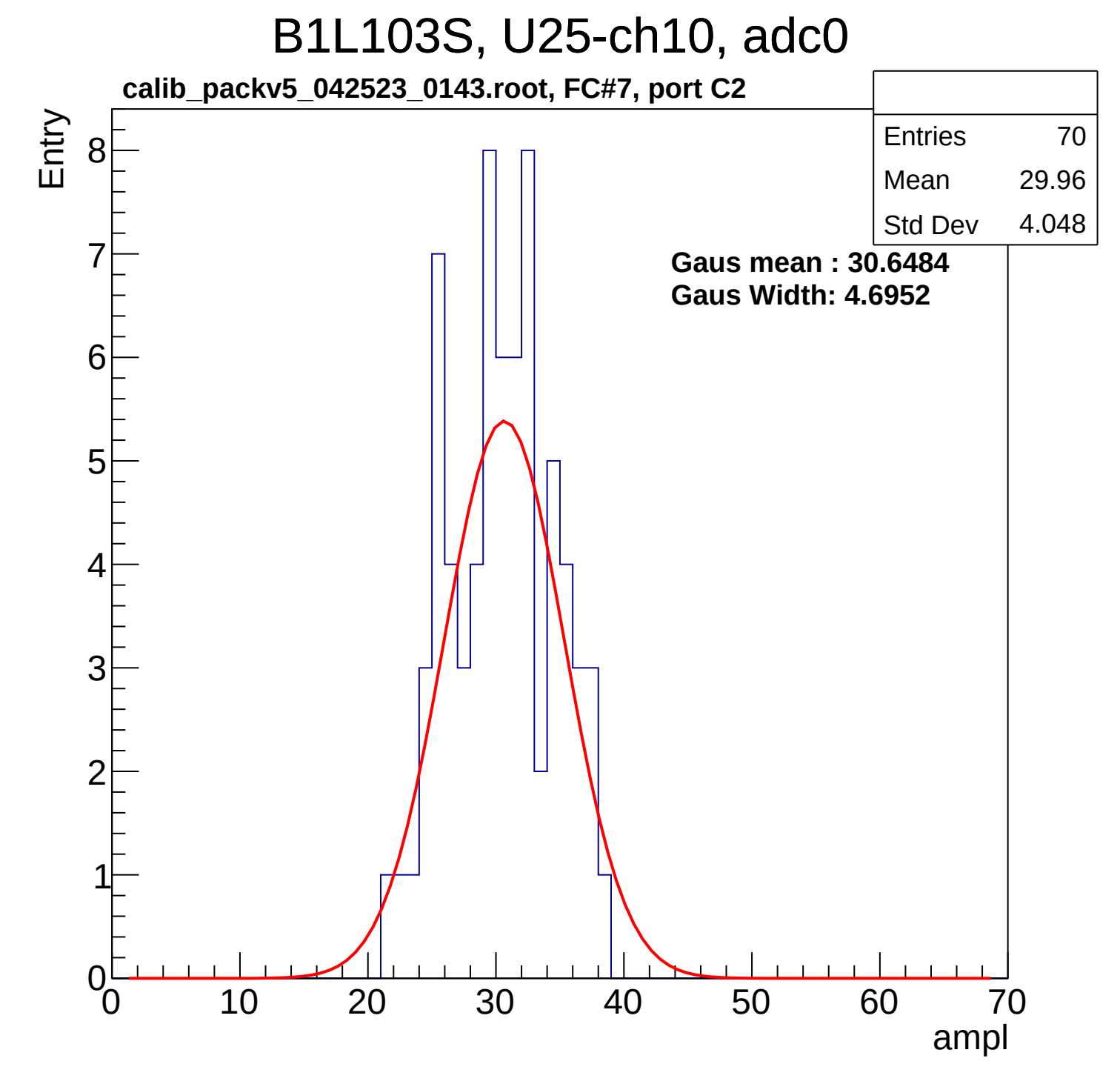
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch9, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch10, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 29.96
Std Dev 4.048
Gaus mean : 30.6484
Gaus Width: 4.6952
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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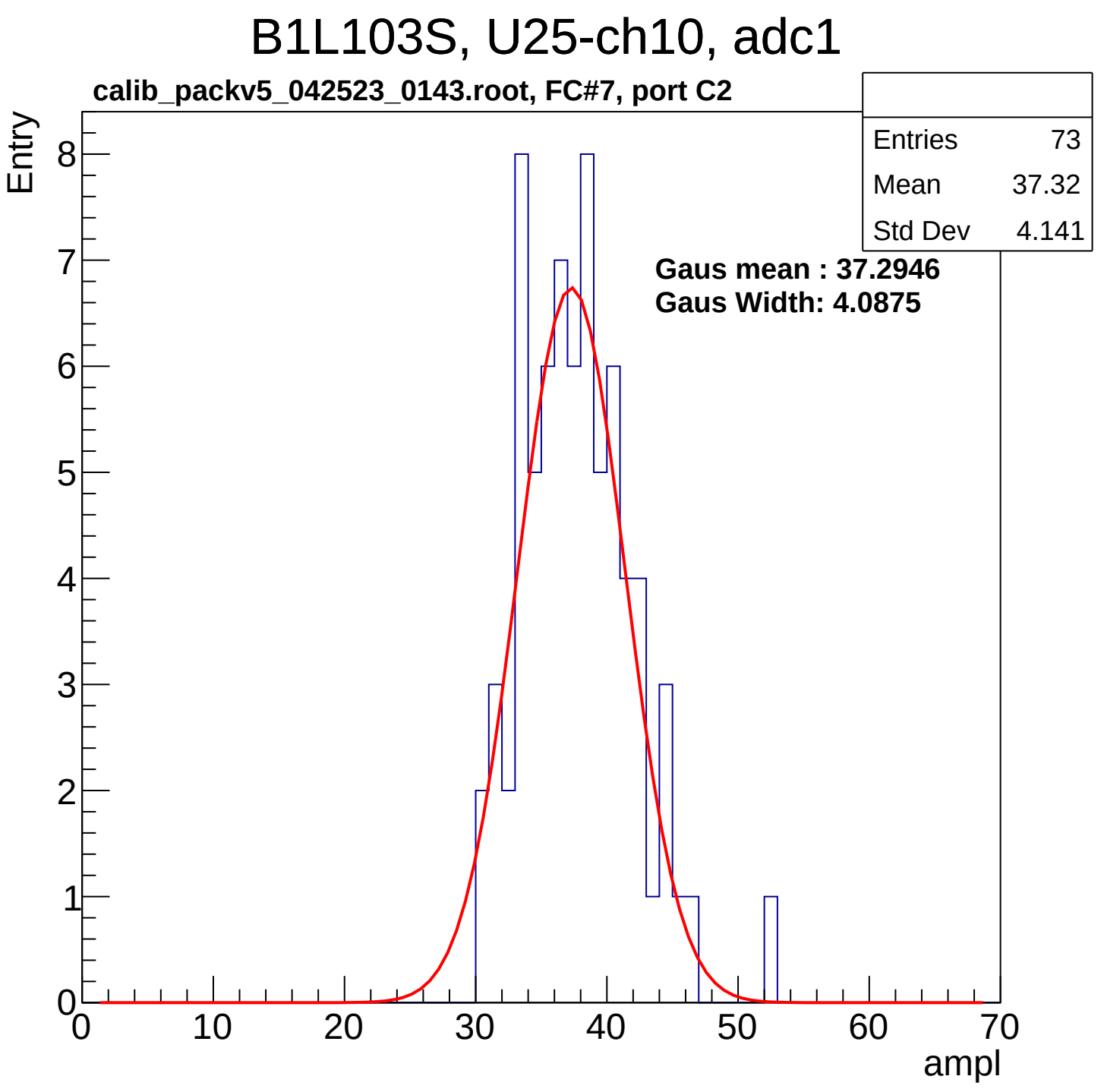
40
50
60
70


B1L103S, U25-ch10, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 37.32
Std Dev 4.141
Gaus mean : 37.2946
Gaus Width: 4.0875
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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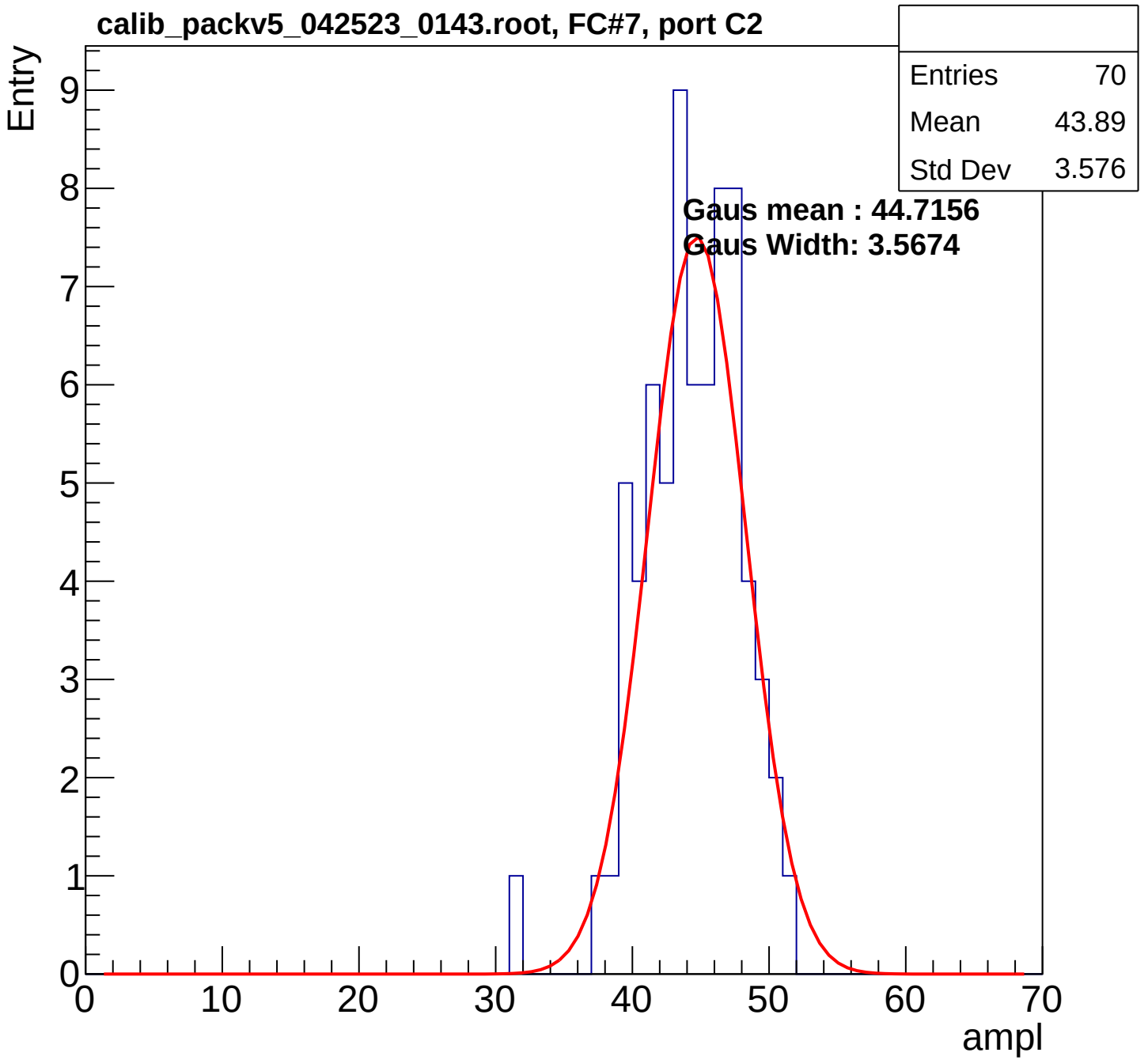
B1L103S, U25-ch10, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 43.89
Std Dev 3.576
Gaus mean : 44.7156
Gaus Width: 3.5674
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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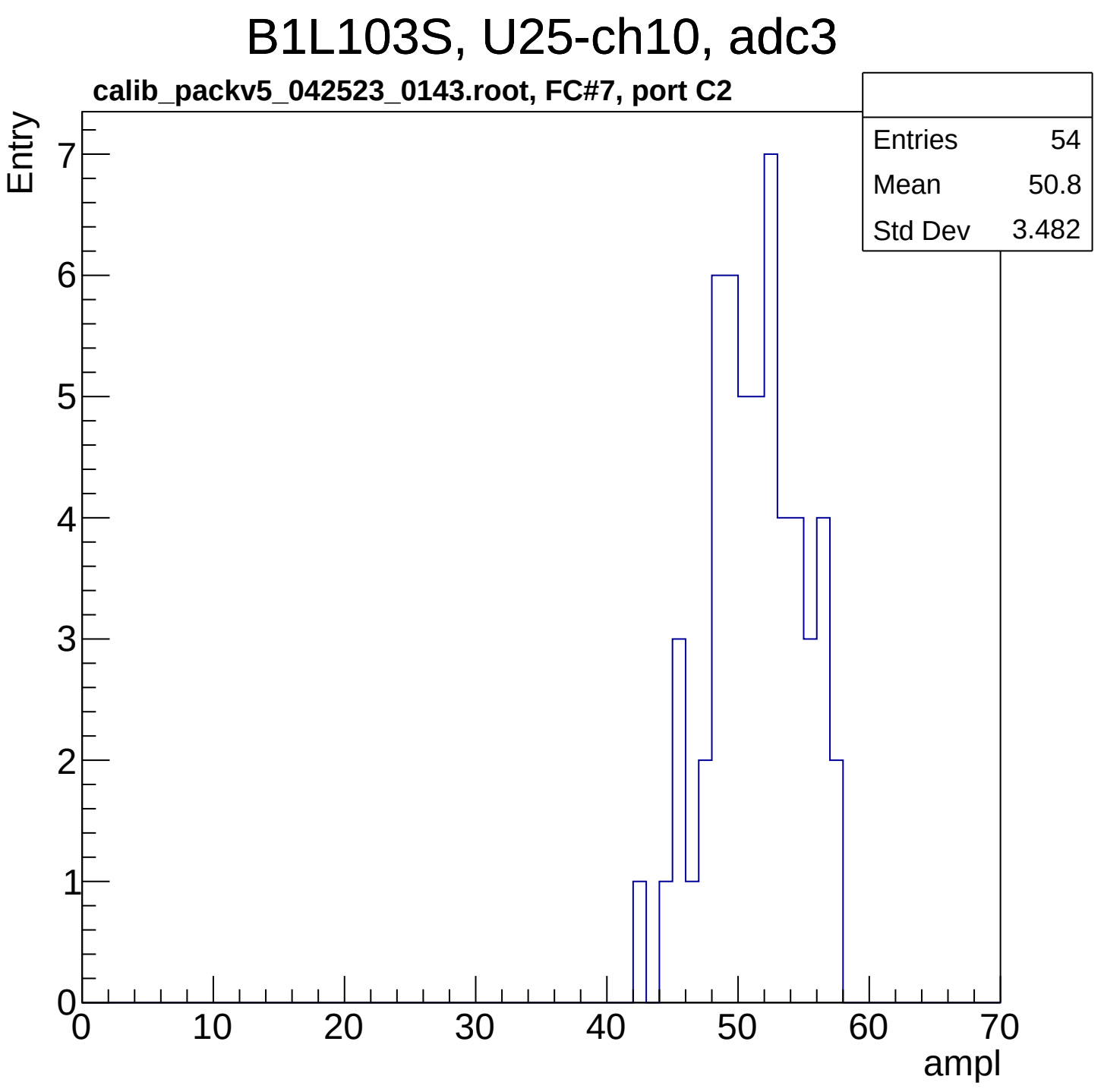
B1L103S, U25-ch10, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 50.8
Std Dev 3.482
Entry
ampl
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3
4
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0
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40
50
60
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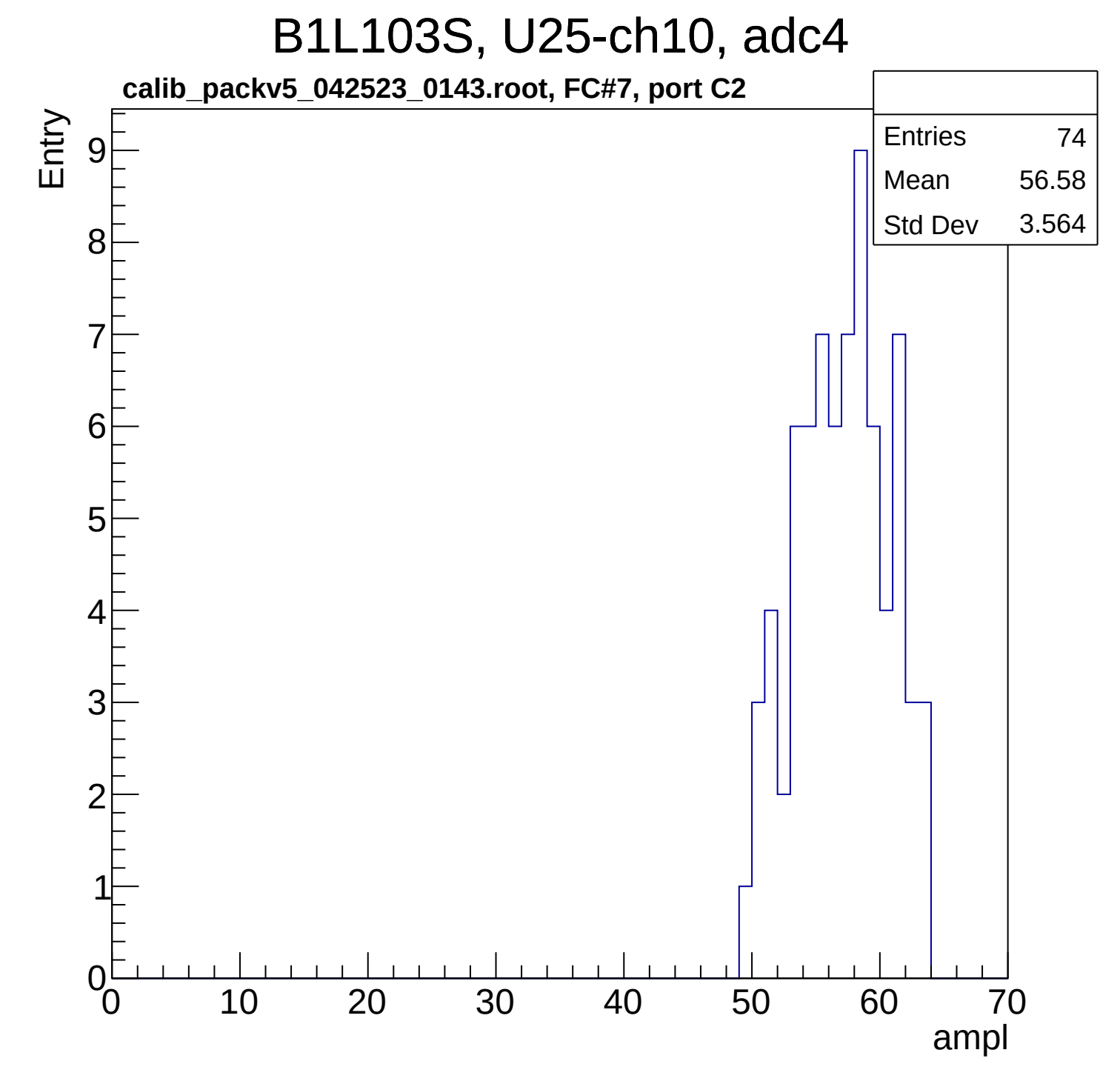
B1L103S, U25-ch10, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 56.58
Std Dev 3.564
Entry
ampl
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4
5
6
7
8
9
0
10
20
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60
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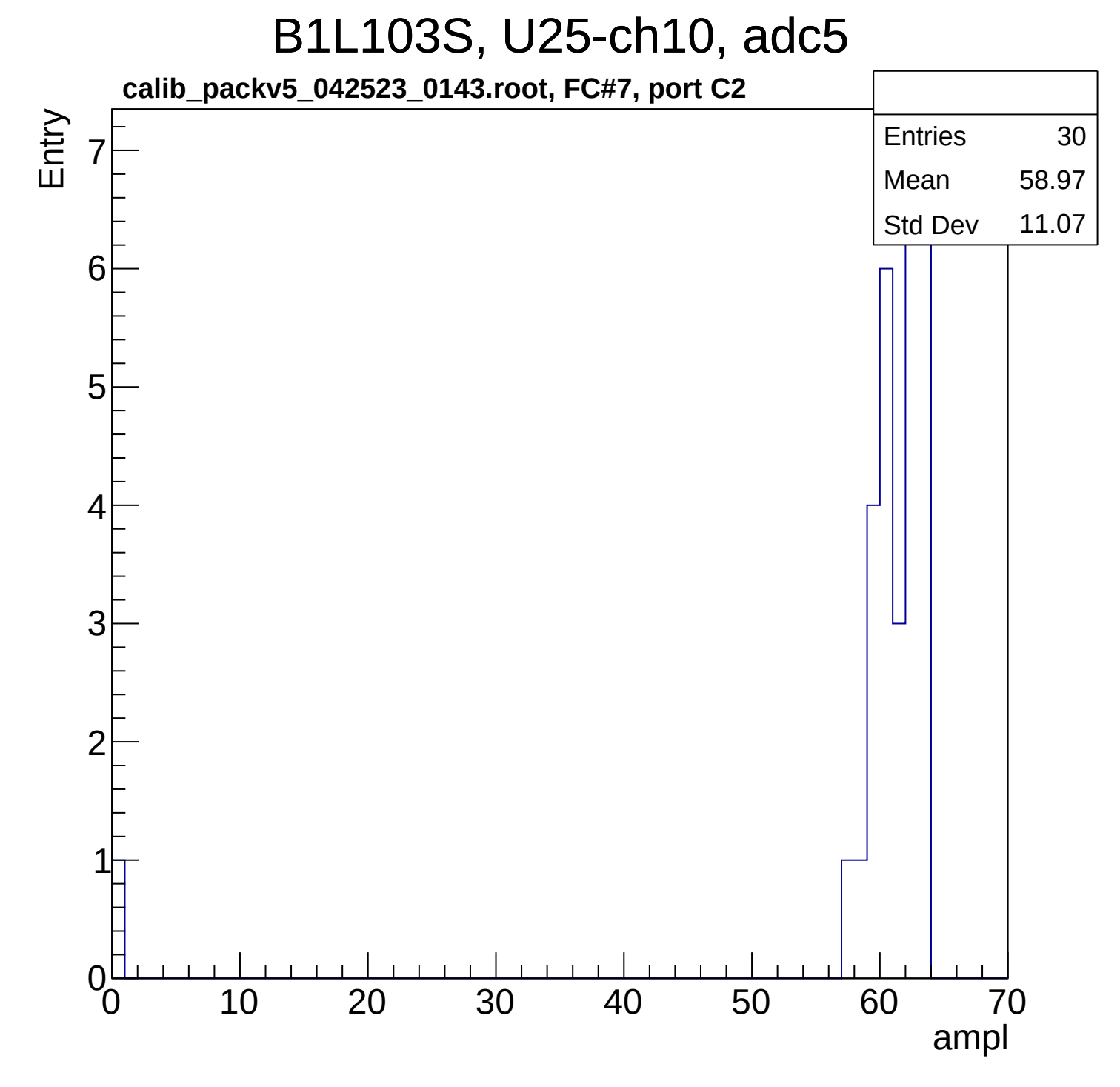
B1L103S, U25-ch10, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 30
Mean 58.97
Std Dev 11.07
Entry
ampl
0
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2
3
4
5
6
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0
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40
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60
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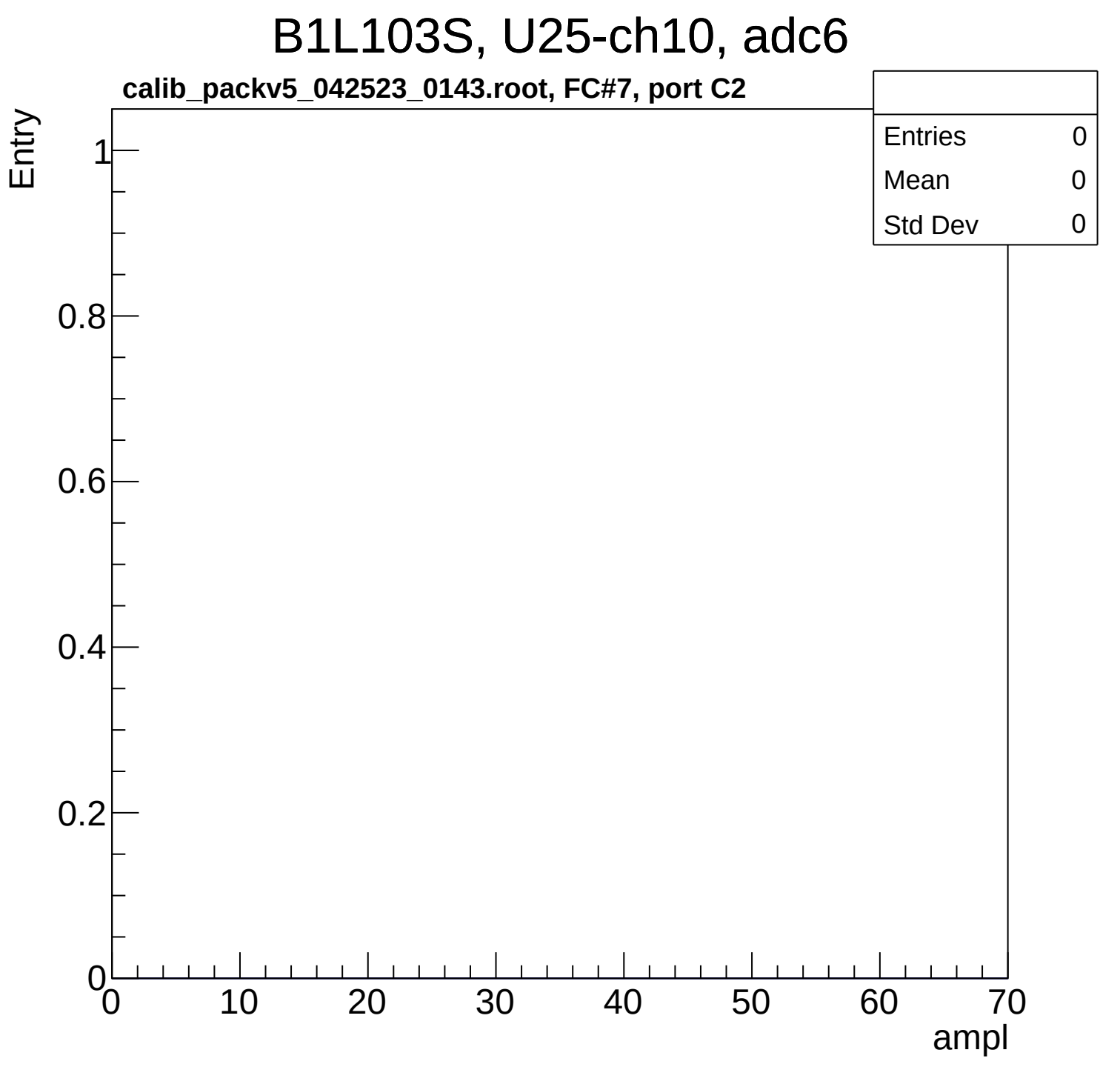
B1L103S, U25-ch10, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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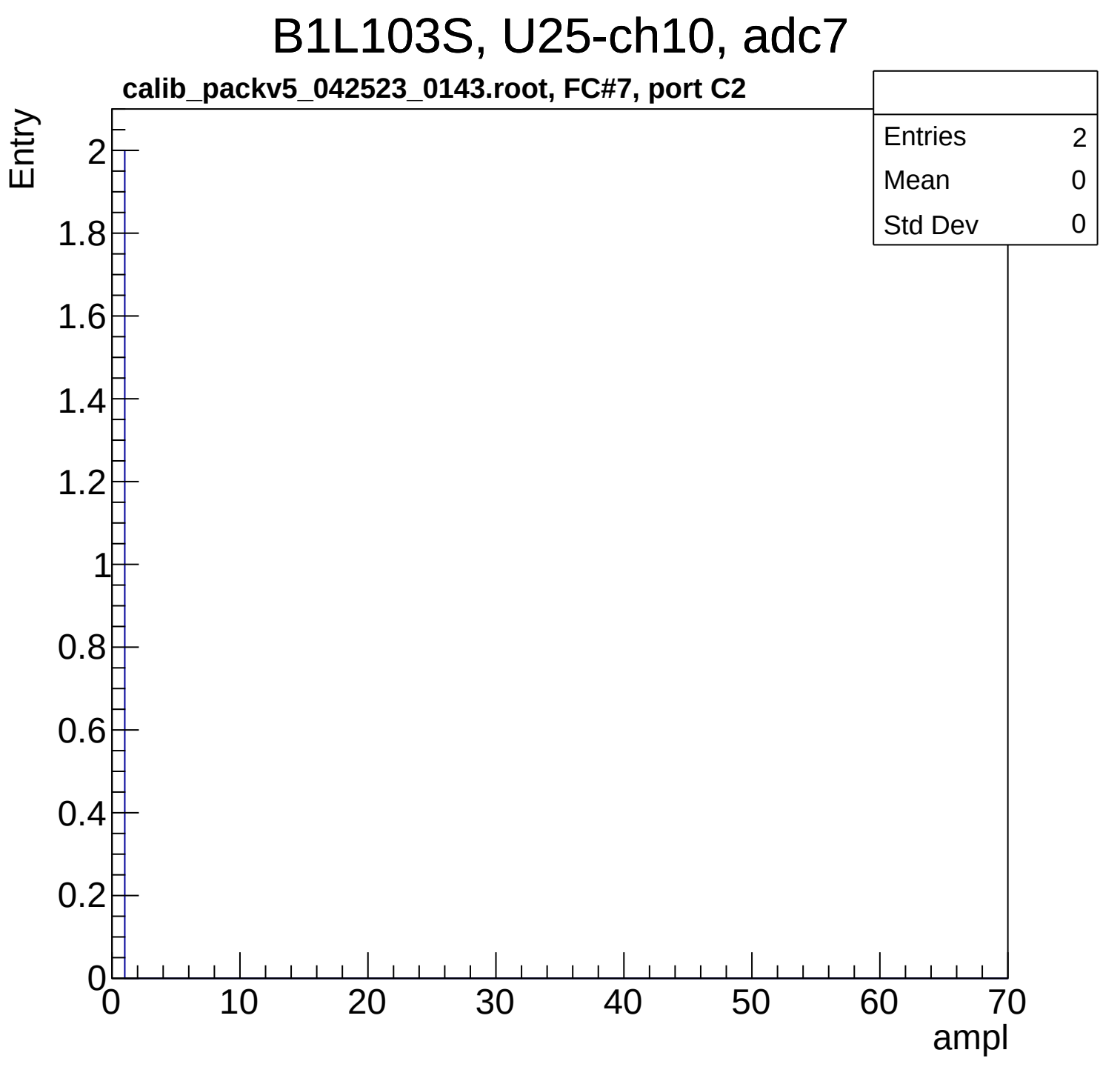
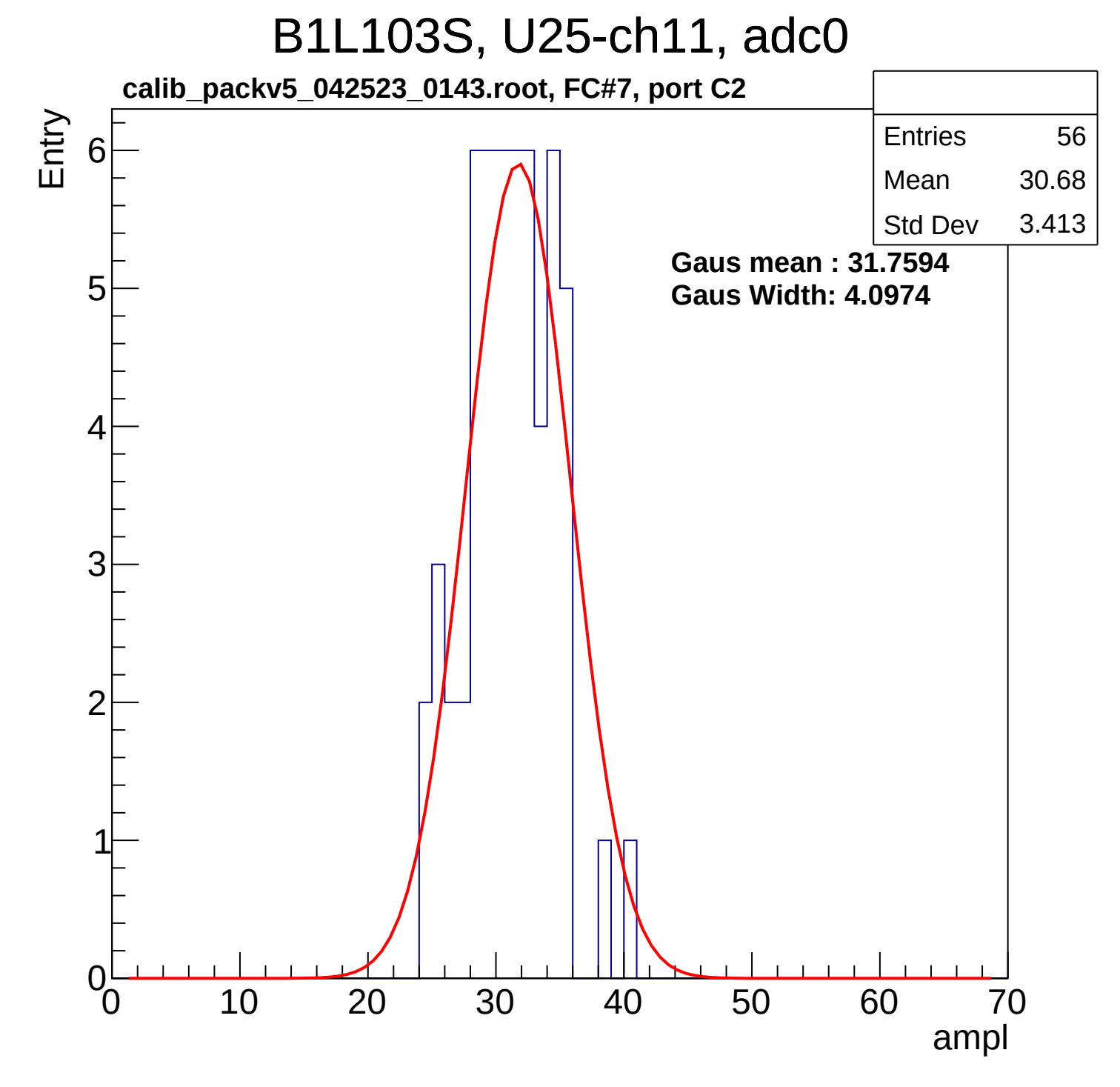
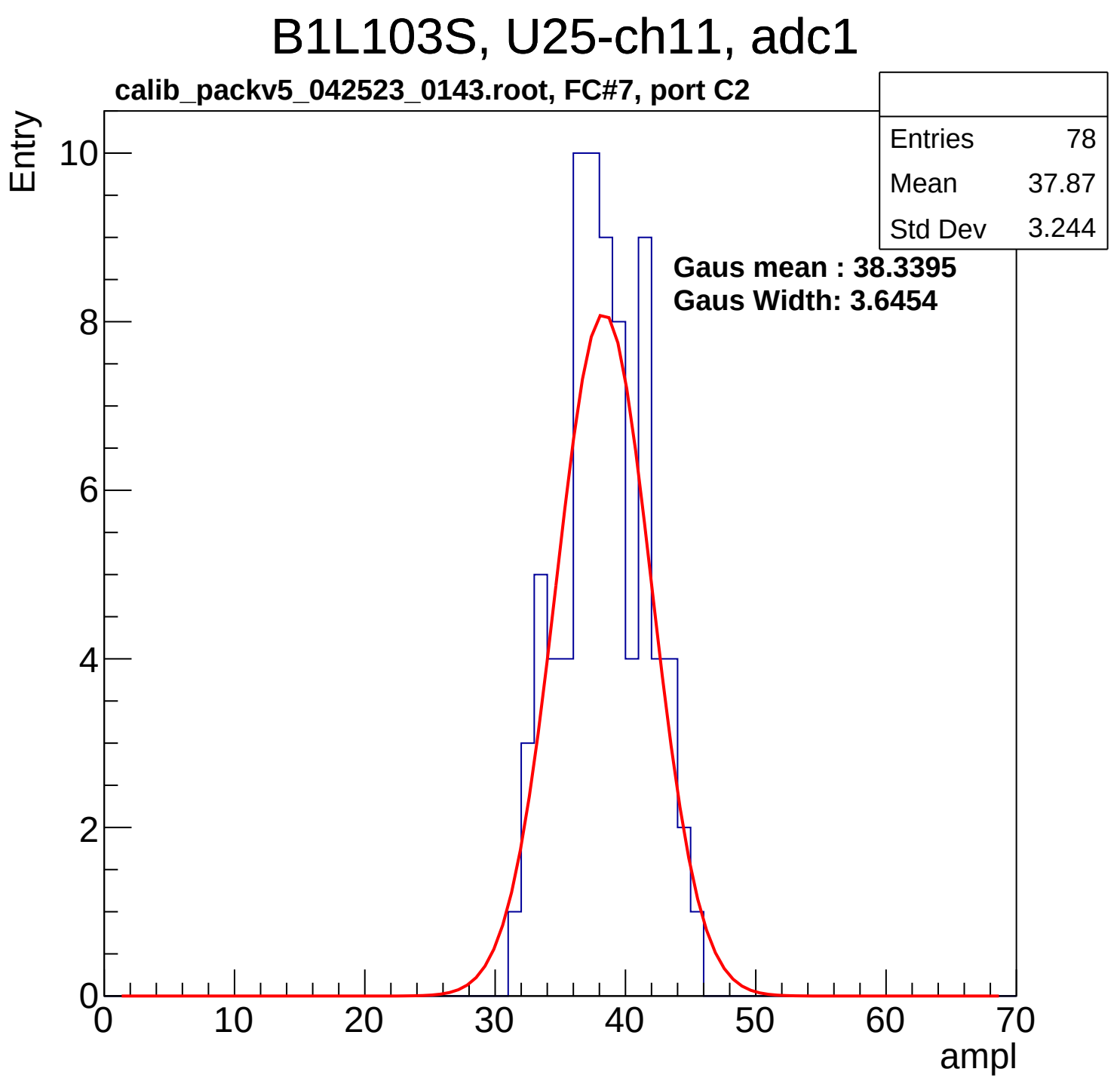
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch10, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch11, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 30.68
Std Dev 3.413
Gaus mean : 31.7594
Gaus Width: 4.0974
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch11, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 37.87
Std Dev 3.244
Gaus mean : 38.3395
Gaus Width: 3.6454
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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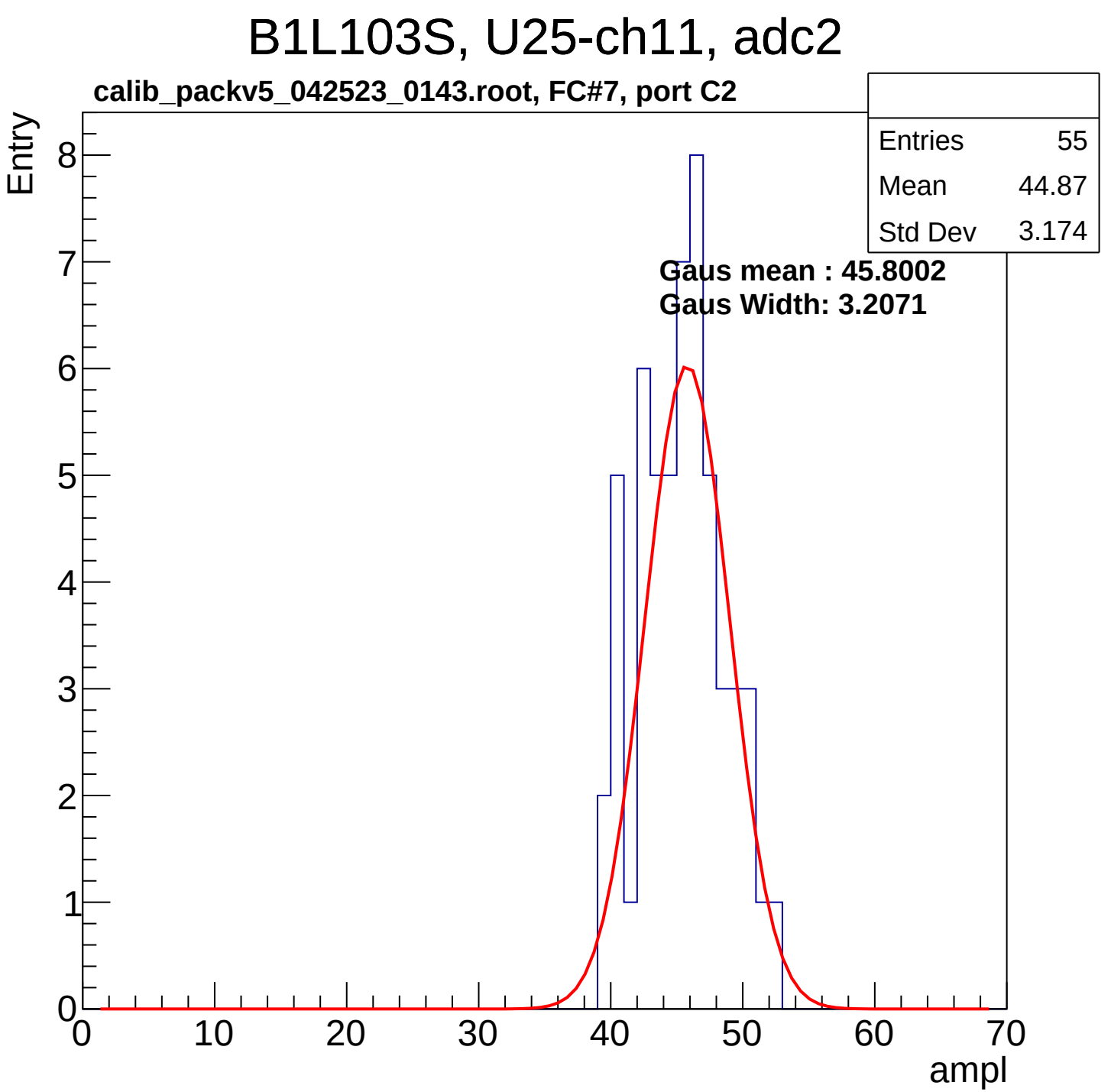
B1L103S, U25-ch11, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 44.87
Std Dev 3.174
Gaus mean : 45.8002
Gaus Width: 3.2071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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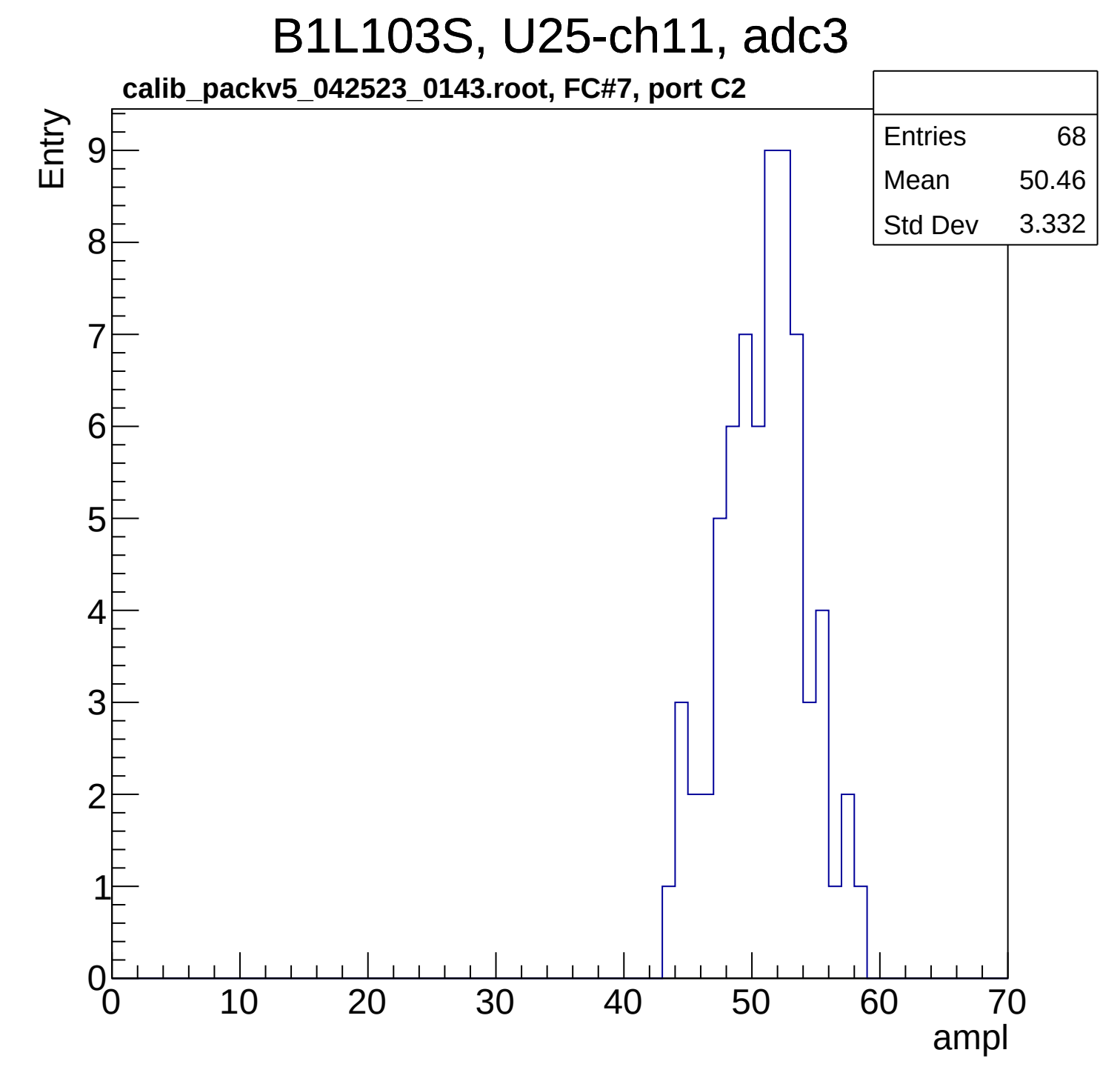
B1L103S, U25-ch11, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 50.46
Std Dev 3.332
Entry
ampl
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3
4
5
6
7
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40
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60
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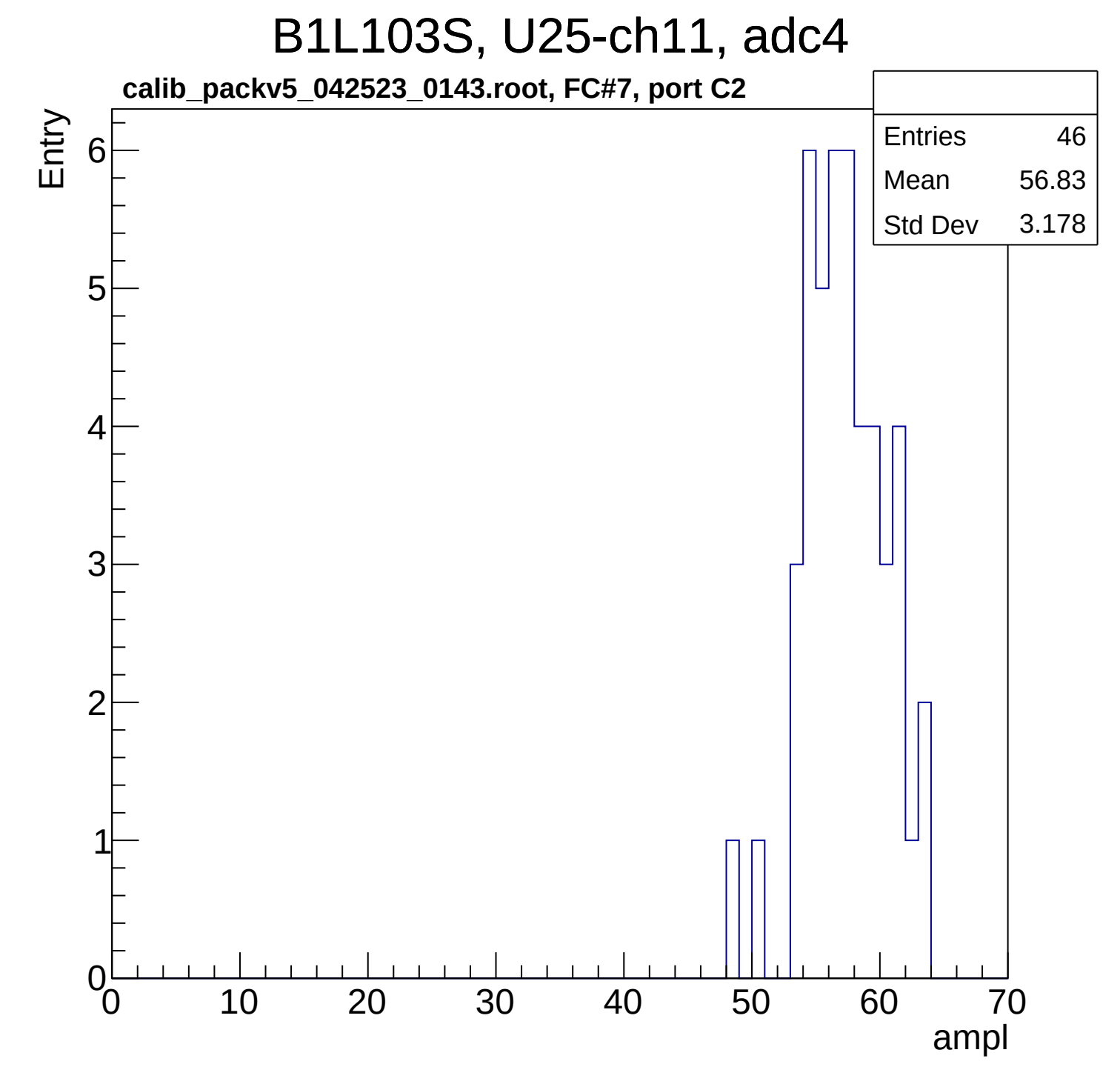
B1L103S, U25-ch11, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.83
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
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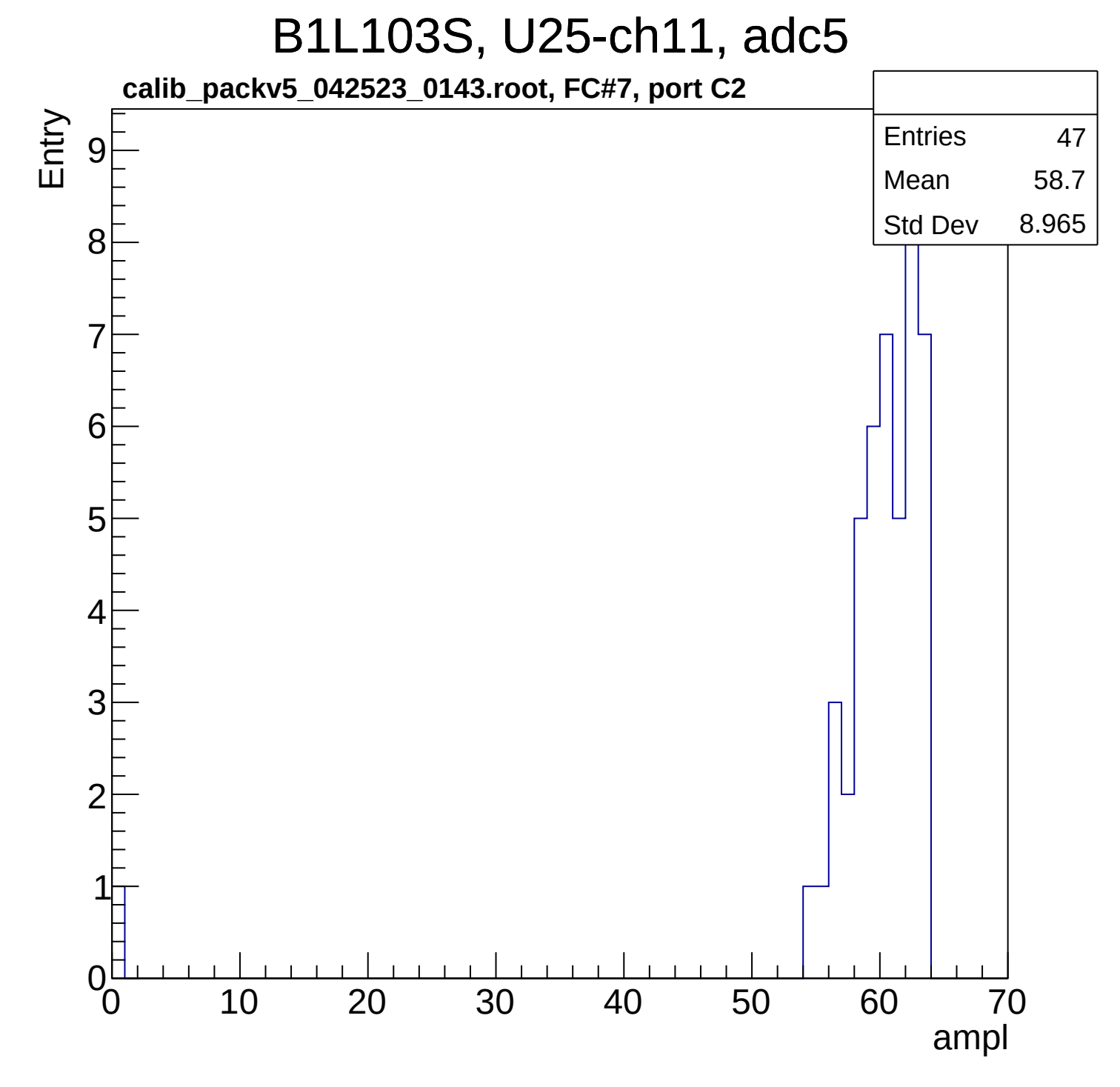
B1L103S, U25-ch11, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.7
Std Dev 8.965
Entry
ampl
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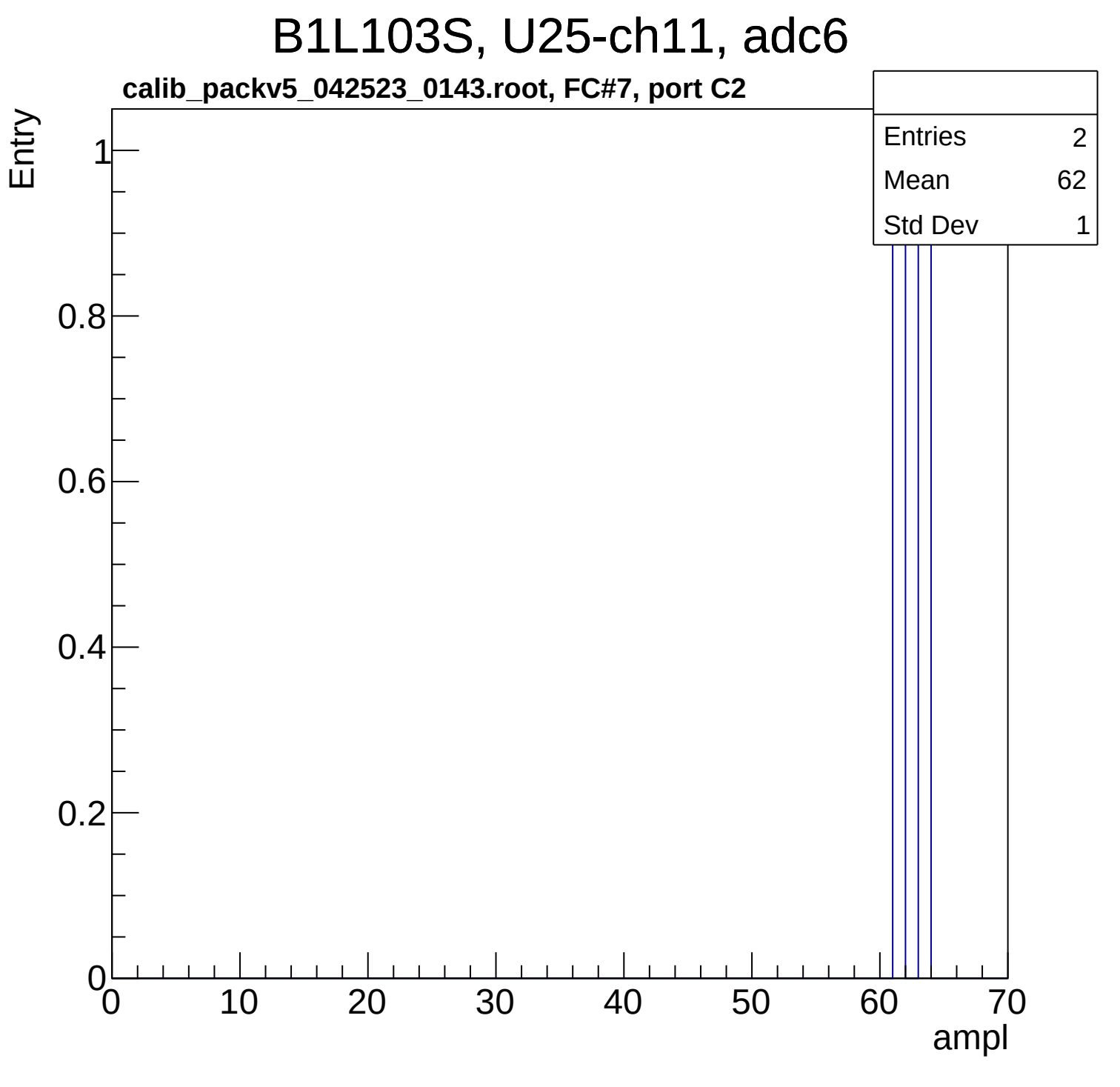
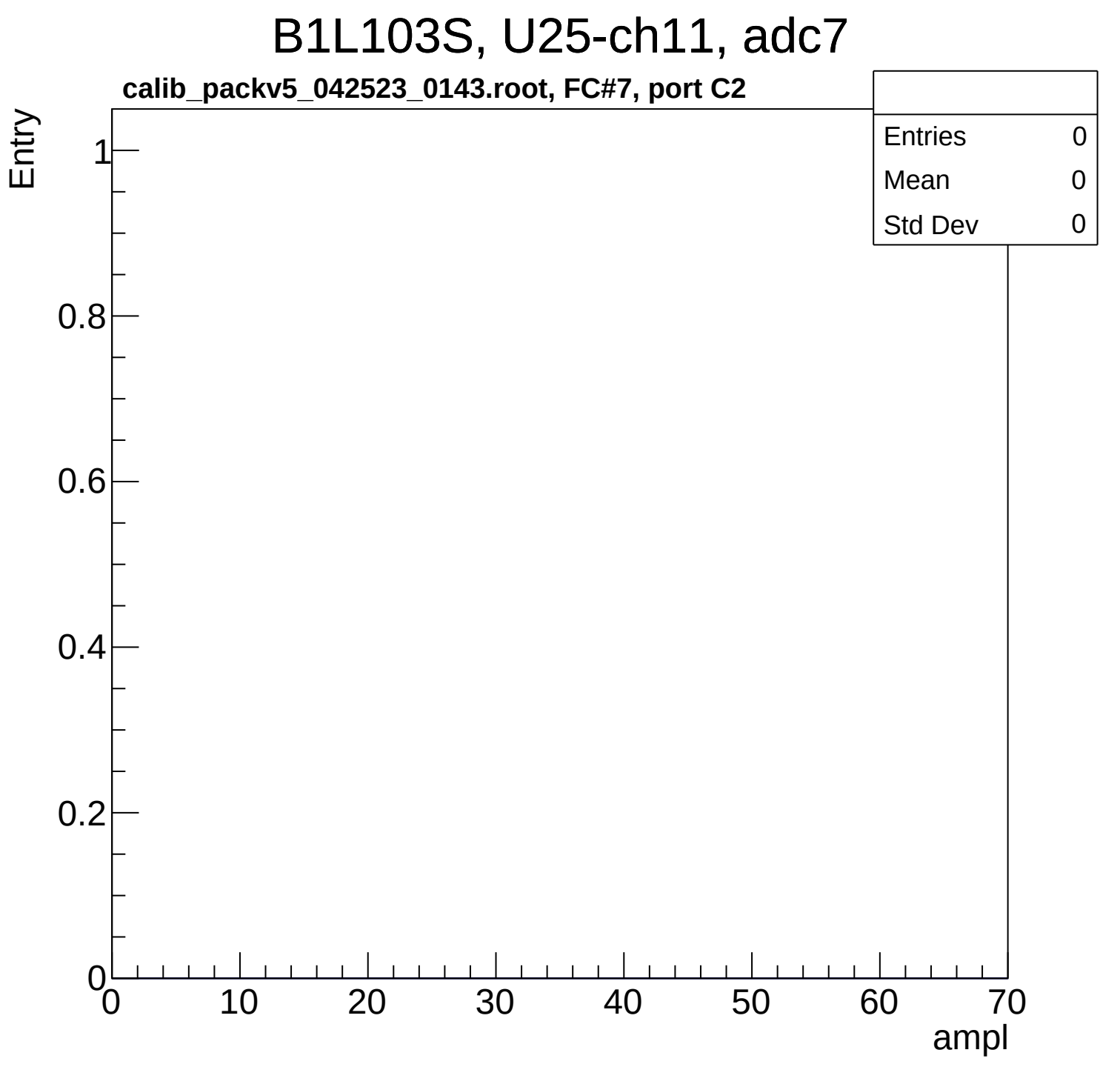

B1L103S, U25-ch11, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch11, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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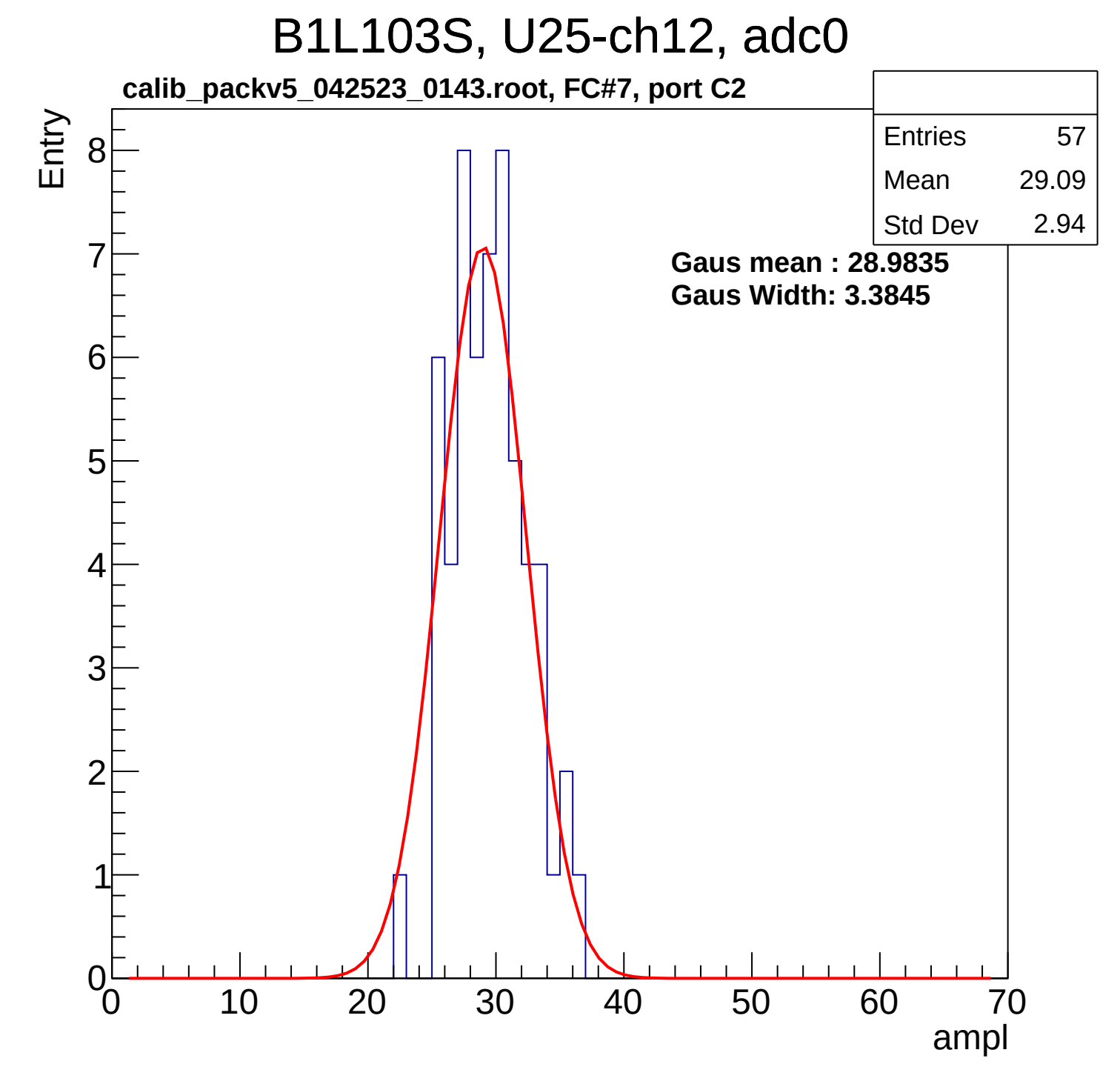
40
50
60
70
ampl


B1L103S, U25-ch12, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 29.09
Std Dev 2.94
Gaus mean : 28.9835
Gaus Width: 3.3845
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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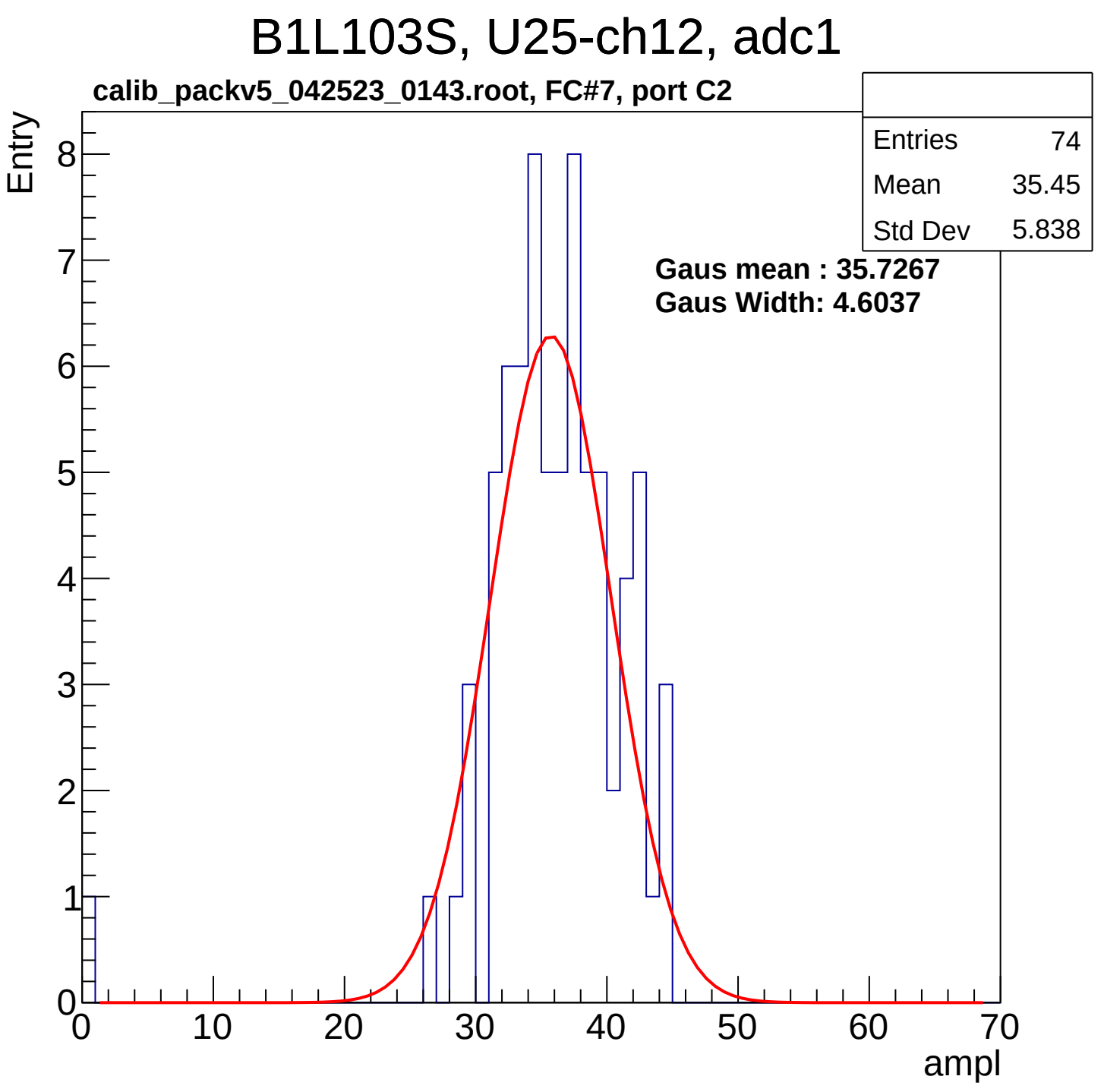
B1L103S, U25-ch12, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 35.45
Std Dev 5.838
Gaus mean : 35.7267
Gaus Width: 4.6037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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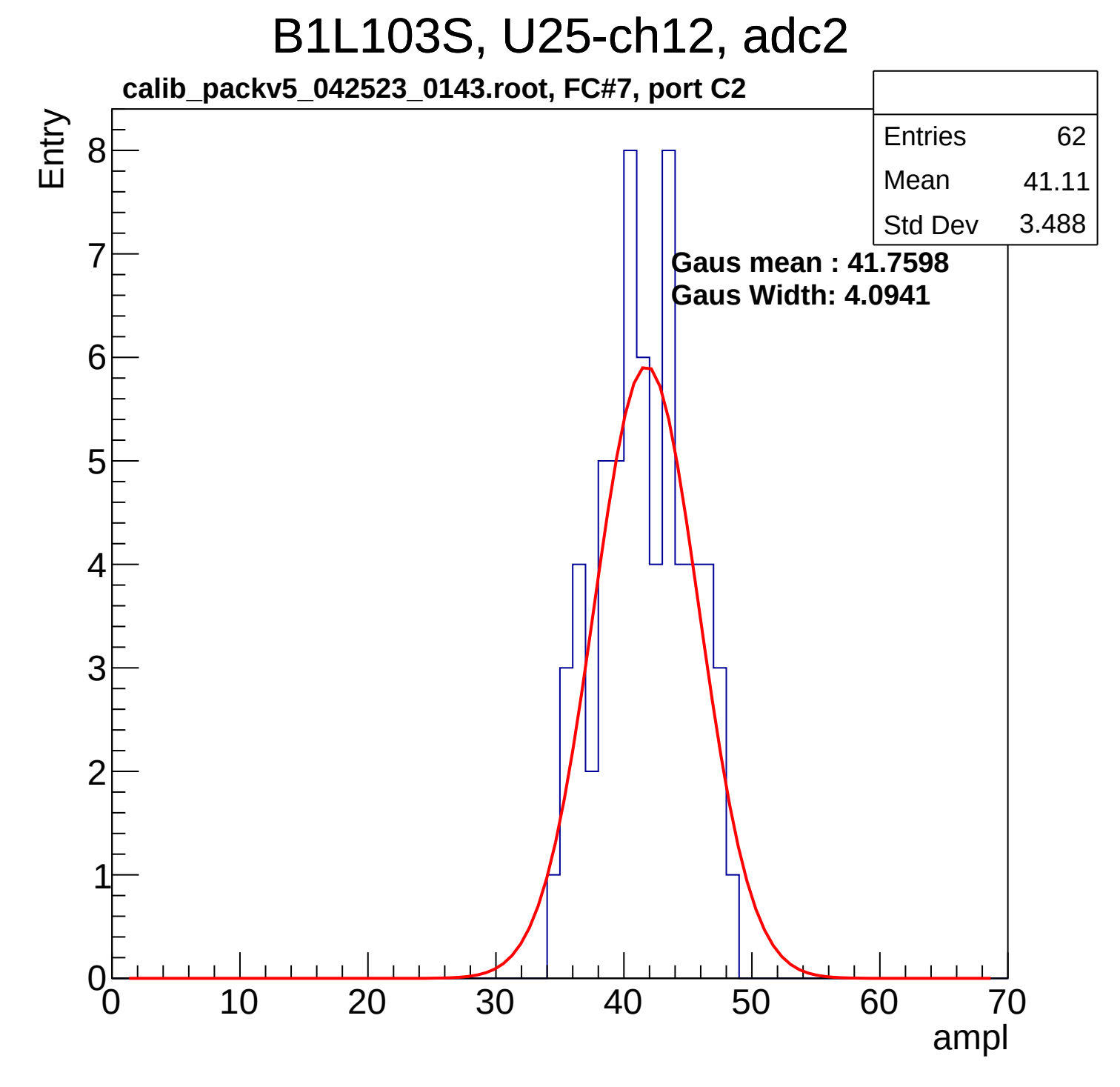
B1L103S, U25-ch12, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 41.11
Std Dev 3.488
Gaus mean : 41.7598
Gaus Width: 4.0941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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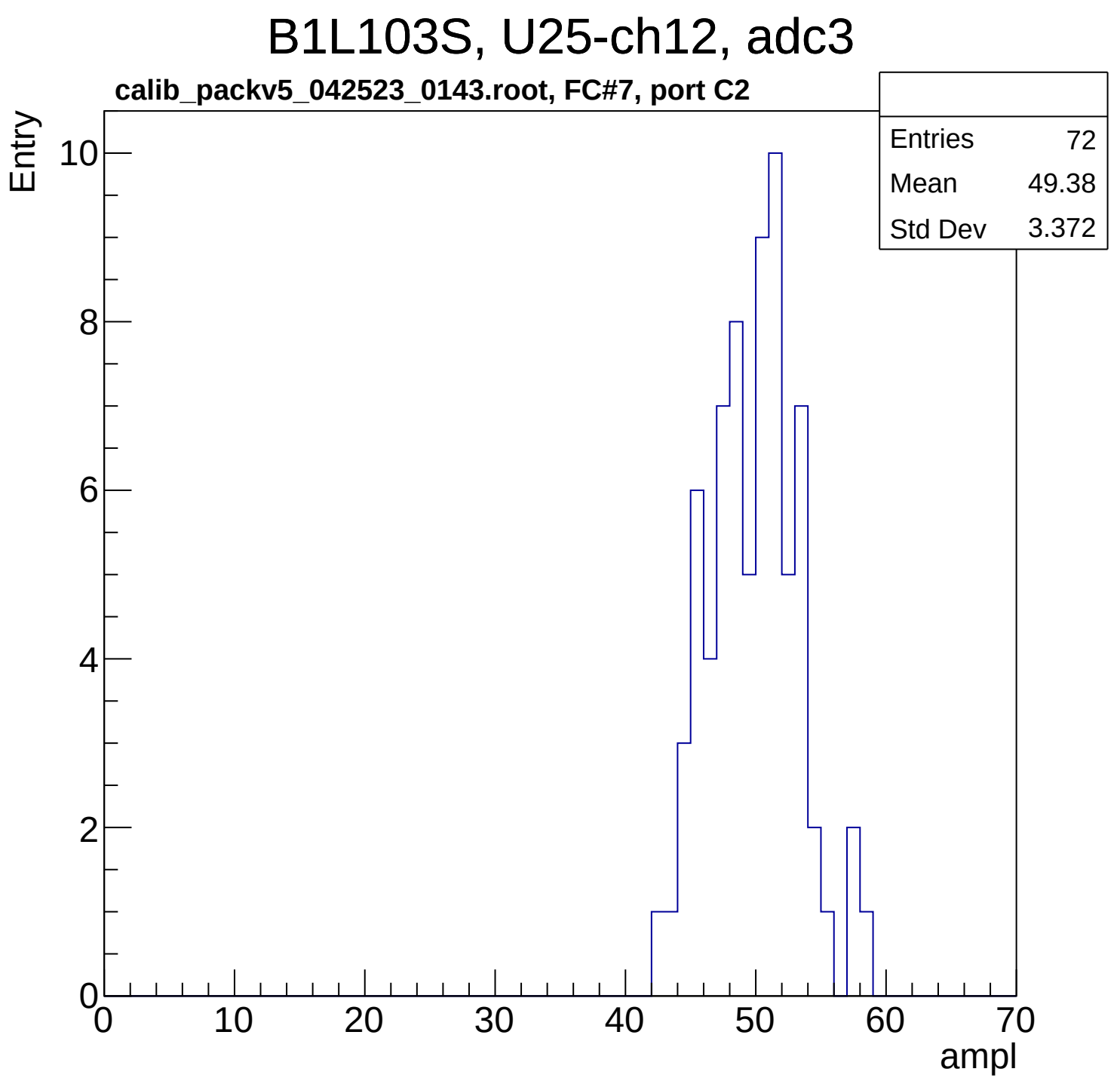
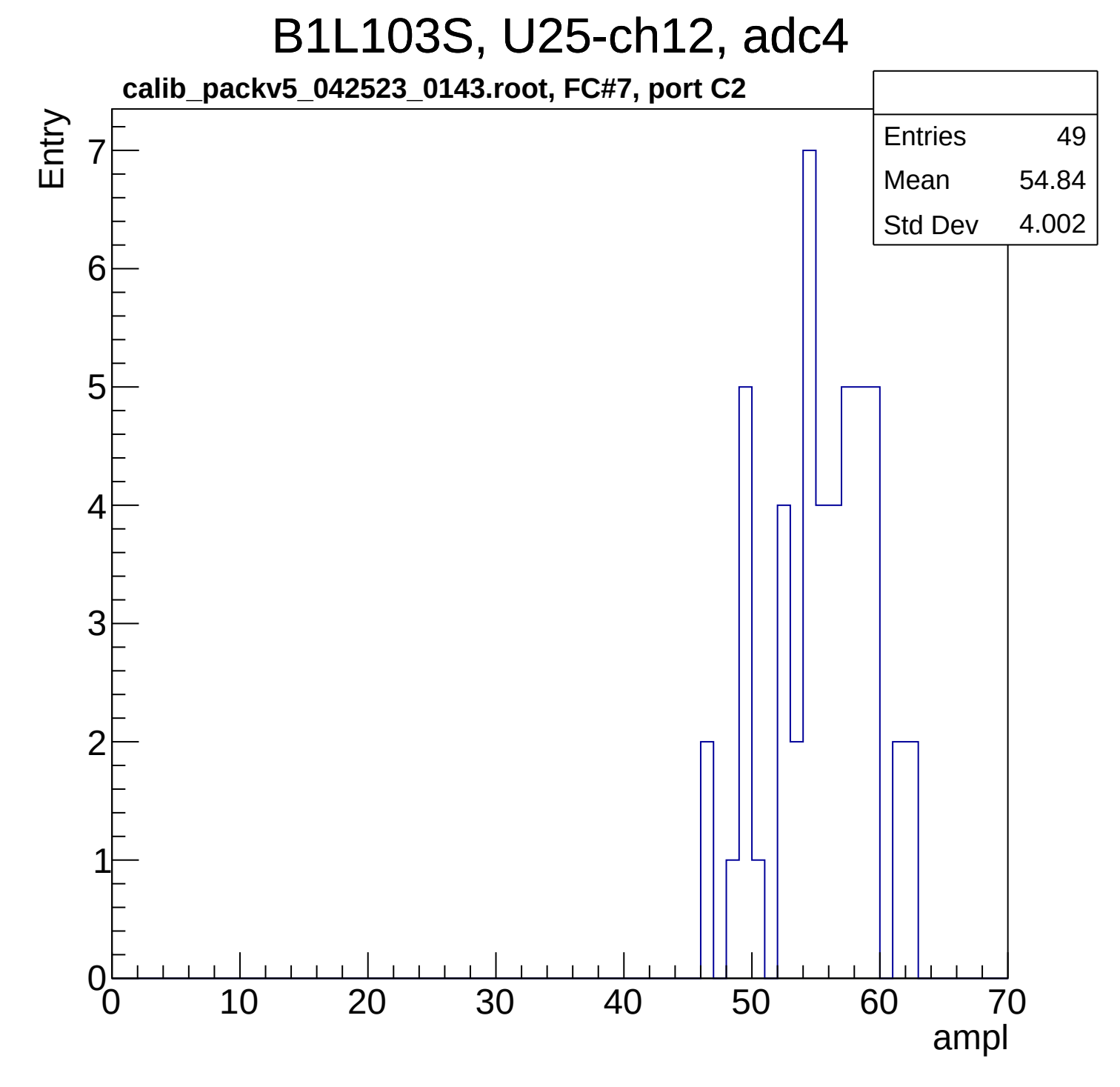

B1L103S, U25-ch12, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.38
Std Dev 3.372
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch12, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 54.84
Std Dev 4.002
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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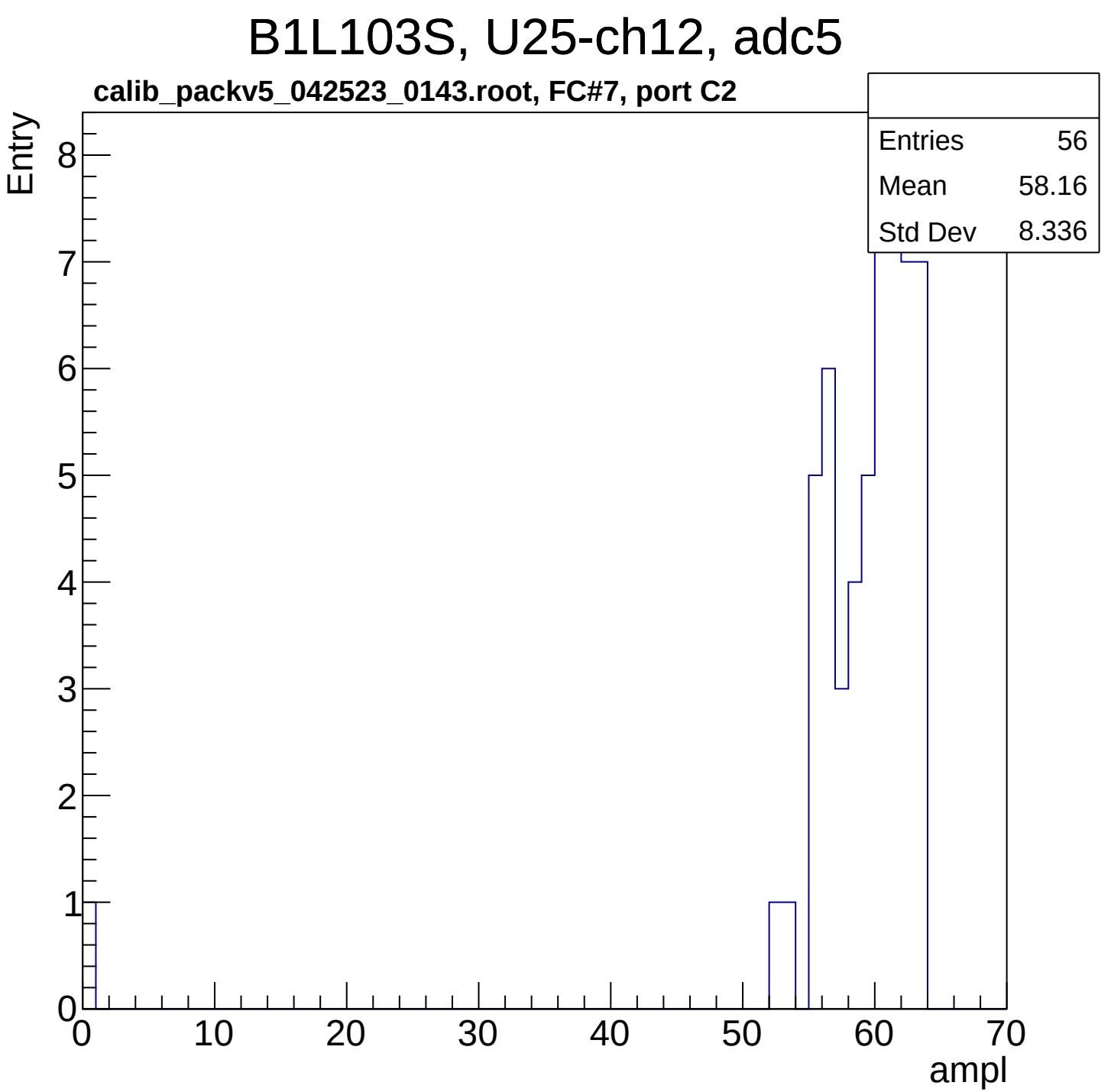
B1L103S, U25-ch12, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.16
Std Dev 8.336
Entry
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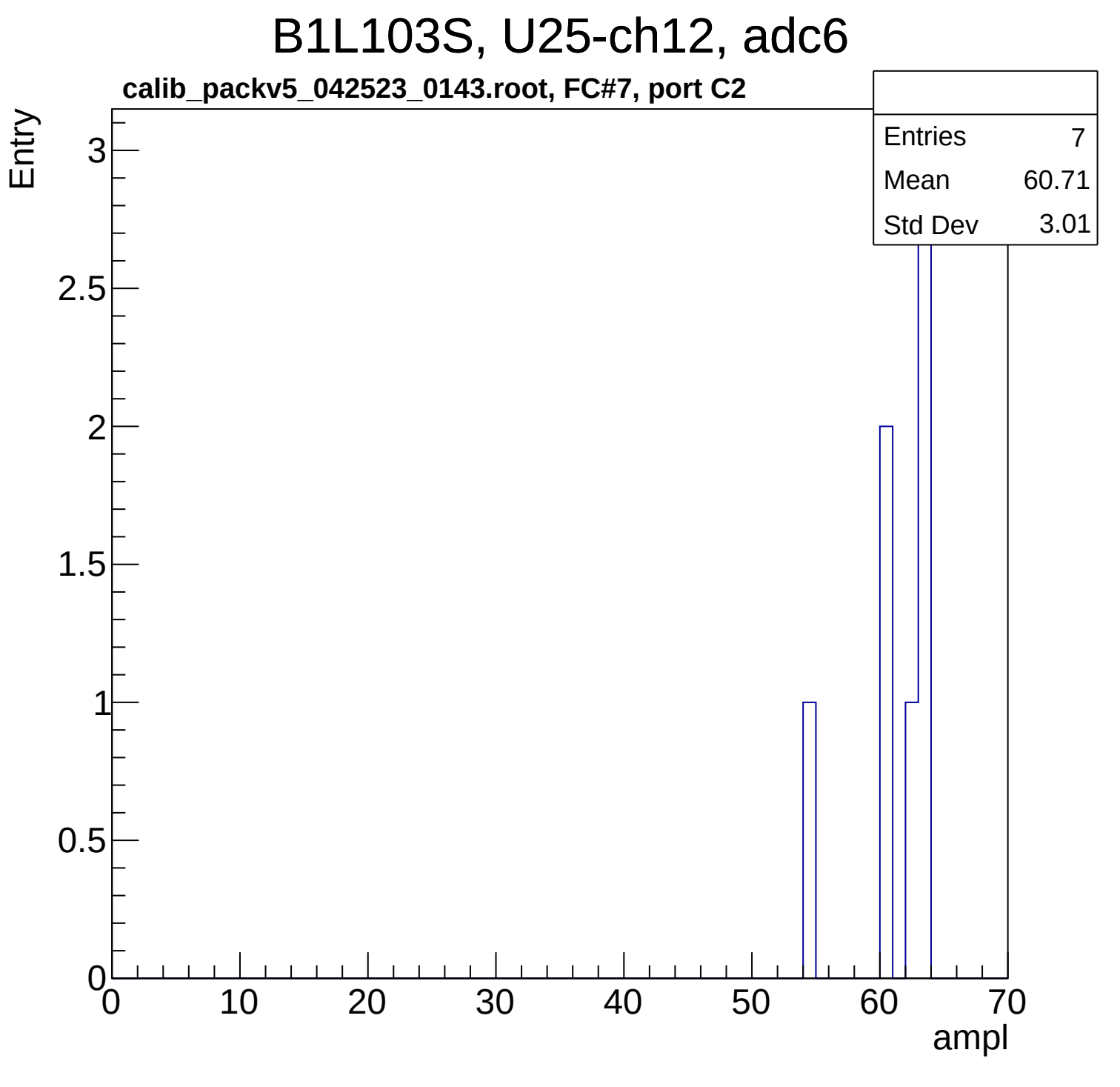
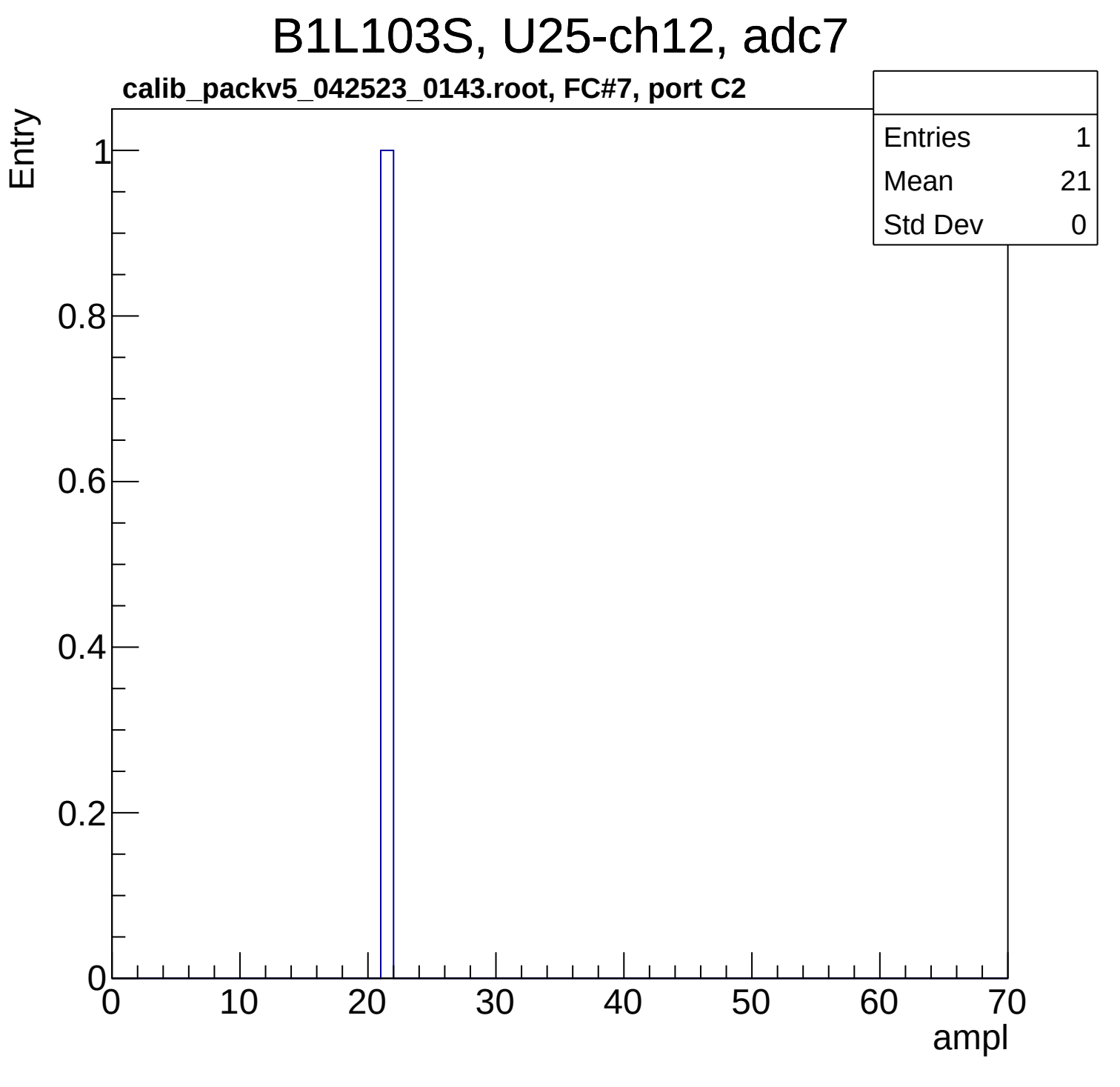
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch12, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 60.71
Std Dev 3.01
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch12, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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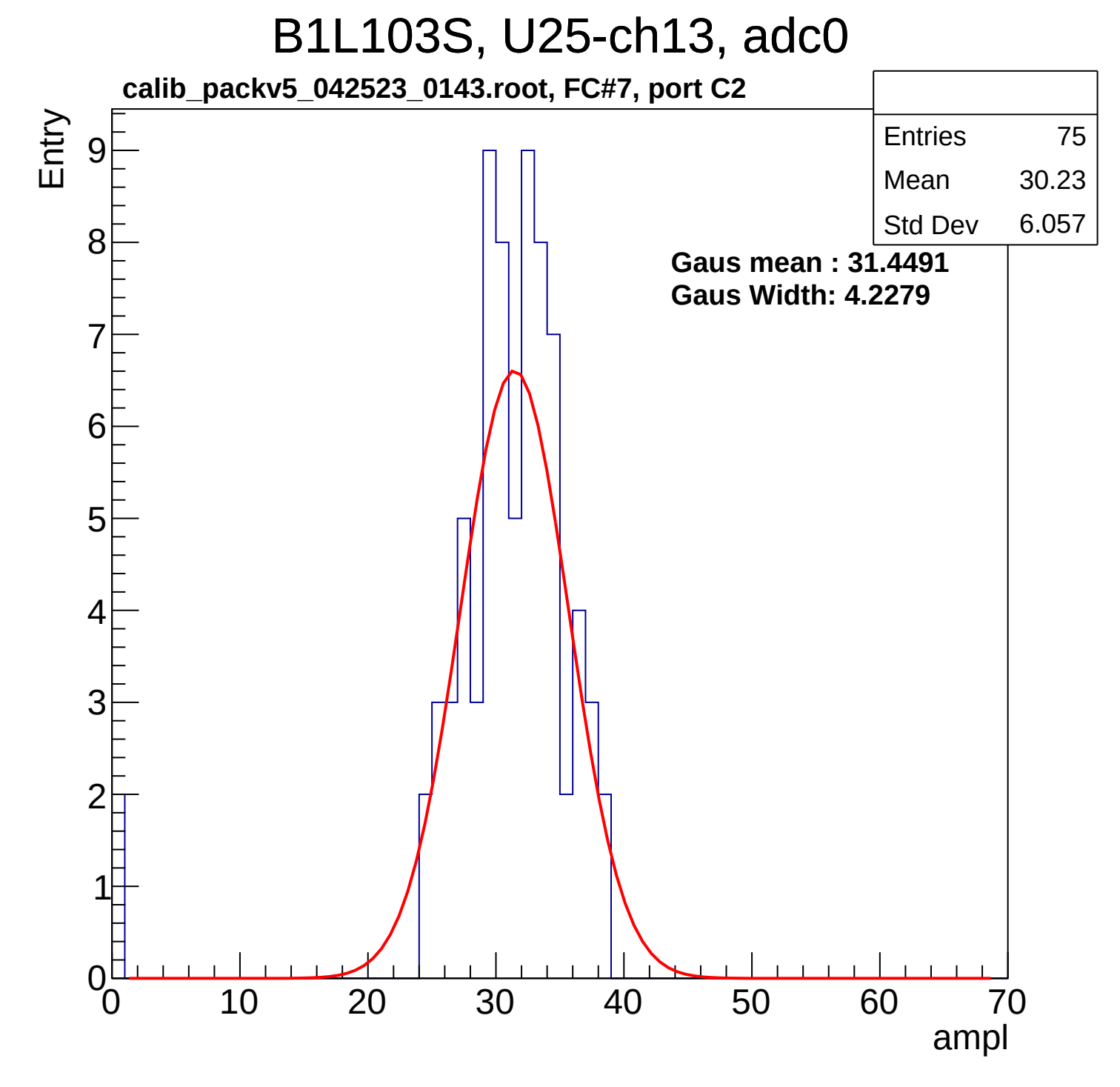
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch13, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 30.23
Std Dev 6.057
Gaus mean : 31.4491
Gaus Width: 4.2279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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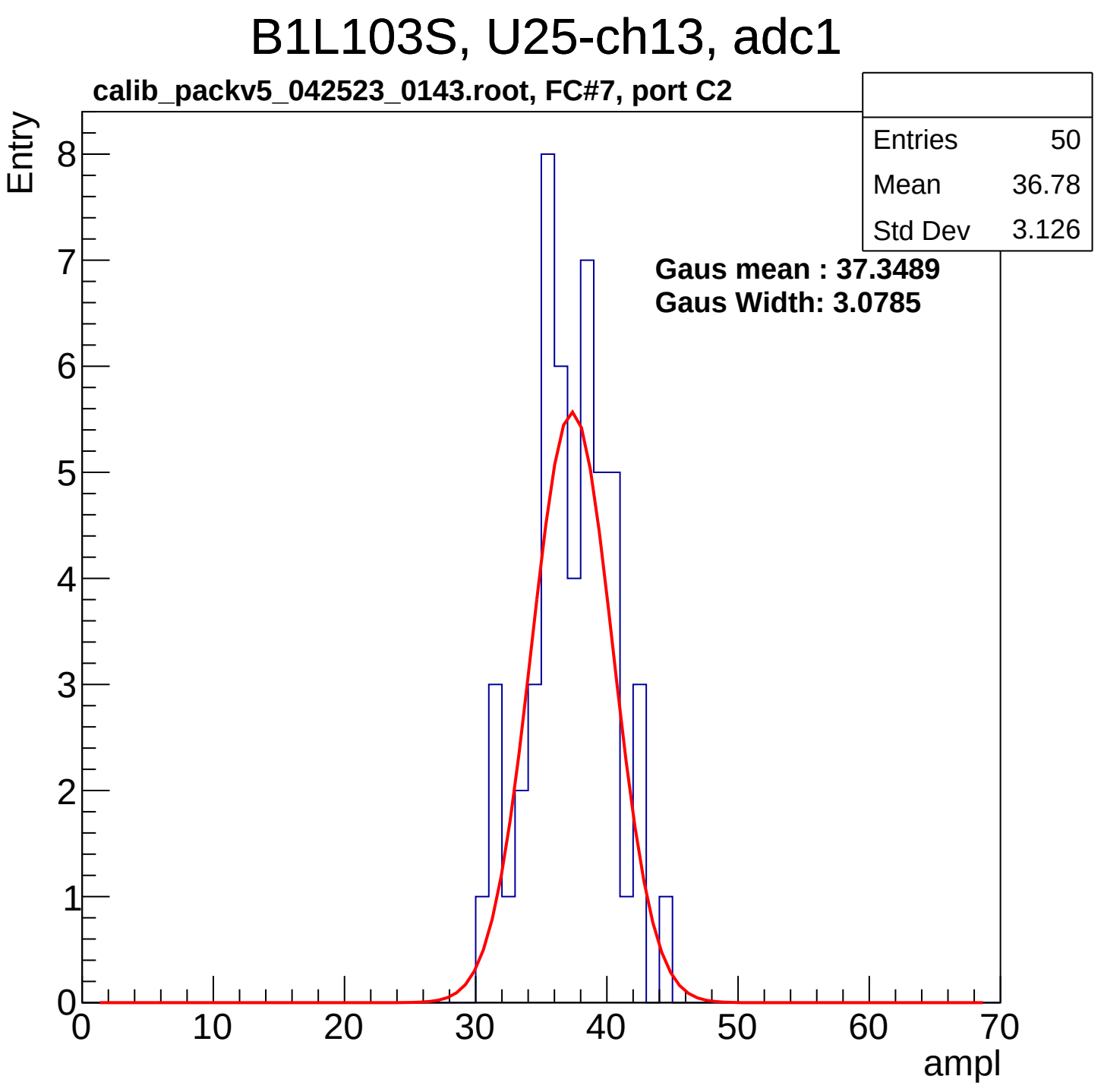
B1L103S, U25-ch13, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 36.78
Std Dev 3.126
Gaus mean : 37.3489
Gaus Width: 3.0785
Entry
ampl
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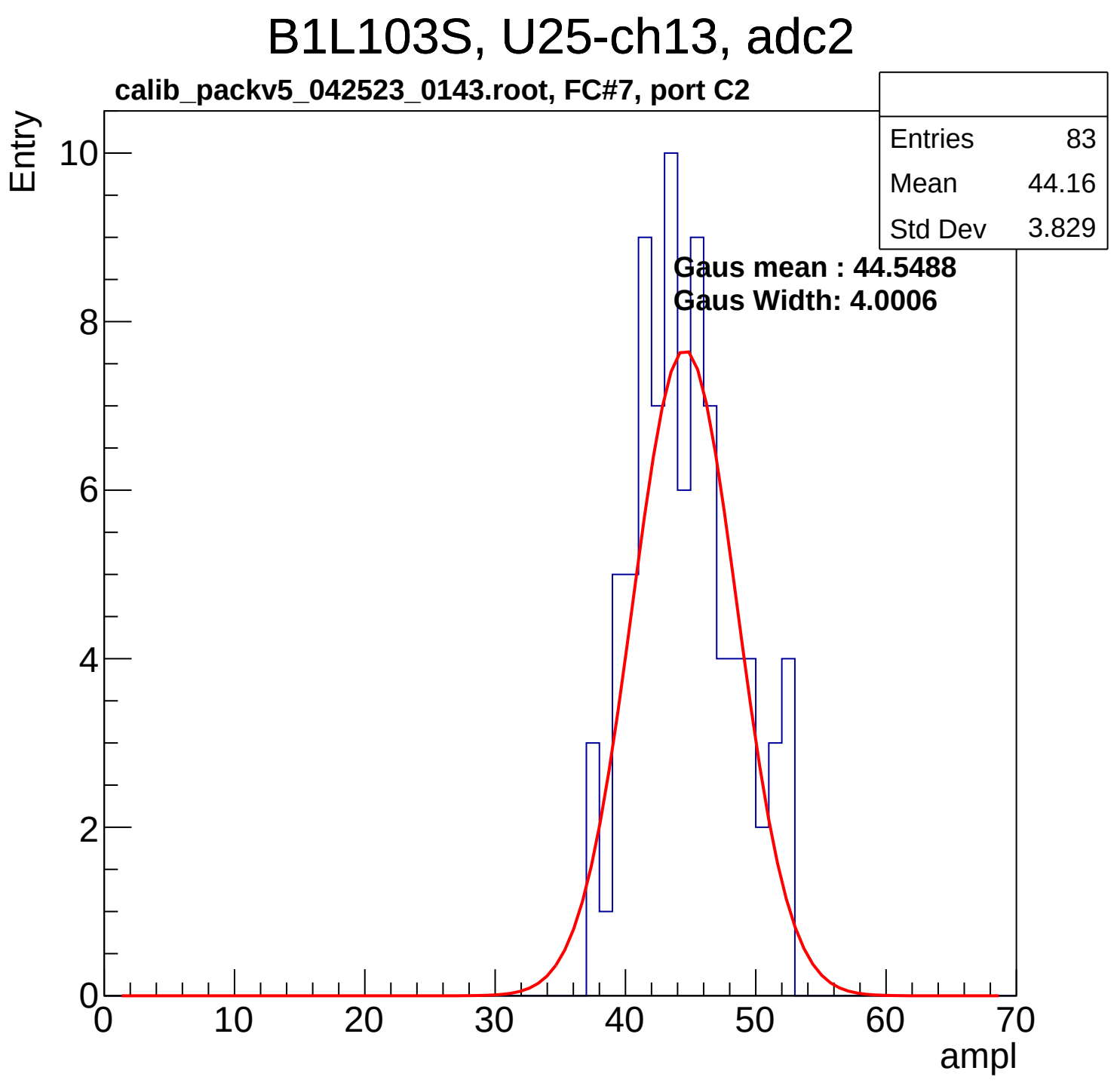
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch13, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 44.16
Std Dev 3.829
Gaus mean : 44.5488
Gaus Width: 4.0006
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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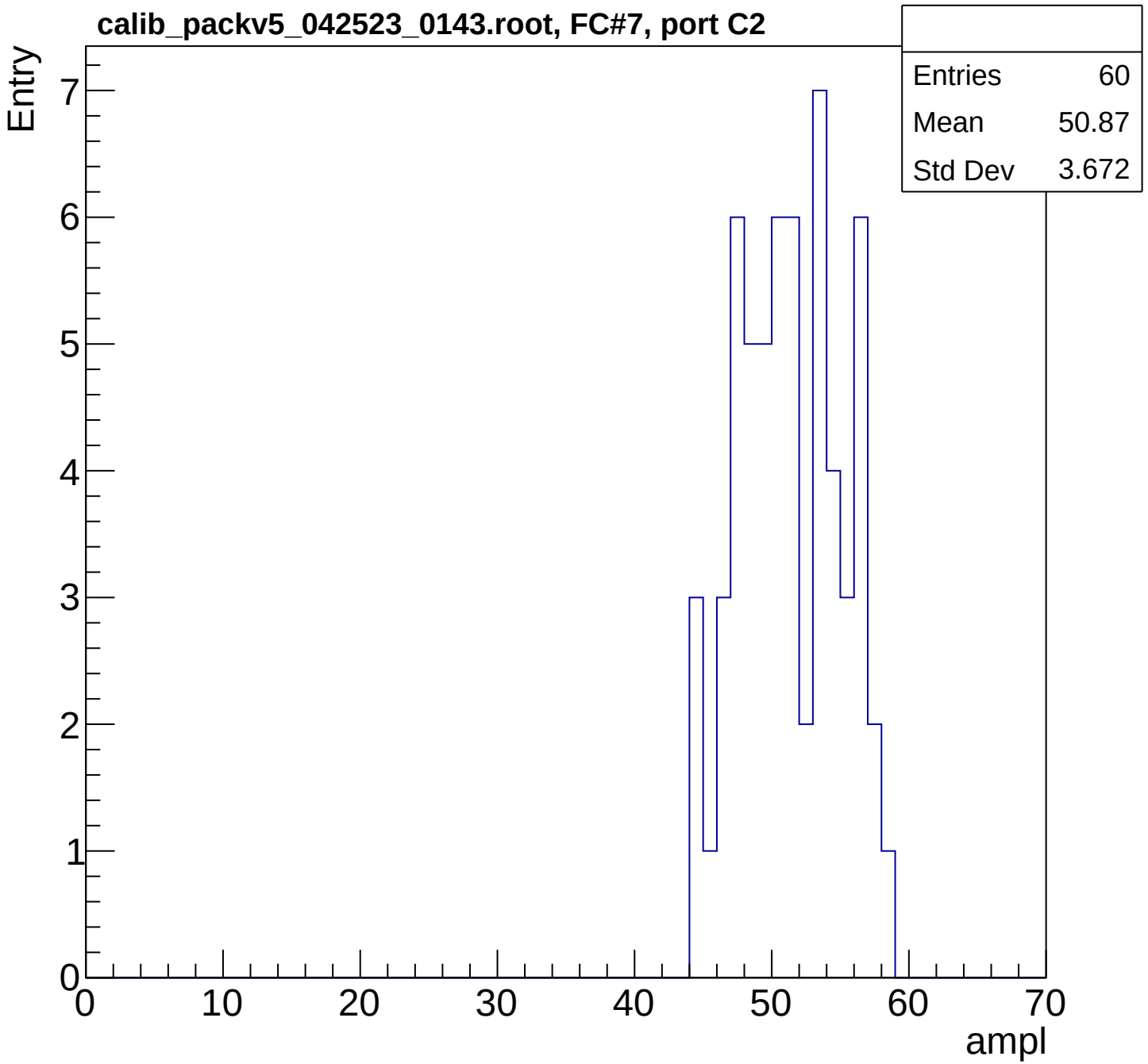
B1L103S, U25-ch13, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.87
Std Dev 3.672
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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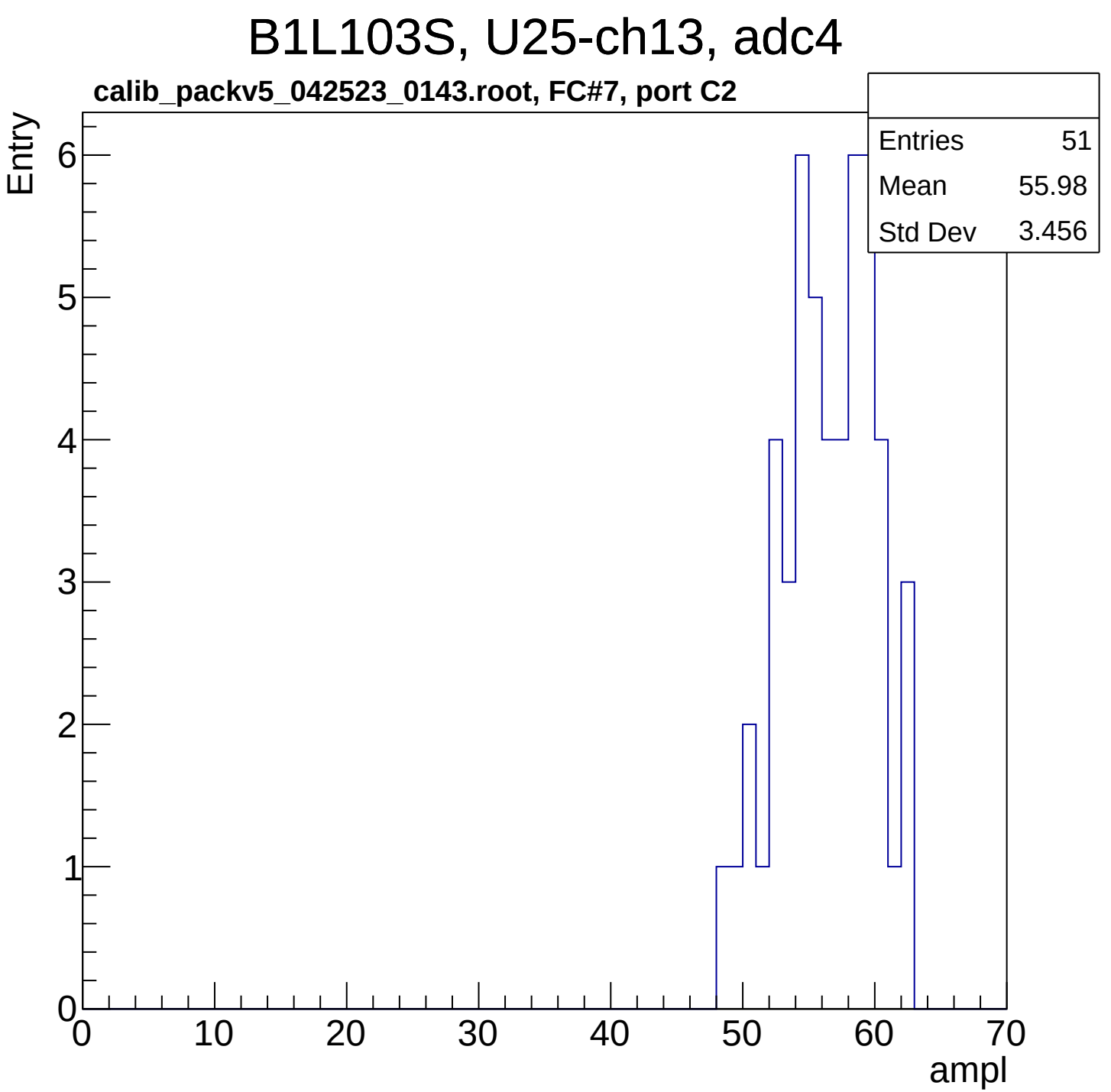
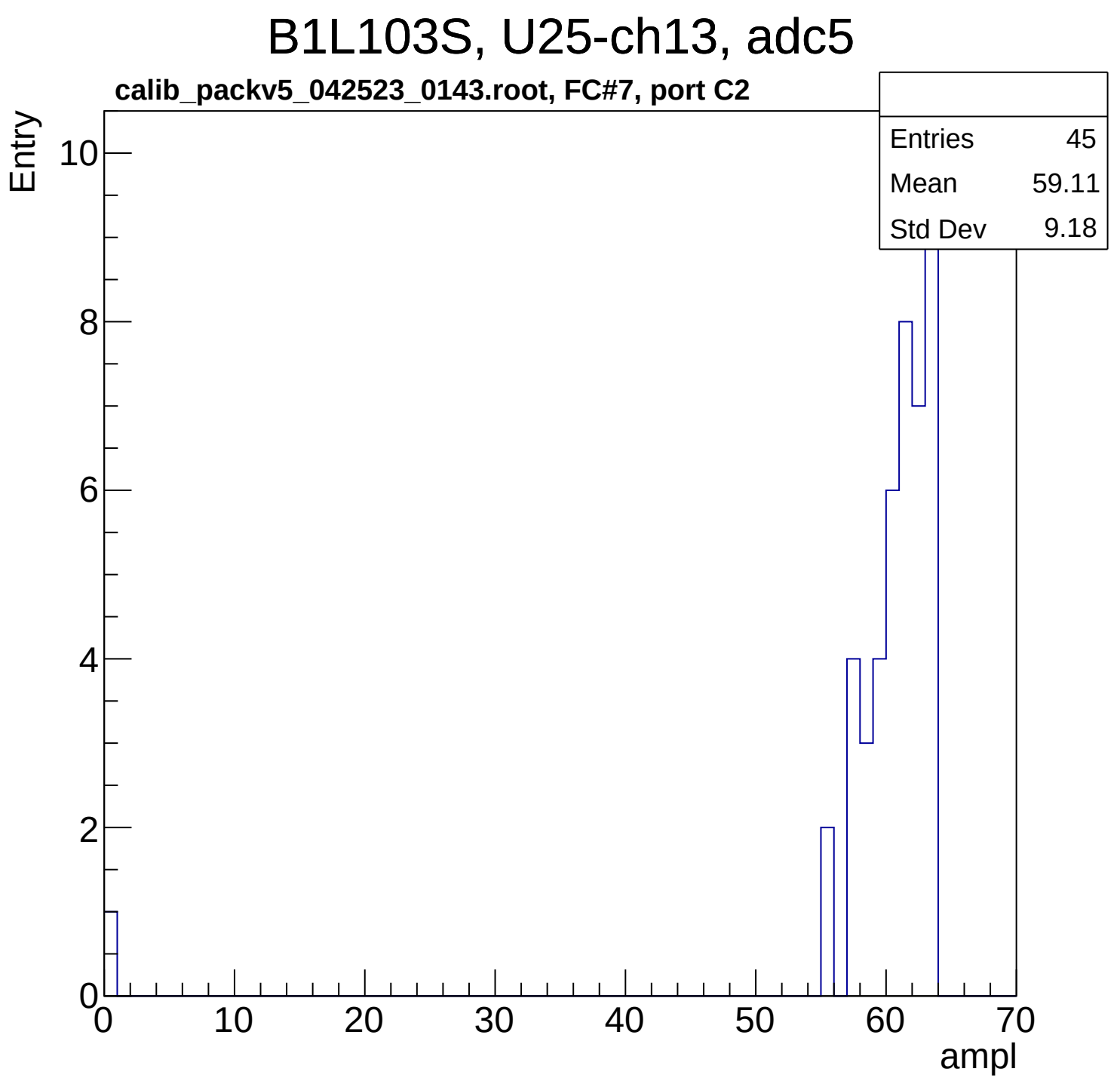
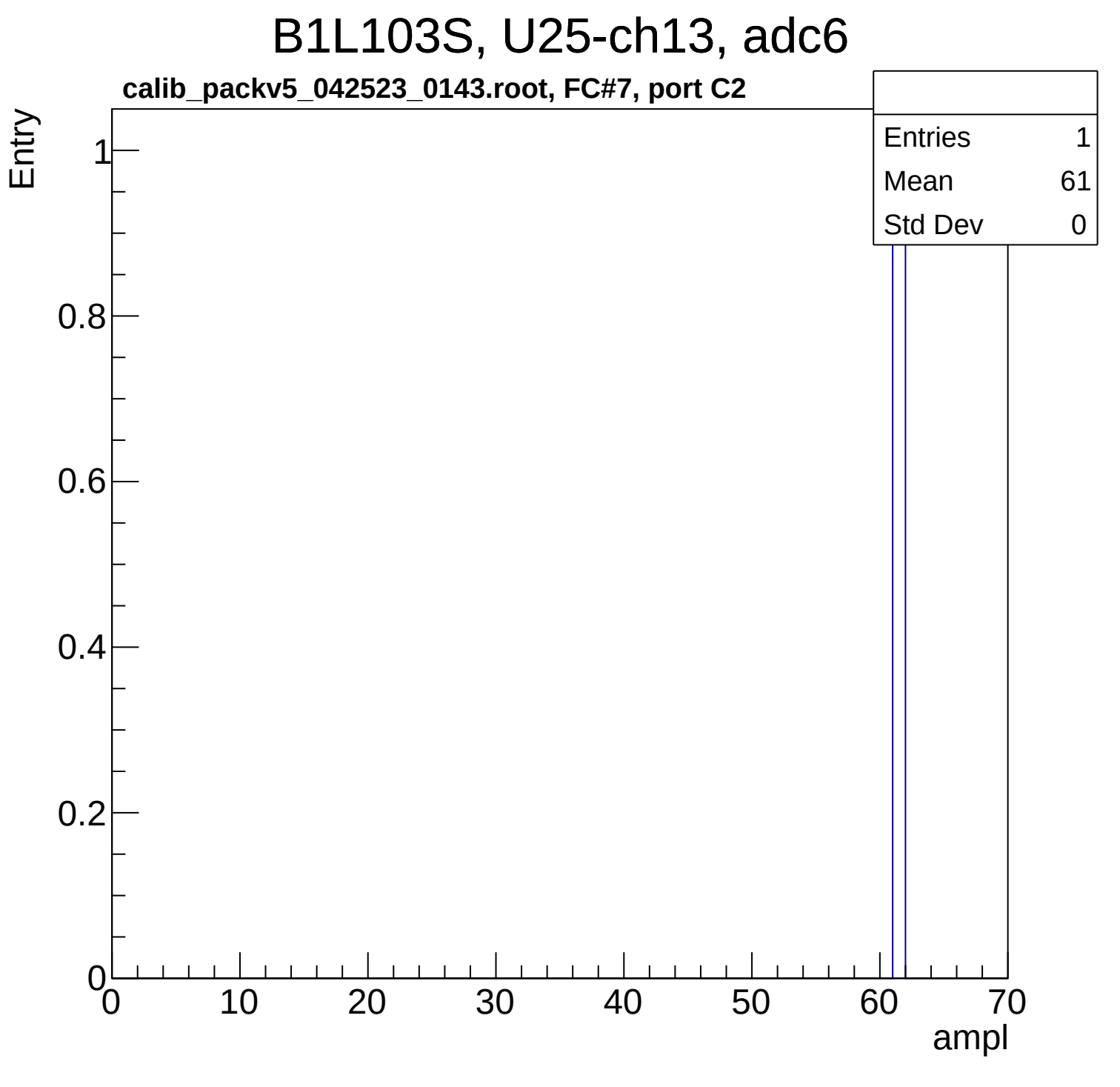

B1L103S, U25-ch13, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.98
Std Dev 3.456
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch13, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.11
Std Dev 9.18
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch13, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
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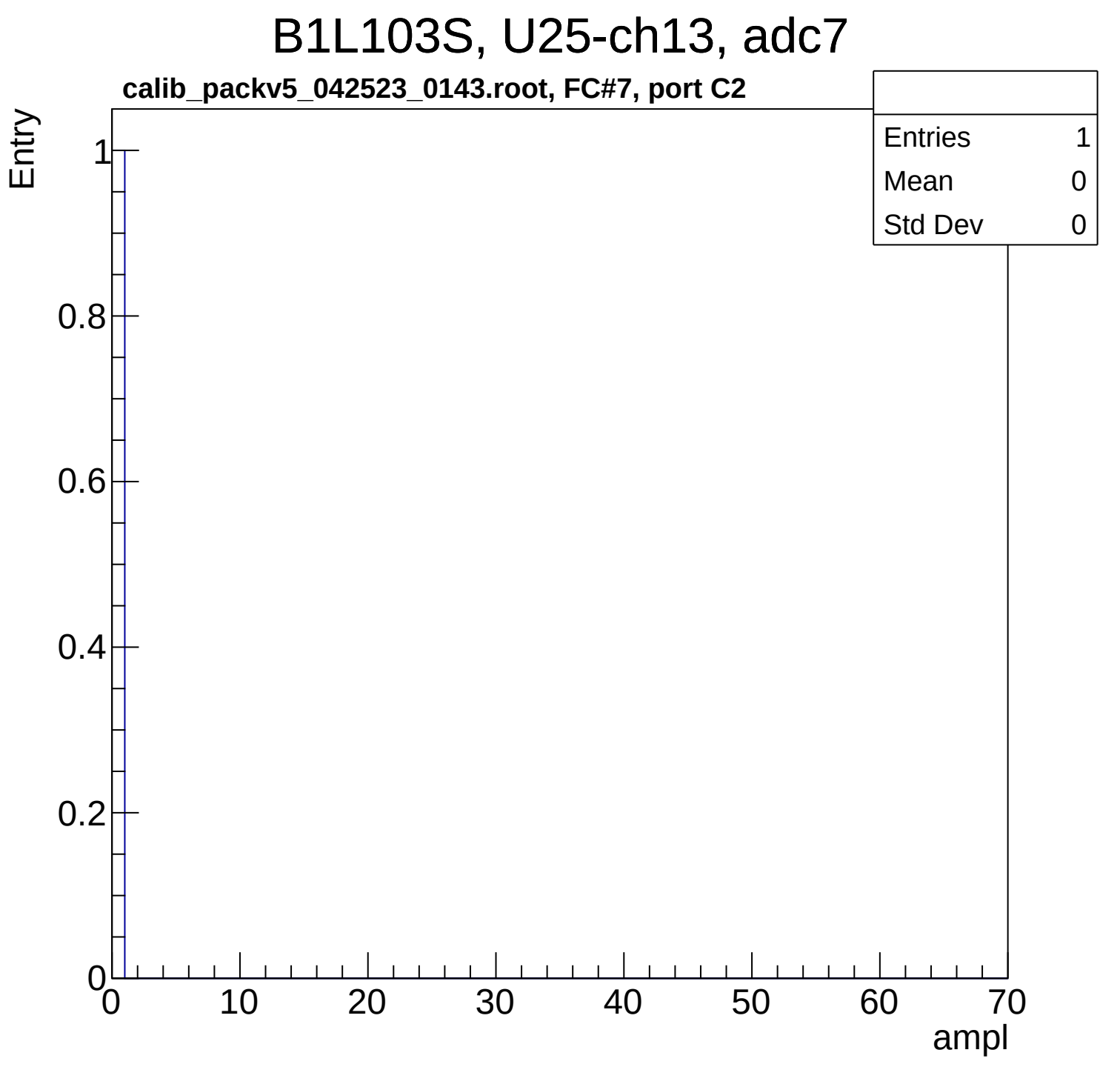
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch13, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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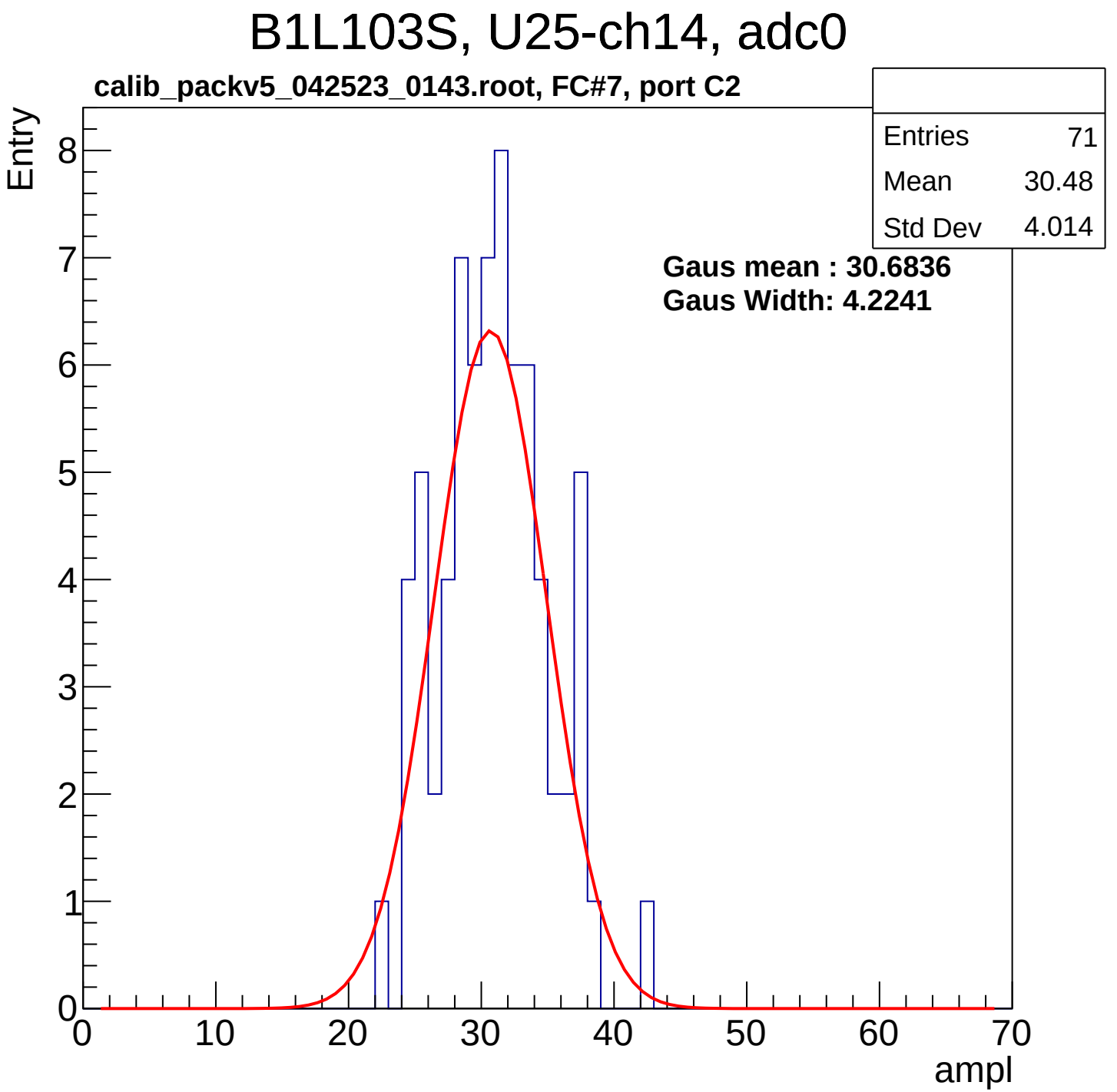
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch14, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 30.48
Std Dev 4.014
Gaus mean : 30.6836
Gaus Width: 4.2241
Entry
ampl
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7
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0
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20
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40
50
60
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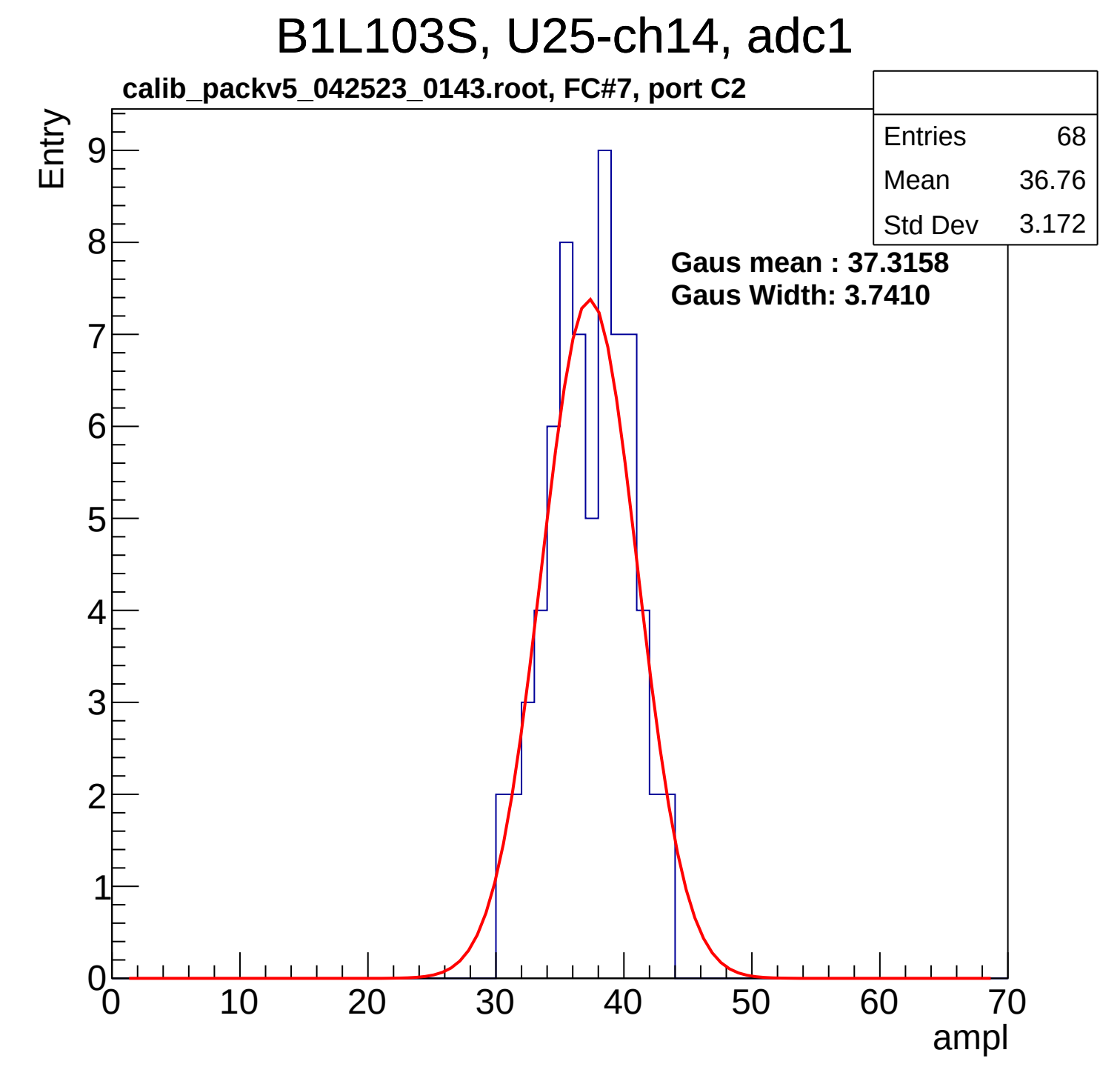
B1L103S, U25-ch14, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.76
Std Dev 3.172
Gaus mean : 37.3158
Gaus Width: 3.7410
Entry
ampl
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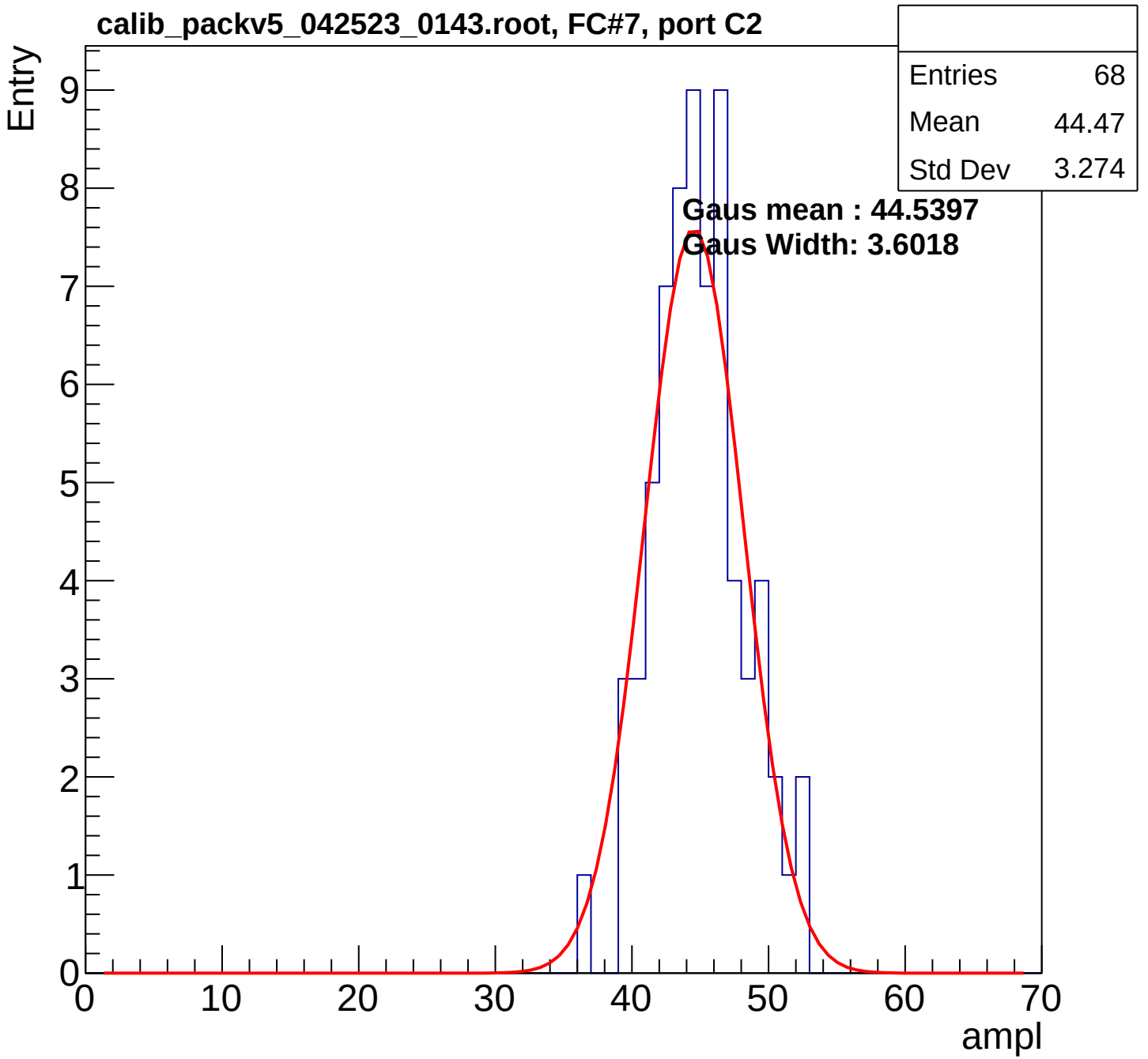
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch14, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 44.47
Std Dev 3.274
Gaus mean : 44.5397
Gaus Width: 3.6018
Entry
ampl
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3
4
5
6
7
8
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0
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60
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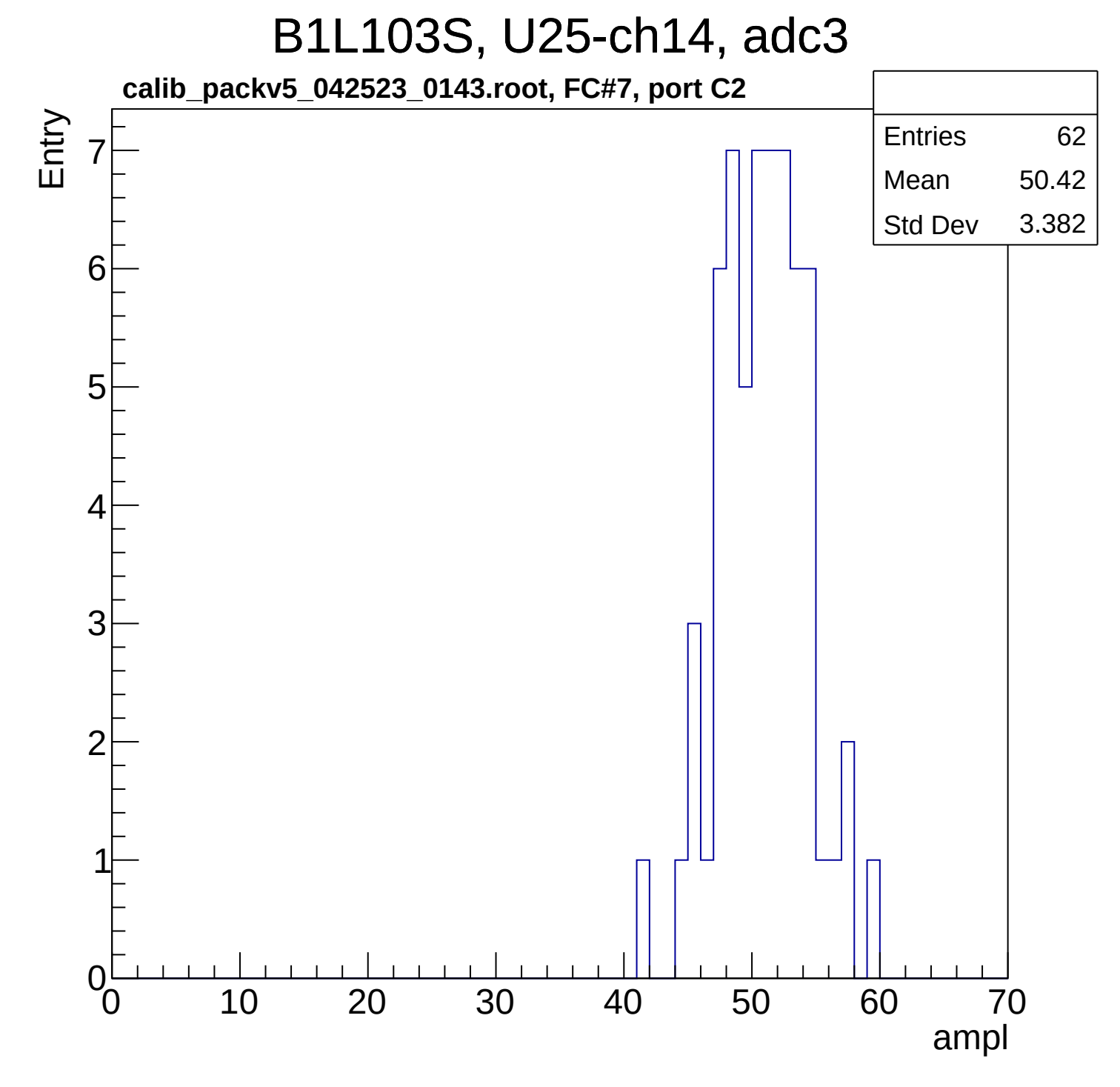
B1L103S, U25-ch14, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 50.42
Std Dev 3.382
Entry
ampl
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3
4
5
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0
10
20
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60
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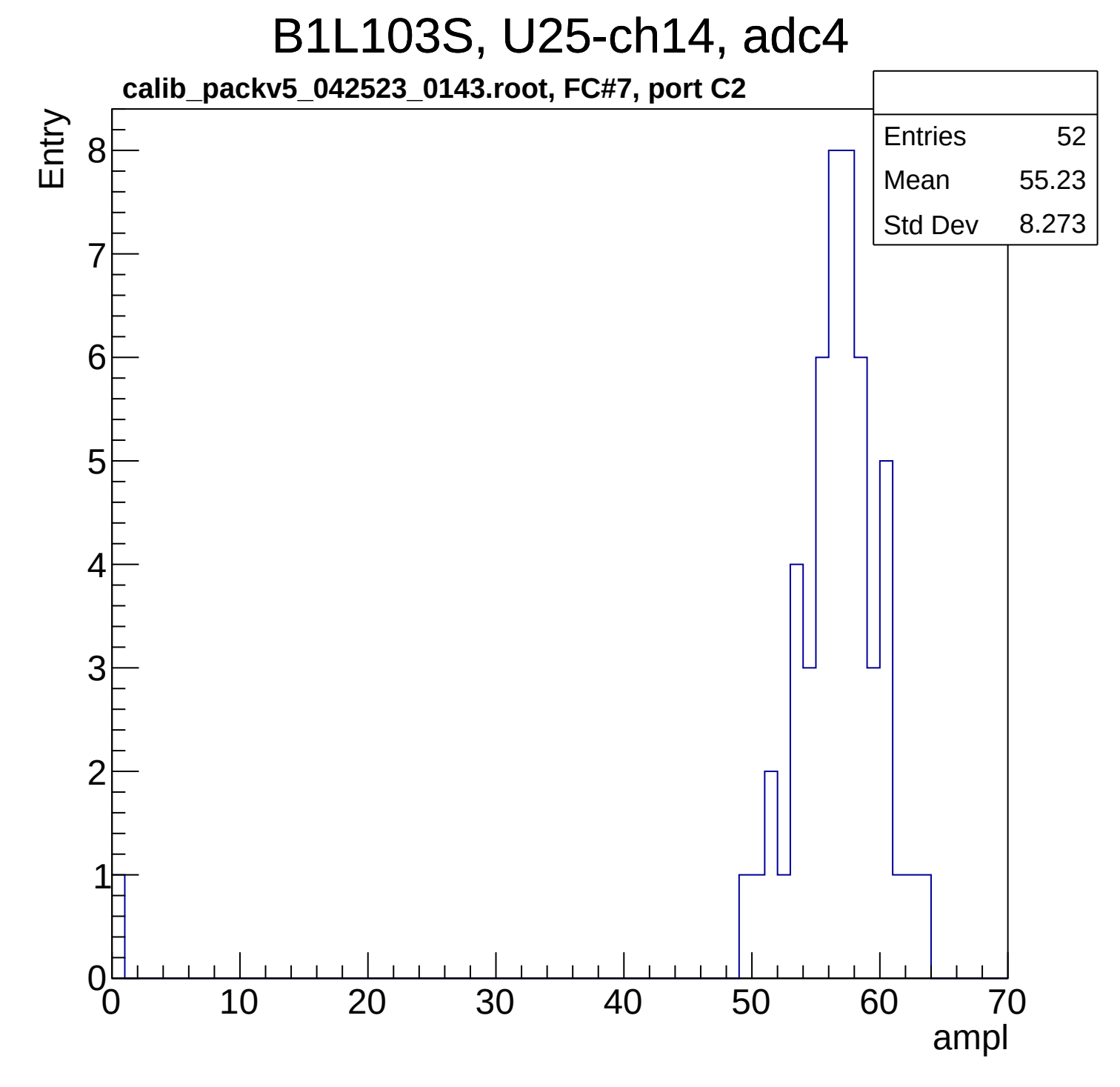
B1L103S, U25-ch14, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 55.23
Std Dev 8.273
Entry
ampl
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3
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5
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0
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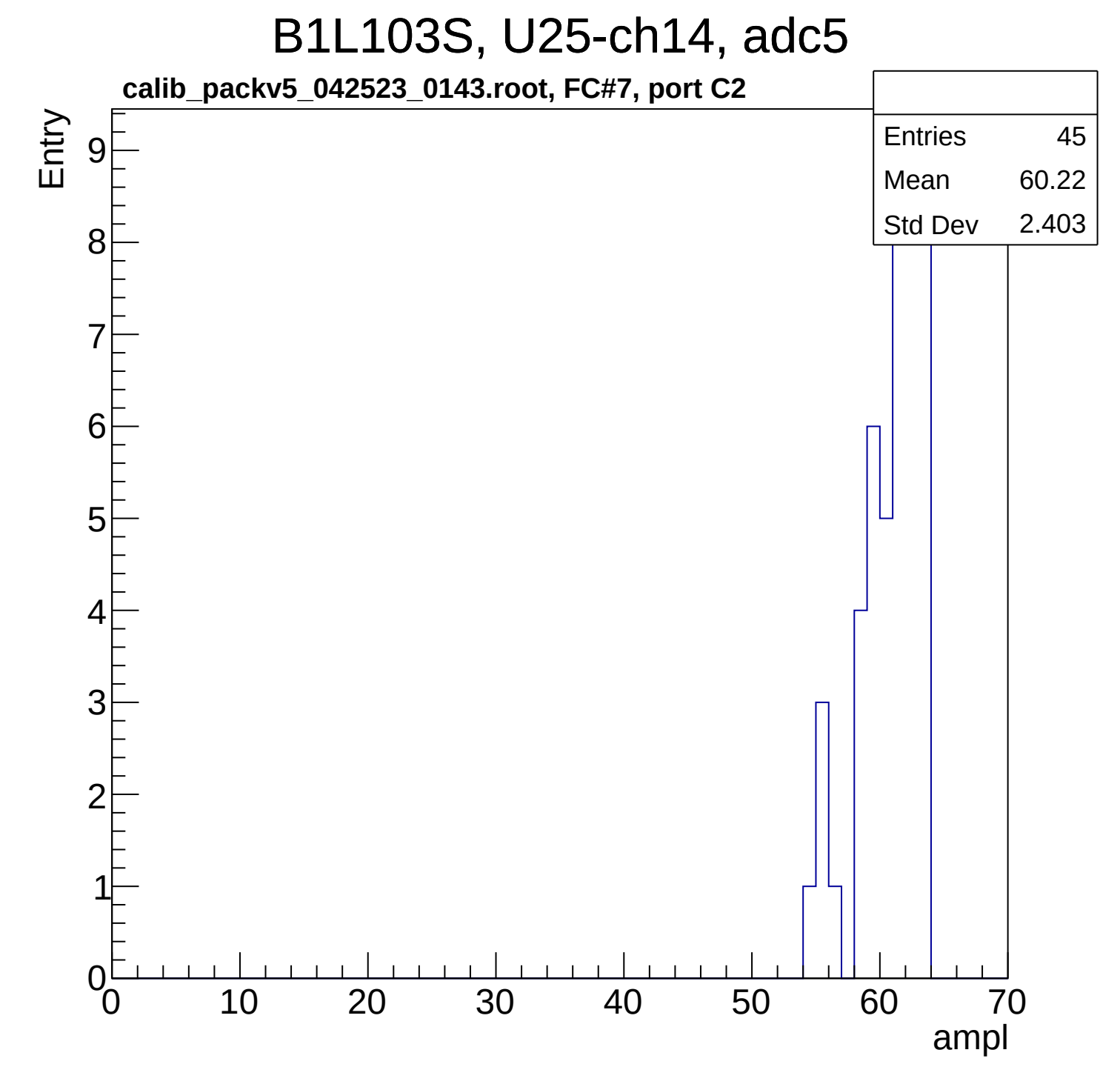
60
70


B1L103S, U25-ch14, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 60.22
Std Dev 2.403
Entry
ampl
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6
7
8
9
0
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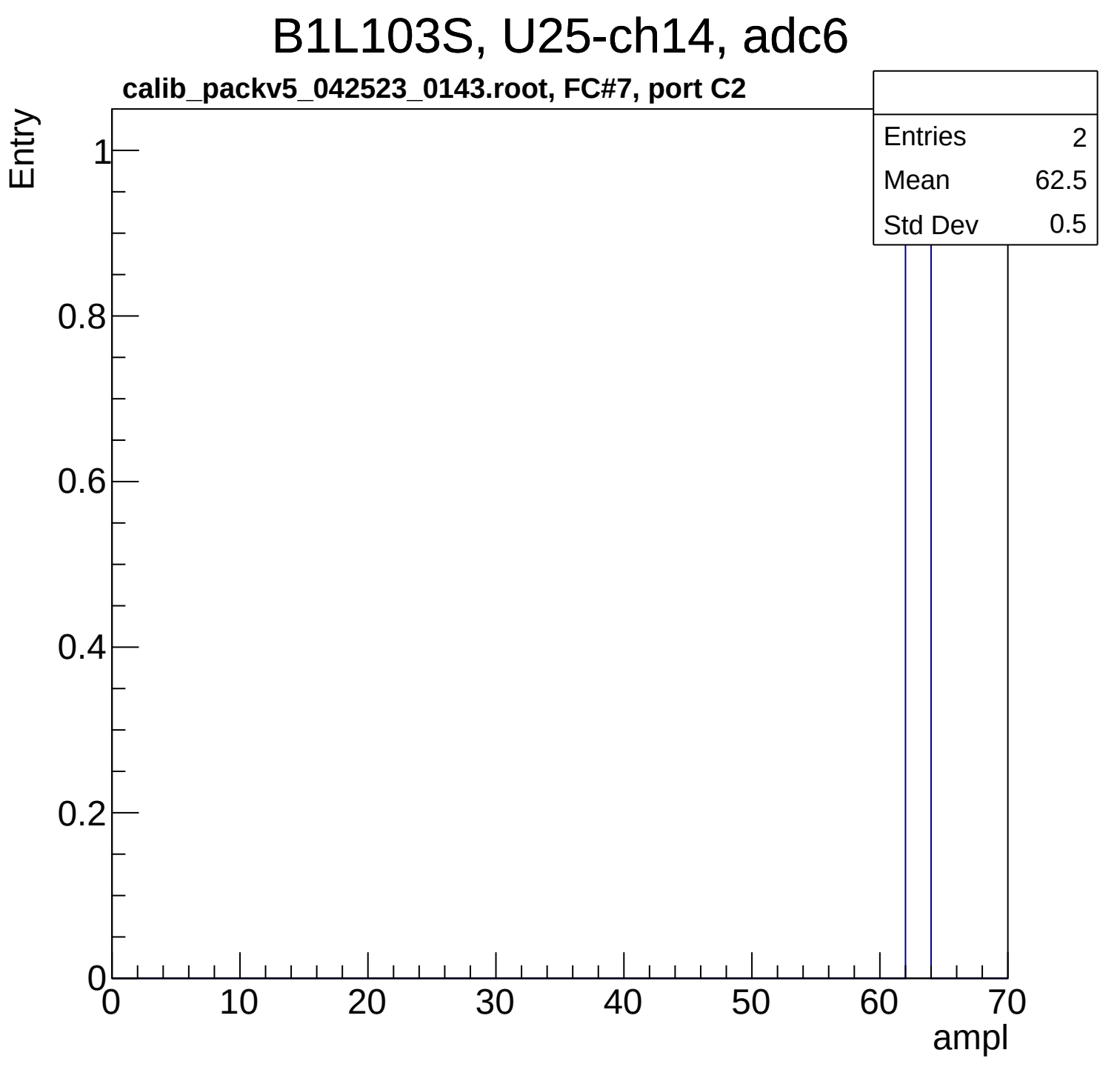
B1L103S, U25-ch14, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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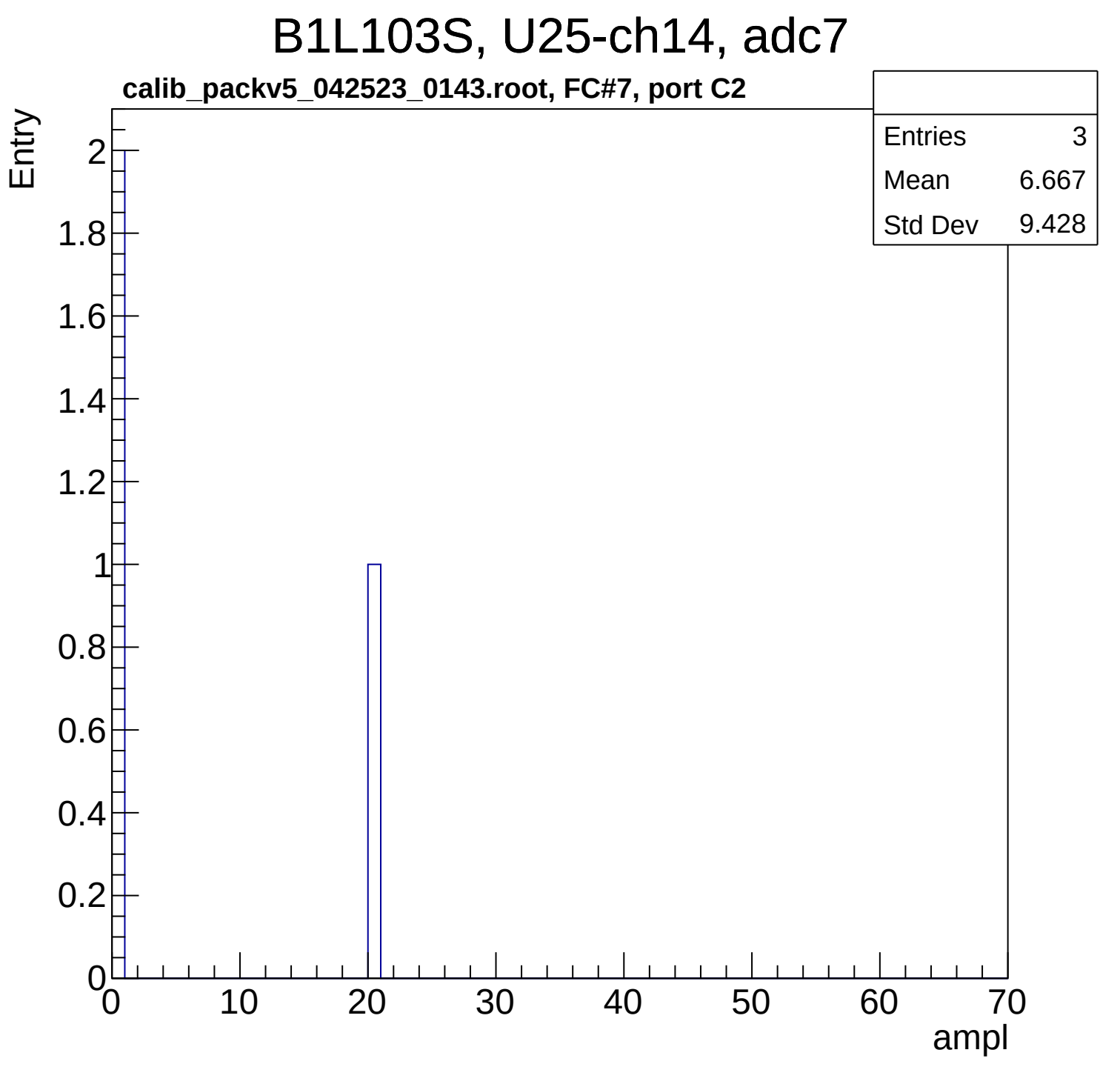
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch14, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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40
50
60
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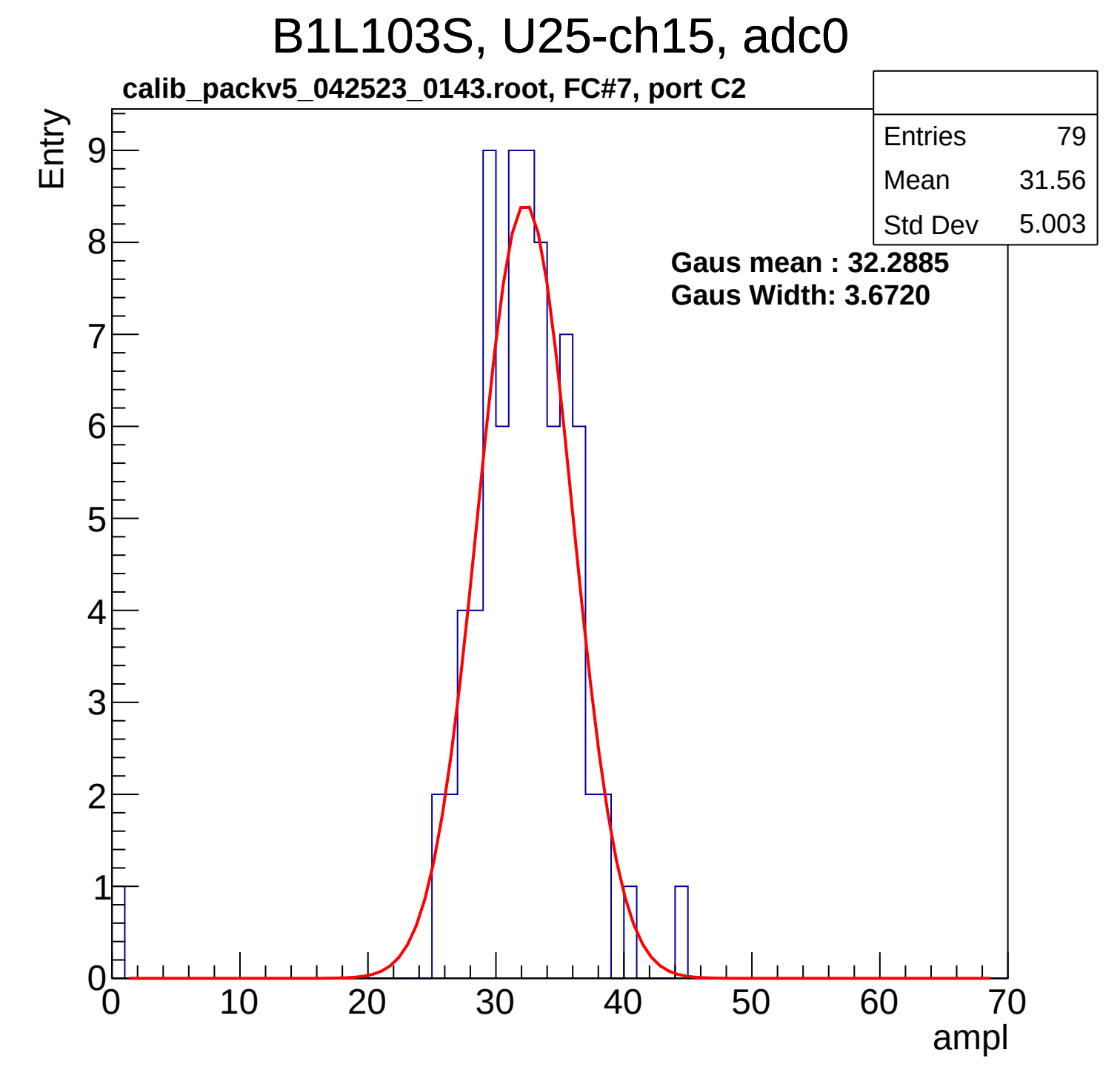
B1L103S, U25-ch15, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 31.56
Std Dev 5.003
Gaus mean : 32.2885
Gaus Width: 3.6720
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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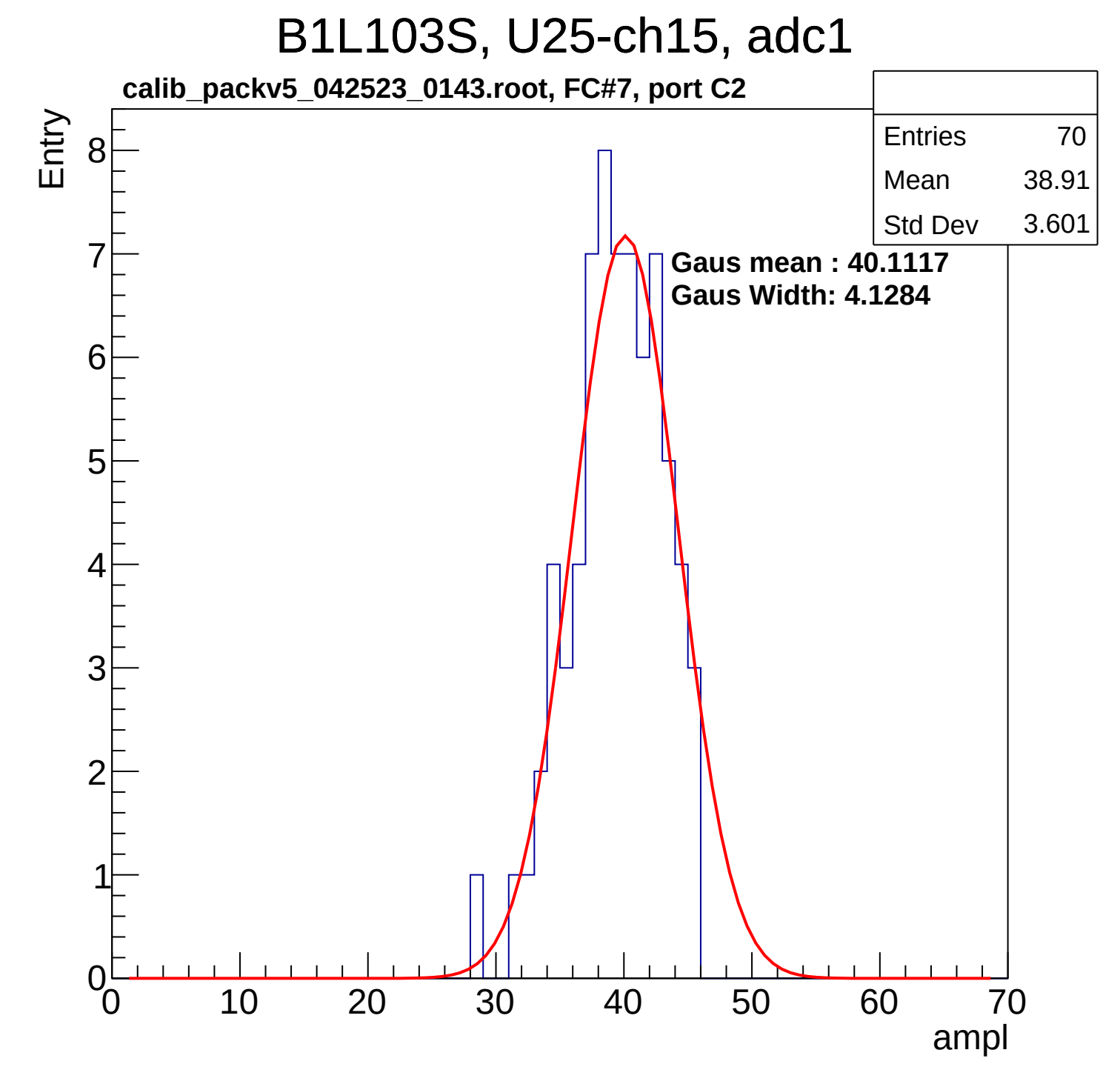
B1L103S, U25-ch15, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 38.91
Std Dev 3.601
Gaus mean : 40.1117
Gaus Width: 4.1284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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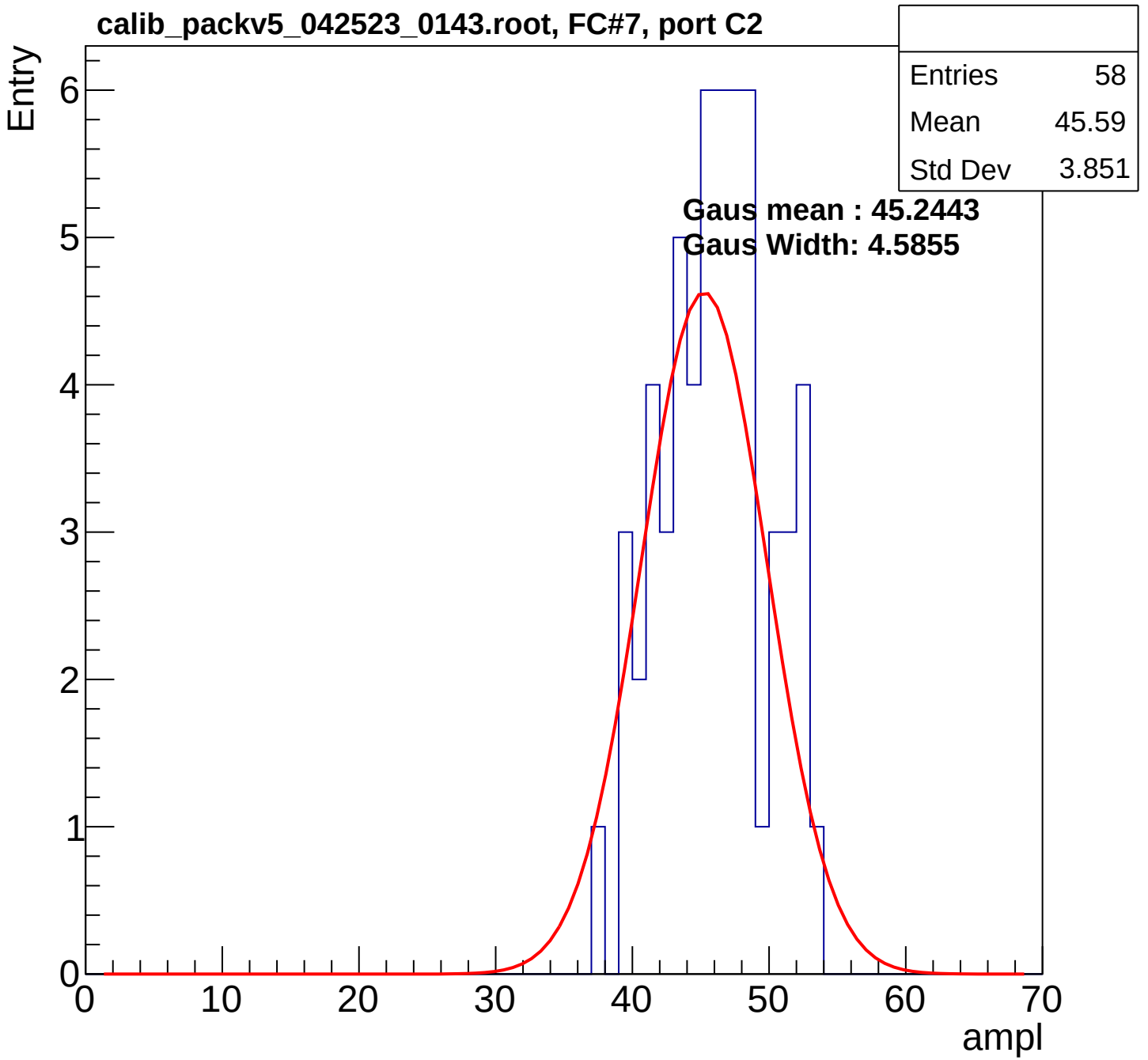
B1L103S, U25-ch15, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 45.59
Std Dev 3.851
Gaus mean : 45.2443
Gaus Width: 4.5855
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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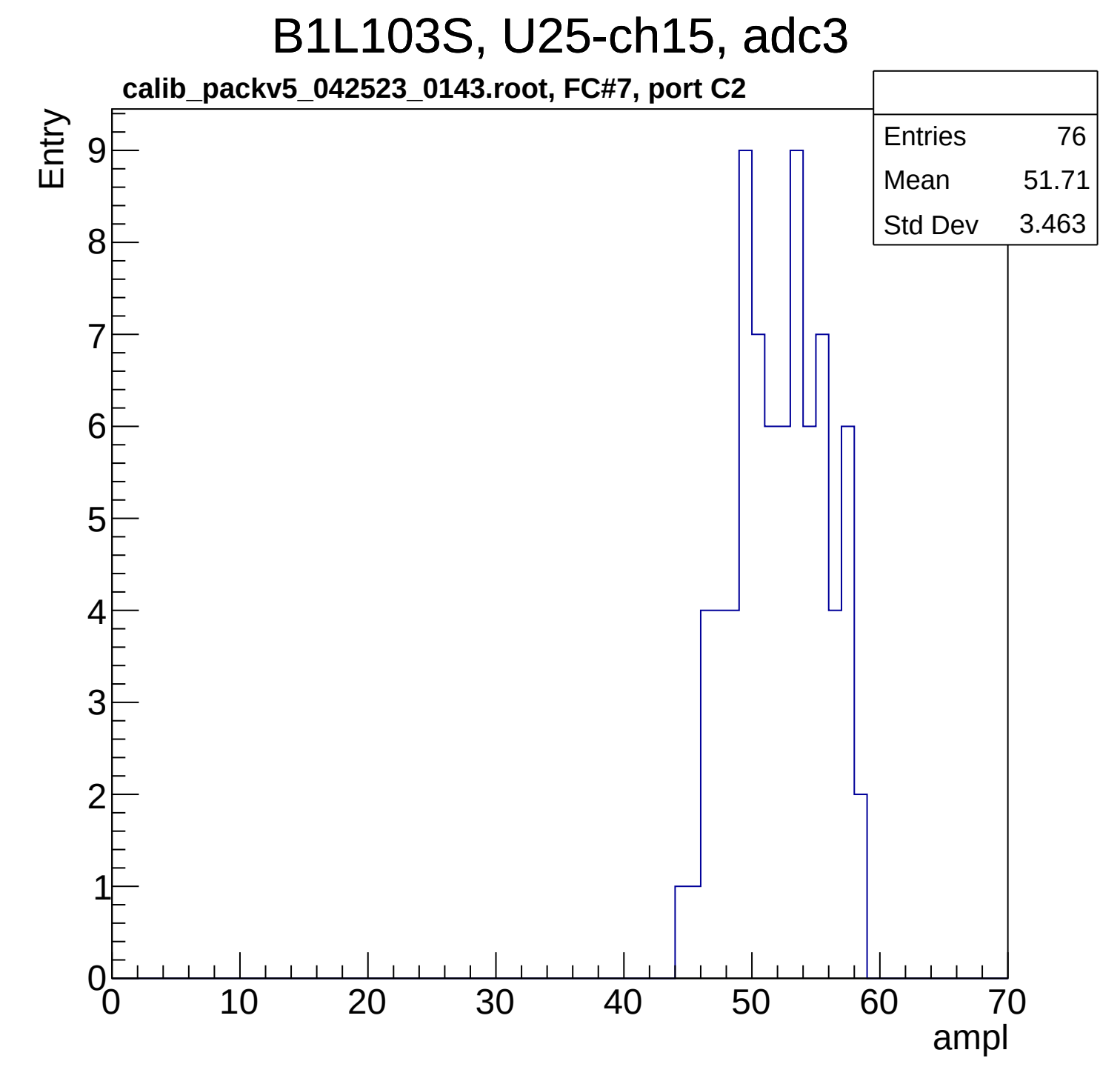
B1L103S, U25-ch15, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 51.71
Std Dev 3.463
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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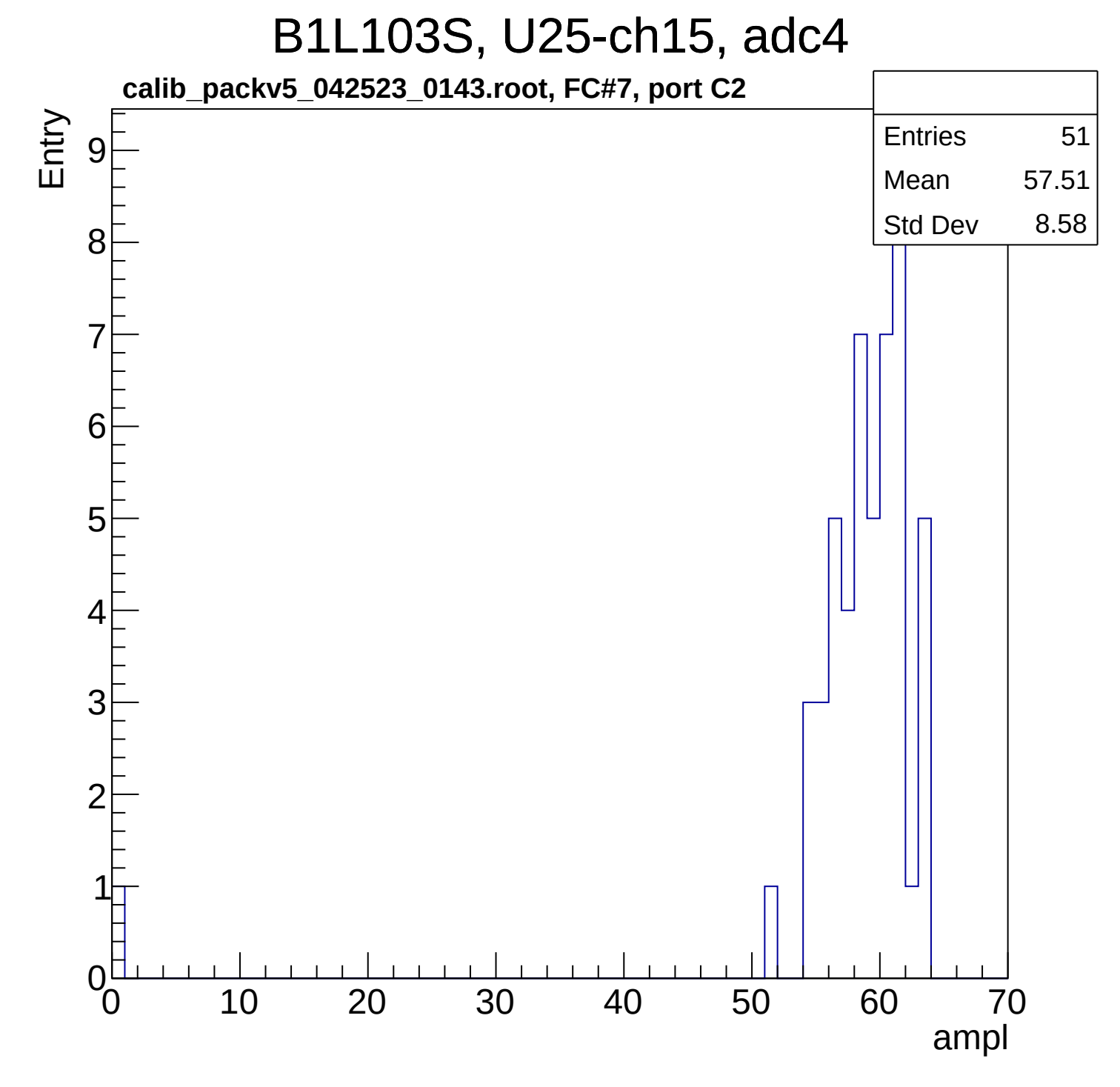
B1L103S, U25-ch15, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.51
Std Dev 8.58
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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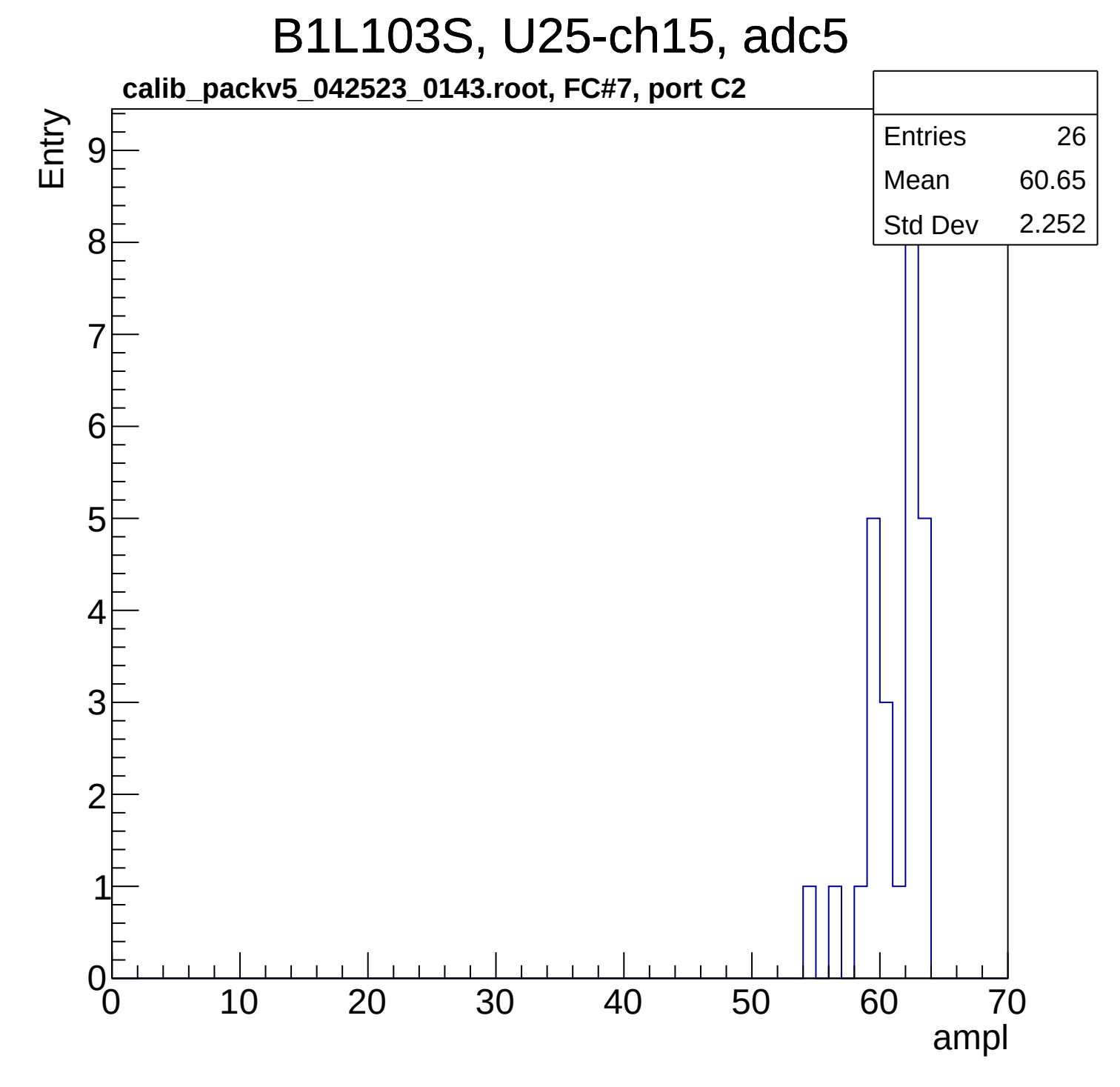
B1L103S, U25-ch15, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.65
Std Dev 2.252
Entry
ampl
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2
3
4
5
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0
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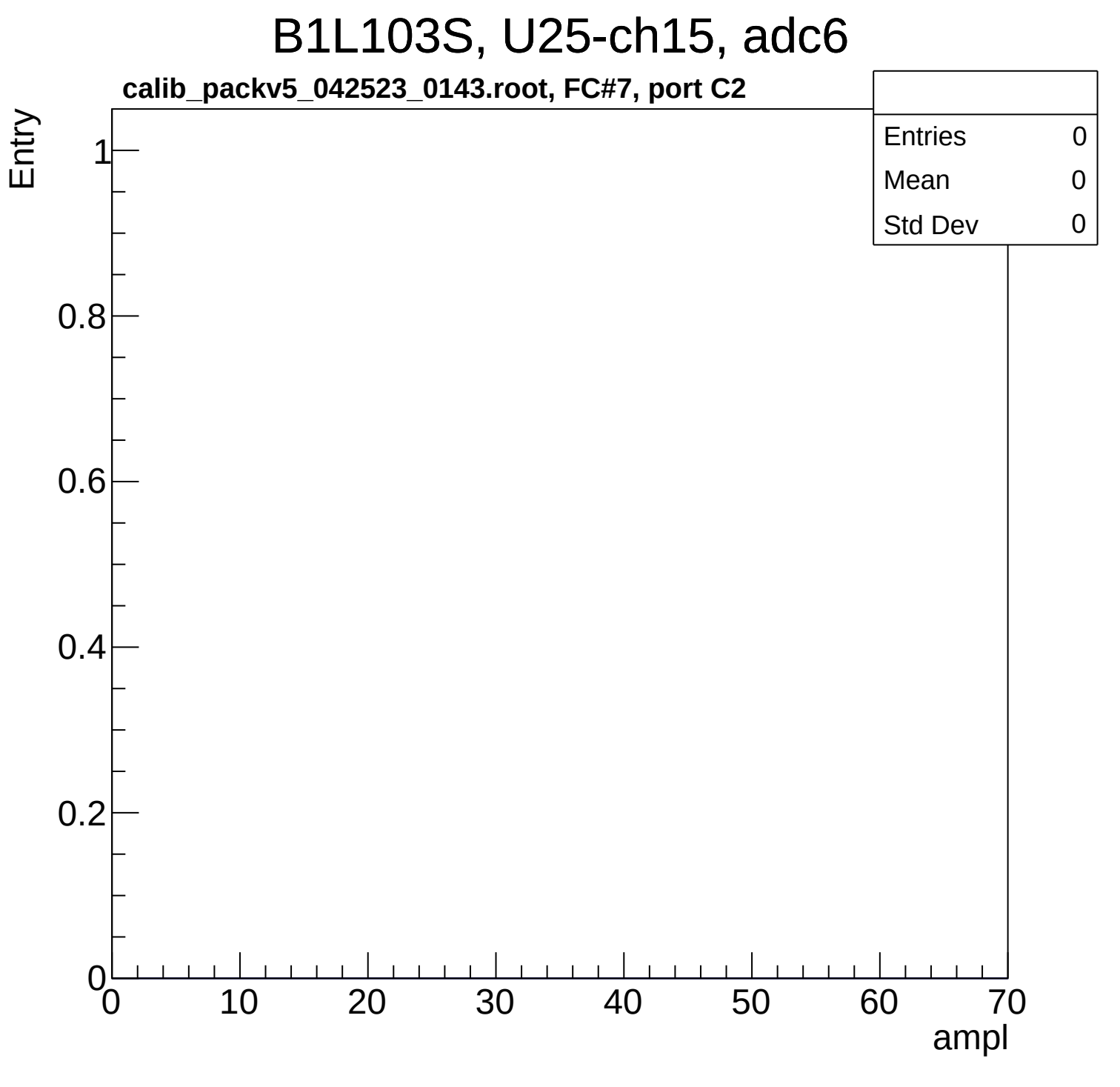
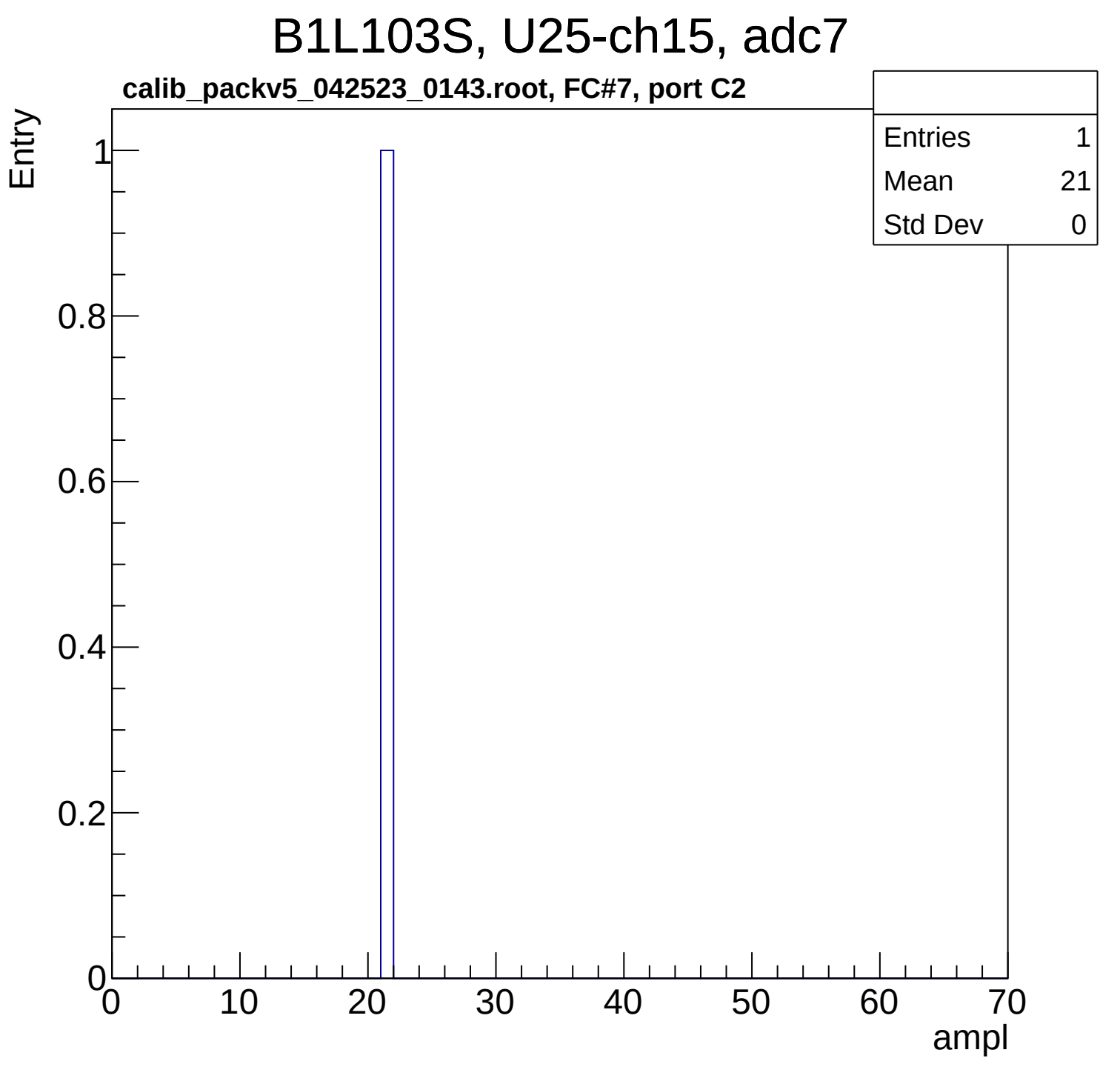

B1L103S, U25-ch15, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch15, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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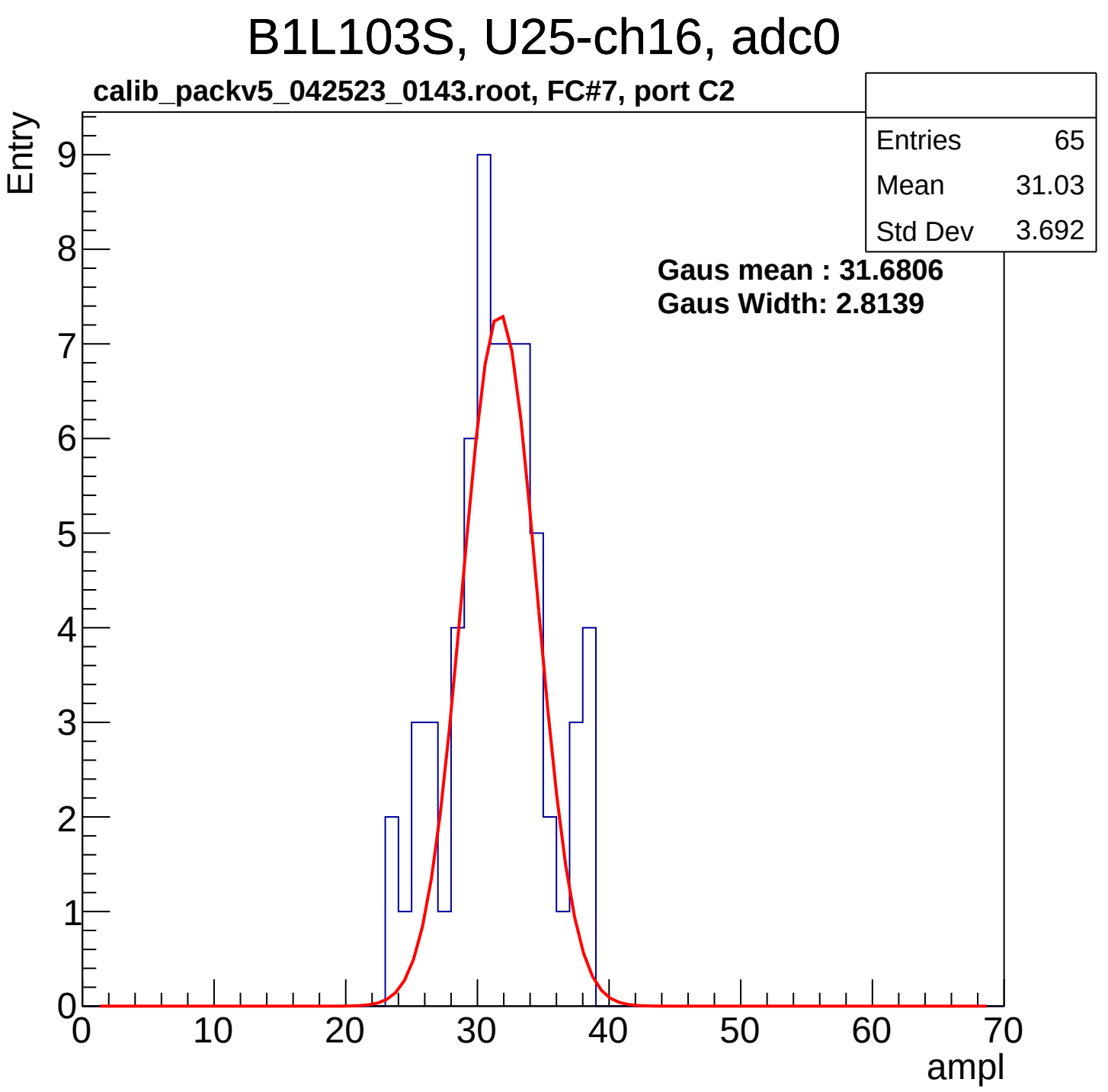
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch16, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 31.03
Std Dev 3.692
Gaus mean : 31.6806
Gaus Width: 2.8139
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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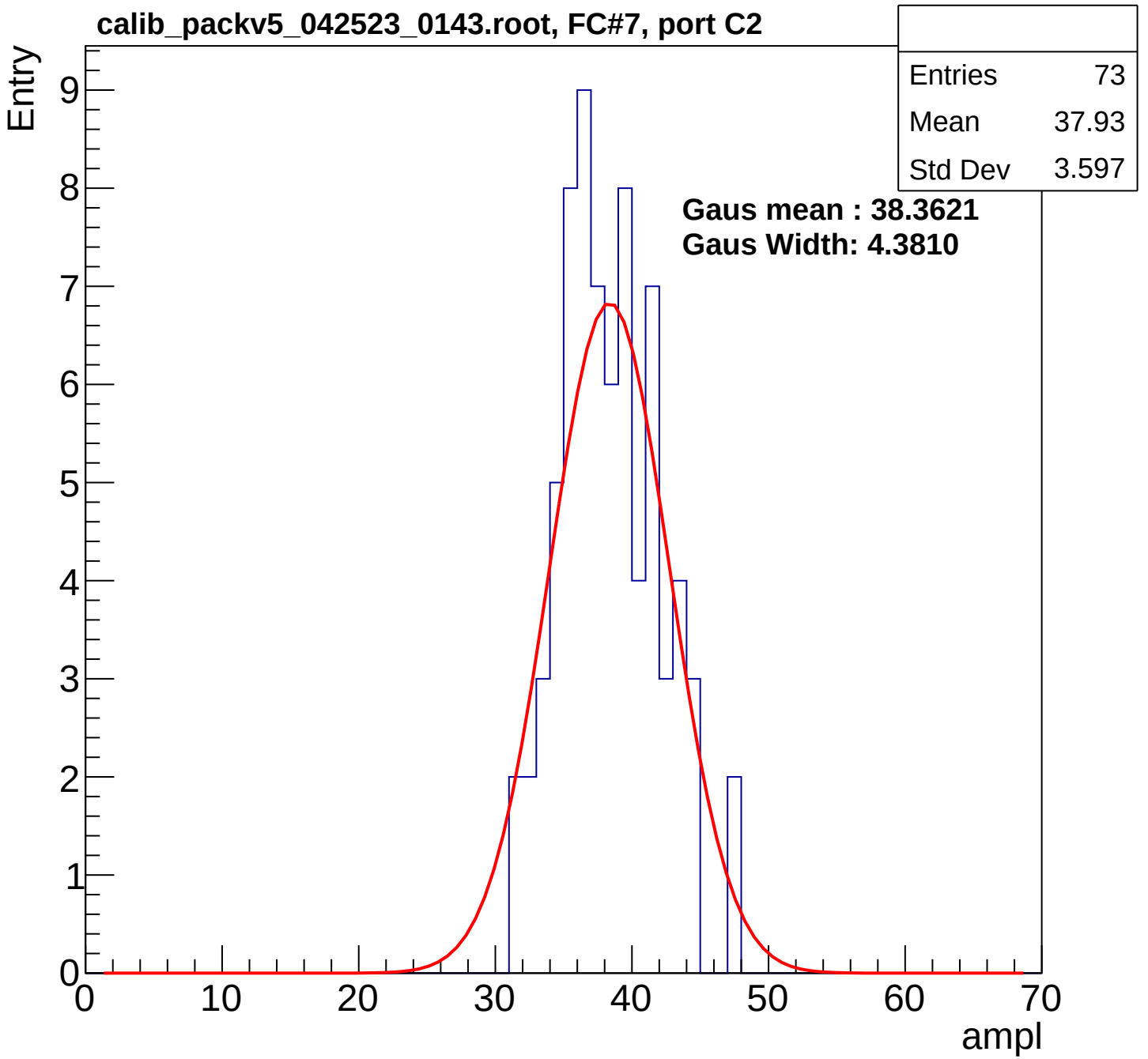
B1L103S, U25-ch16, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 37.93
Std Dev 3.597
Gaus mean : 38.3621
Gaus Width: 4.3810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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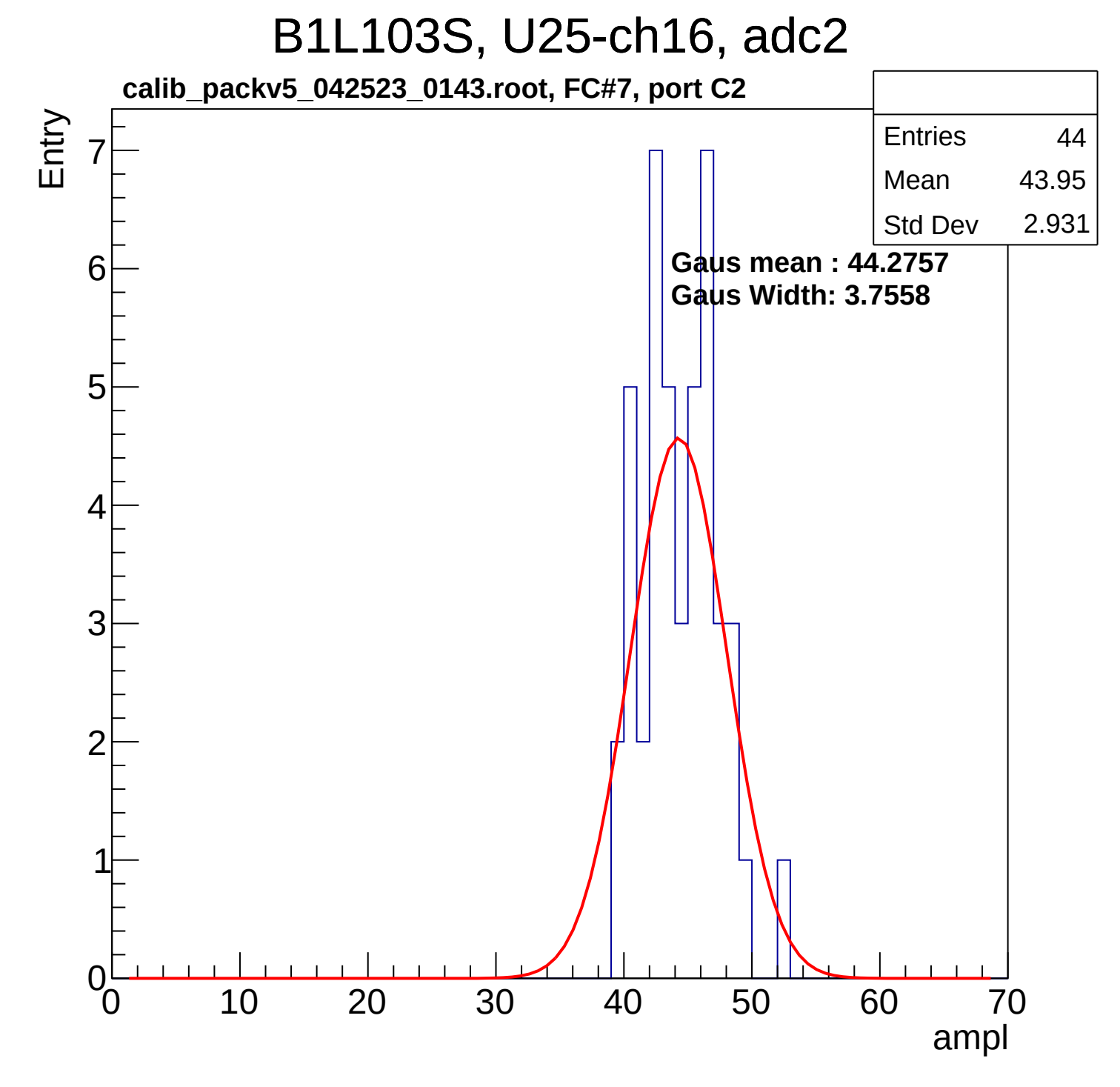
B1L103S, U25-ch16, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 43.95
Std Dev 2.931
Gaus mean : 44.2757
Gaus Width: 3.7558
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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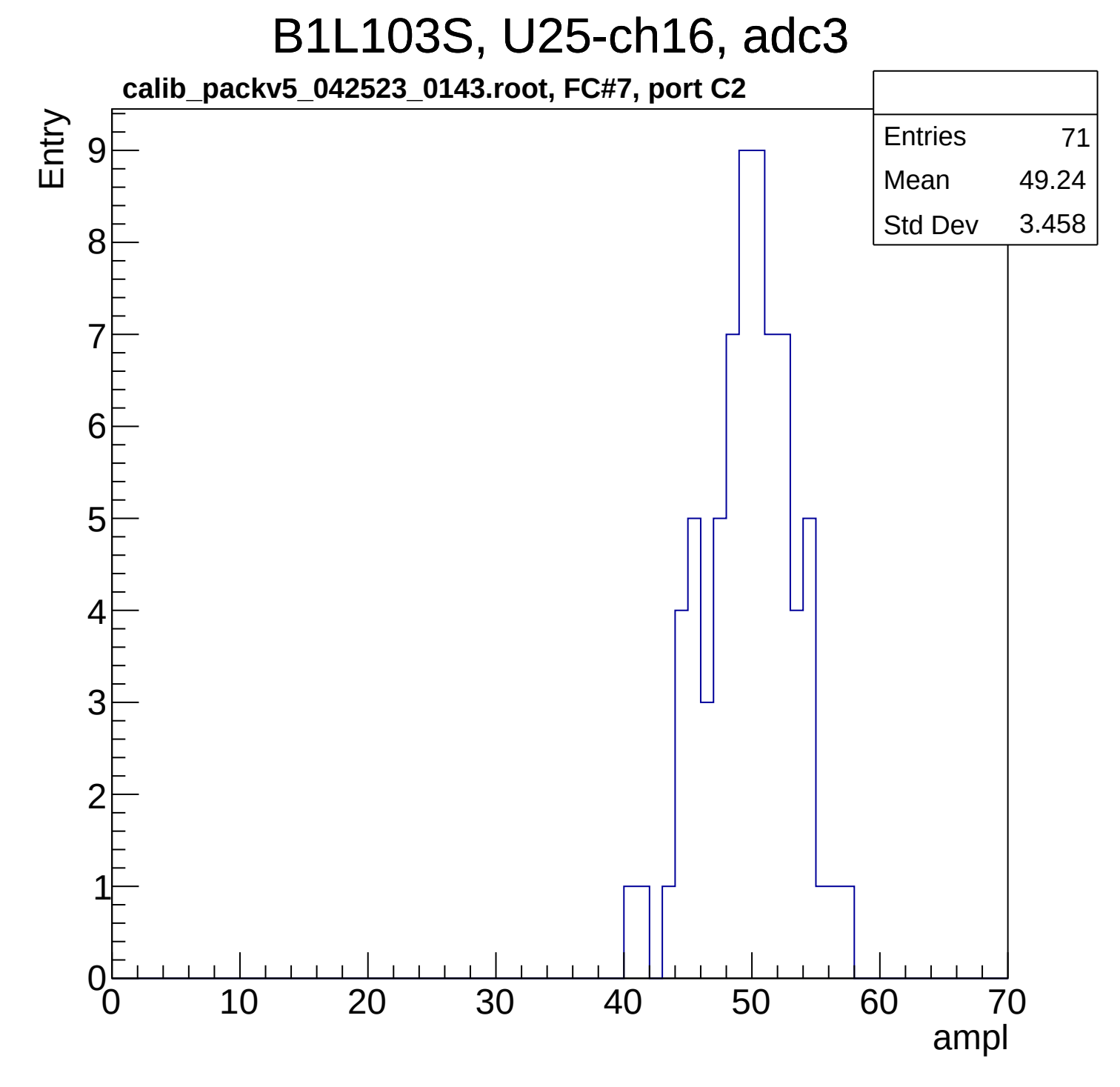
B1L103S, U25-ch16, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 49.24
Std Dev 3.458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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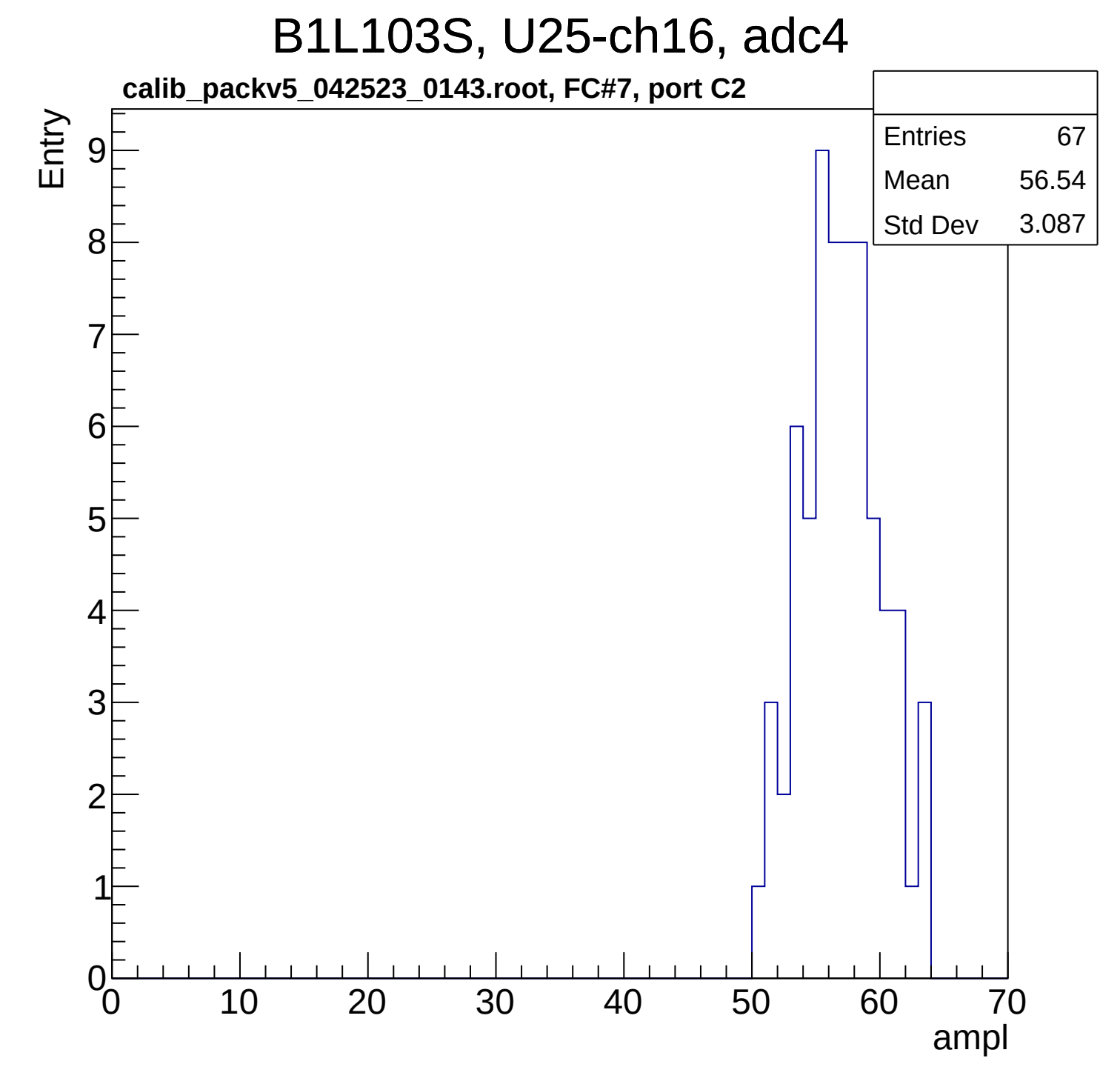
B1L103S, U25-ch16, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 56.54
Std Dev 3.087
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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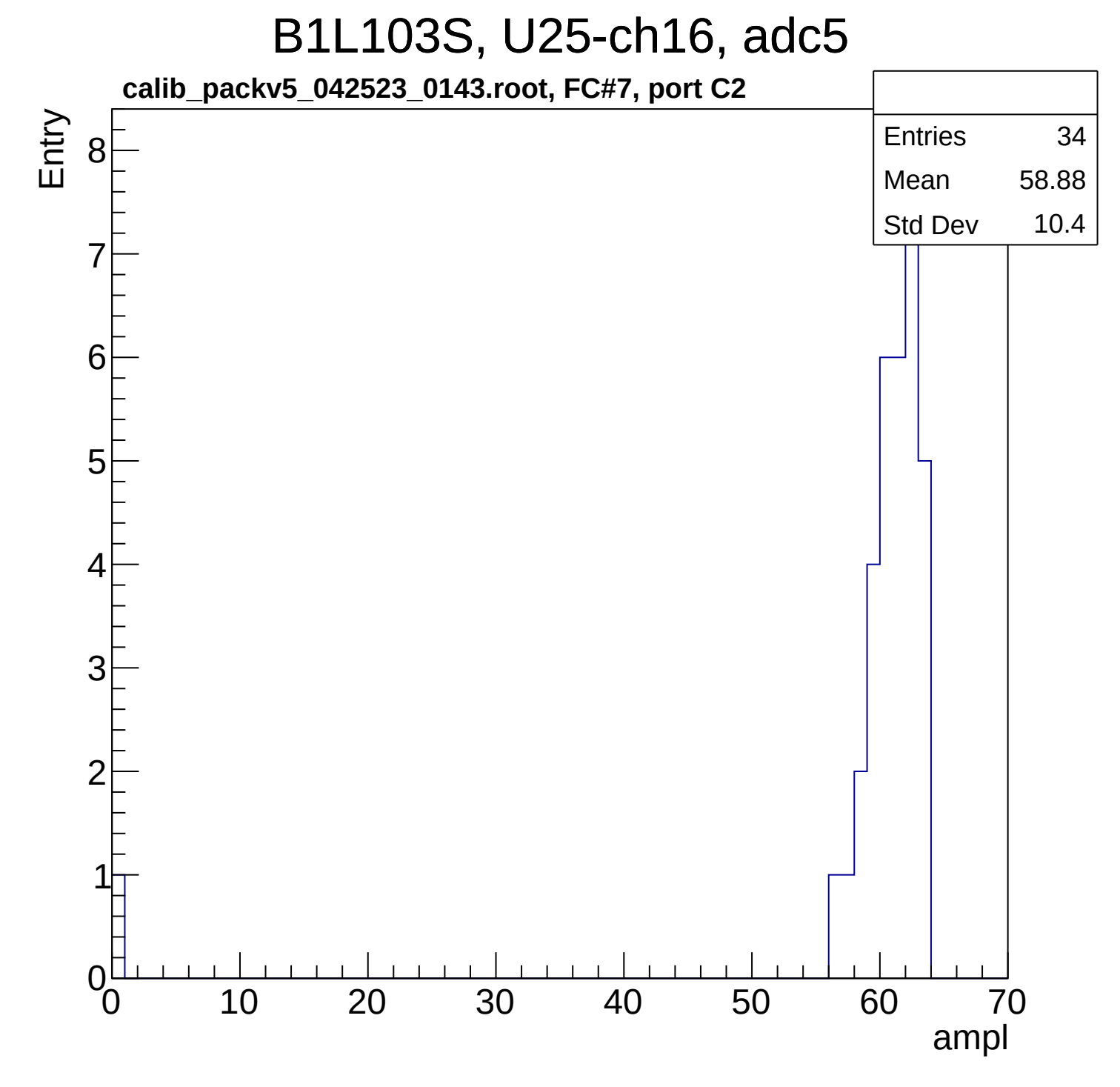
20
30
40
50
60
70


B1L103S, U25-ch16, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 58.88
Std Dev 10.4
Entry
ampl
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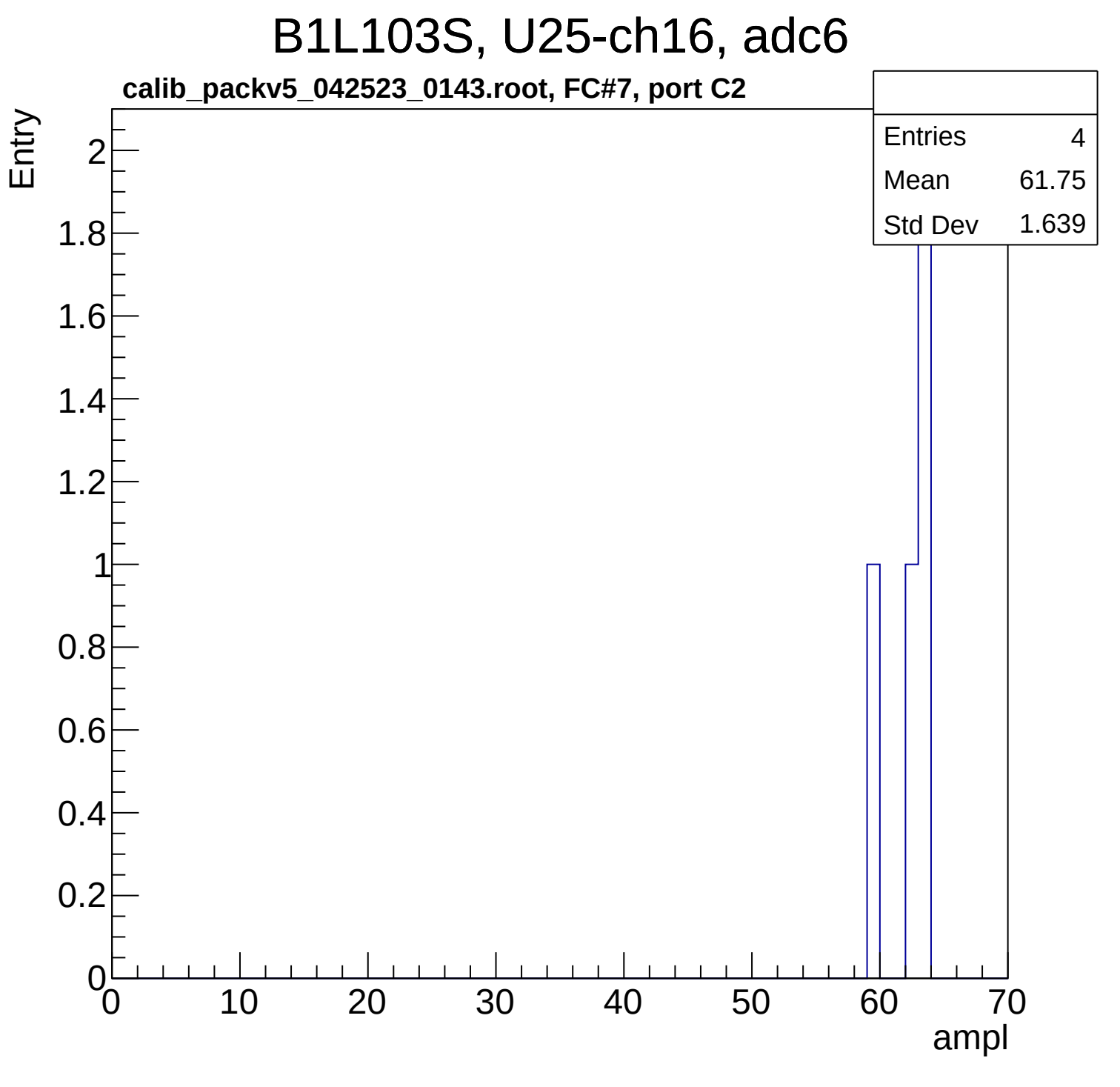
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch16, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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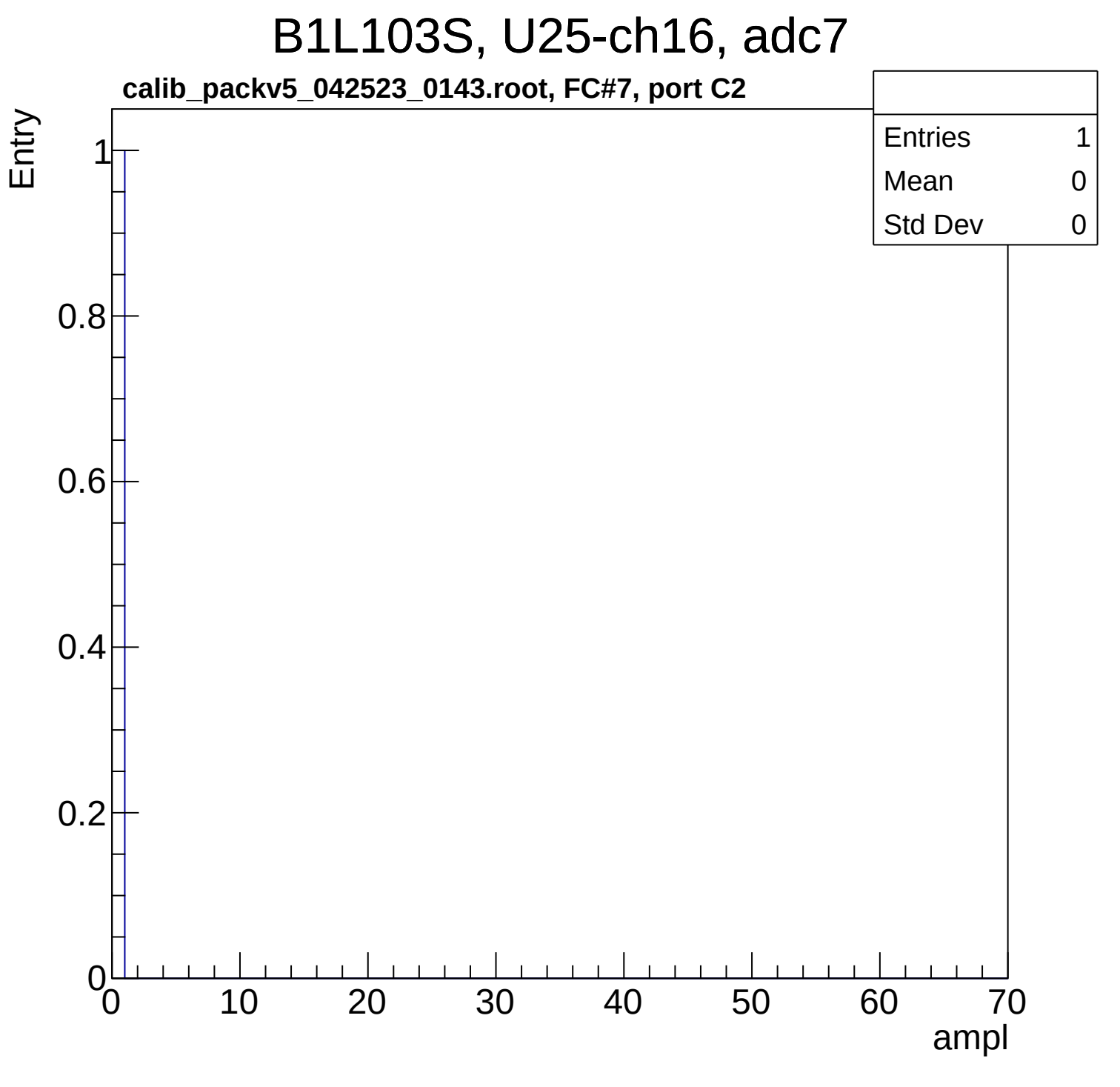
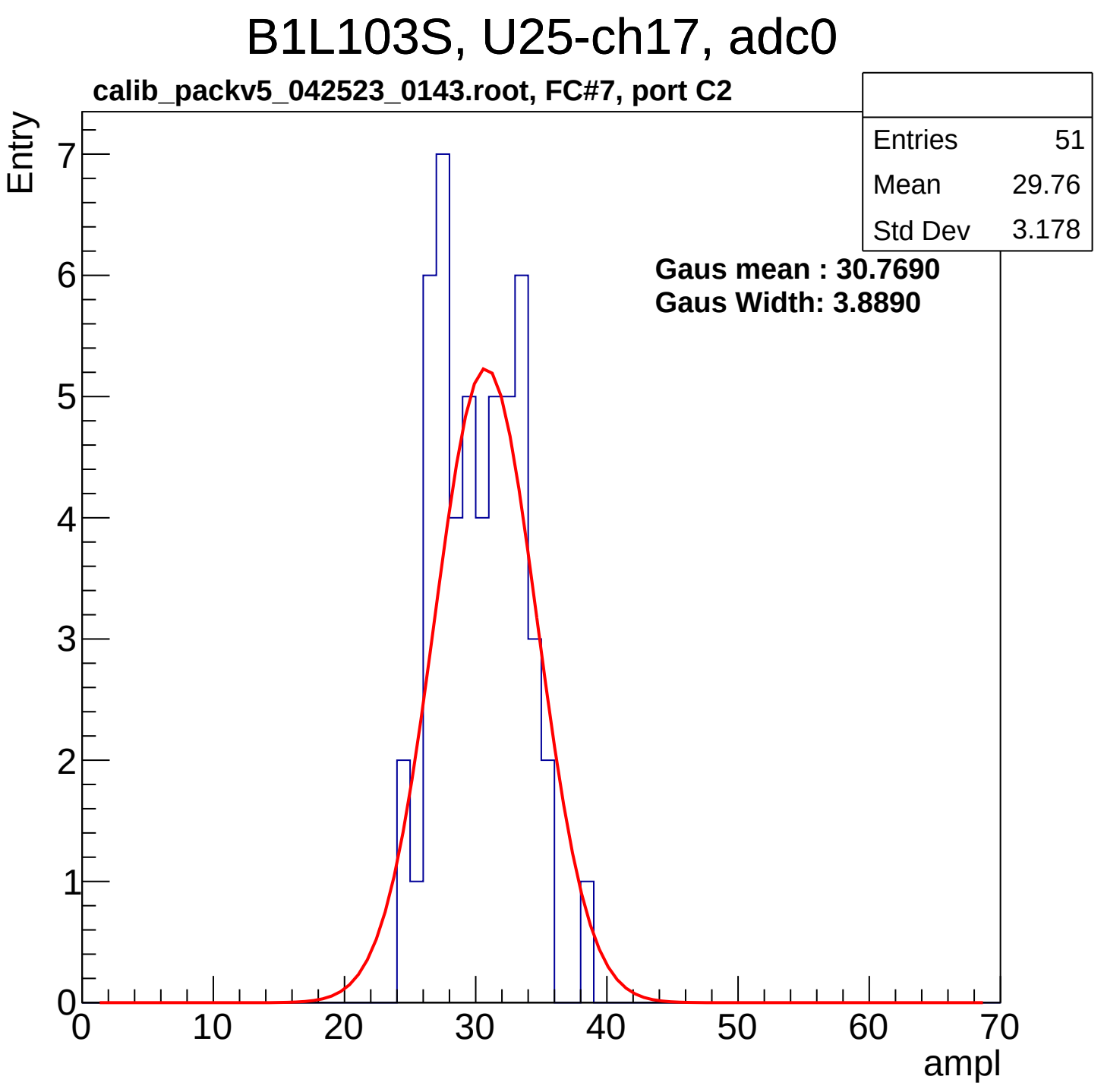

B1L103S, U25-ch16, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch17, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 29.76
Std Dev 3.178
Gaus mean : 30.7690
Gaus Width: 3.8890
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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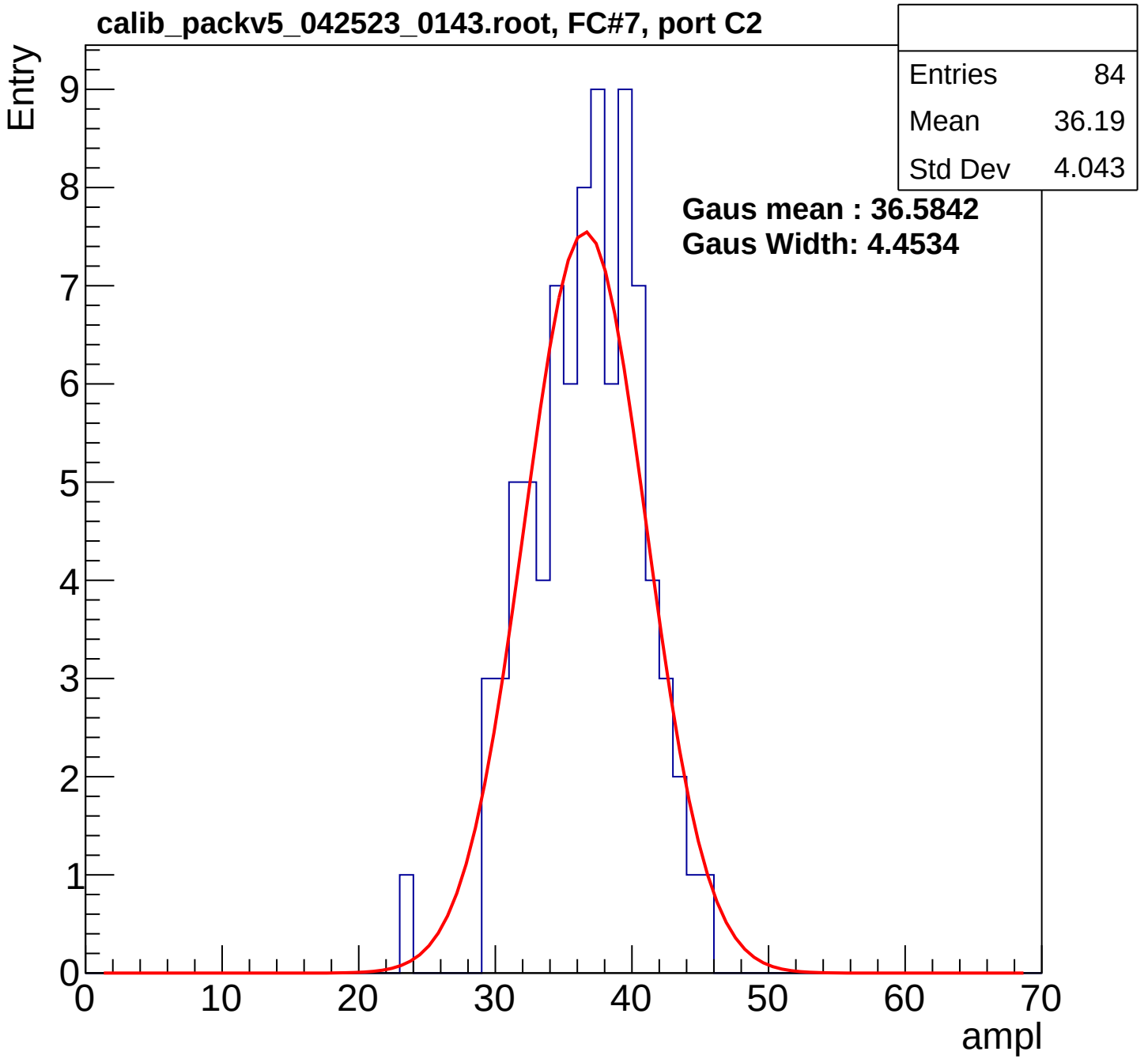
B1L103S, U25-ch17, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 36.19
Std Dev 4.043
Gaus mean : 36.5842
Gaus Width: 4.4534
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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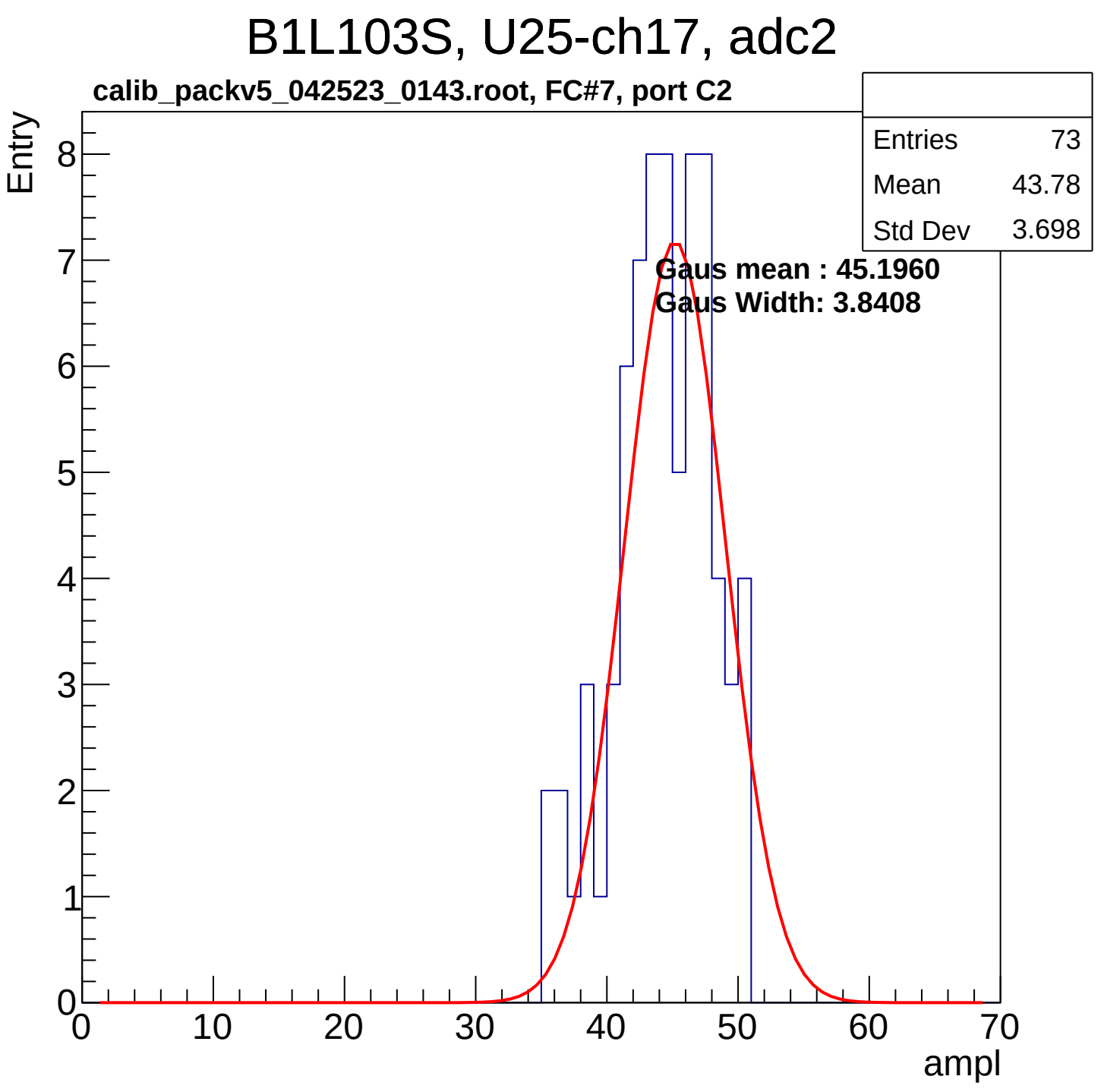
B1L103S, U25-ch17, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 43.78
Std Dev 3.698
Gaus mean : 45.1960
Gaus Width: 3.8408
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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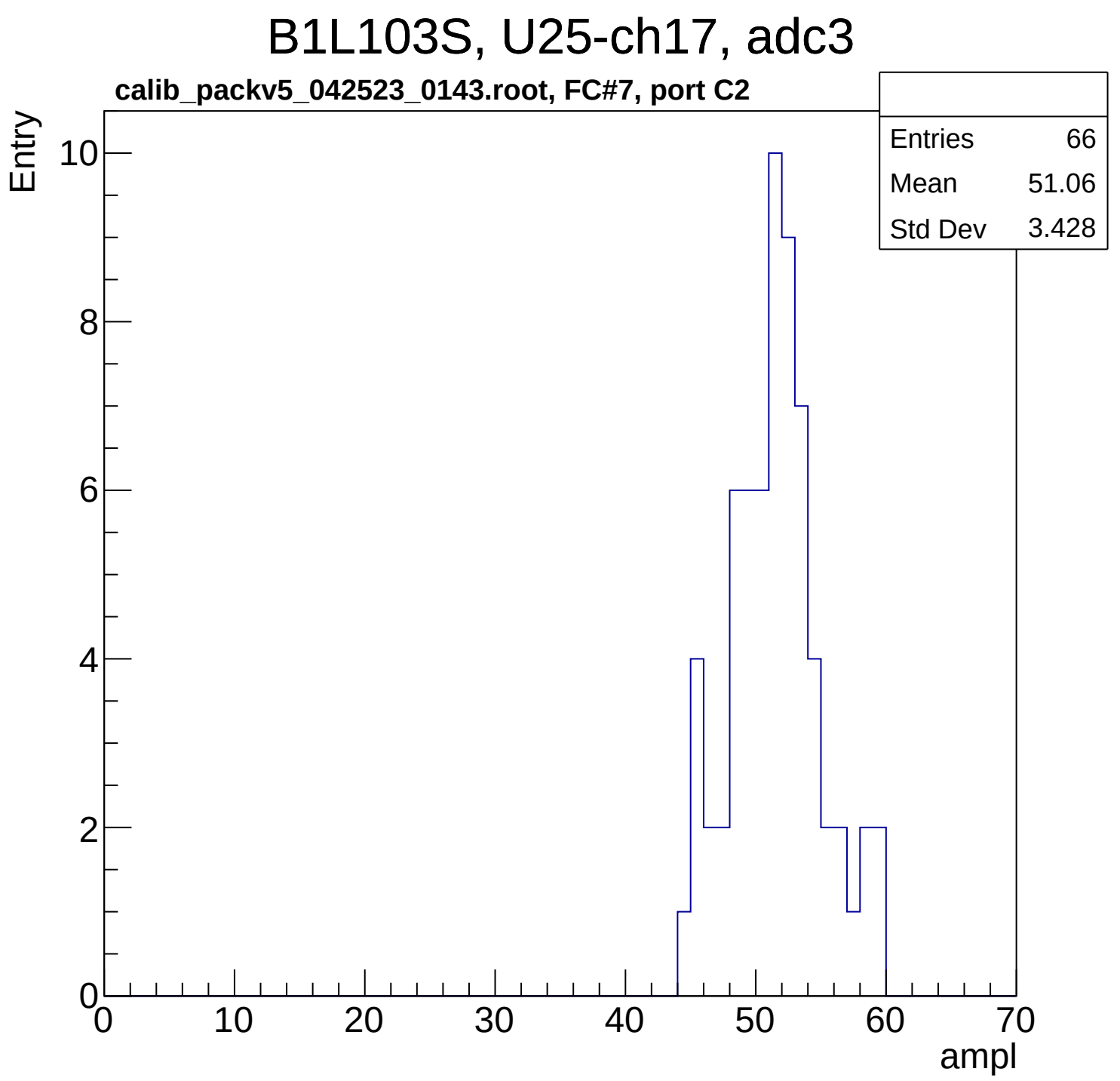
B1L103S, U25-ch17, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 51.06
Std Dev 3.428
Entry
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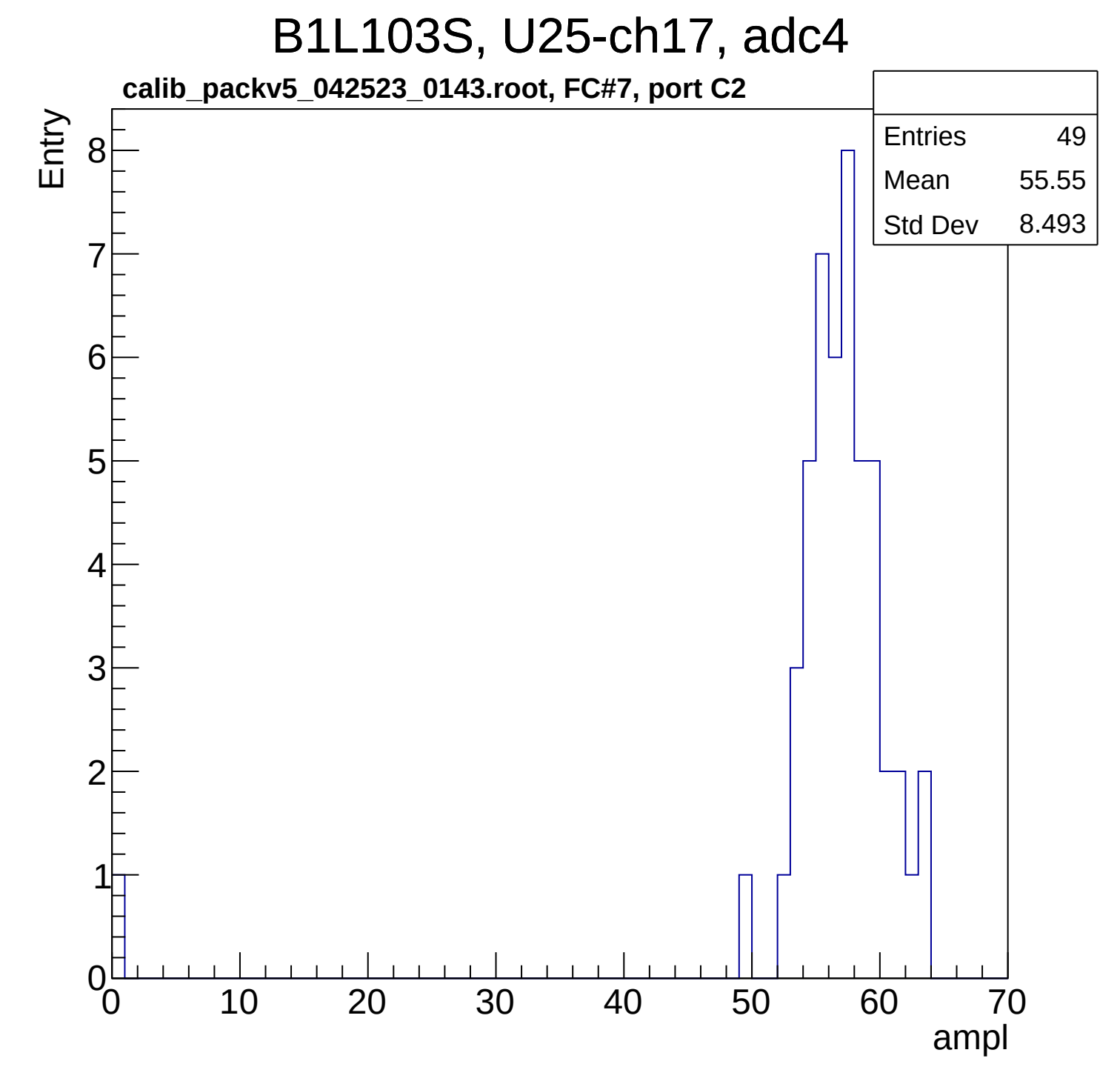
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch17, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 55.55
Std Dev 8.493
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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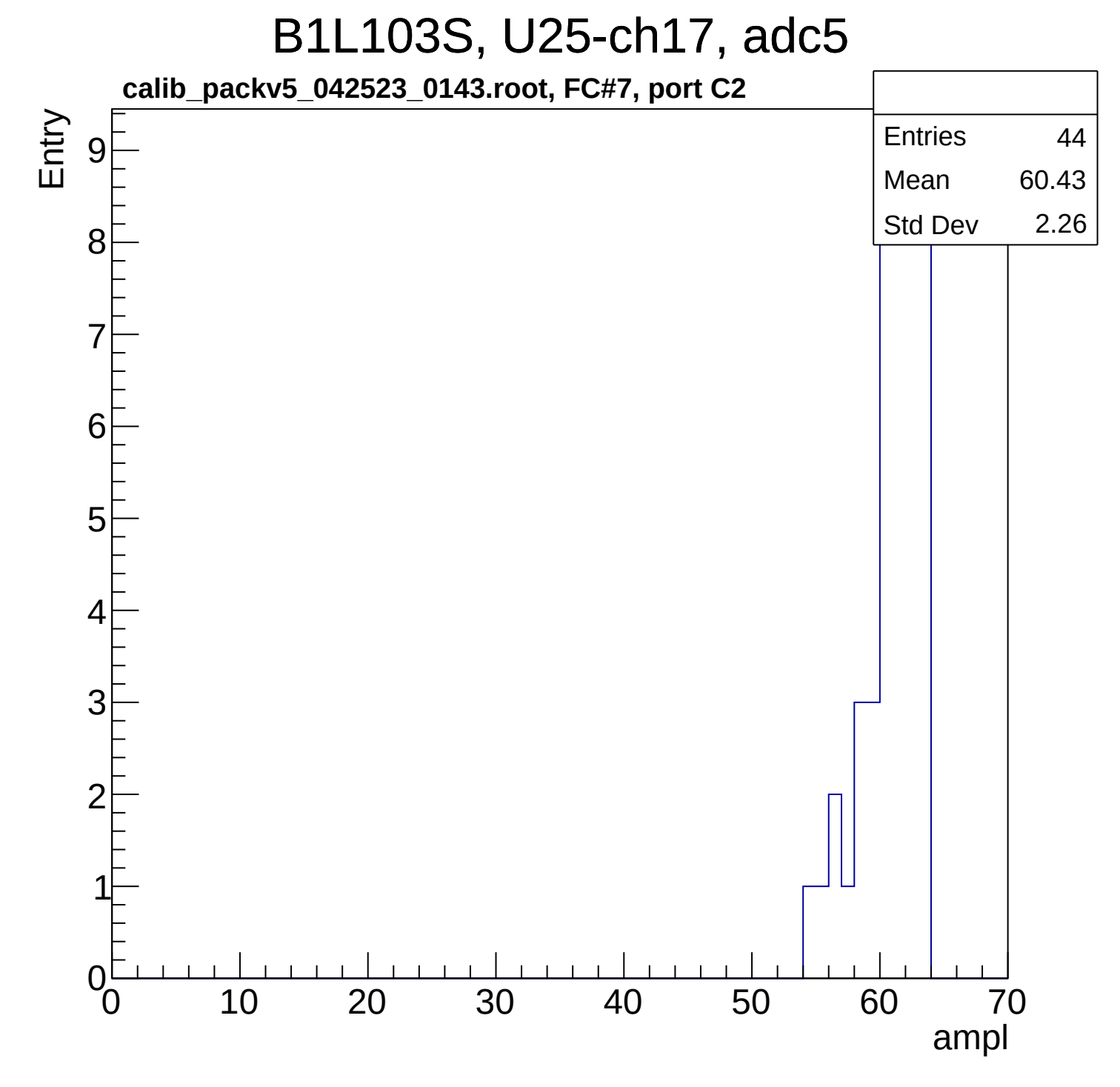
B1L103S, U25-ch17, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 60.43
Std Dev 2.26
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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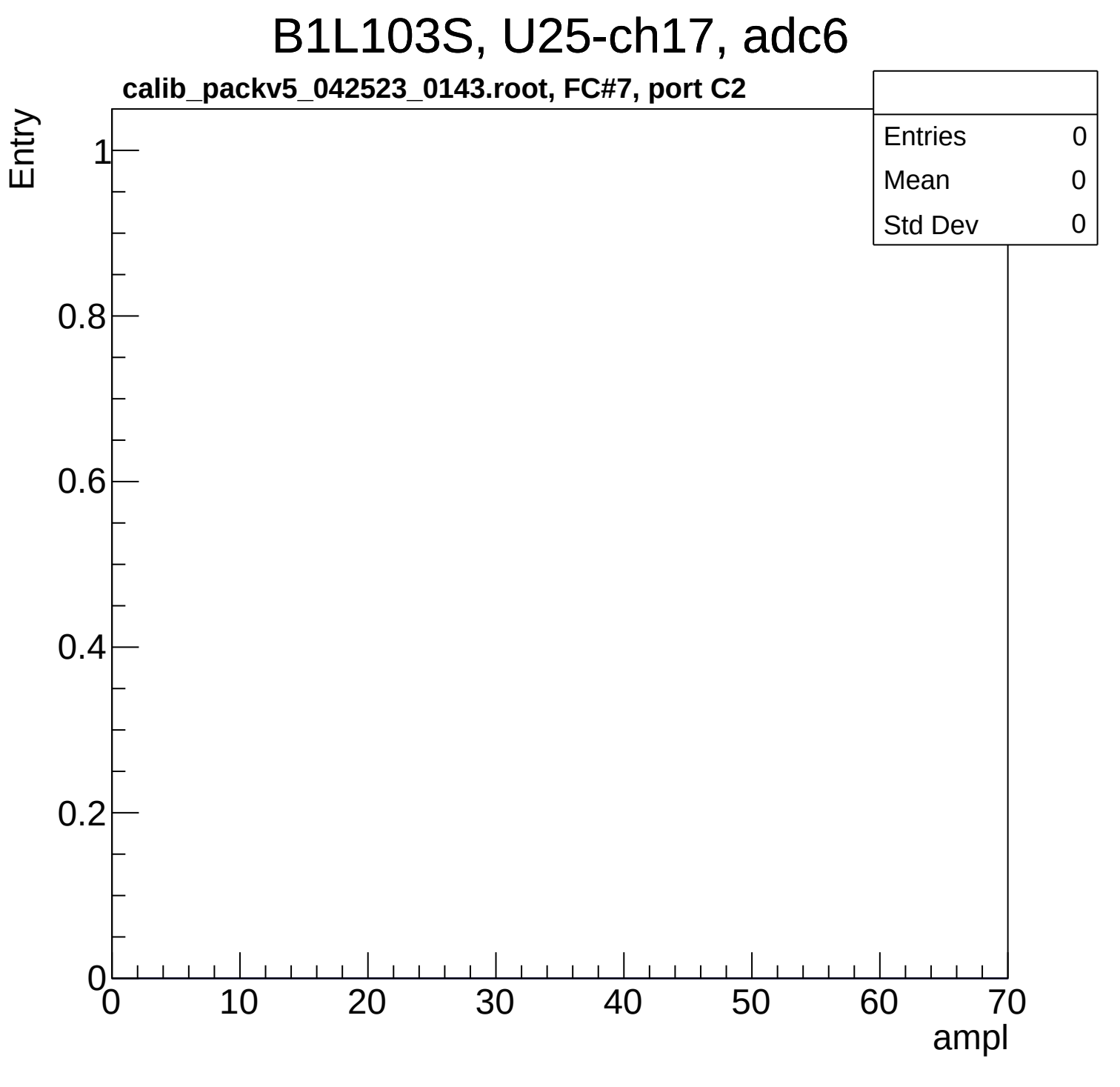
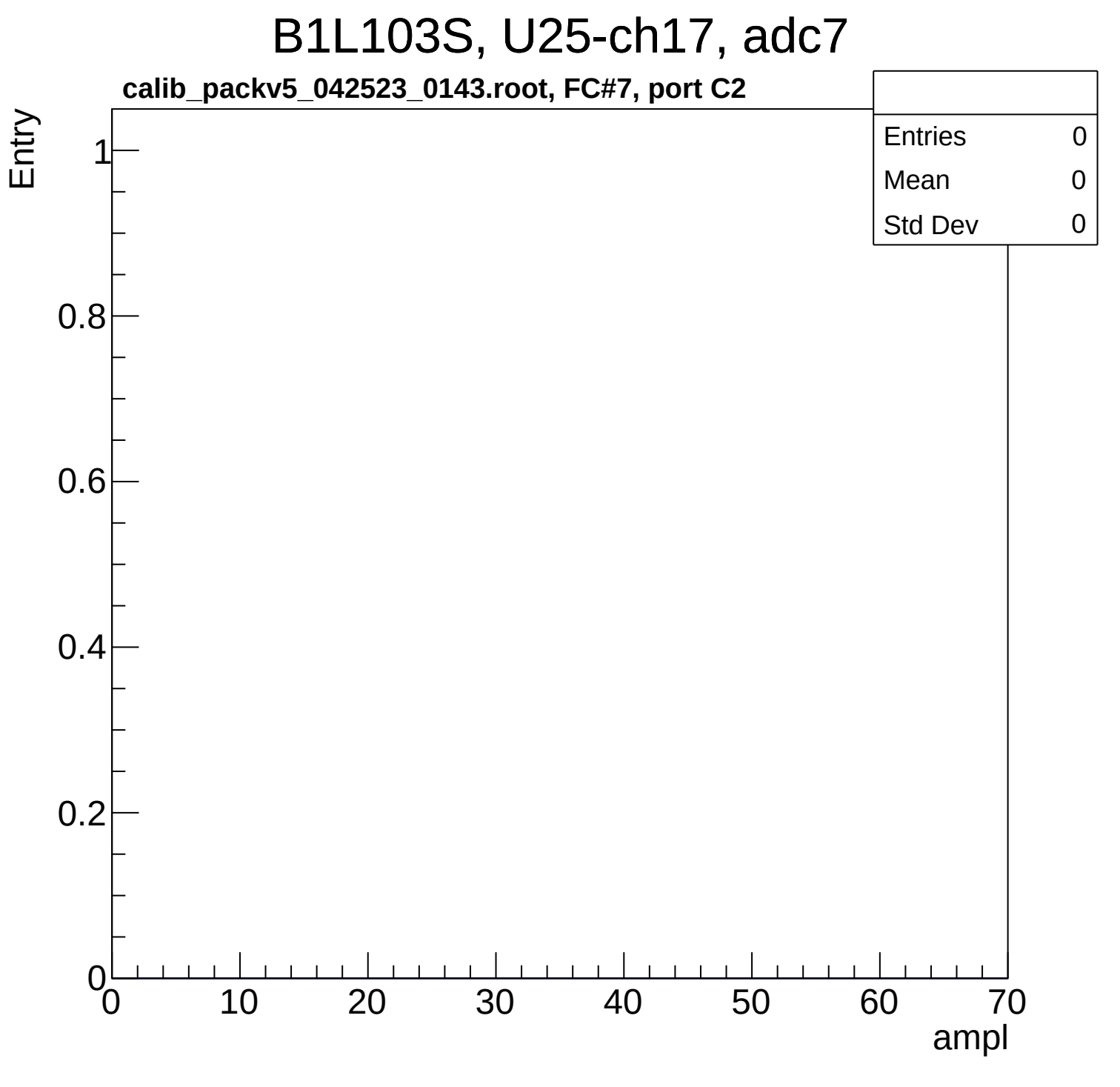

B1L103S, U25-ch17, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch17, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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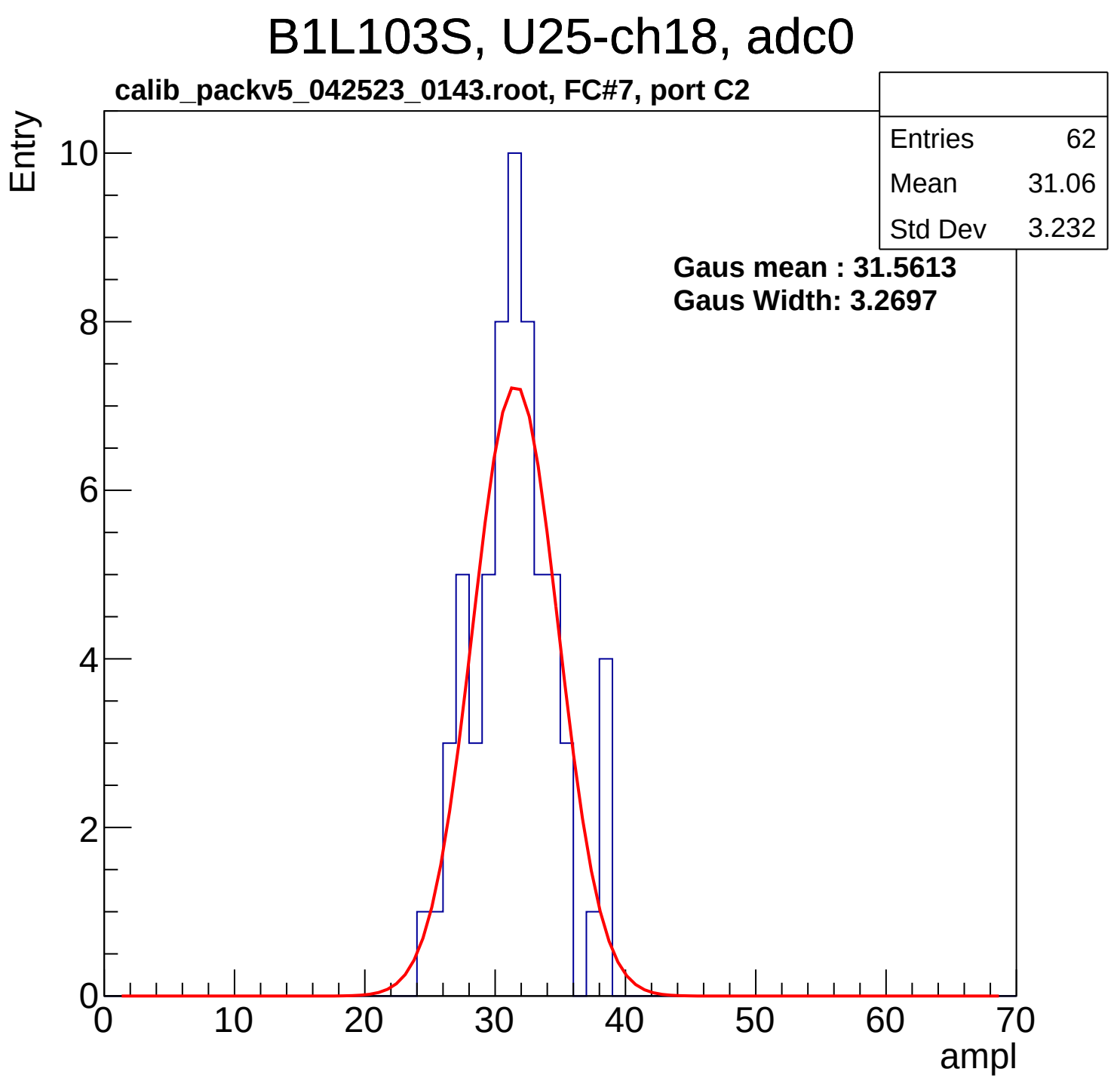
50
60
70
ampl


B1L103S, U25-ch18, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 31.06
Std Dev 3.232
Gaus mean : 31.5613
Gaus Width: 3.2697
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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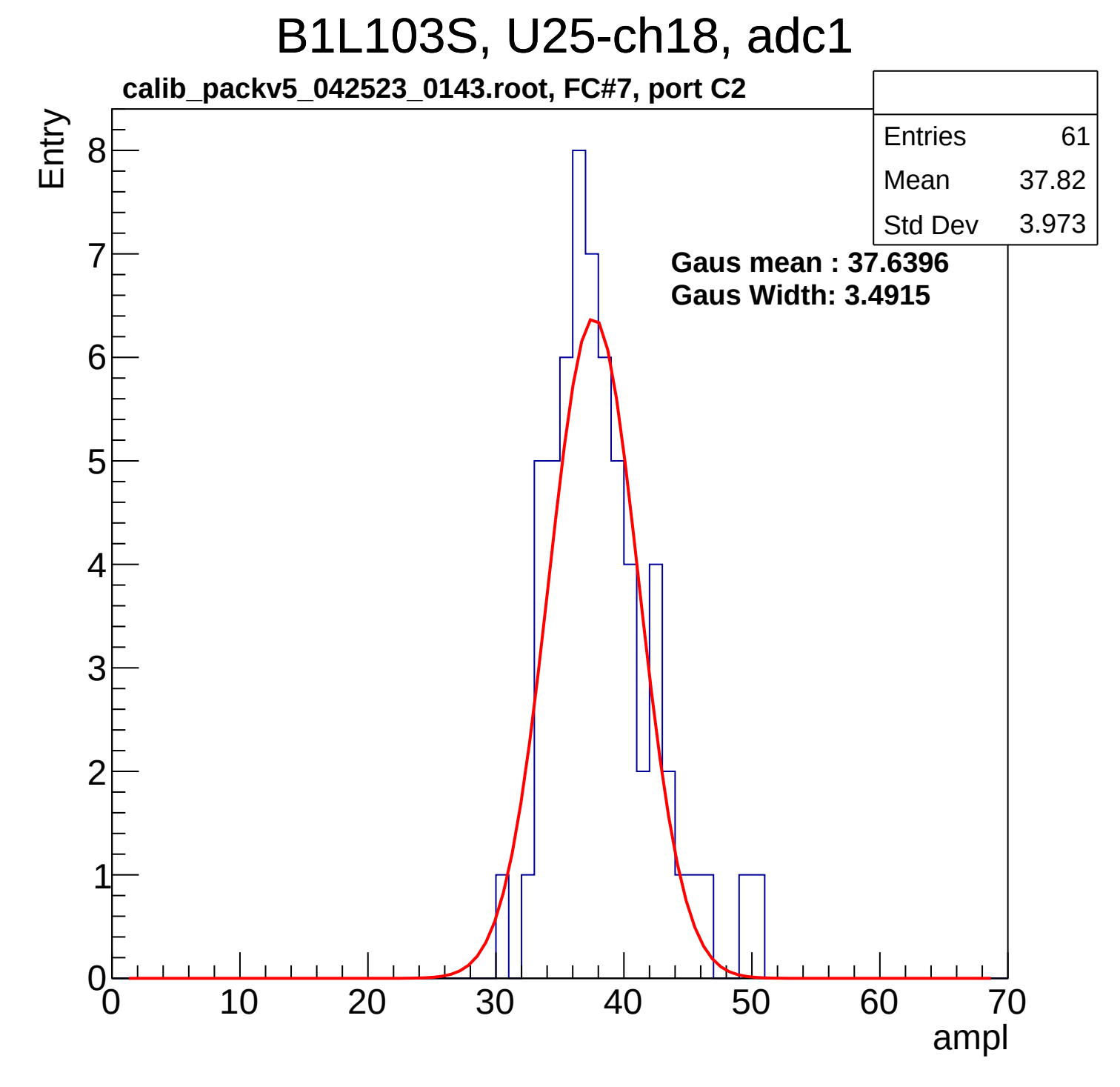
60
70


B1L103S, U25-ch18, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37.82
Std Dev 3.973
Gaus mean : 37.6396
Gaus Width: 3.4915
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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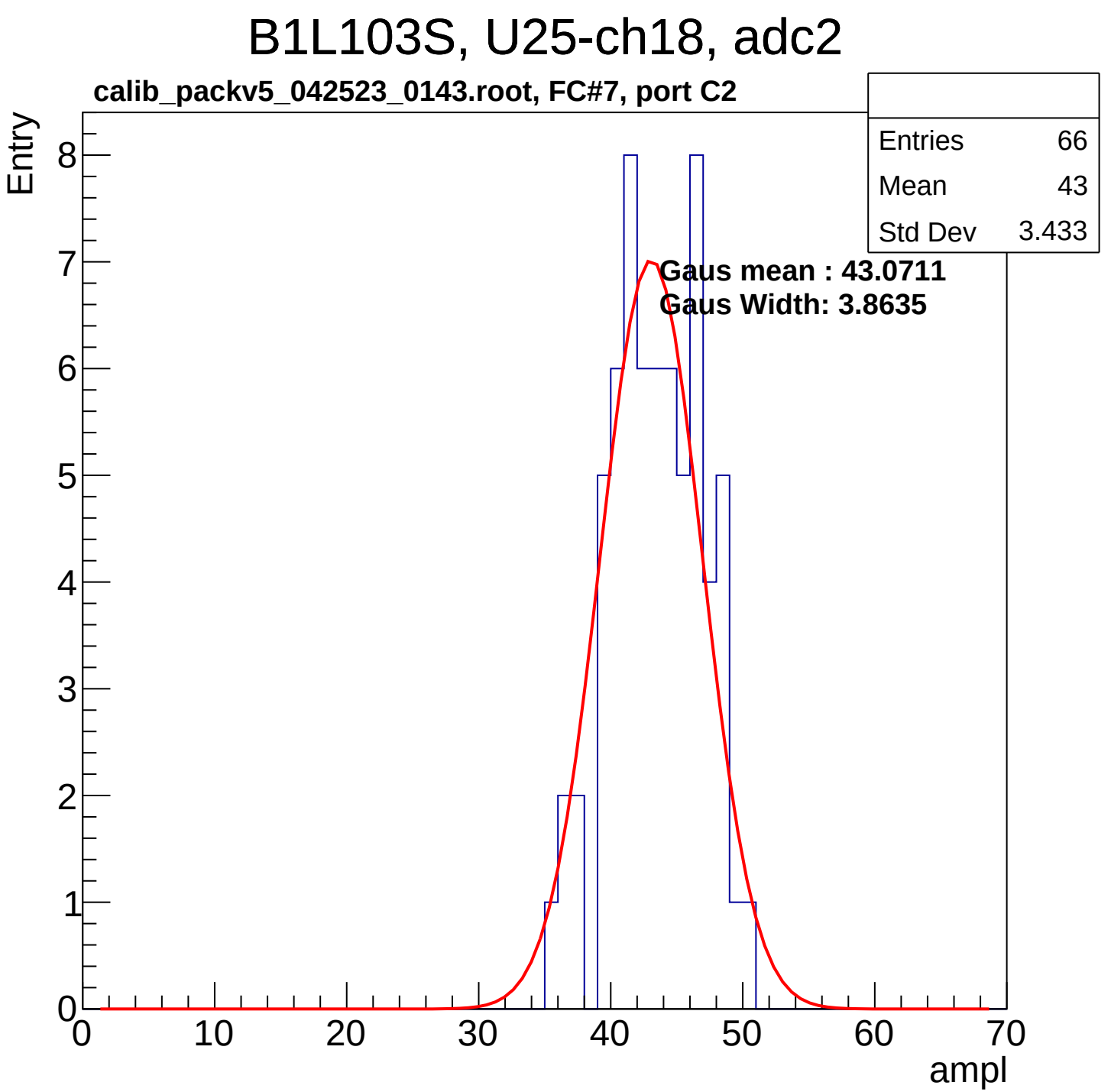
60
70


B1L103S, U25-ch18, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43
Std Dev 3.433
Gaus mean : 43.0711
Gaus Width: 3.8635
Entry
ampl
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4
5
6
7
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0
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60
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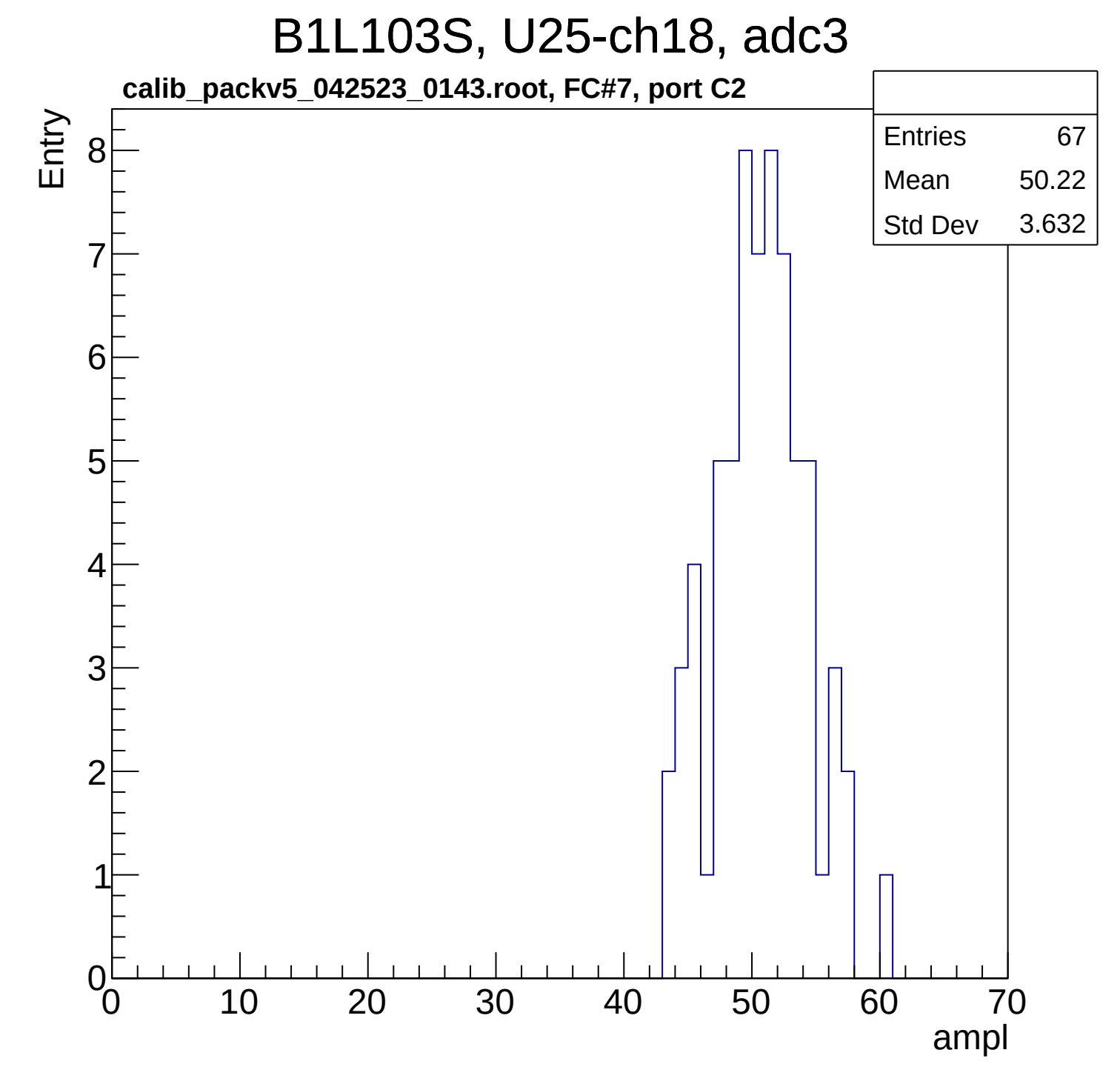
B1L103S, U25-ch18, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 50.22
Std Dev 3.632
Entry
ampl
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0
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60
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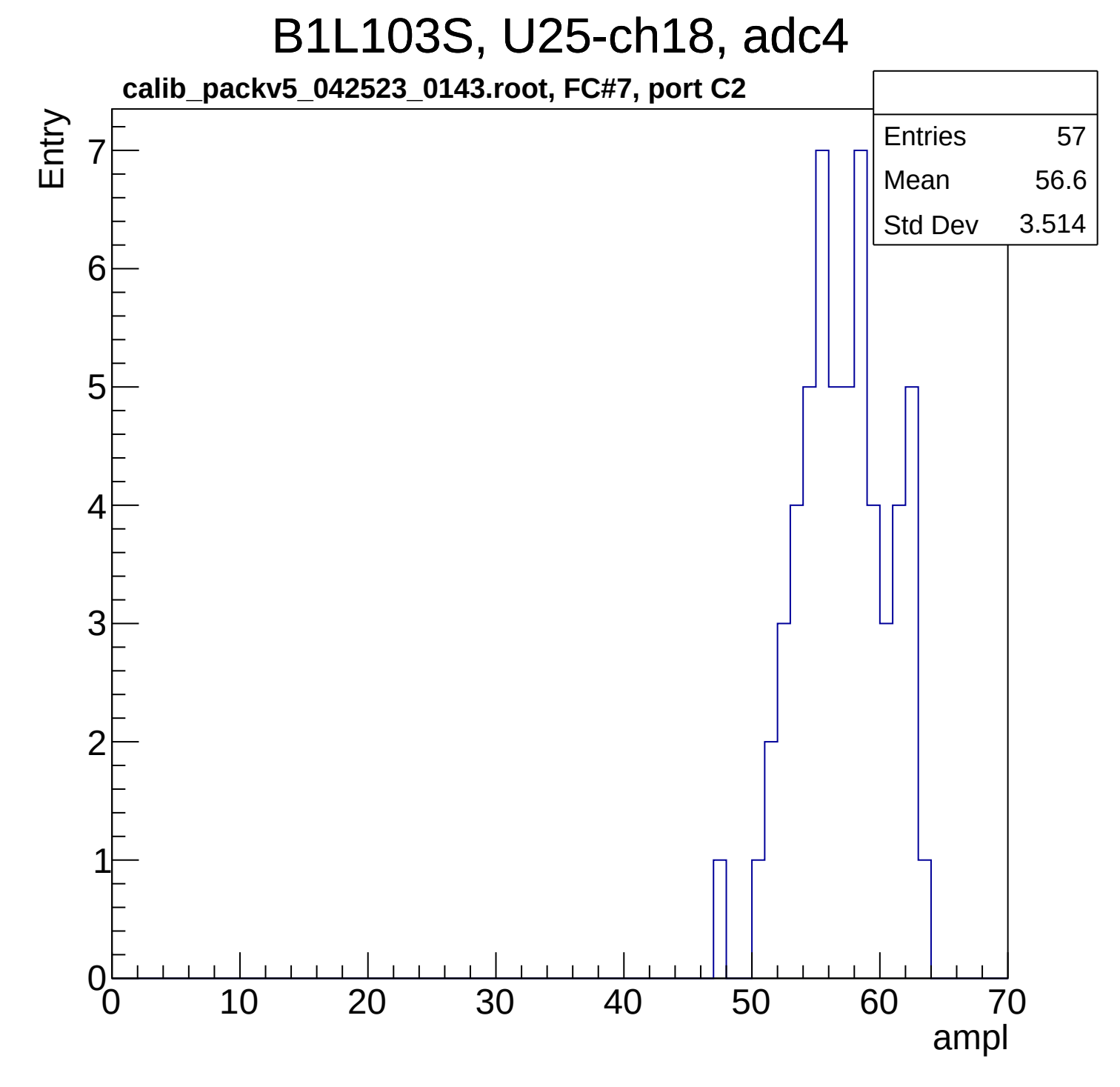
B1L103S, U25-ch18, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 56.6
Std Dev 3.514
Entry
ampl
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4
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0
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60
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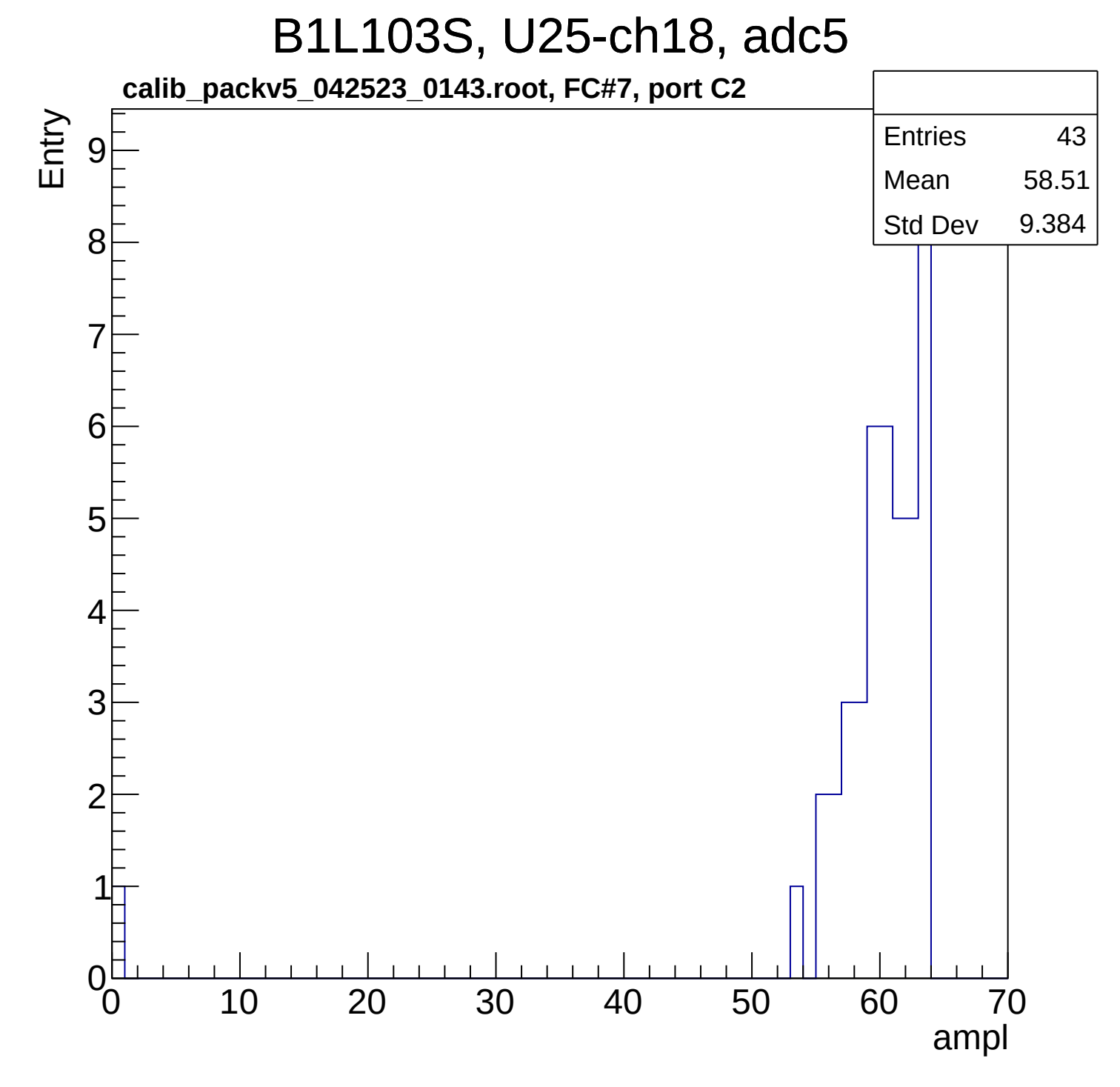
B1L103S, U25-ch18, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 58.51
Std Dev 9.384
Entry
ampl
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3
4
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0
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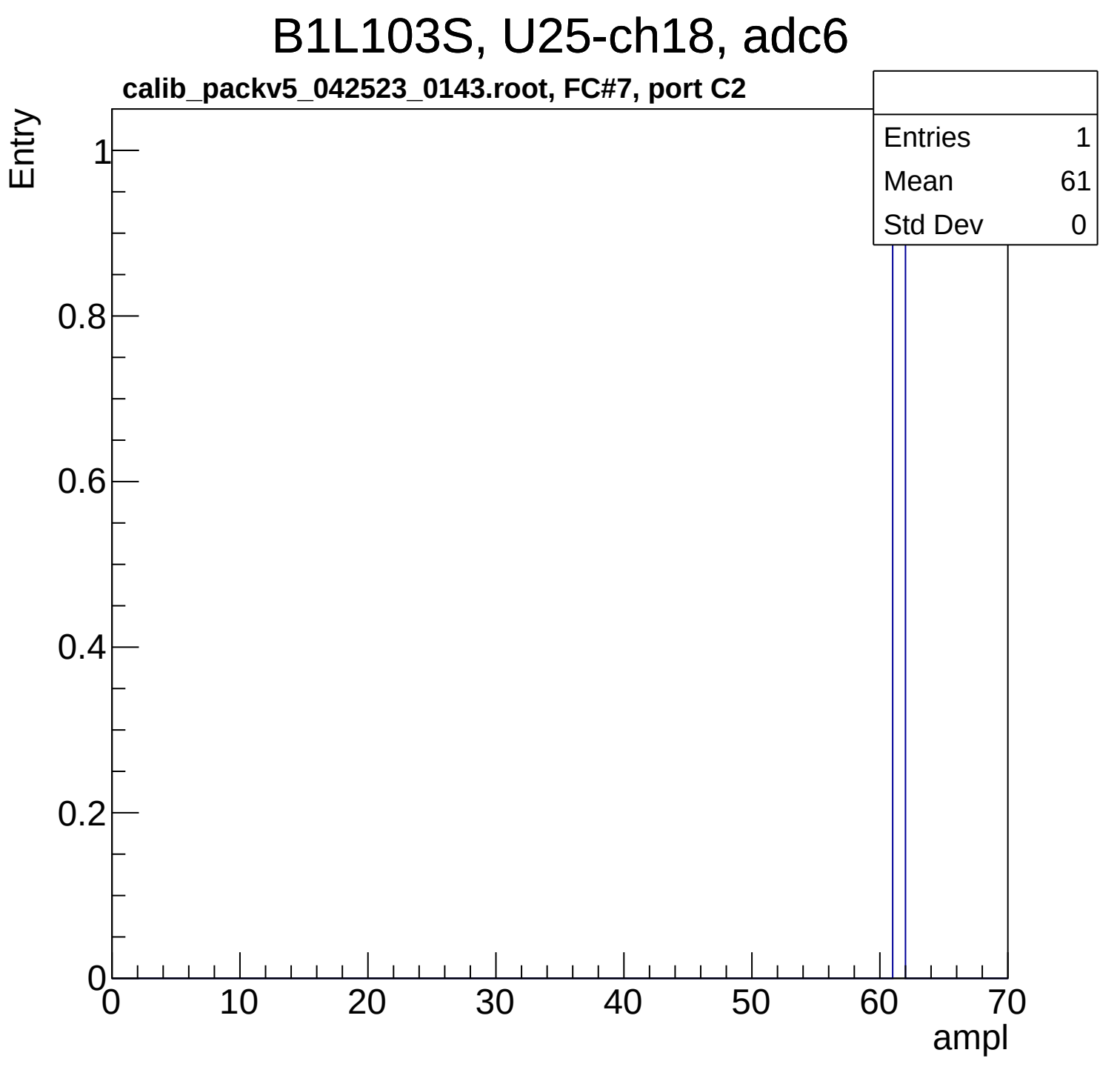
B1L103S, U25-ch18, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 61
Std Dev 0
Entry
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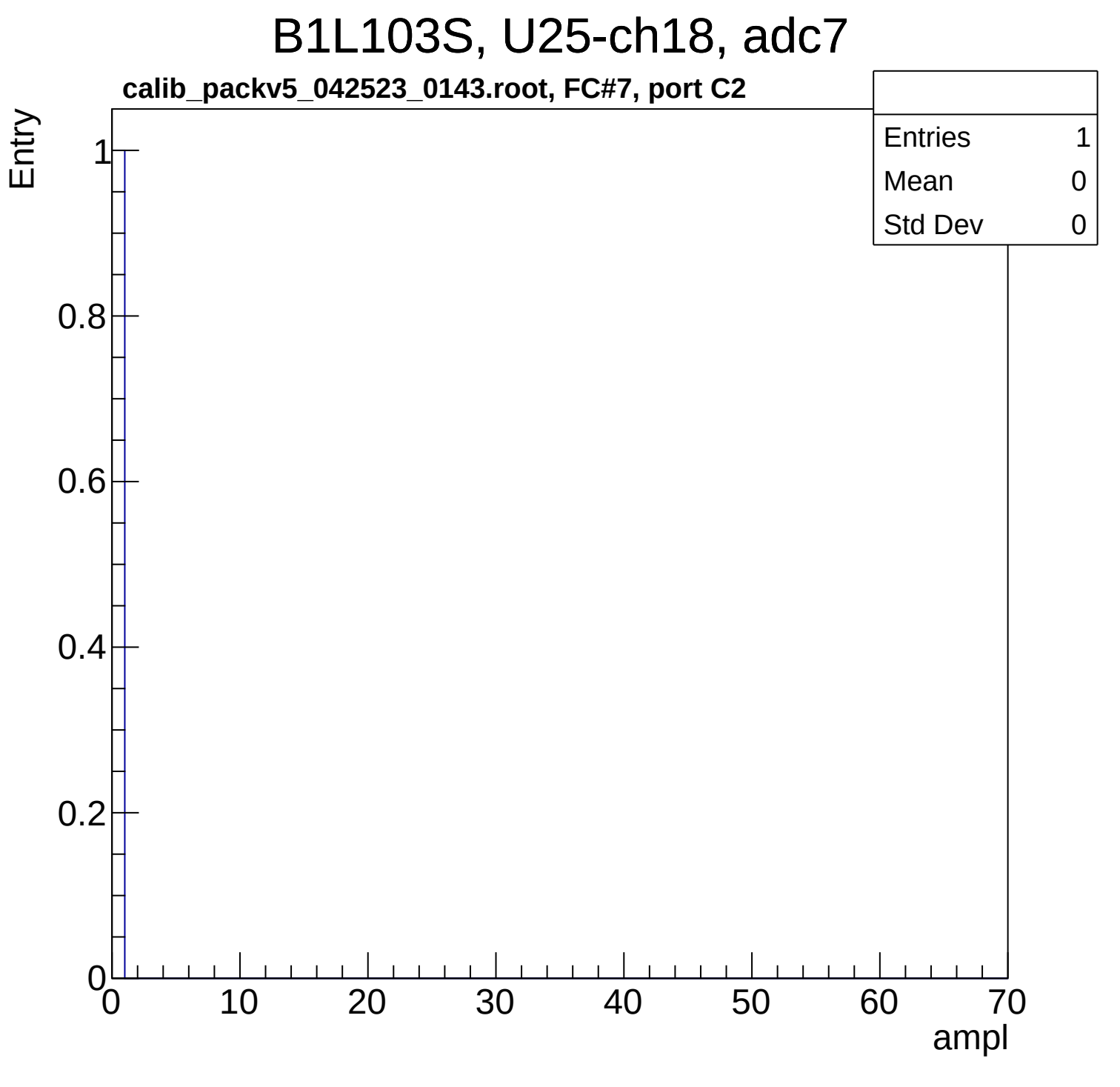
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch18, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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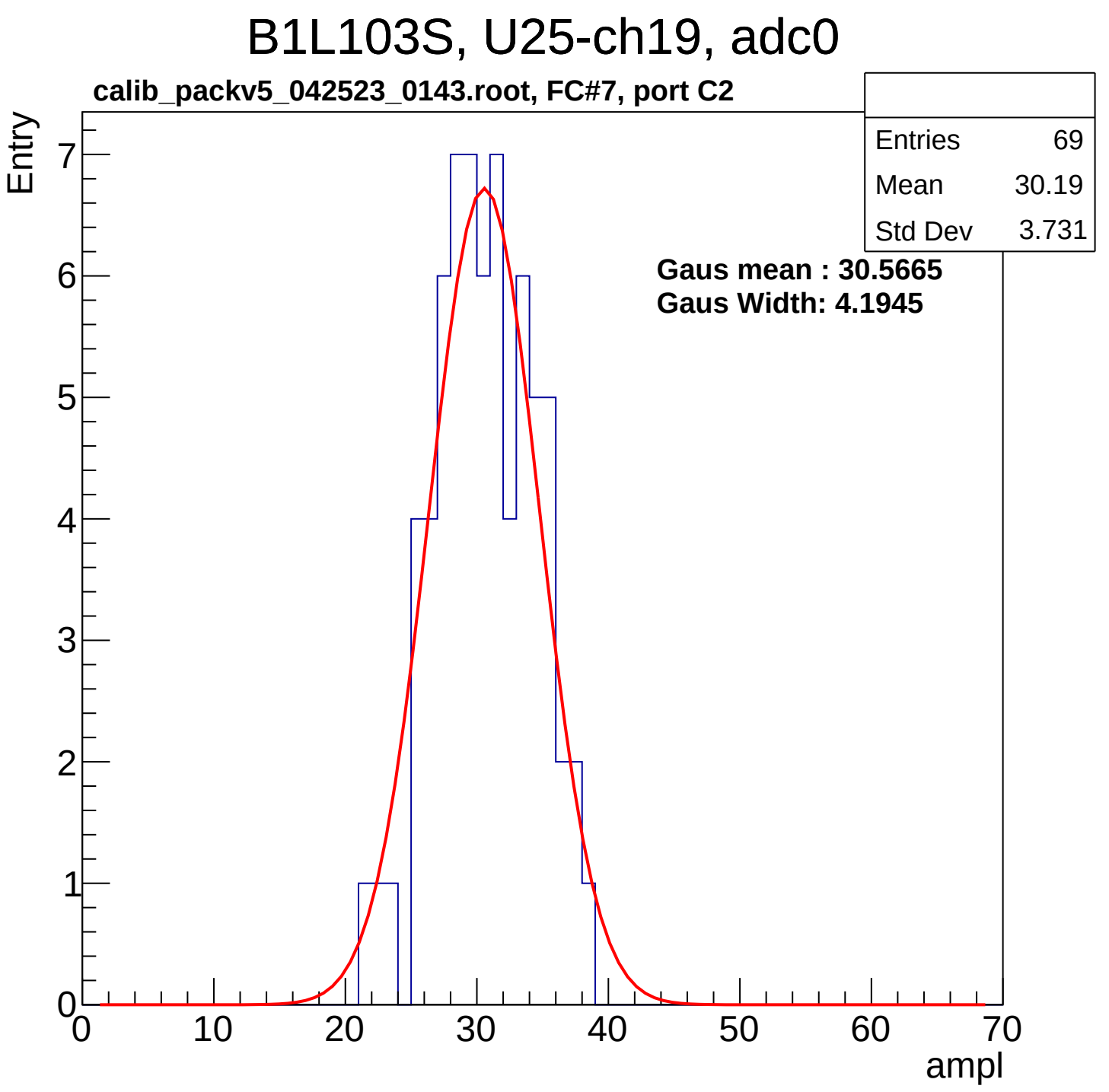
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch19, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 30.19
Std Dev 3.731
Gaus mean : 30.5665
Gaus Width: 4.1945
Entry
ampl
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3
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5
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0
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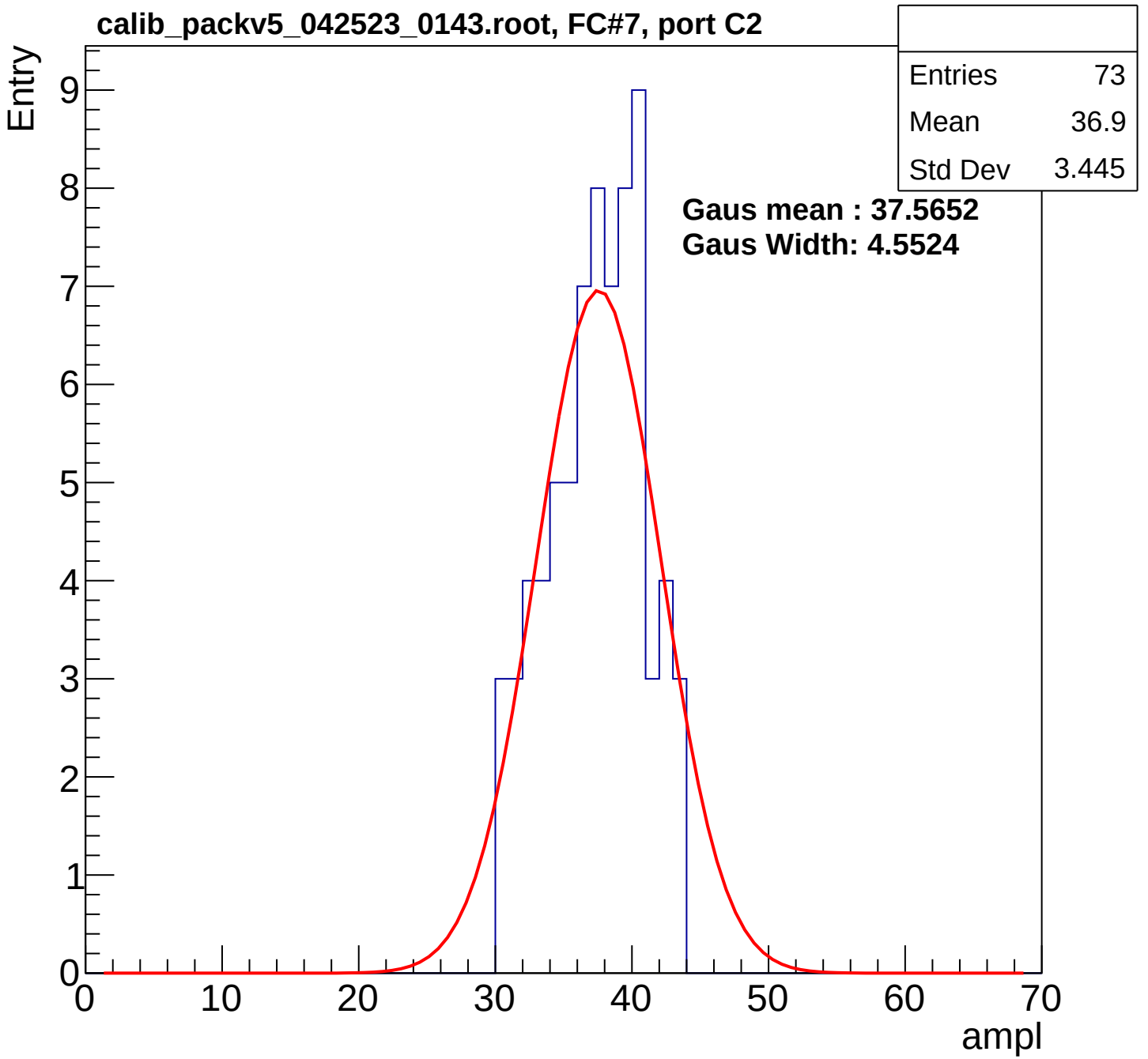
B1L103S, U25-ch19, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.9
Std Dev 3.445
Gaus mean : 37.5652
Gaus Width: 4.5524
Entry
ampl
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3
4
5
6
7
8
9
0
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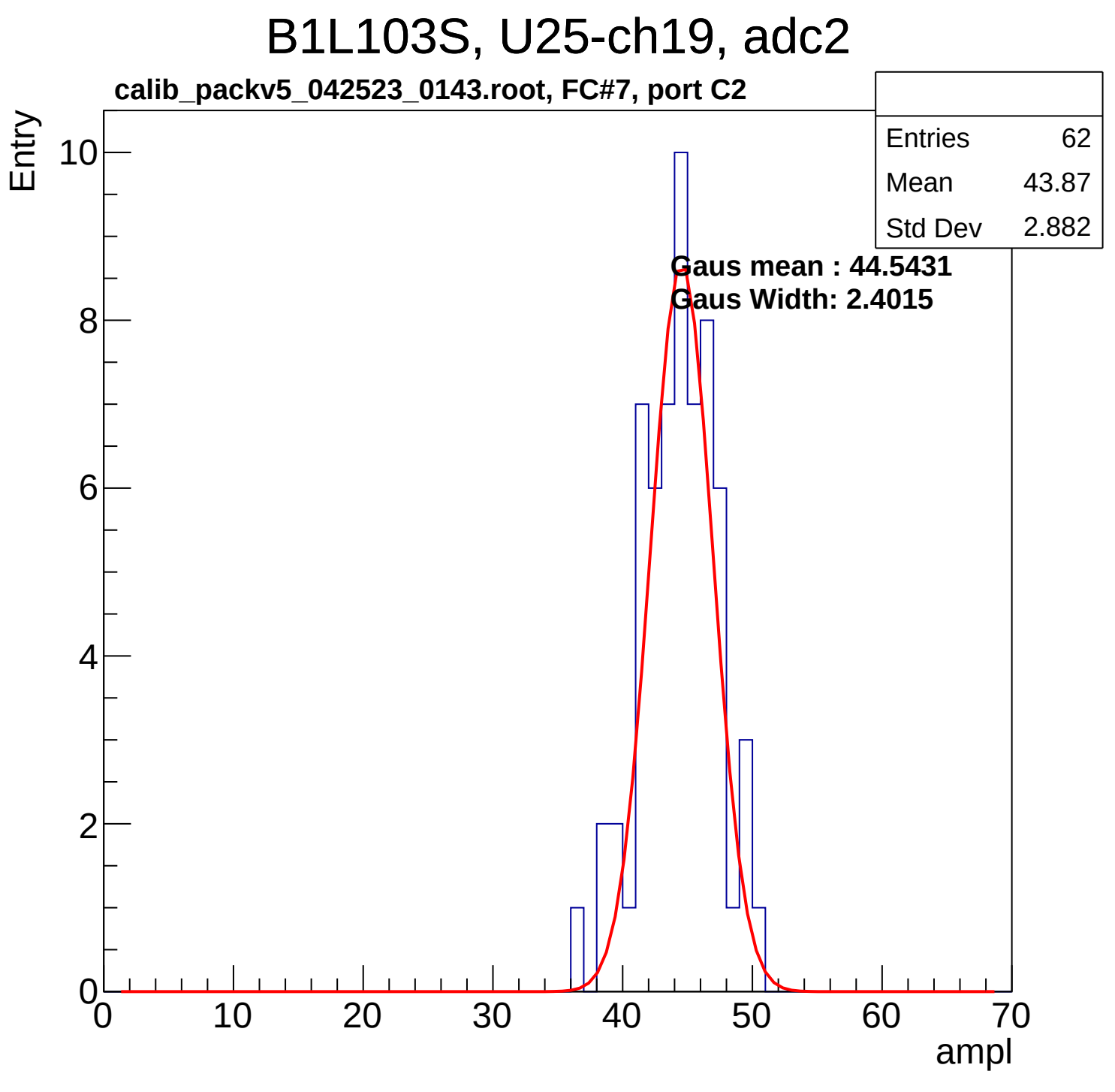
B1L103S, U25-ch19, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 43.87
Std Dev 2.882
Gaus mean : 44.5431
Gaus Width: 2.4015
Entry
ampl
0
2
4
6
8
10
0
10
20
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60
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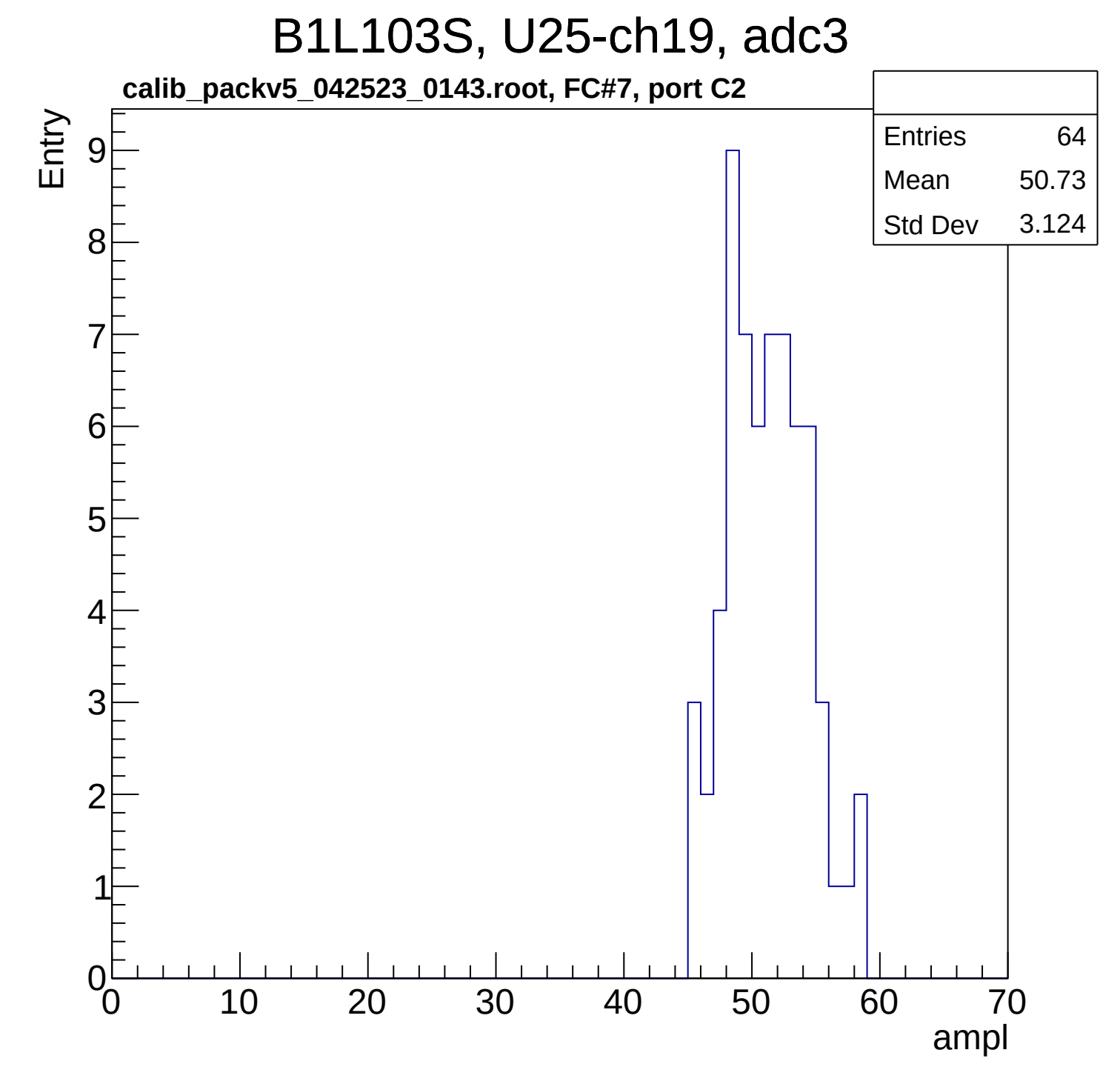
B1L103S, U25-ch19, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 50.73
Std Dev 3.124
Entry
ampl
0
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2
3
4
5
6
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9
0
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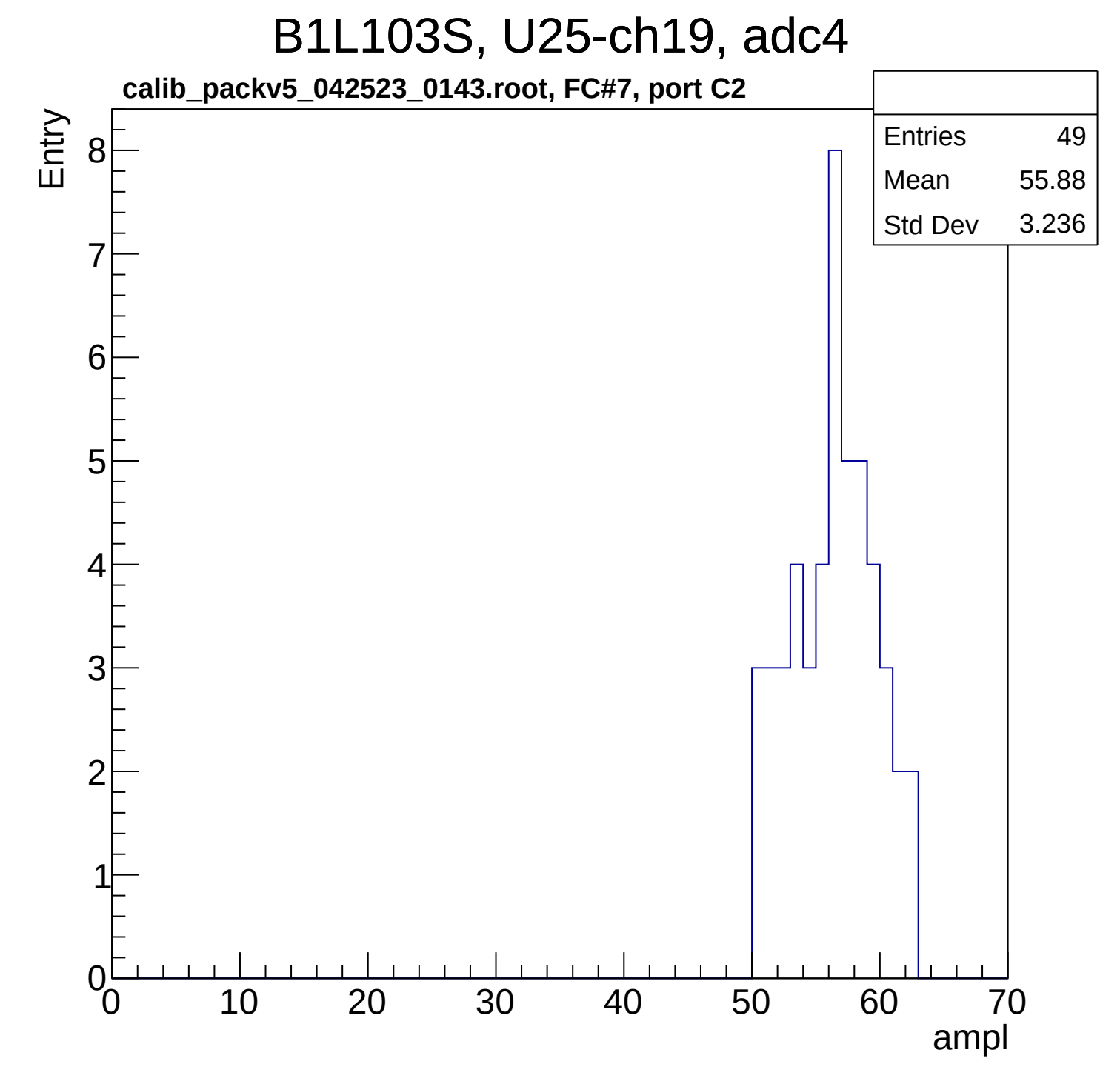
B1L103S, U25-ch19, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 55.88
Std Dev 3.236
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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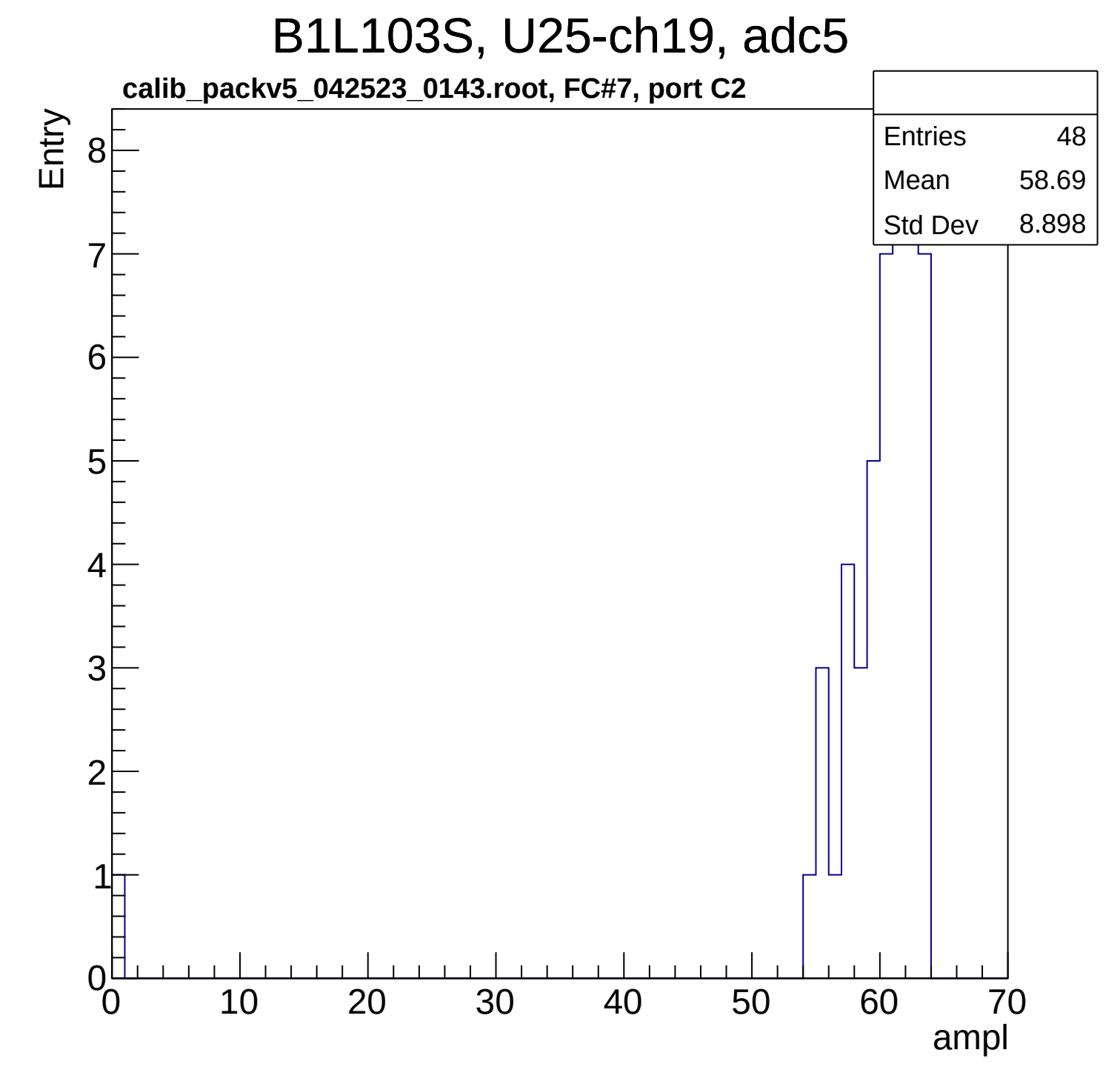
B1L103S, U25-ch19, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.69
Std Dev 8.898
Entry
0
1
2
3
4
5
6
7
8
0
10
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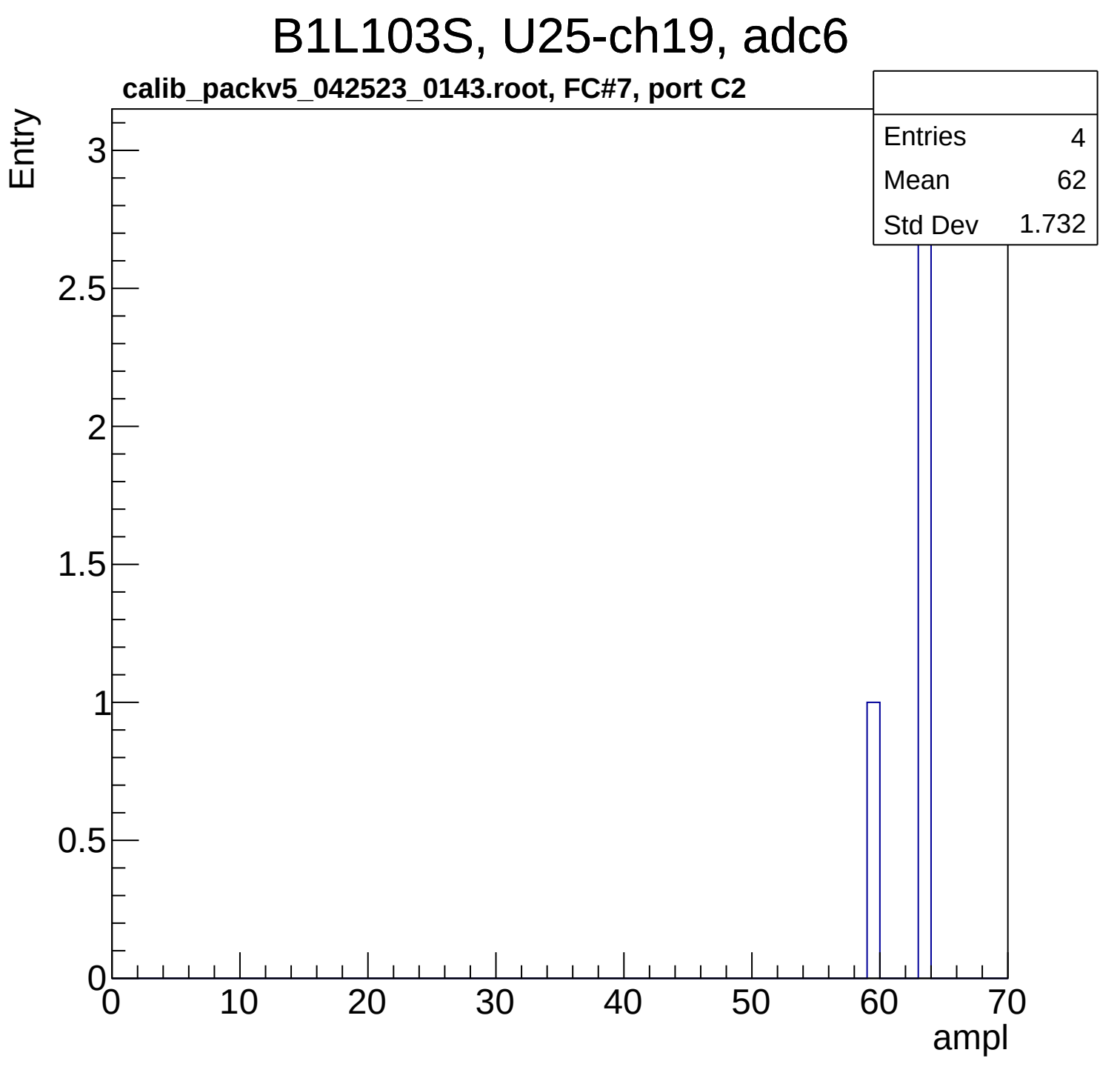
30
40
50
60
70
ampl


B1L103S, U25-ch19, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62
Std Dev 1.732
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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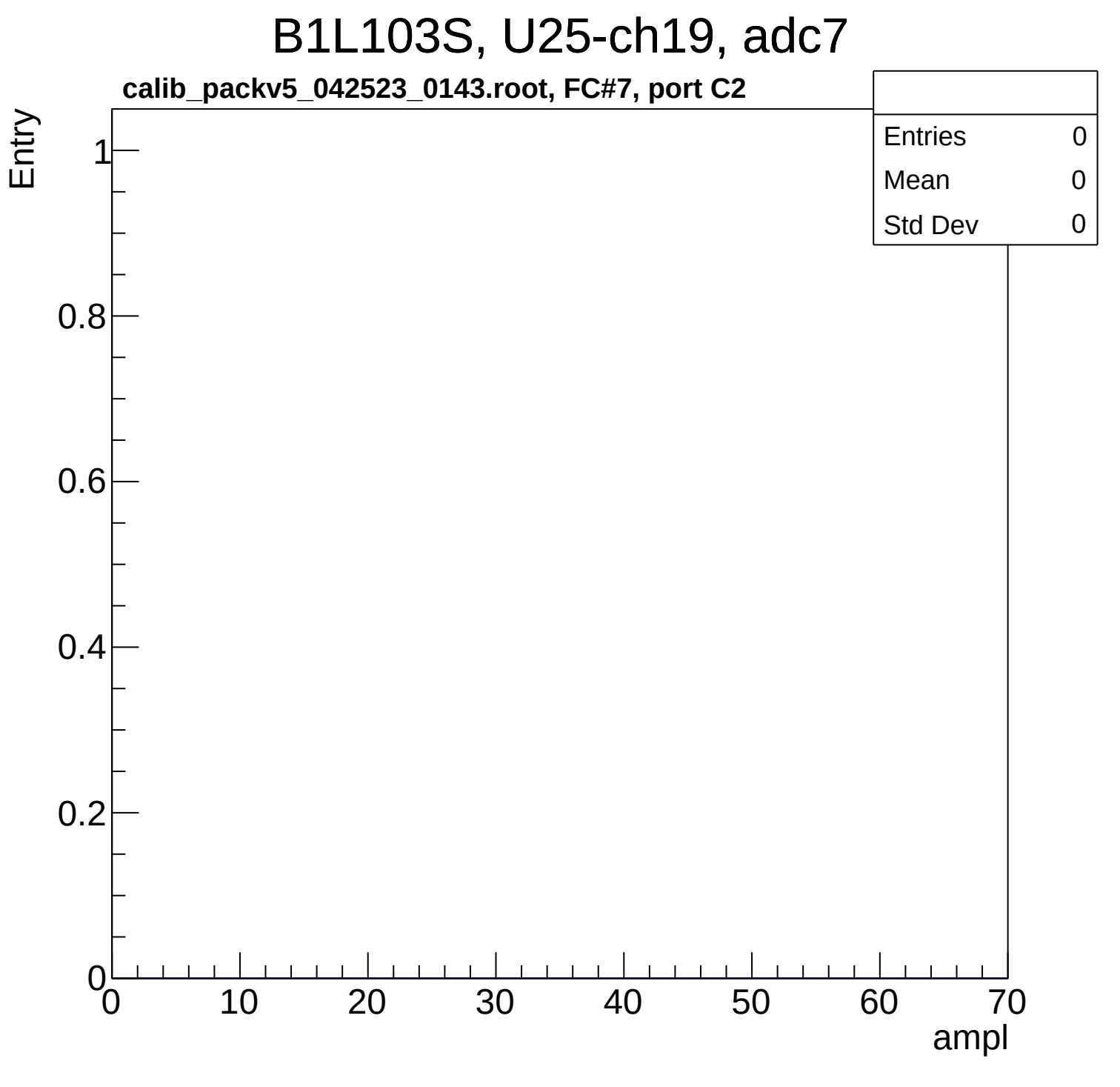
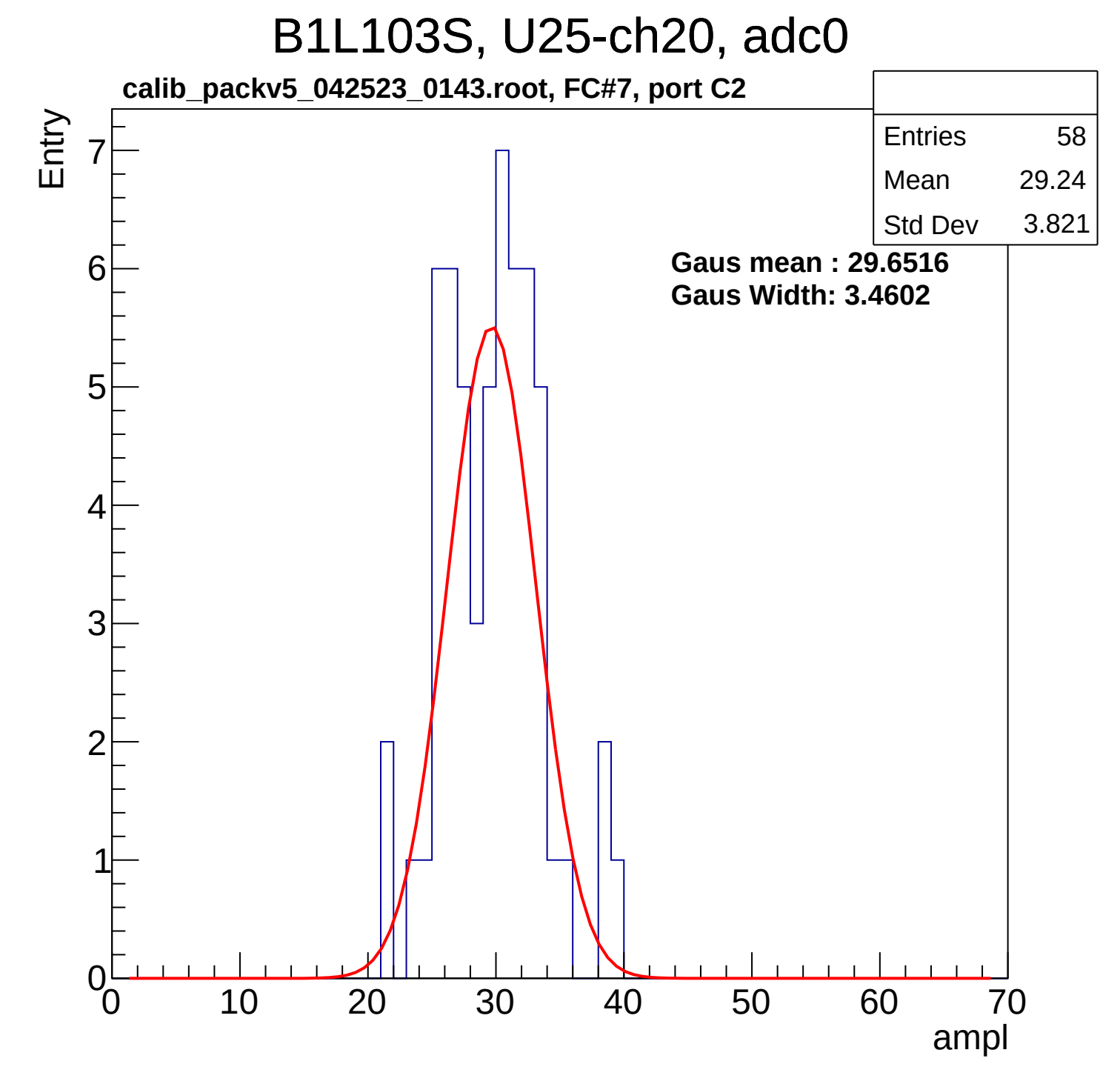

B1L103S, U25-ch19, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch20, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 29.24
Std Dev 3.821
Gaus mean : 29.6516
Gaus Width: 3.4602
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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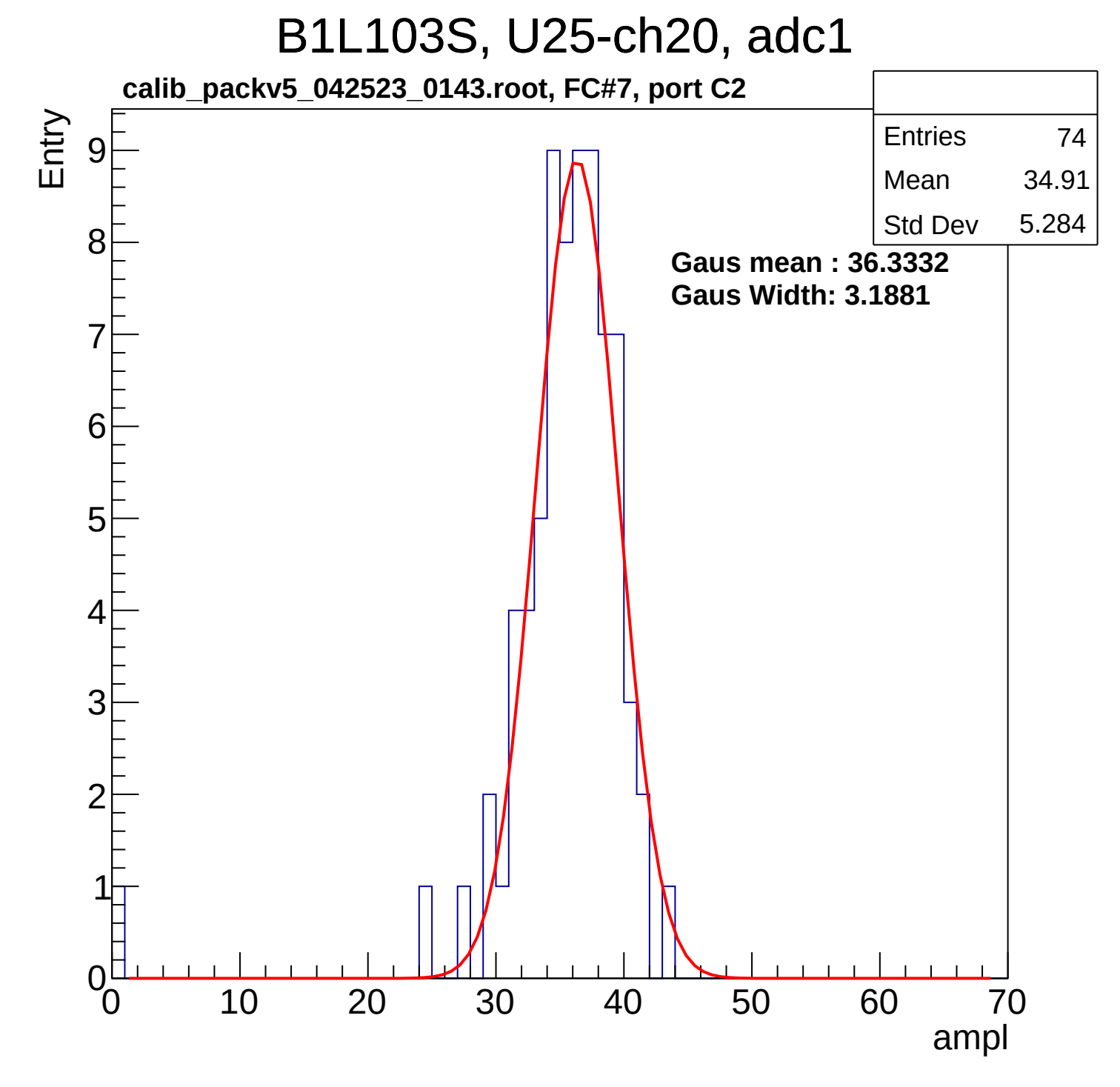
B1L103S, U25-ch20, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 34.91
Std Dev 5.284
Gaus mean : 36.3332
Gaus Width: 3.1881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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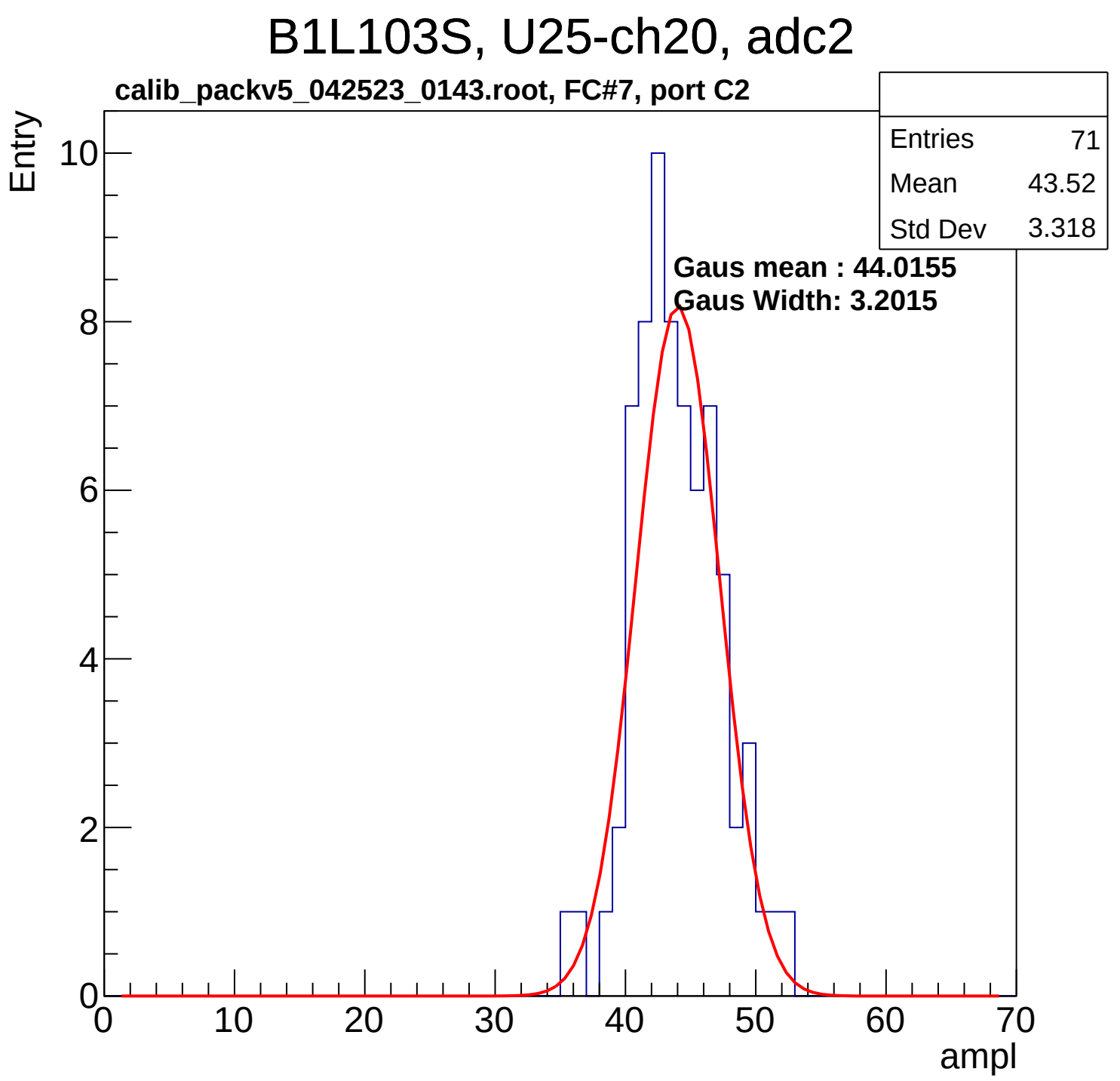
B1L103S, U25-ch20, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 43.52
Std Dev 3.318
Gaus mean : 44.0155
Gaus Width: 3.2015
Entry
ampl
0
2
4
6
8
10
0
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40
50
60
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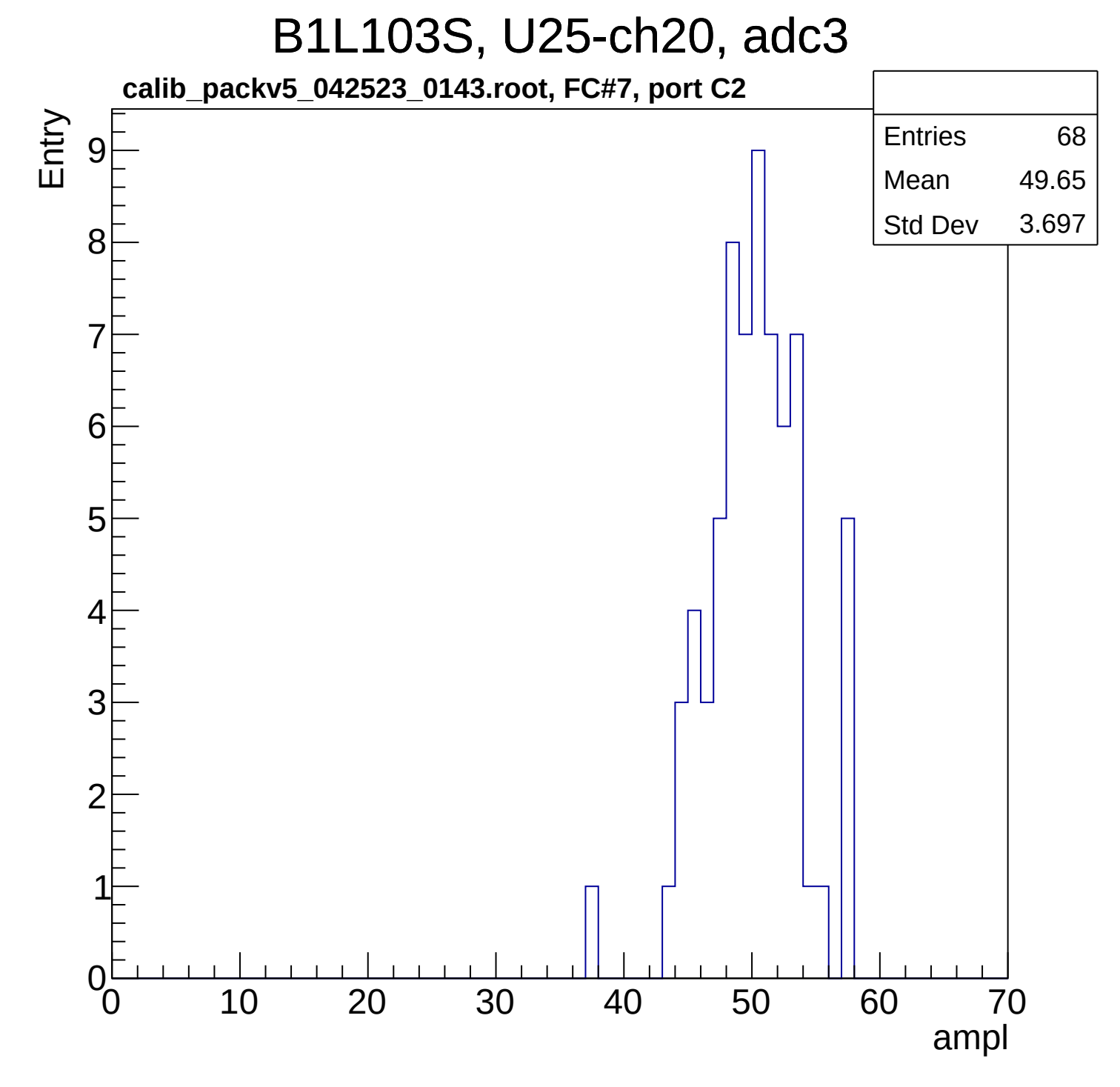
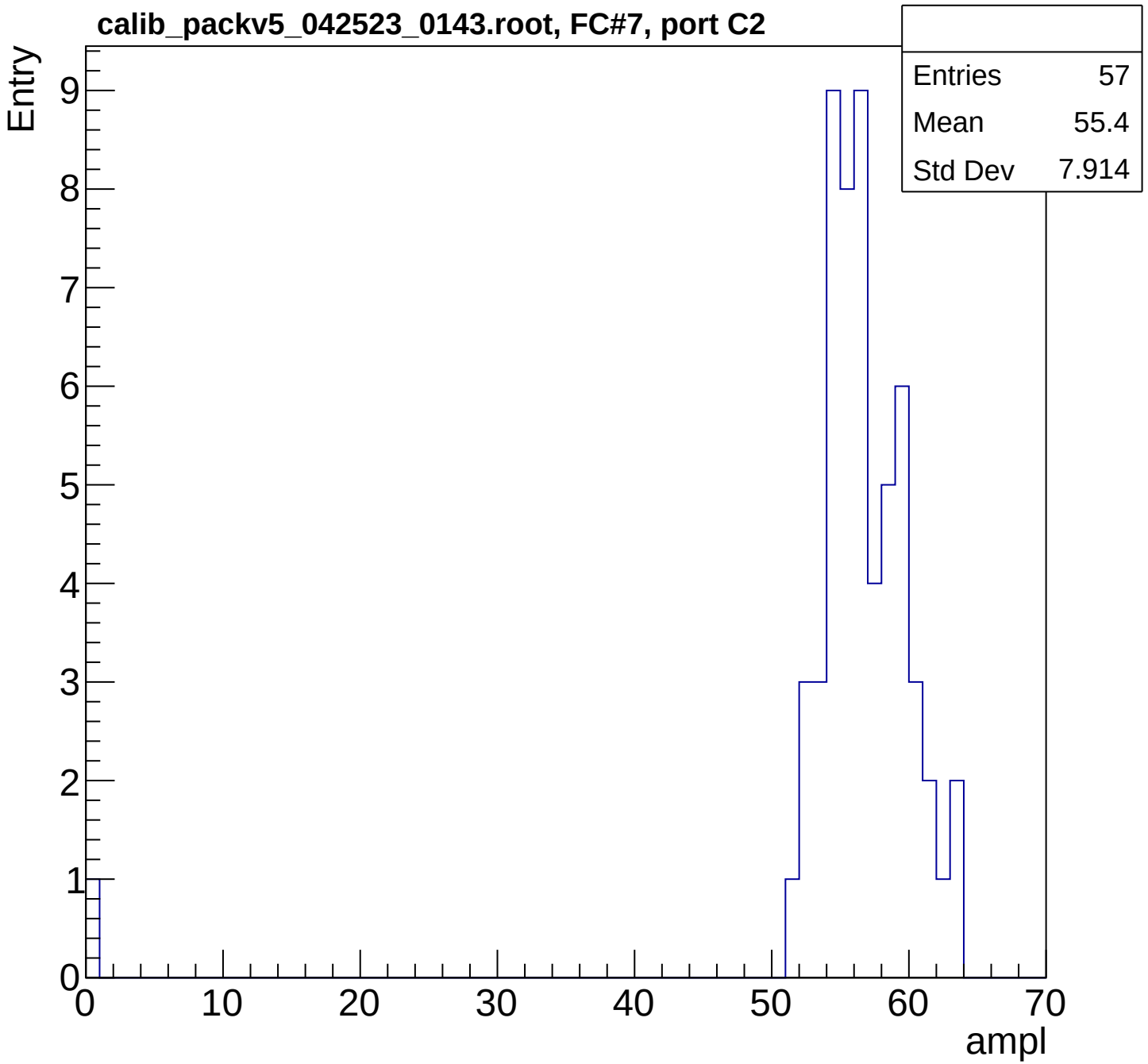
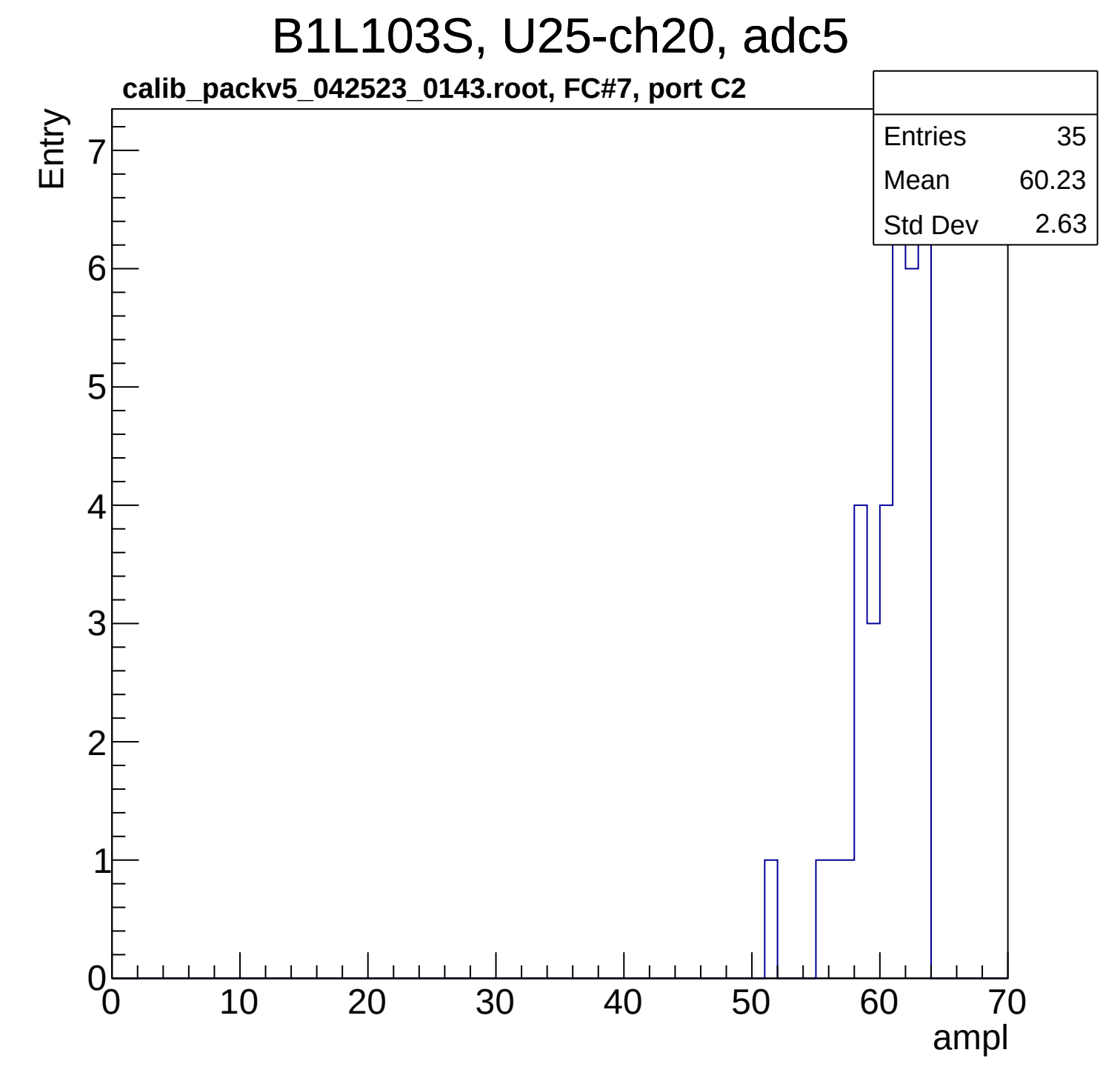
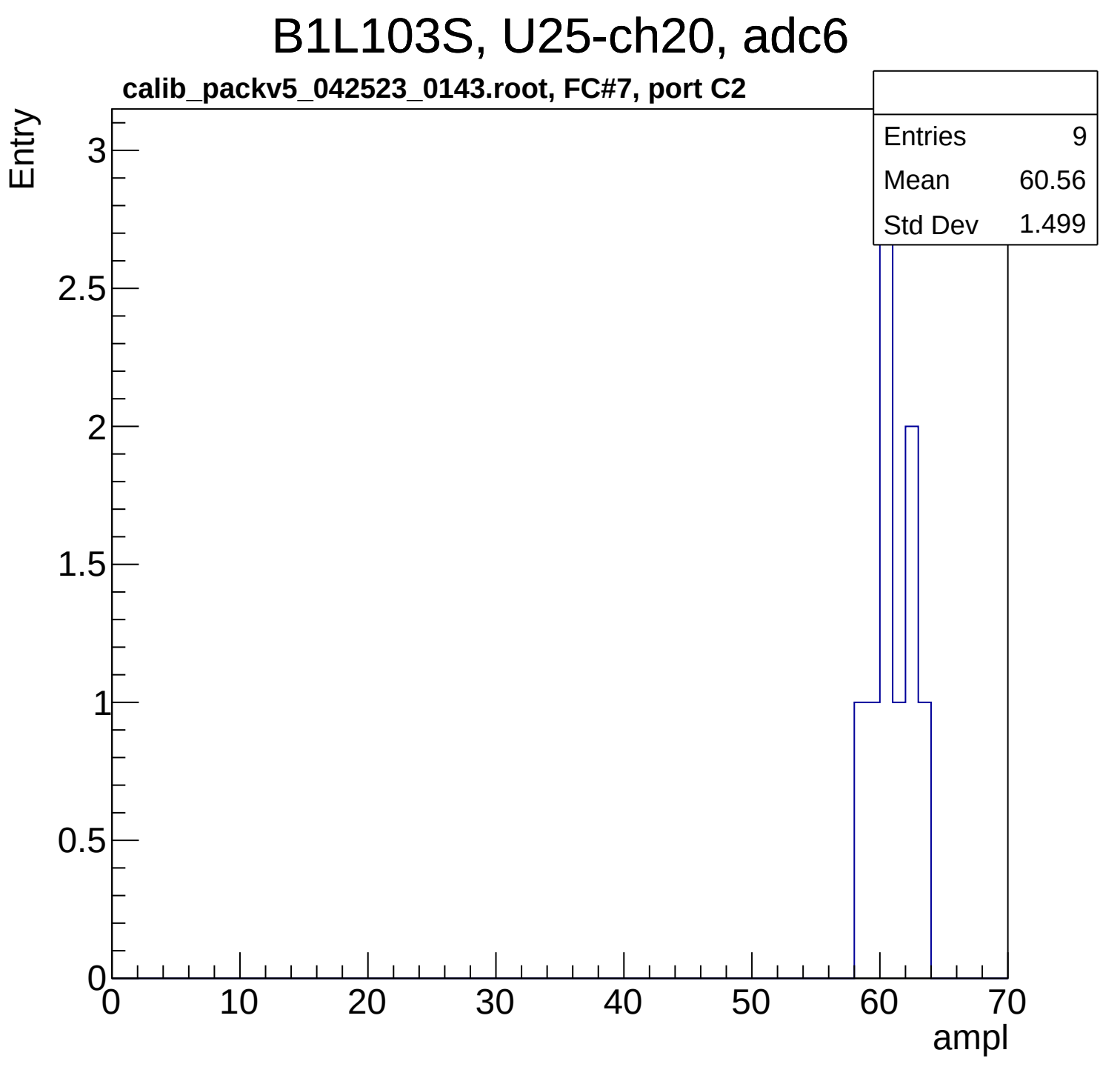

B1L103S, U25-ch20, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 49.65
Std Dev 3.697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch20, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.4
Std Dev 7.914
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch20, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 60.23
Std Dev 2.63
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch20, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 60.56
Std Dev 1.499
Entry
ampl
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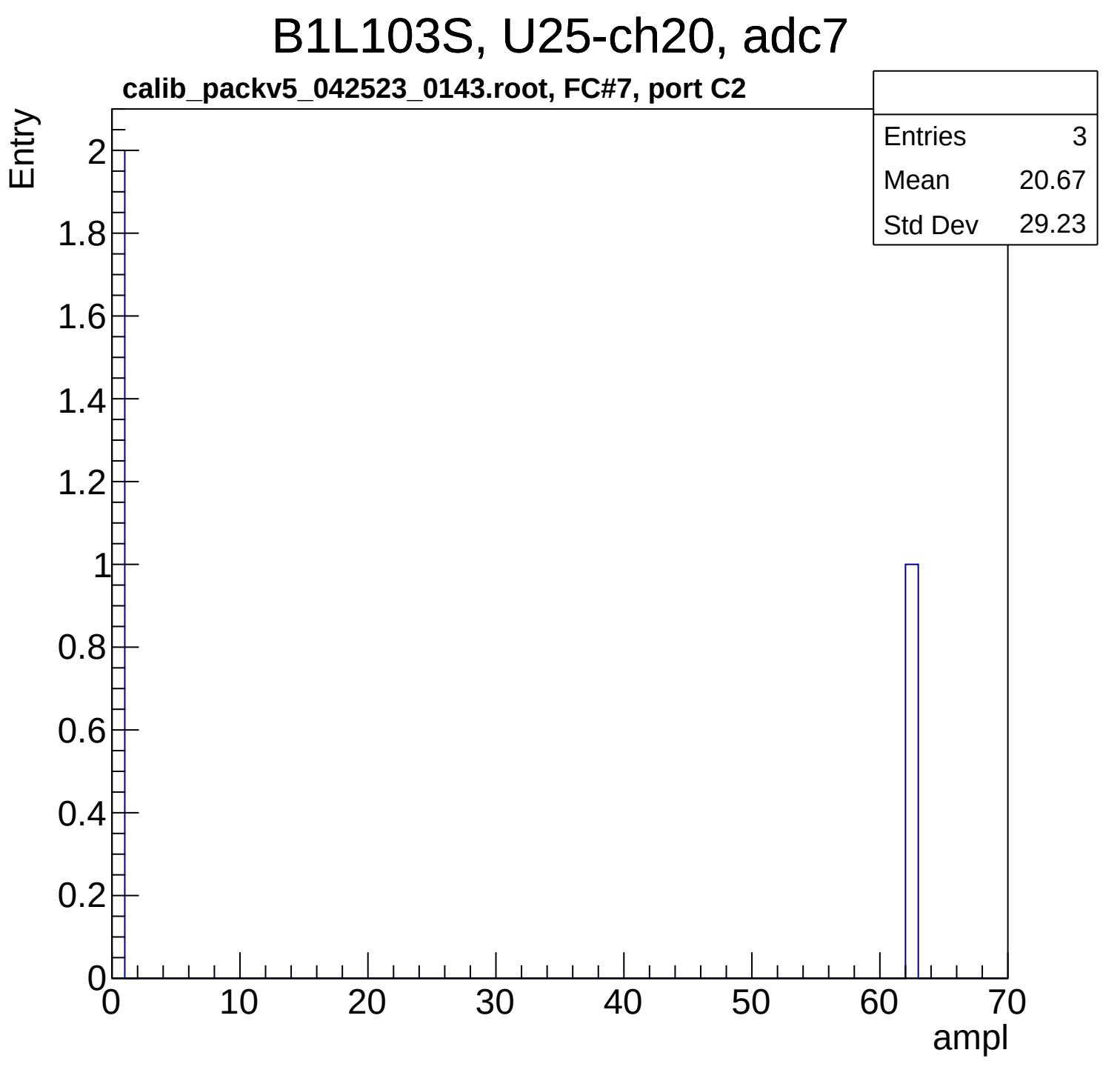
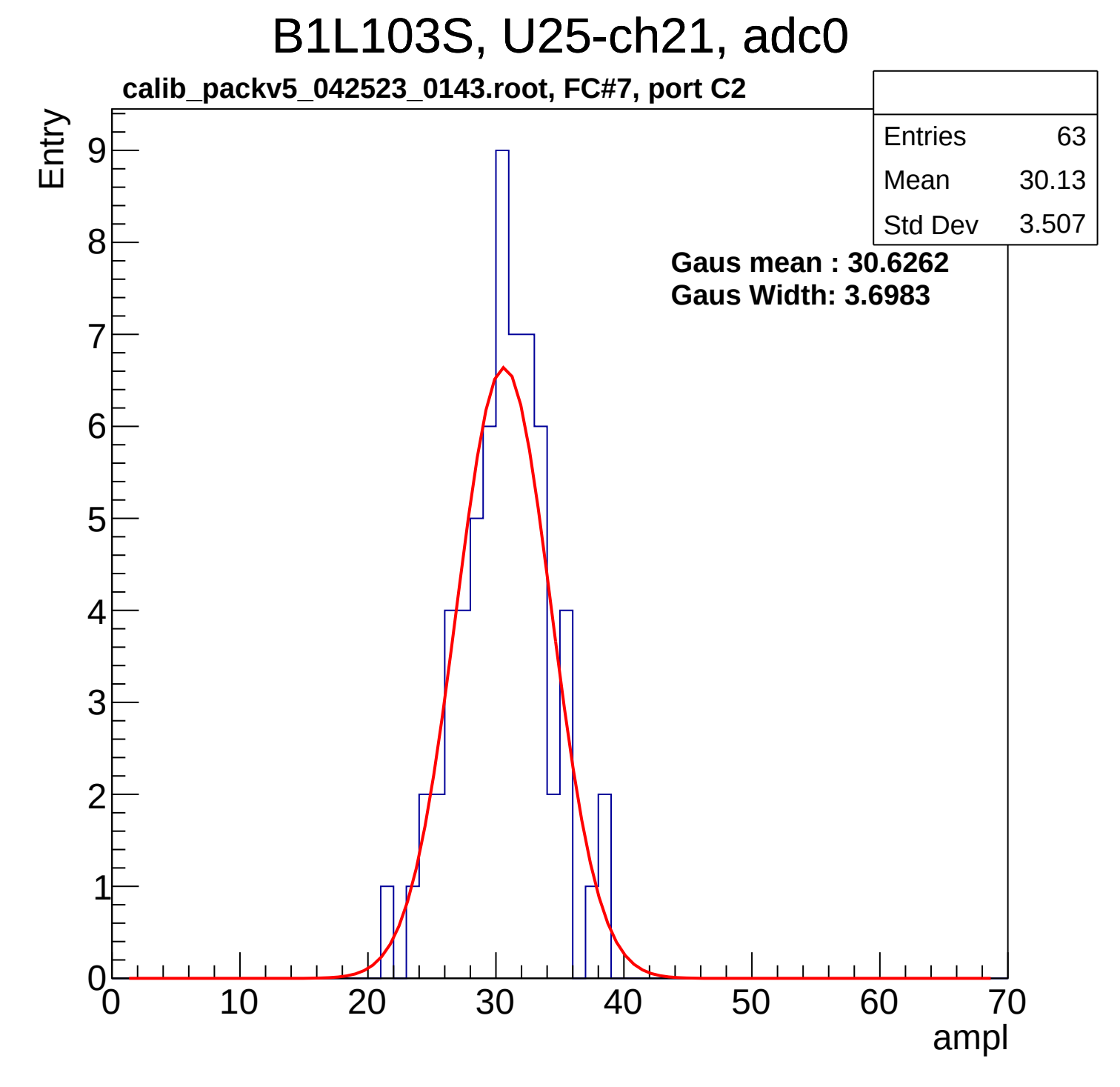
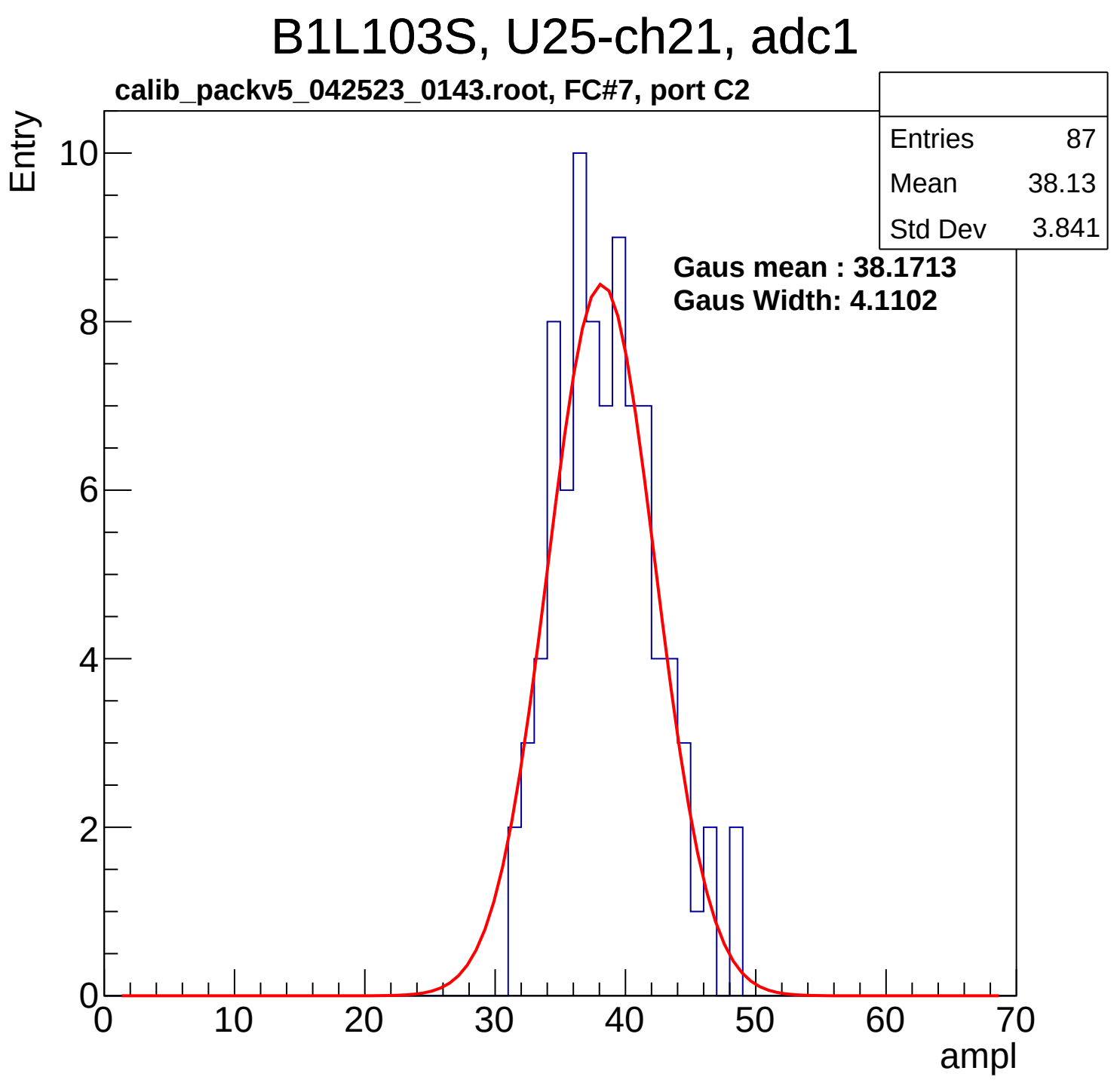
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch20, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch21, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 30.13
Std Dev 3.507
Gaus mean : 30.6262
Gaus Width: 3.6983
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch21, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 38.13
Std Dev 3.841
Gaus mean : 38.1713
Gaus Width: 4.1102
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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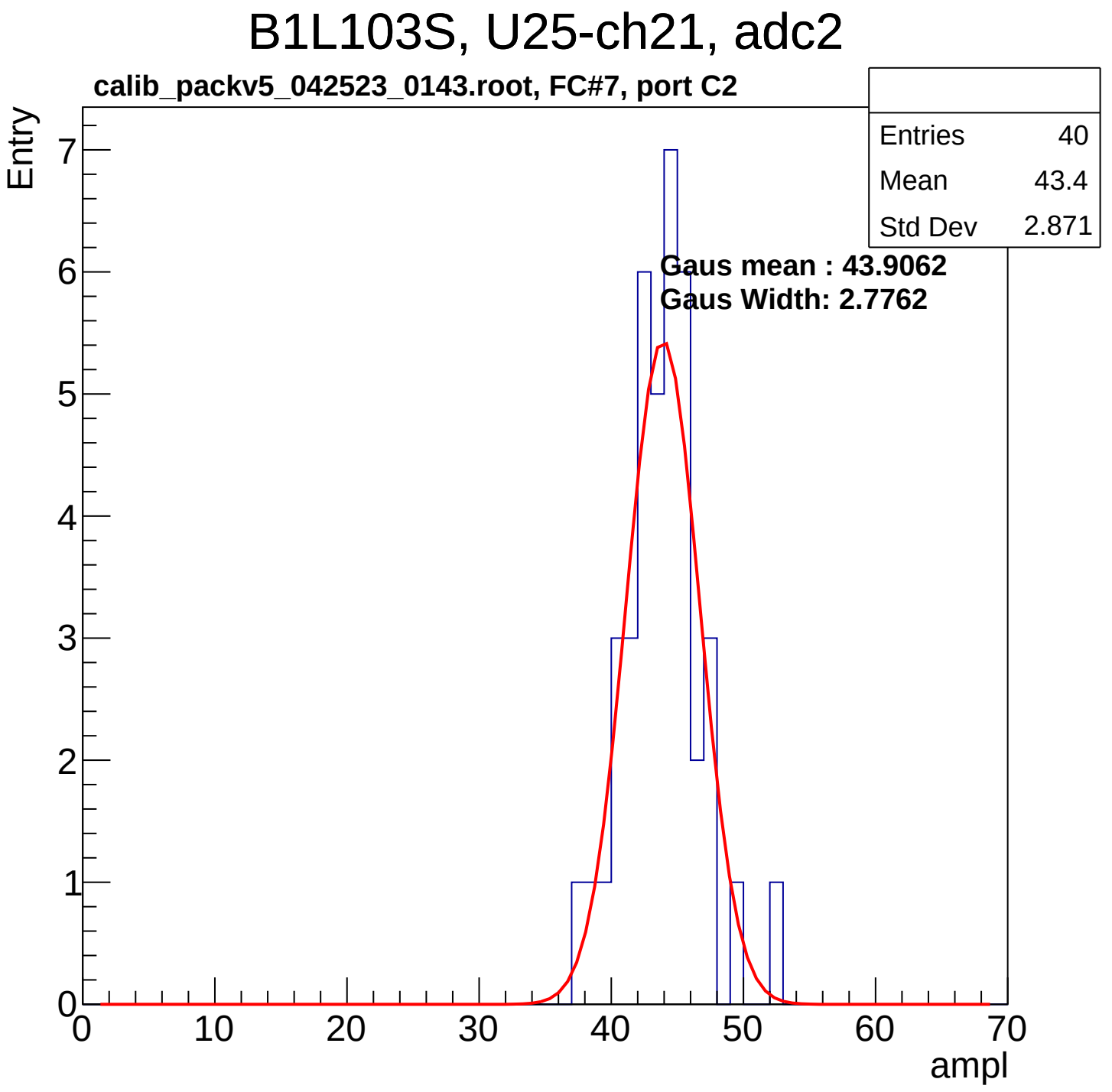
B1L103S, U25-ch21, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 43.4
Std Dev 2.871
Gaus mean : 43.9062
Gaus Width: 2.7762
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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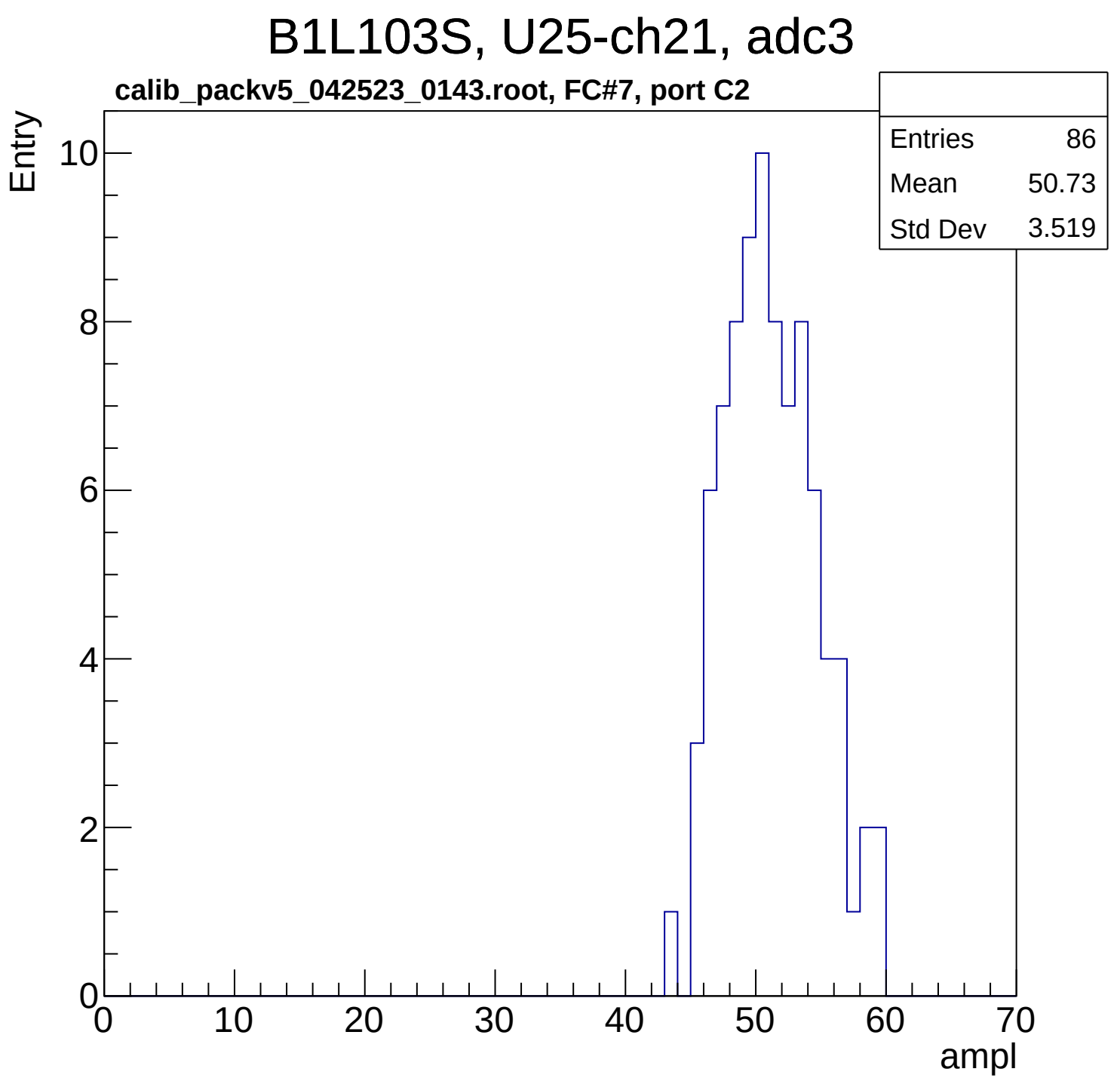
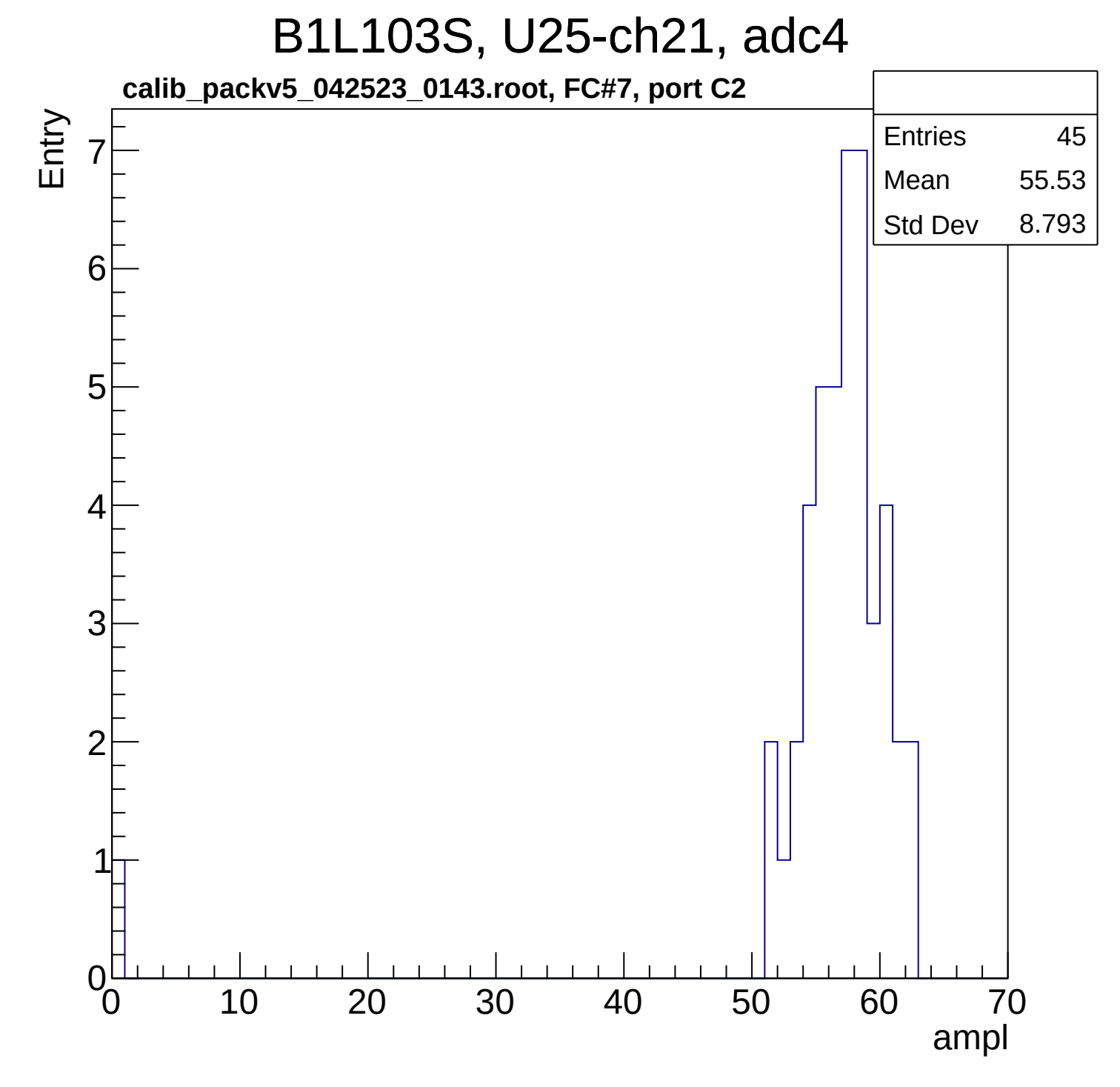

B1L103S, U25-ch21, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 50.73
Std Dev 3.519
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch21, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 55.53
Std Dev 8.793
Entry
ampl
0
1
2
3
4
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6
7
0
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20
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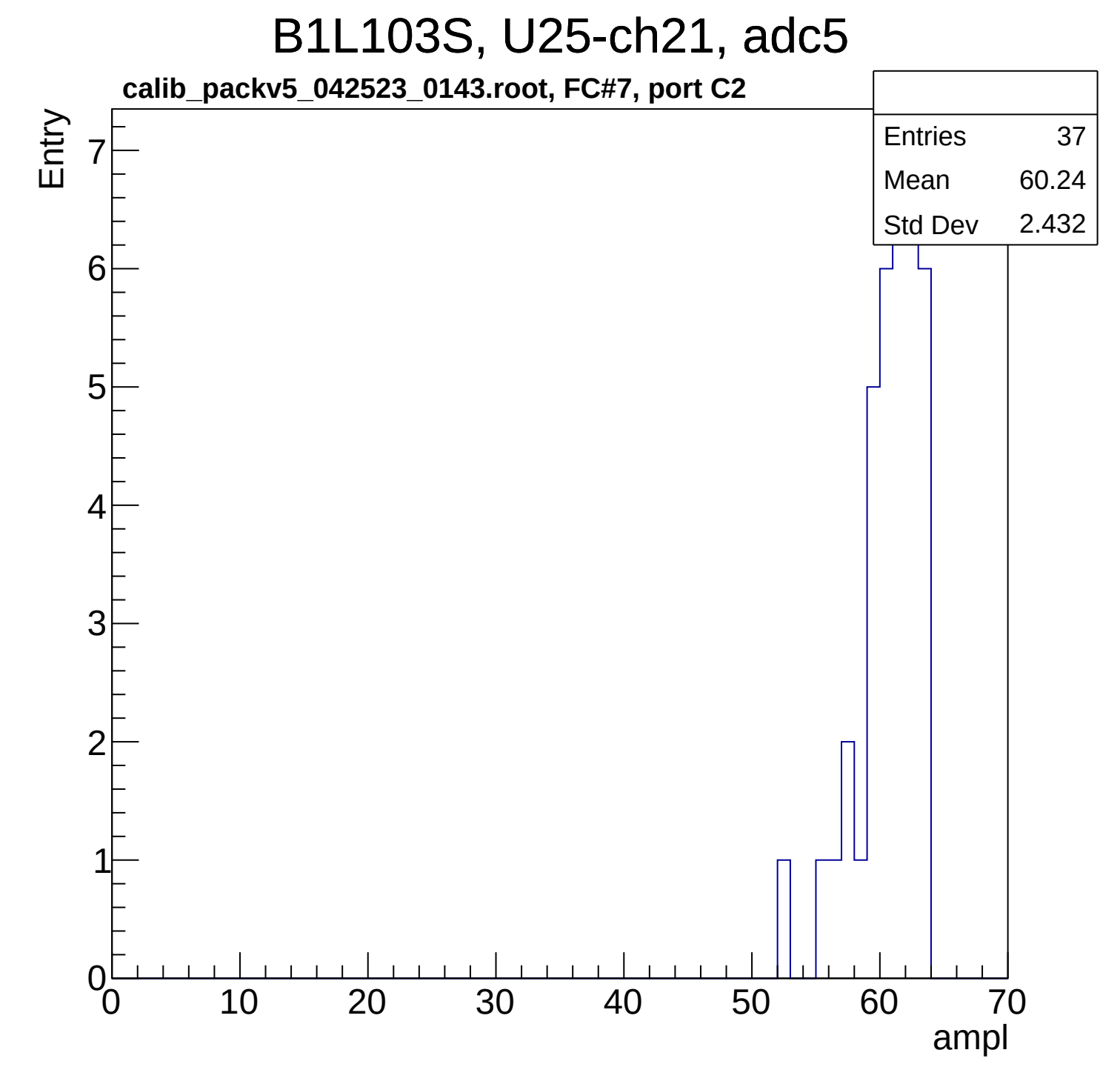
40
50
60
70


B1L103S, U25-ch21, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 37
Mean 60.24
Std Dev 2.432
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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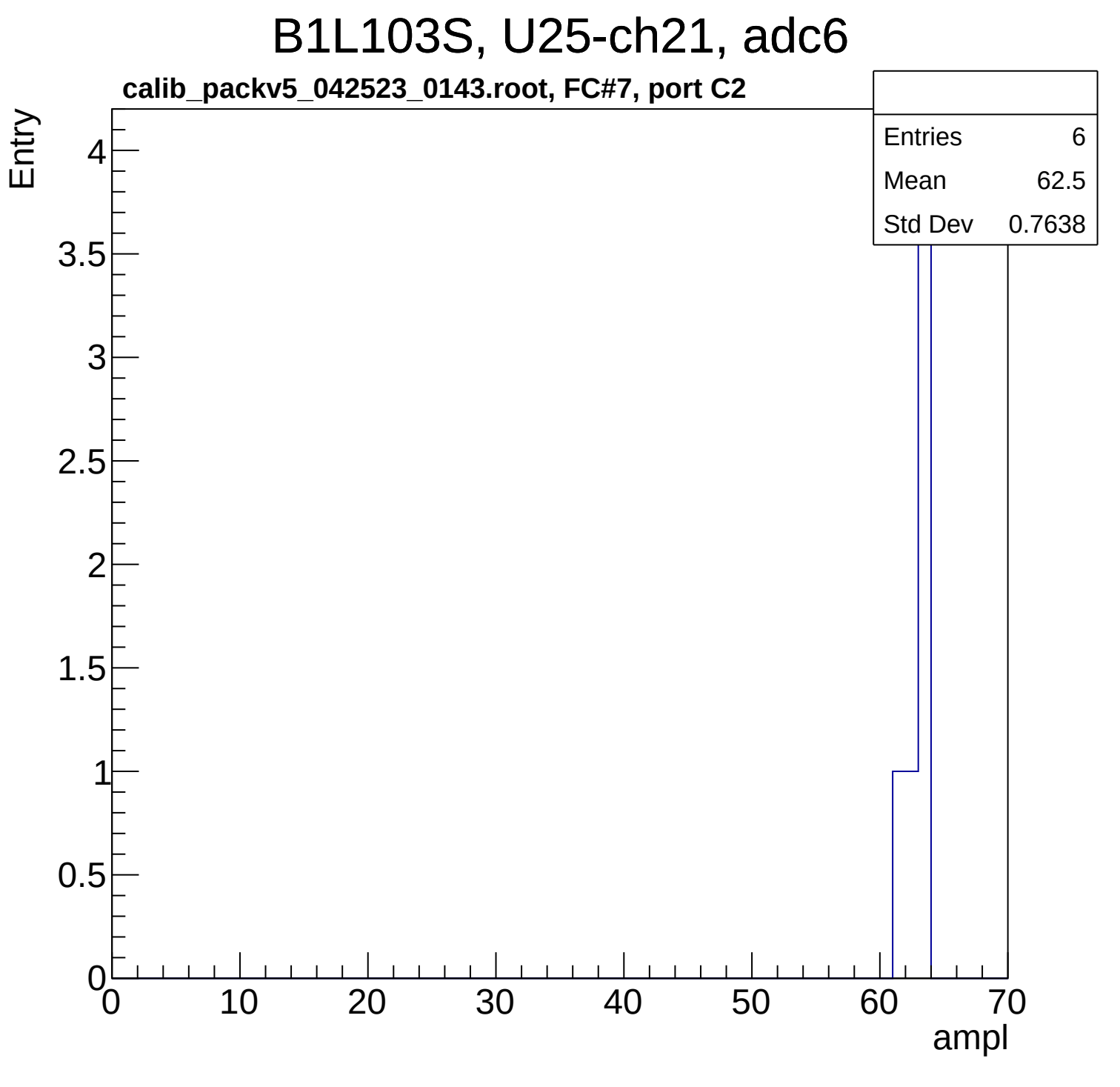
B1L103S, U25-ch21, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
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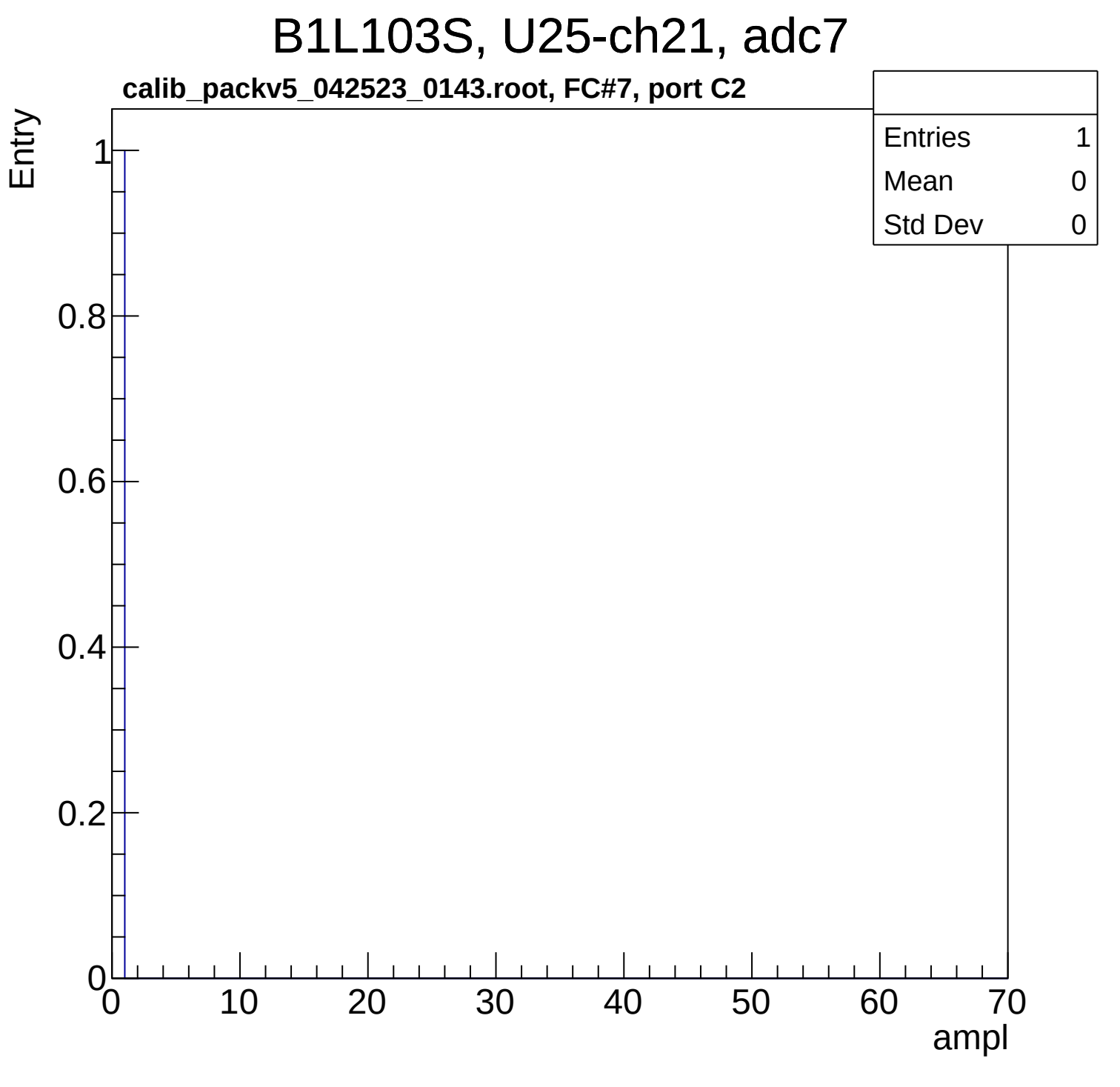
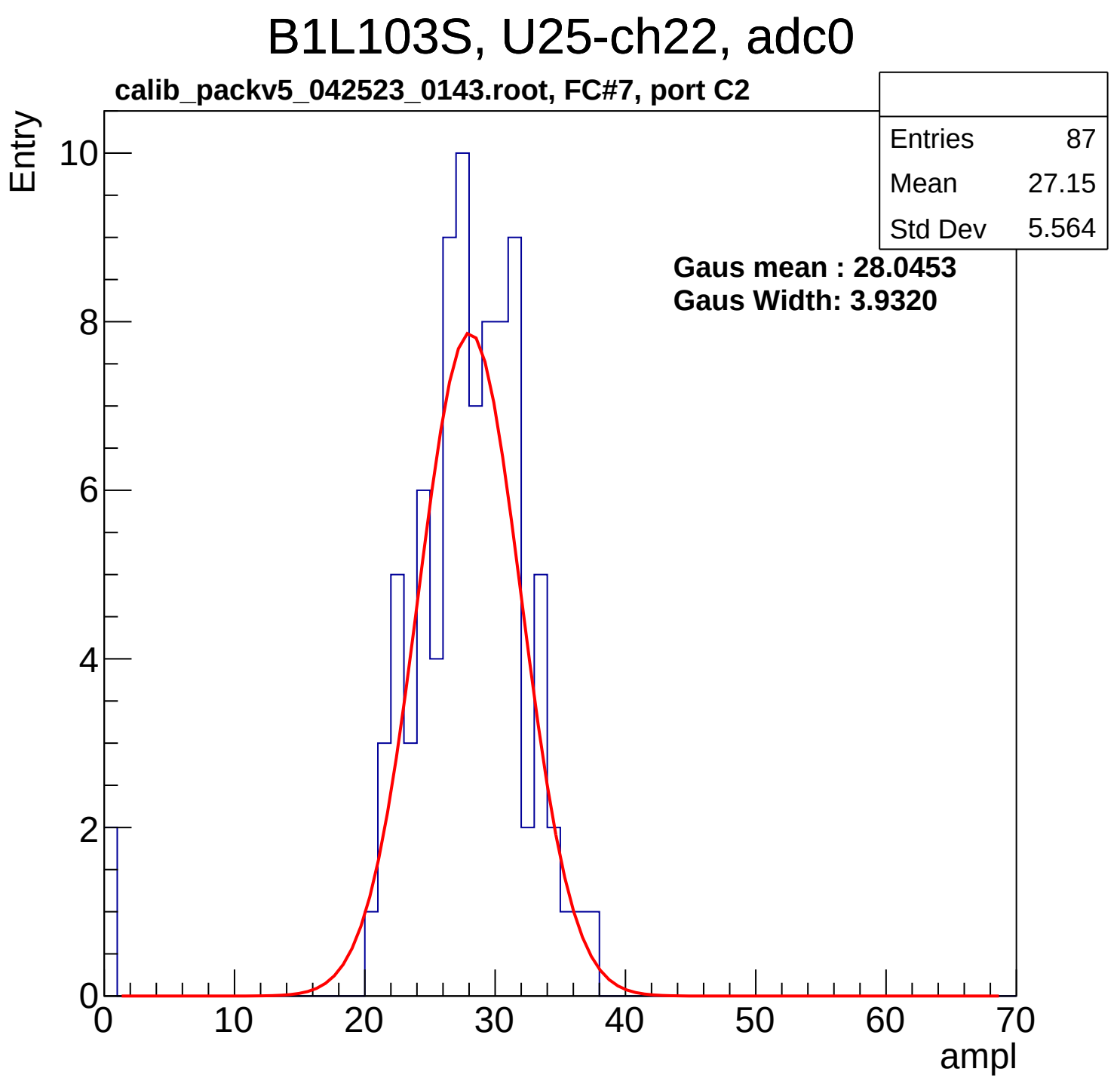
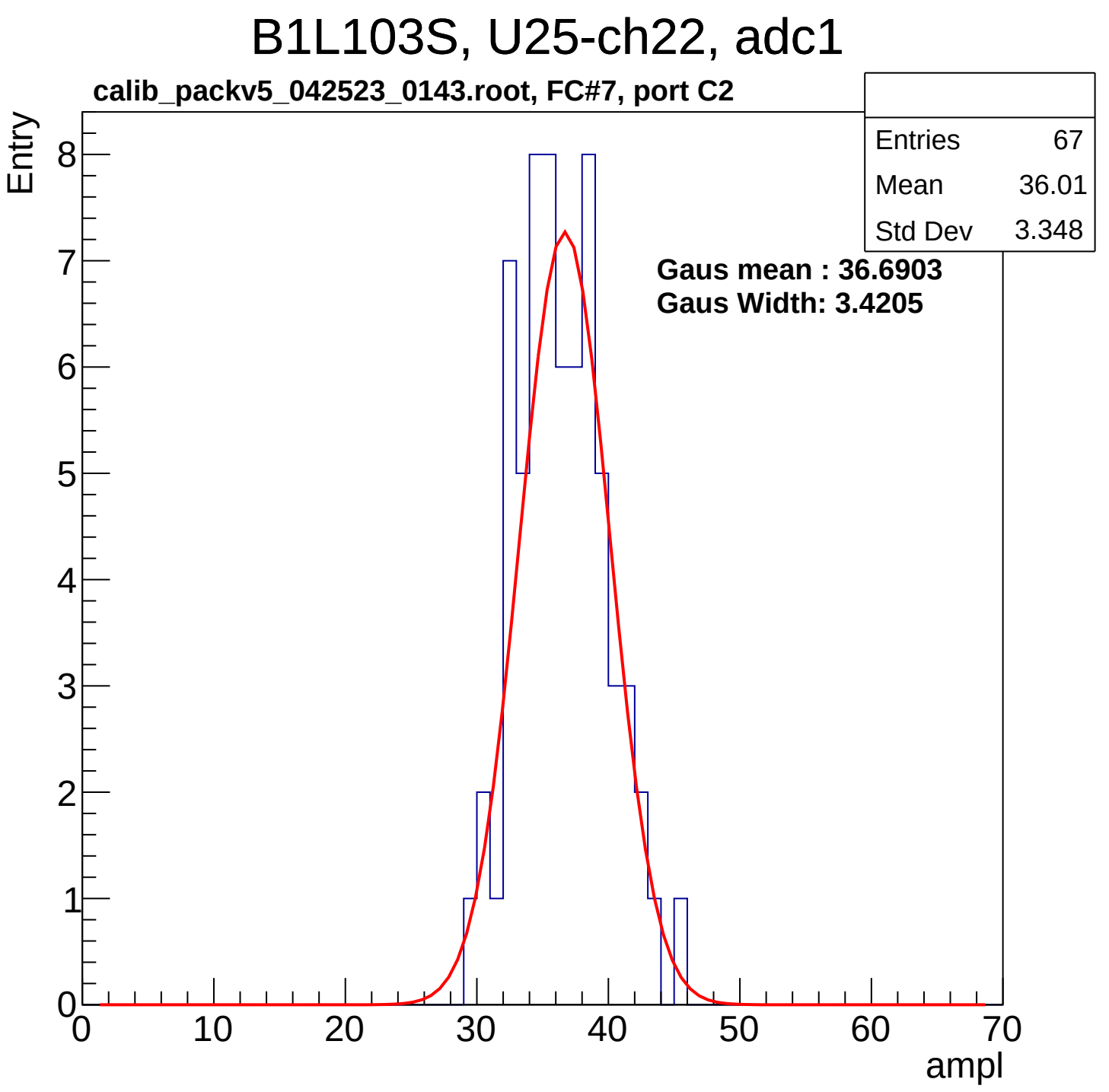
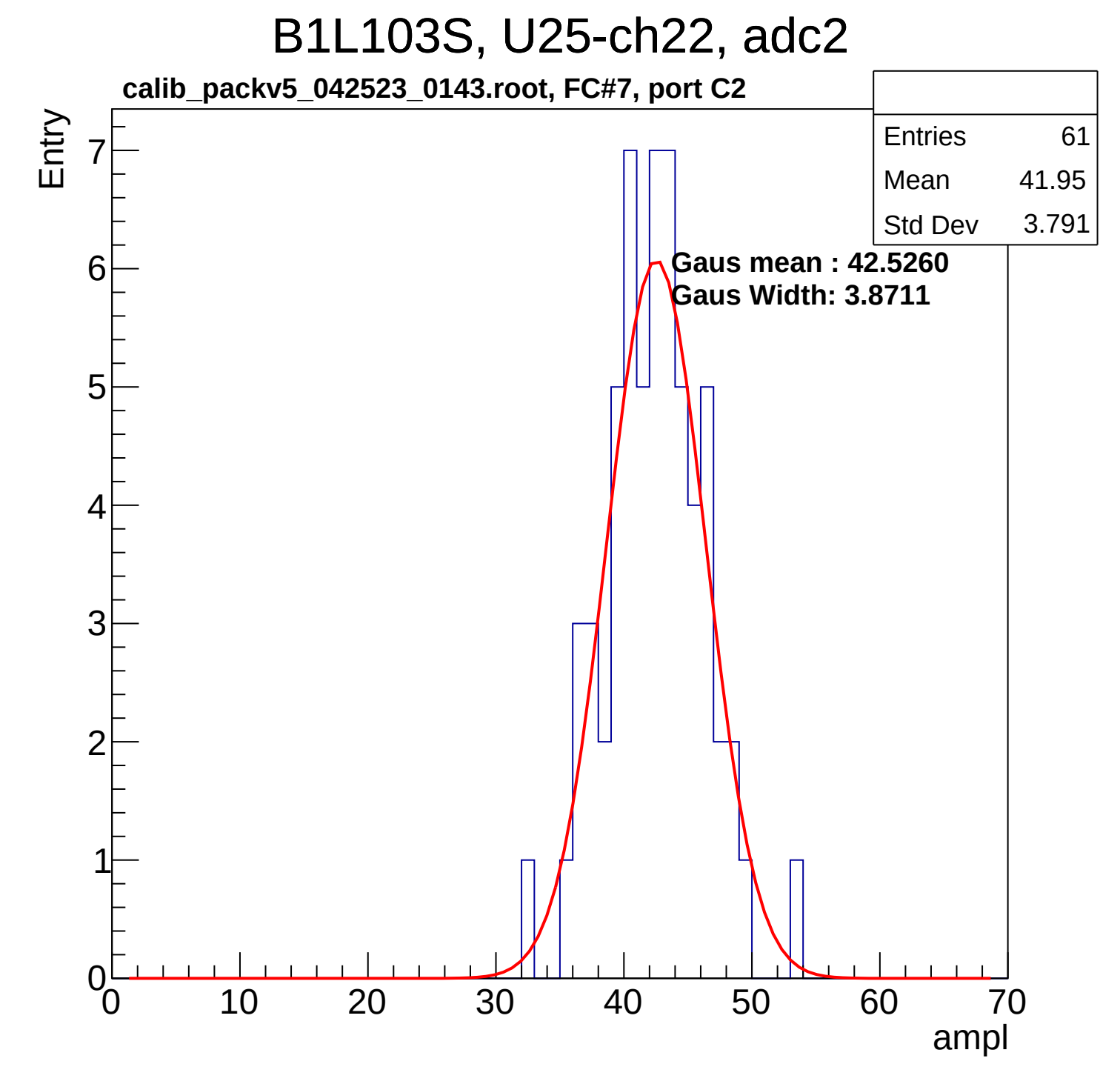
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U25-ch21, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch22, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 27.15
Std Dev 5.564
Gaus mean : 28.0453
Gaus Width: 3.9320
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch22, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.01
Std Dev 3.348
Gaus mean : 36.6903
Gaus Width: 3.4205
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch22, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.95
Std Dev 3.791
Gaus mean : 42.5260
Gaus Width: 3.8711
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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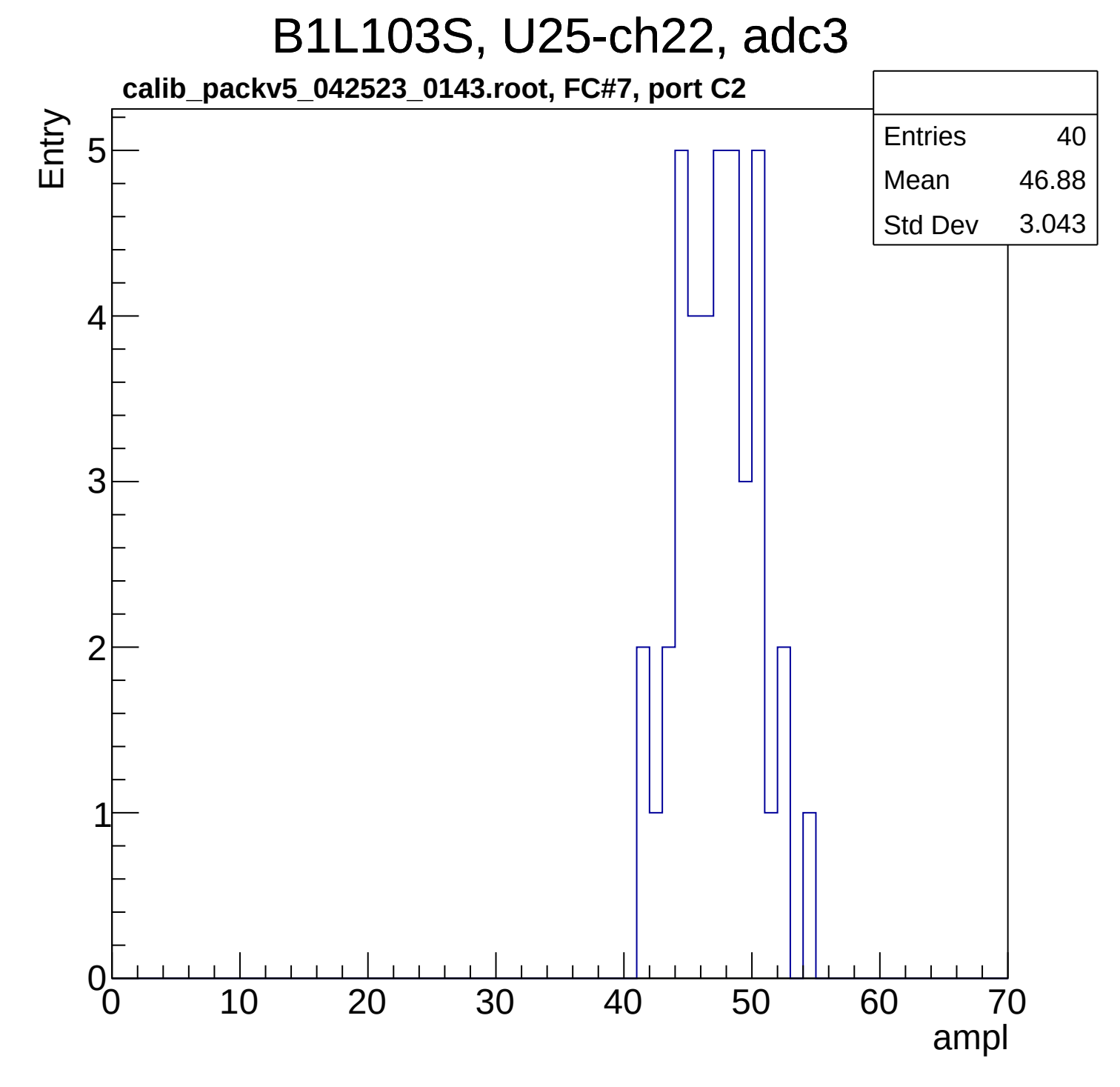
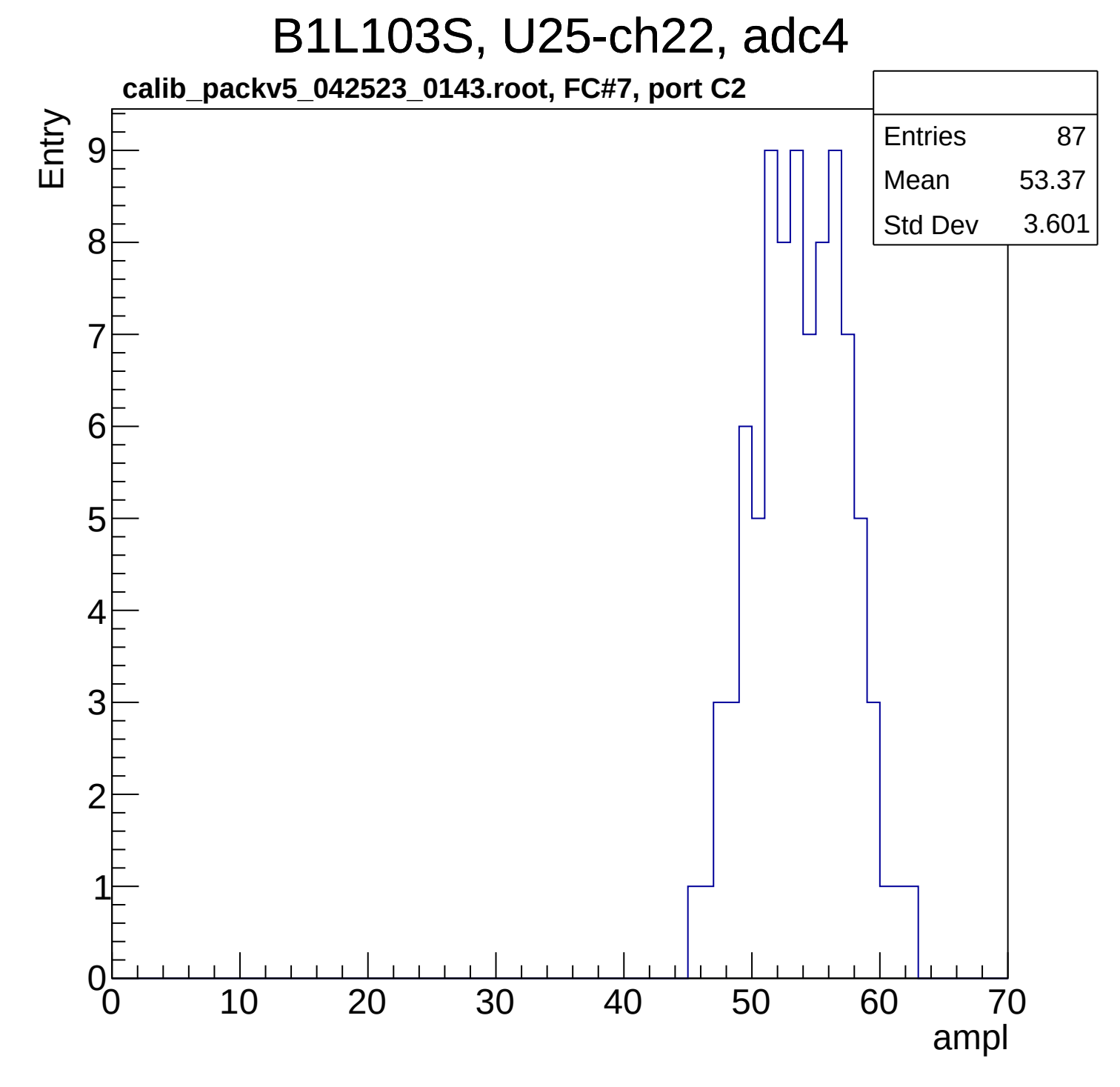

B1L103S, U25-ch22, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 46.88
Std Dev 3.043
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch22, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 53.37
Std Dev 3.601
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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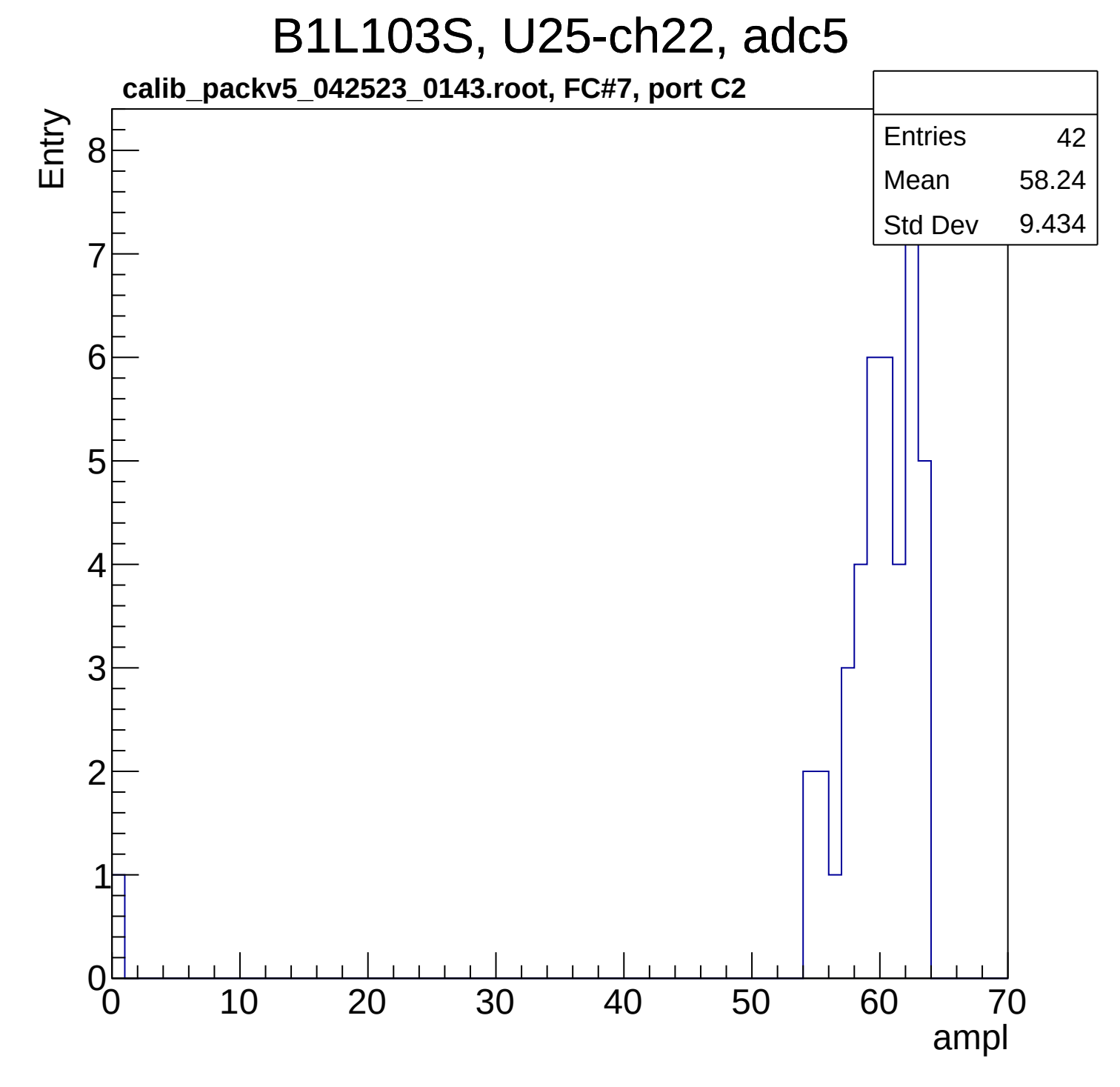
B1L103S, U25-ch22, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.24
Std Dev 9.434
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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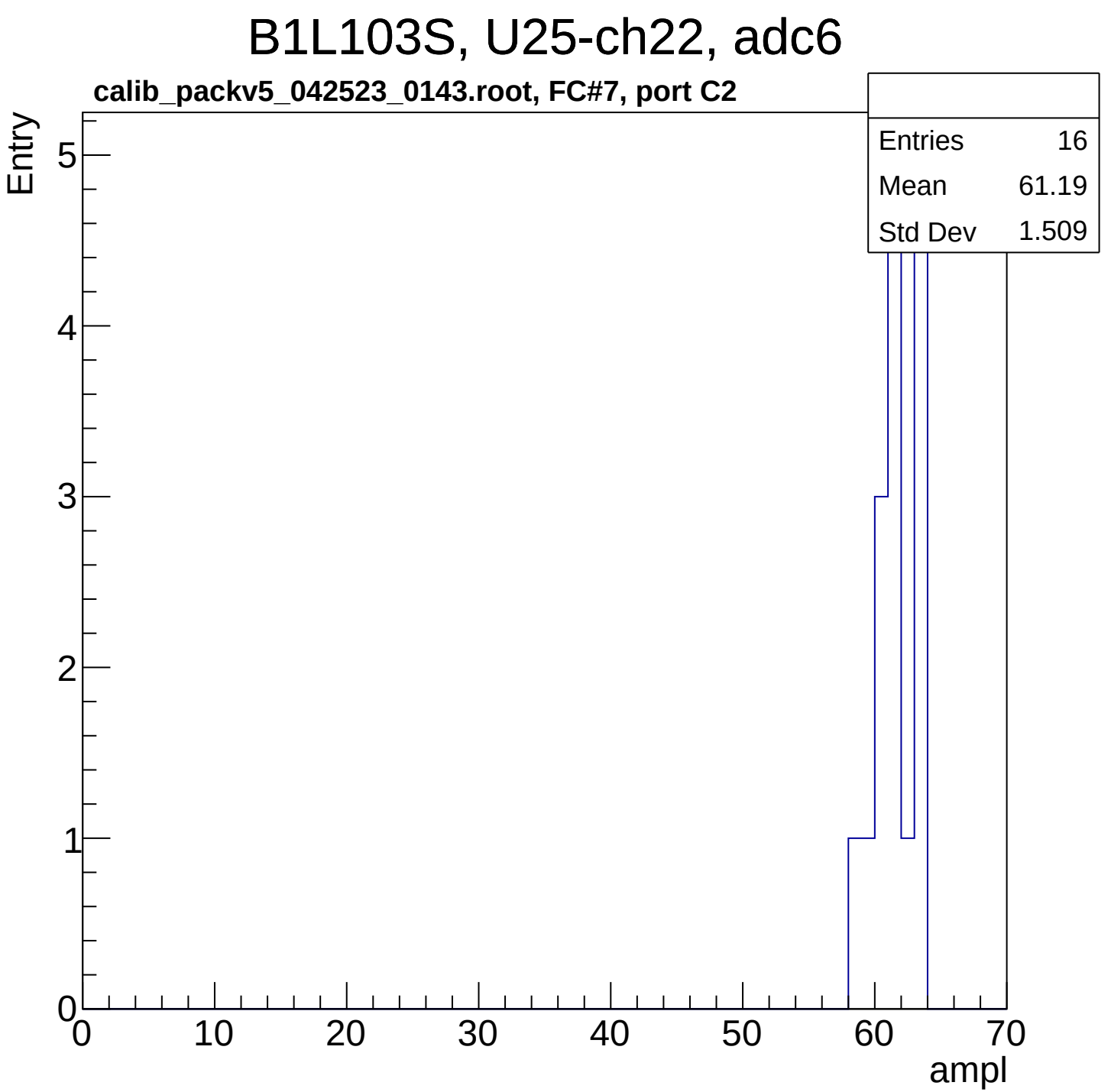
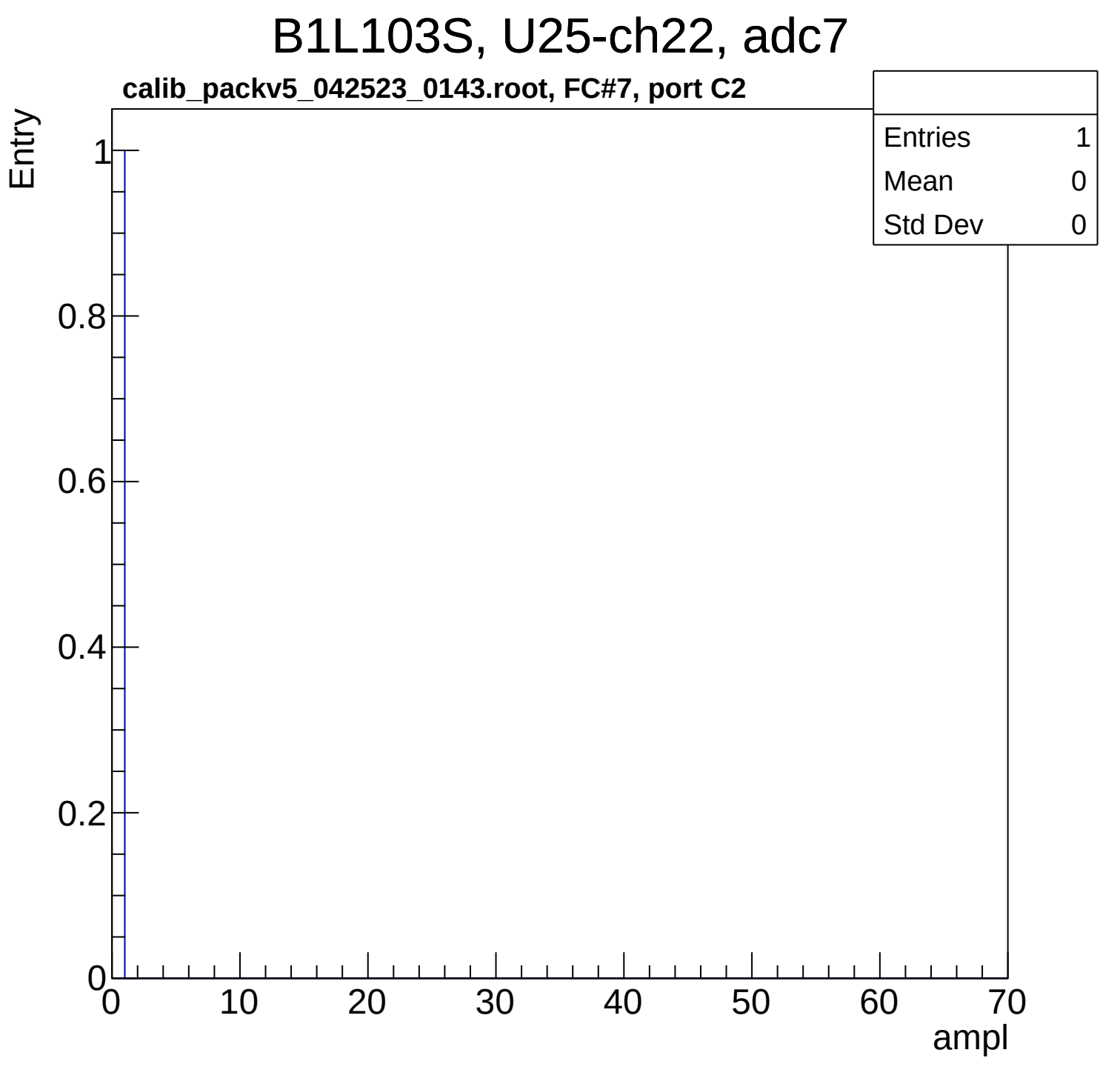

B1L103S, U25-ch22, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.19
Std Dev 1.509
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch22, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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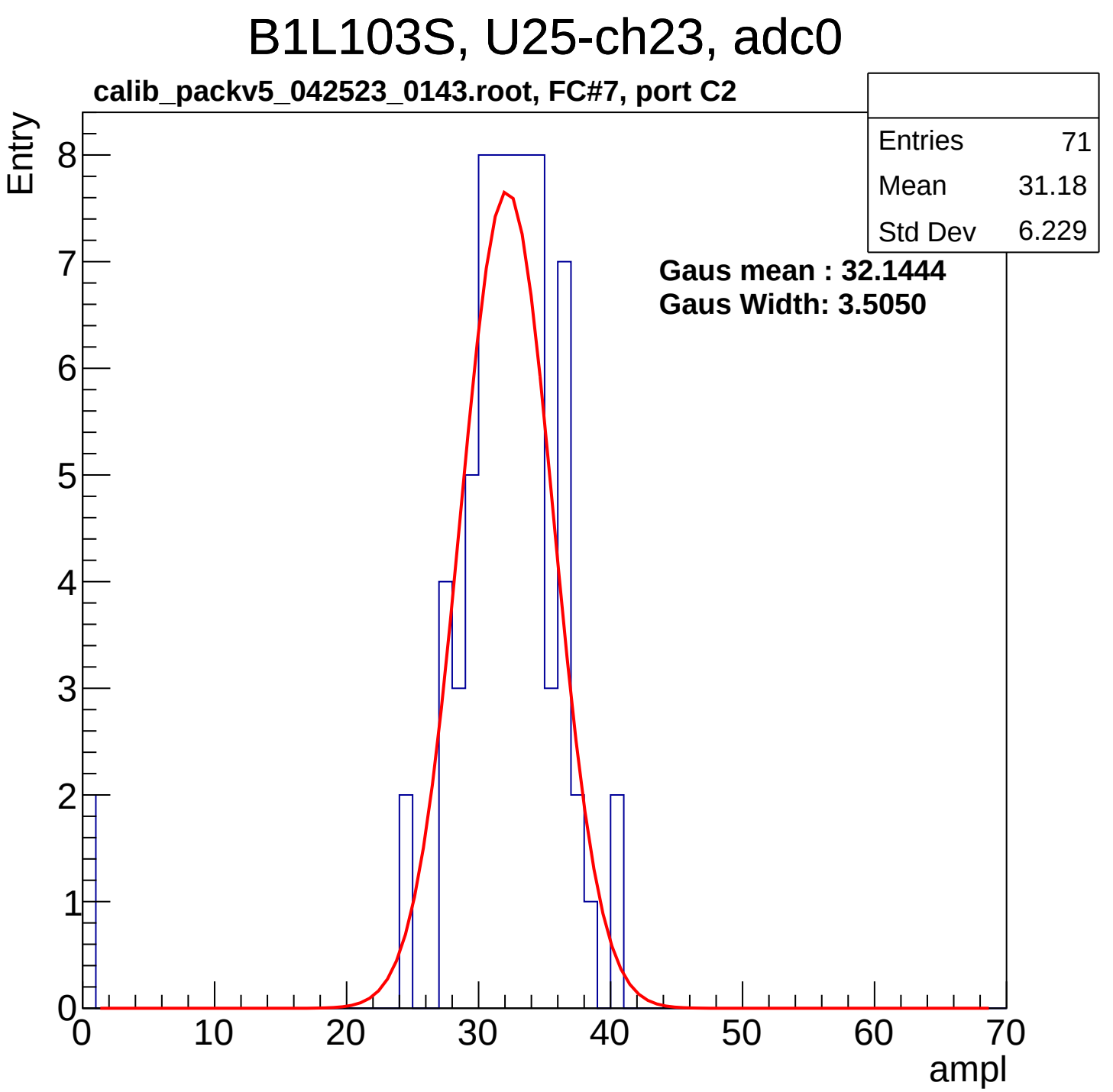
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch23, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 31.18
Std Dev 6.229
Gaus mean : 32.1444
Gaus Width: 3.5050
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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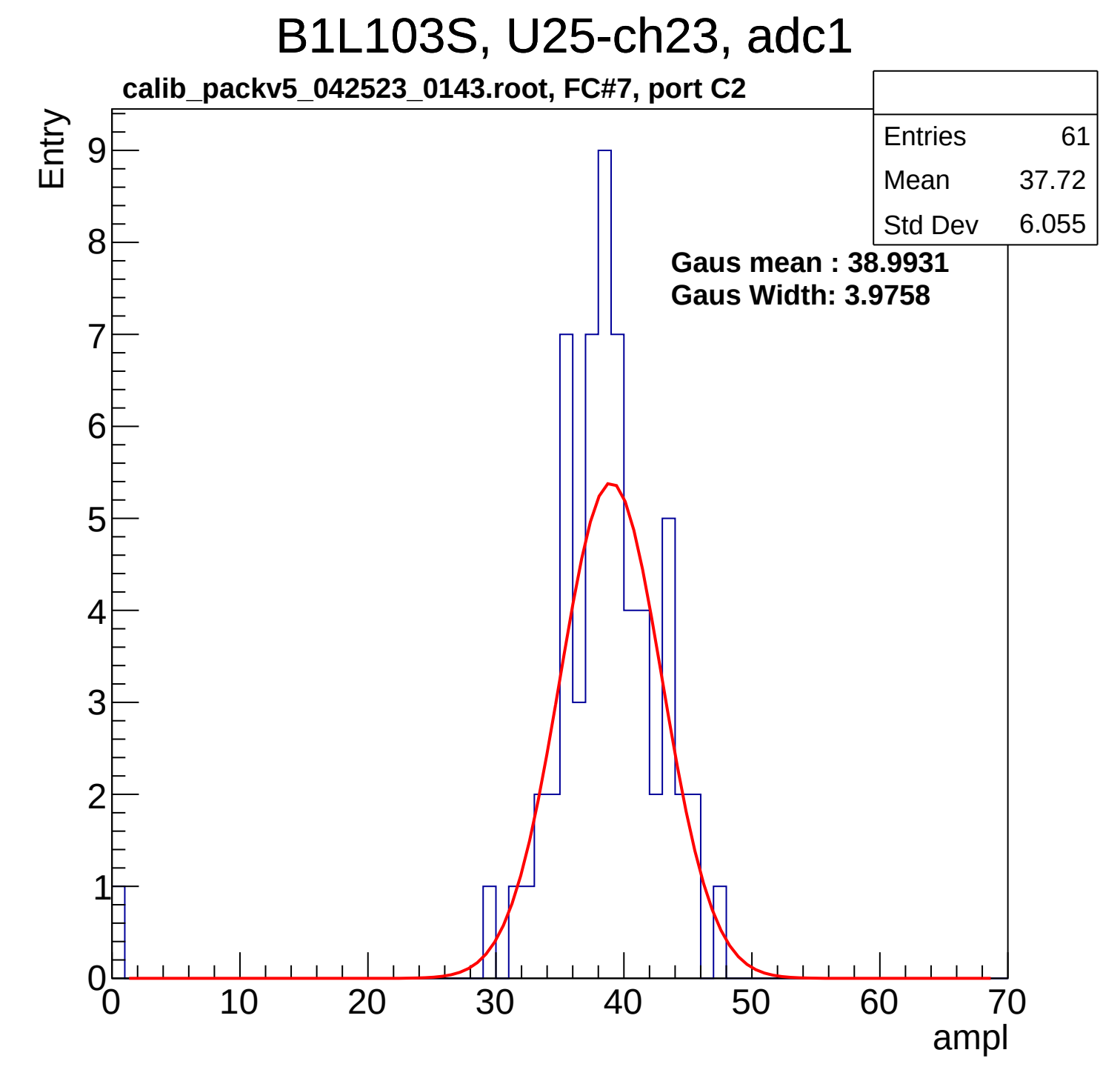
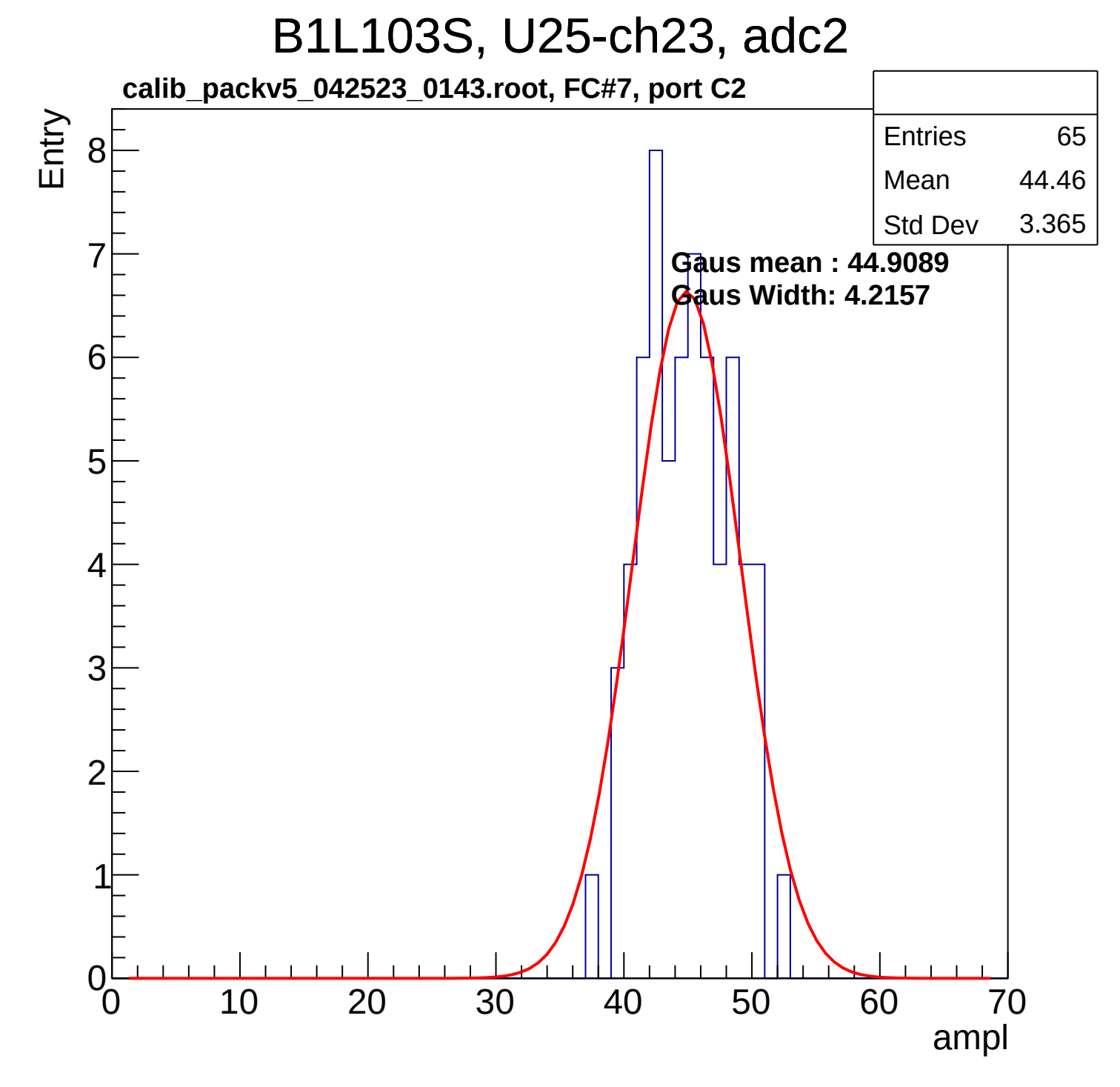
40
50
60
70


B1L103S, U25-ch23, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 37.72
Std Dev 6.055
Gaus mean : 38.9931
Gaus Width: 3.9758
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch23, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 44.46
Std Dev 3.365
Gaus mean : 44.9089
Gaus Width: 4.2157
Entry
ampl
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1
2
3
4
5
6
7
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0
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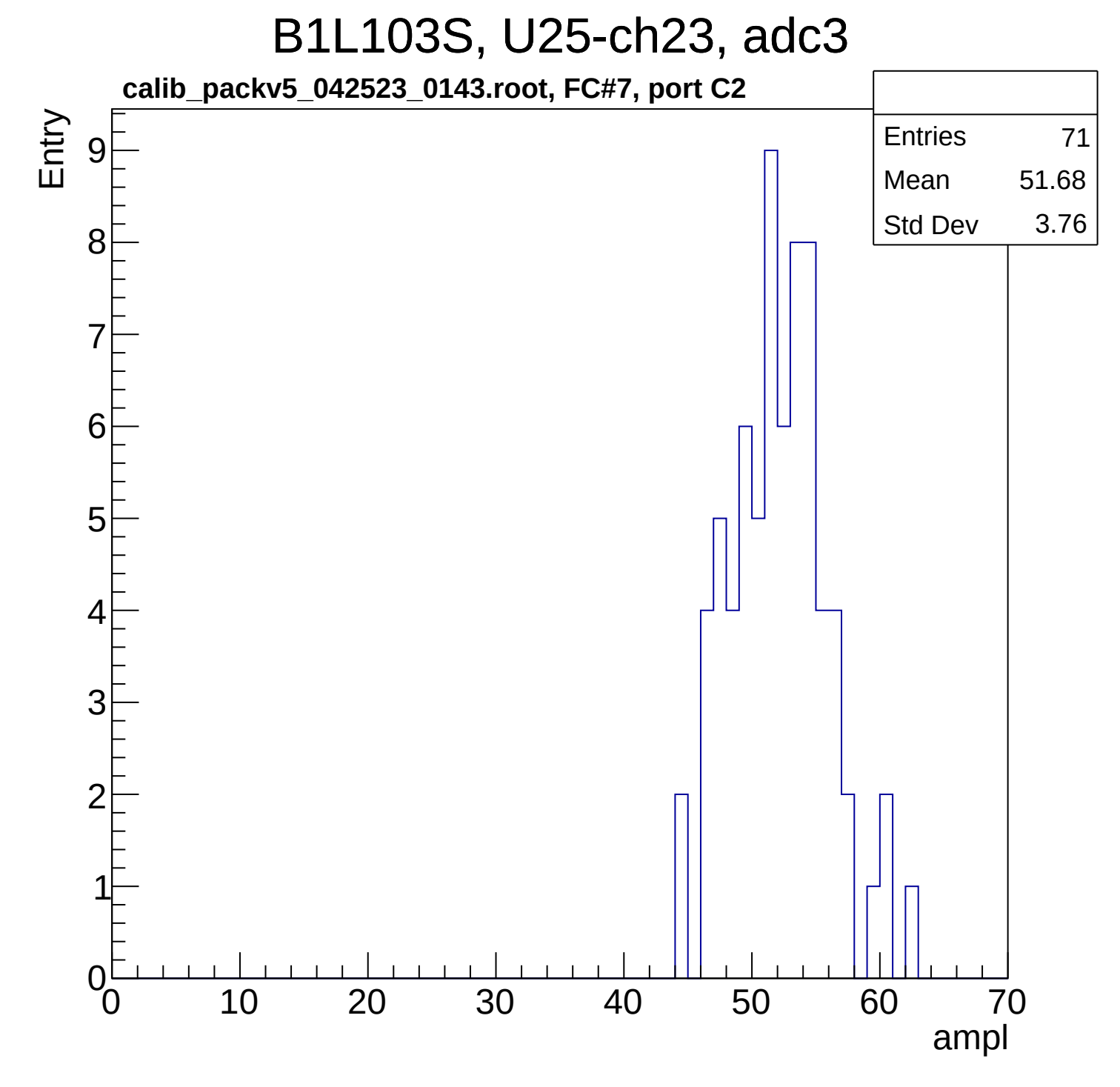
B1L103S, U25-ch23, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 51.68
Std Dev 3.76
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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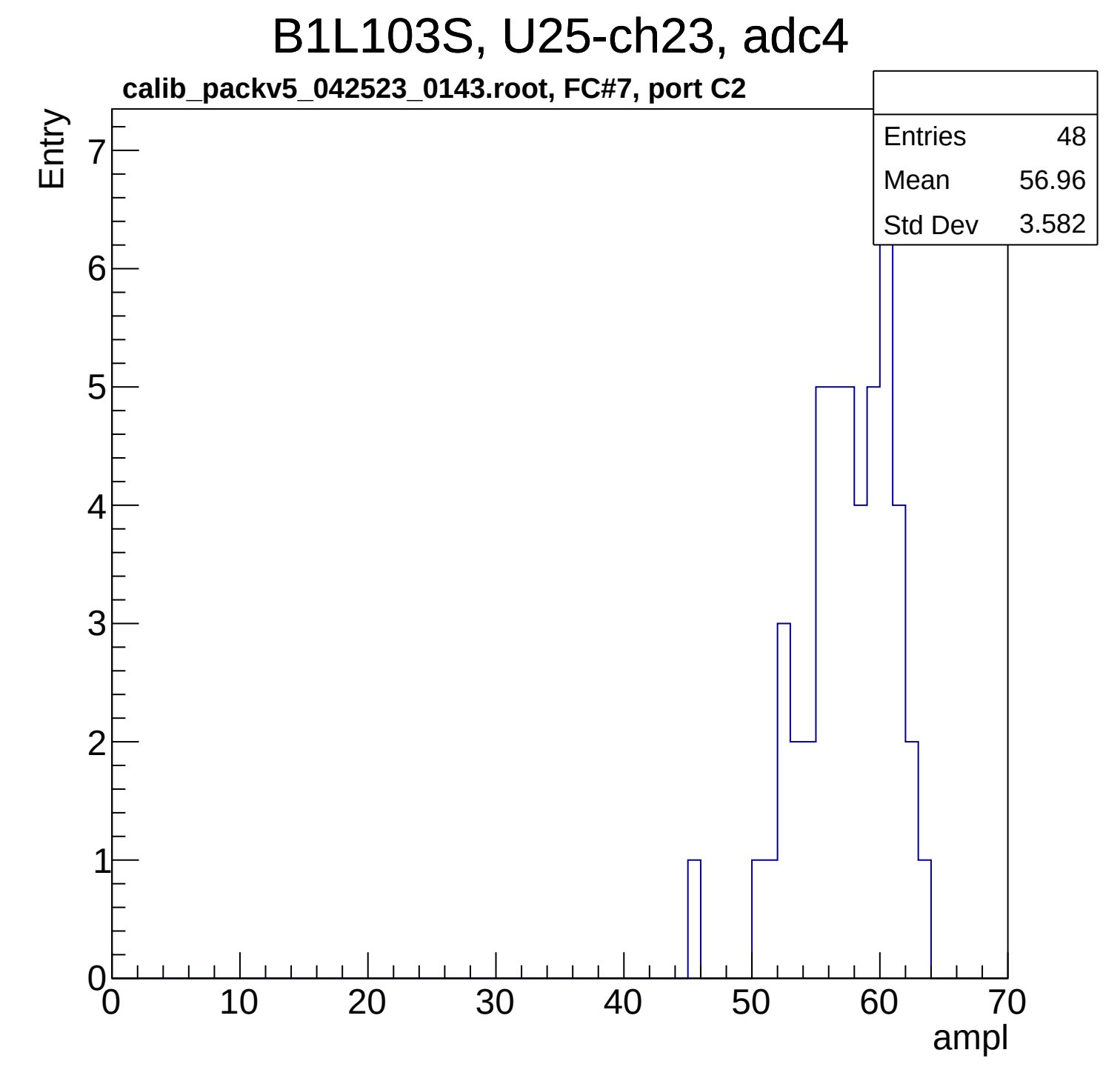
B1L103S, U25-ch23, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 56.96
Std Dev 3.582
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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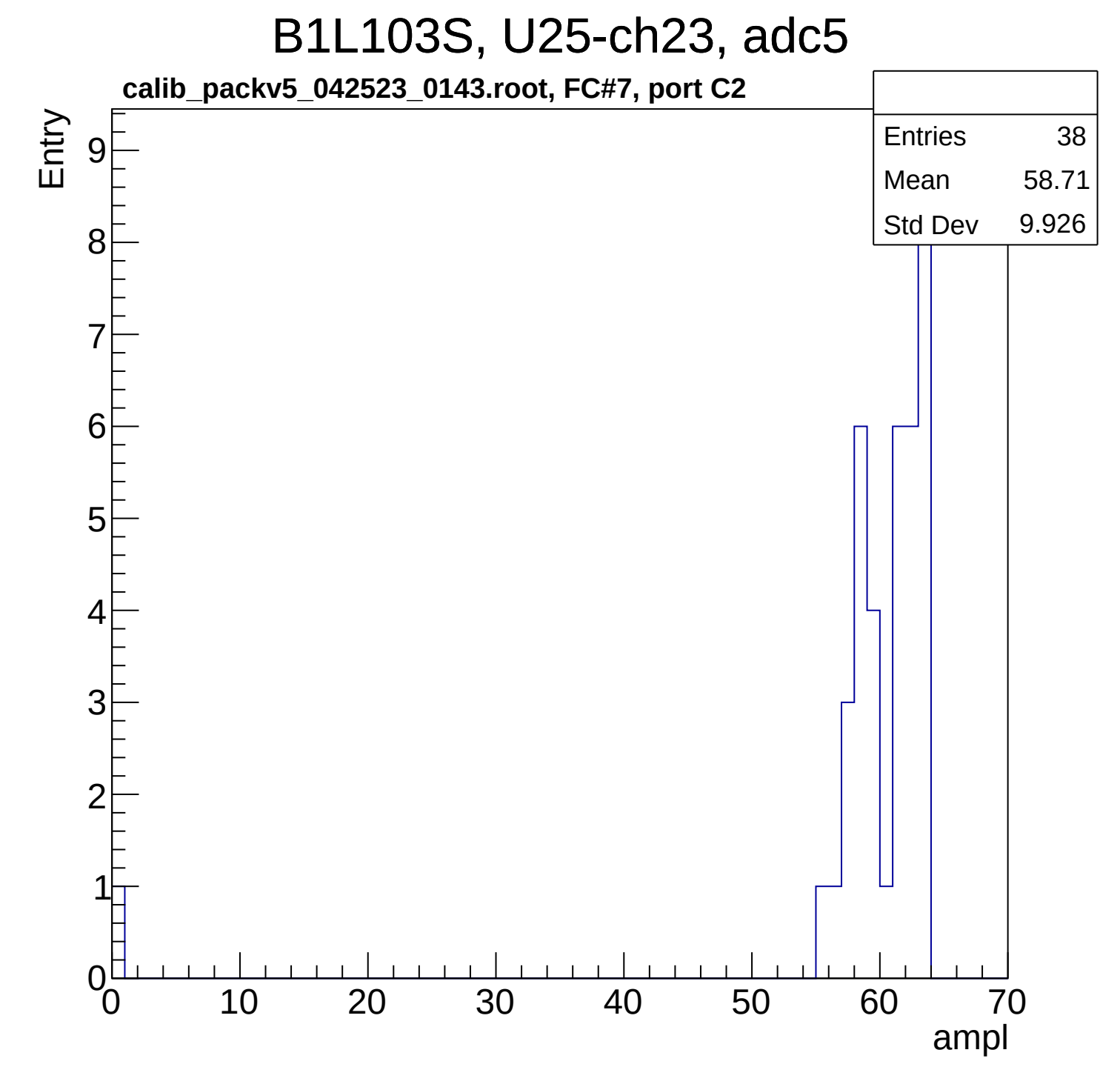
B1L103S, U25-ch23, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.71
Std Dev 9.926
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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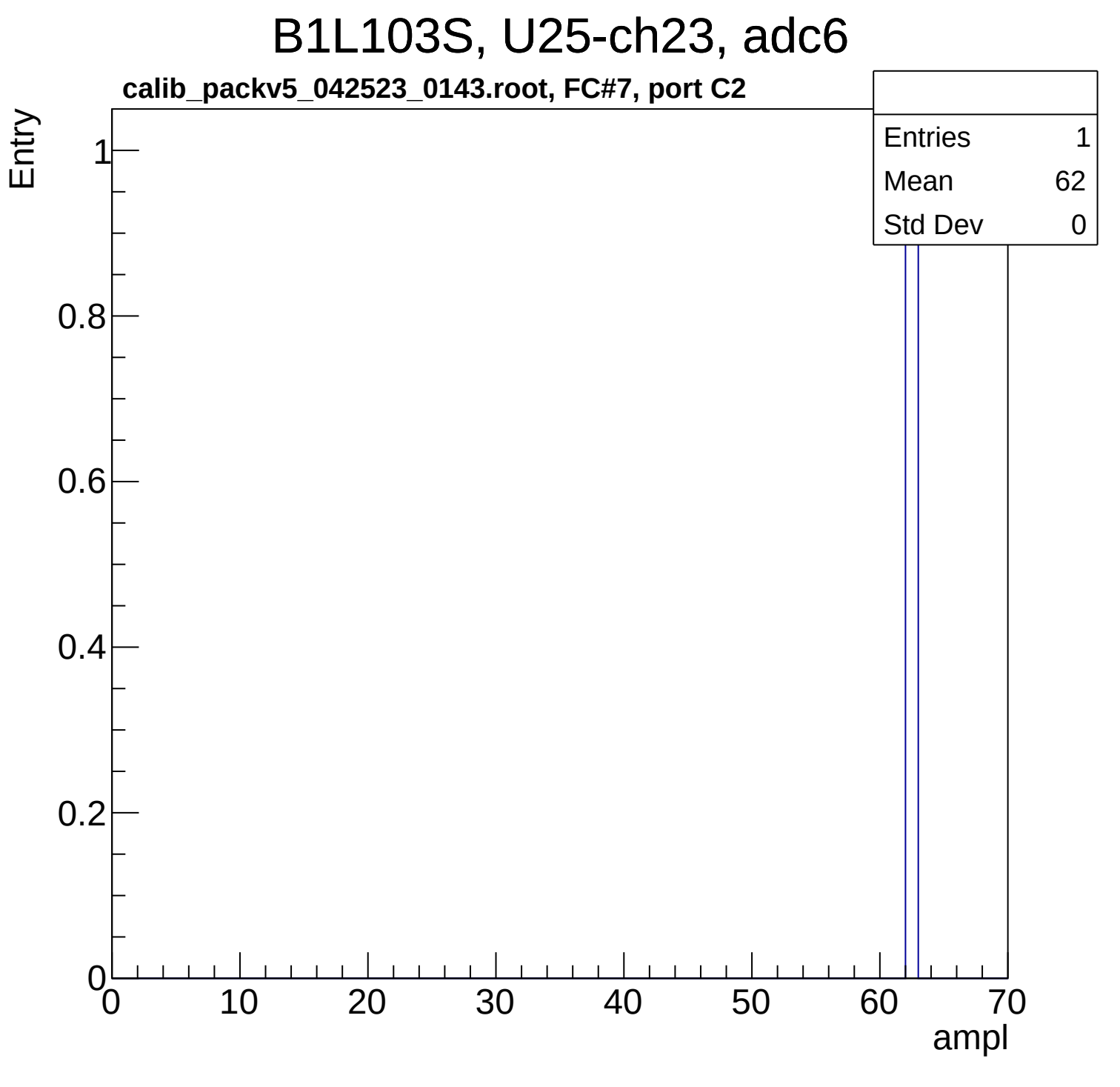
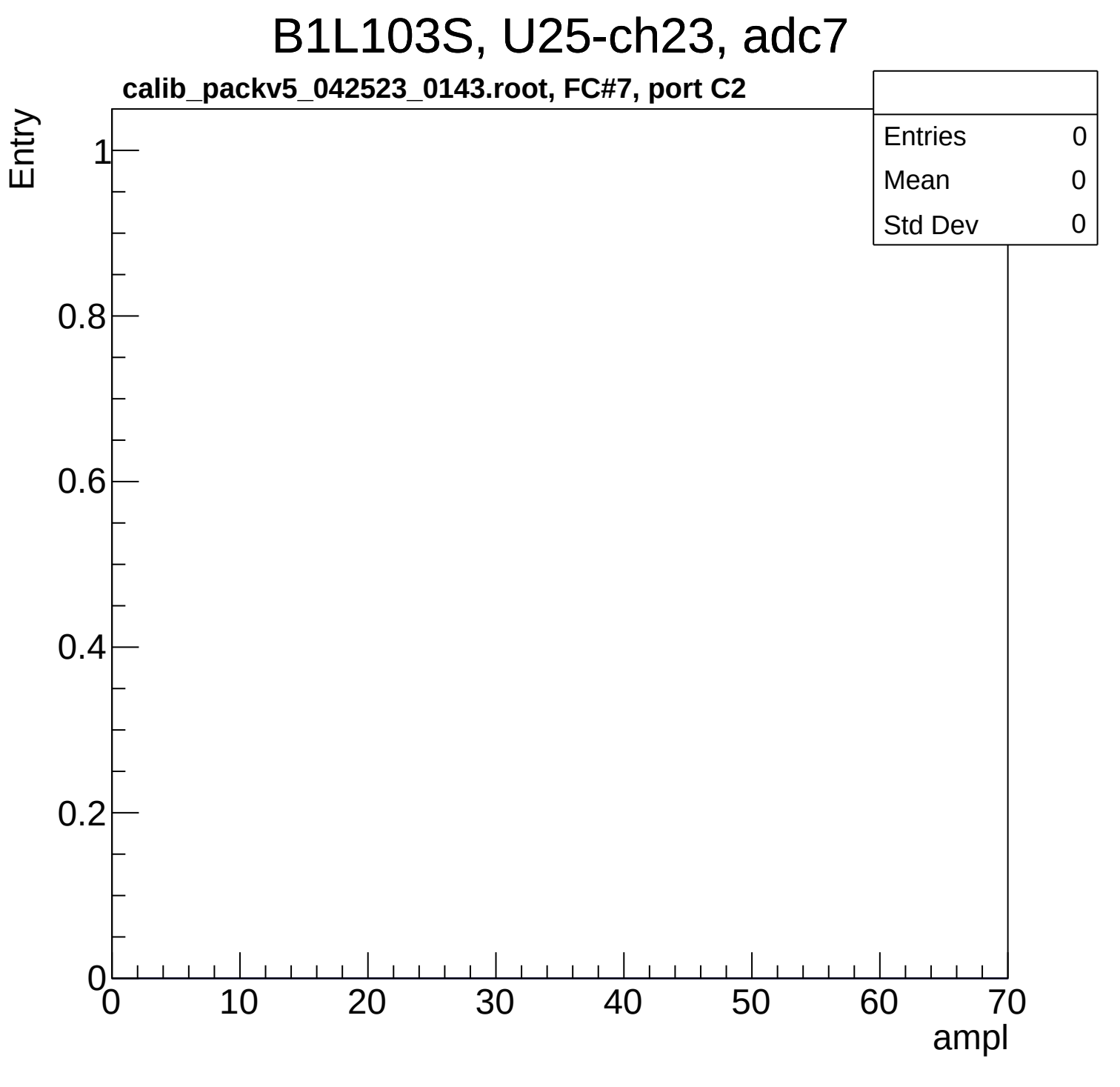
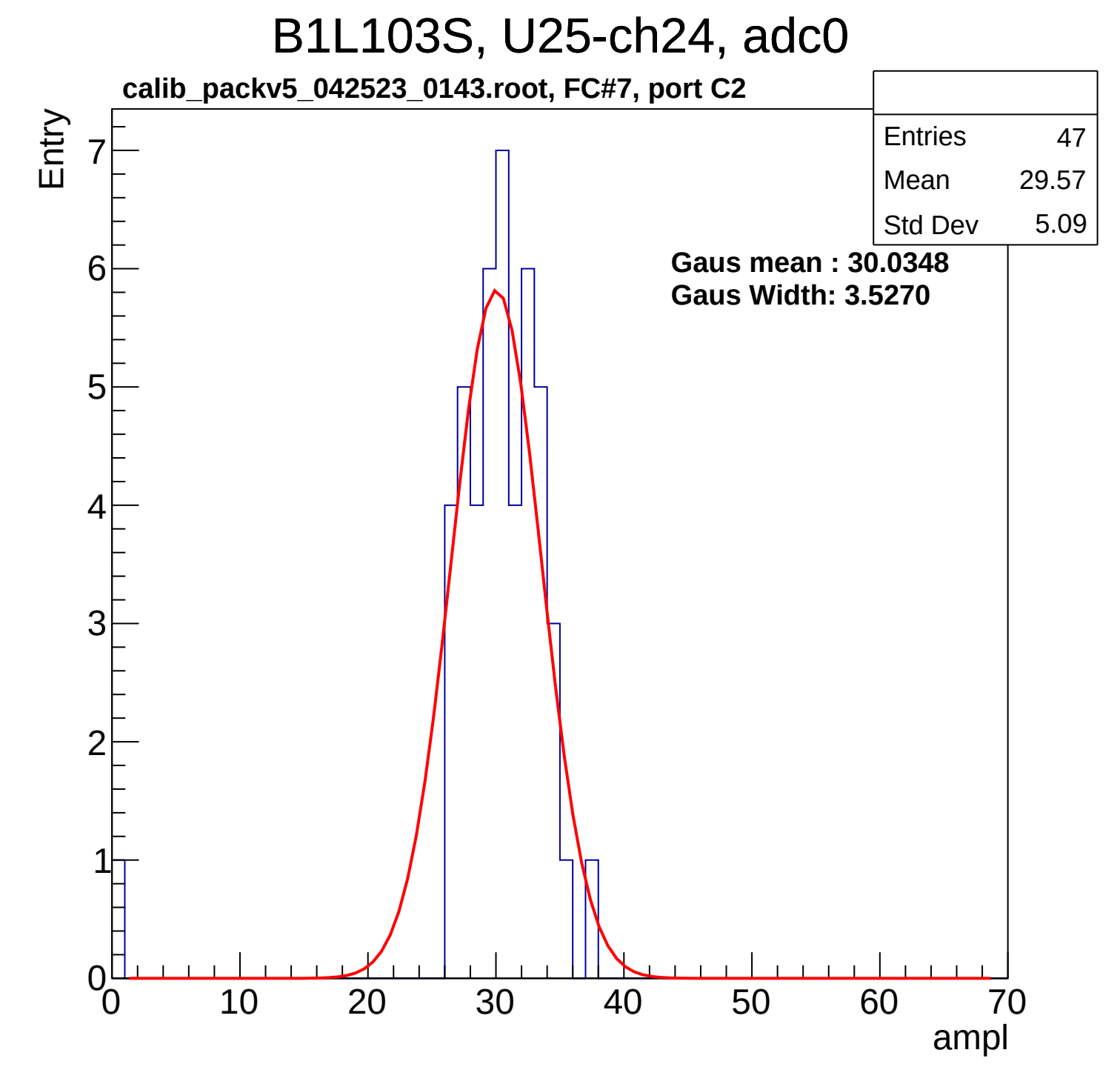
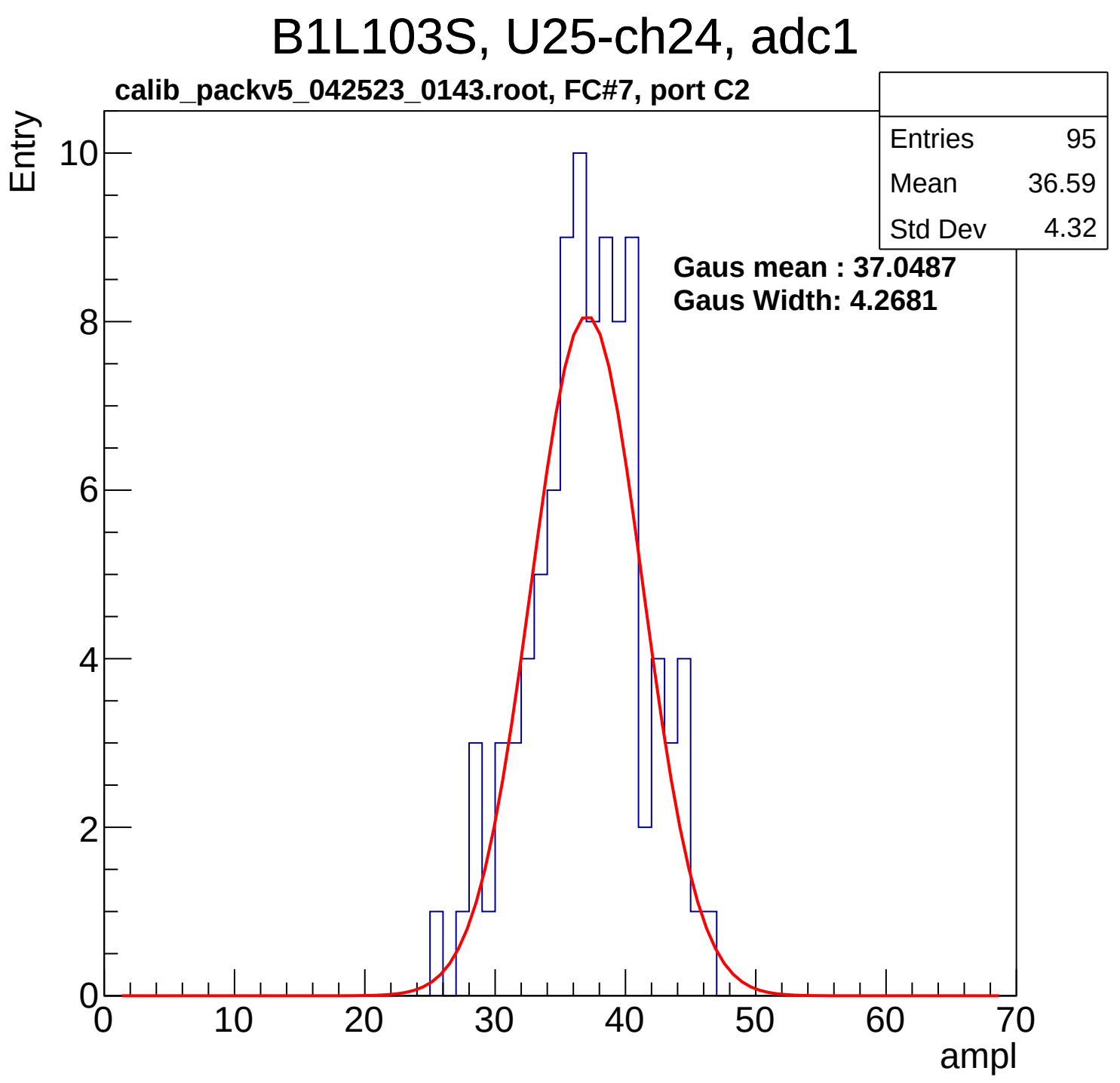

B1L103S, U25-ch23, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch23, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch24, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 29.57
Std Dev 5.09
Gaus mean : 30.0348
Gaus Width: 3.5270
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch24, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 95
Mean 36.59
Std Dev 4.32
Gaus mean : 37.0487
Gaus Width: 4.2681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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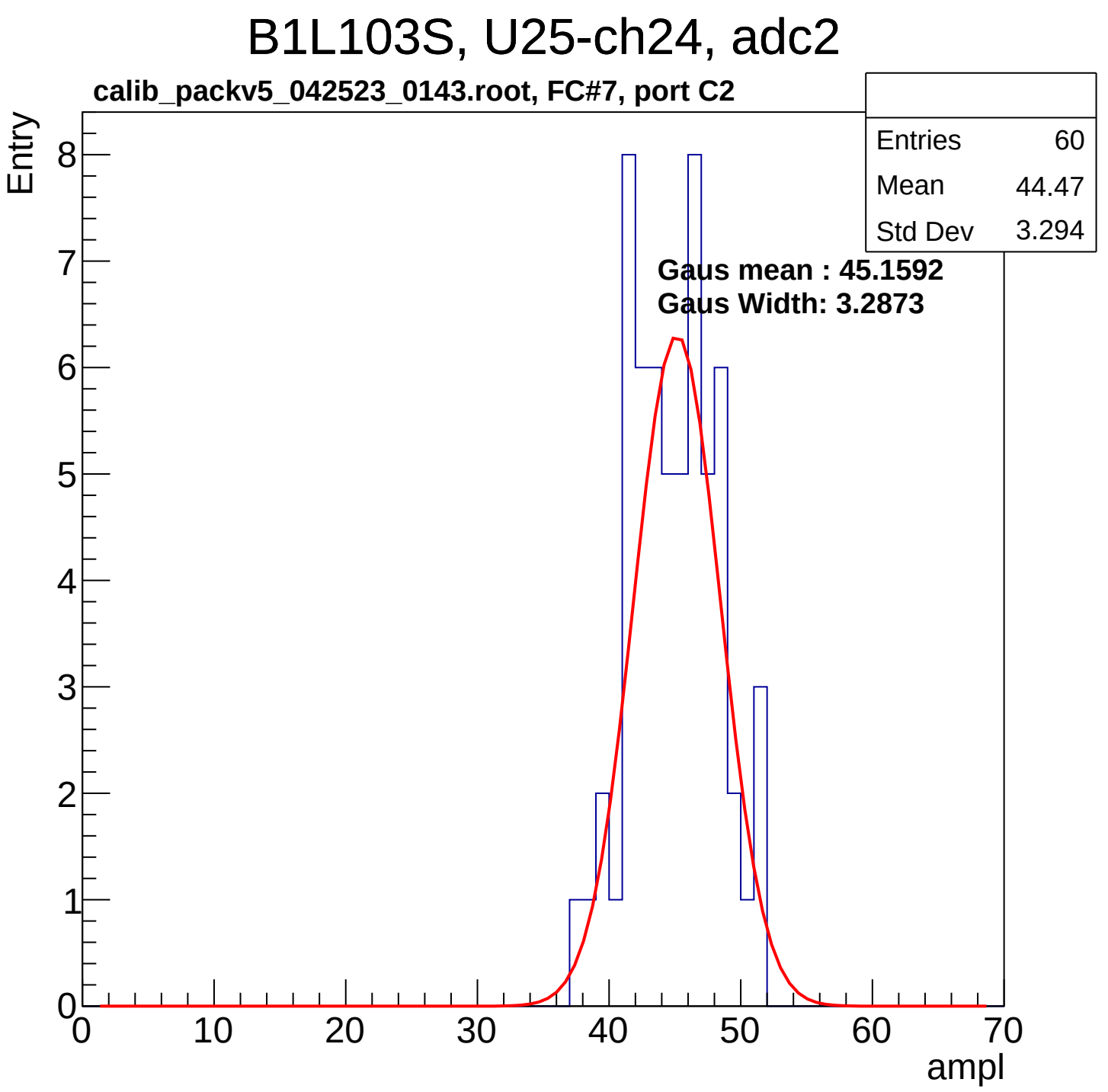
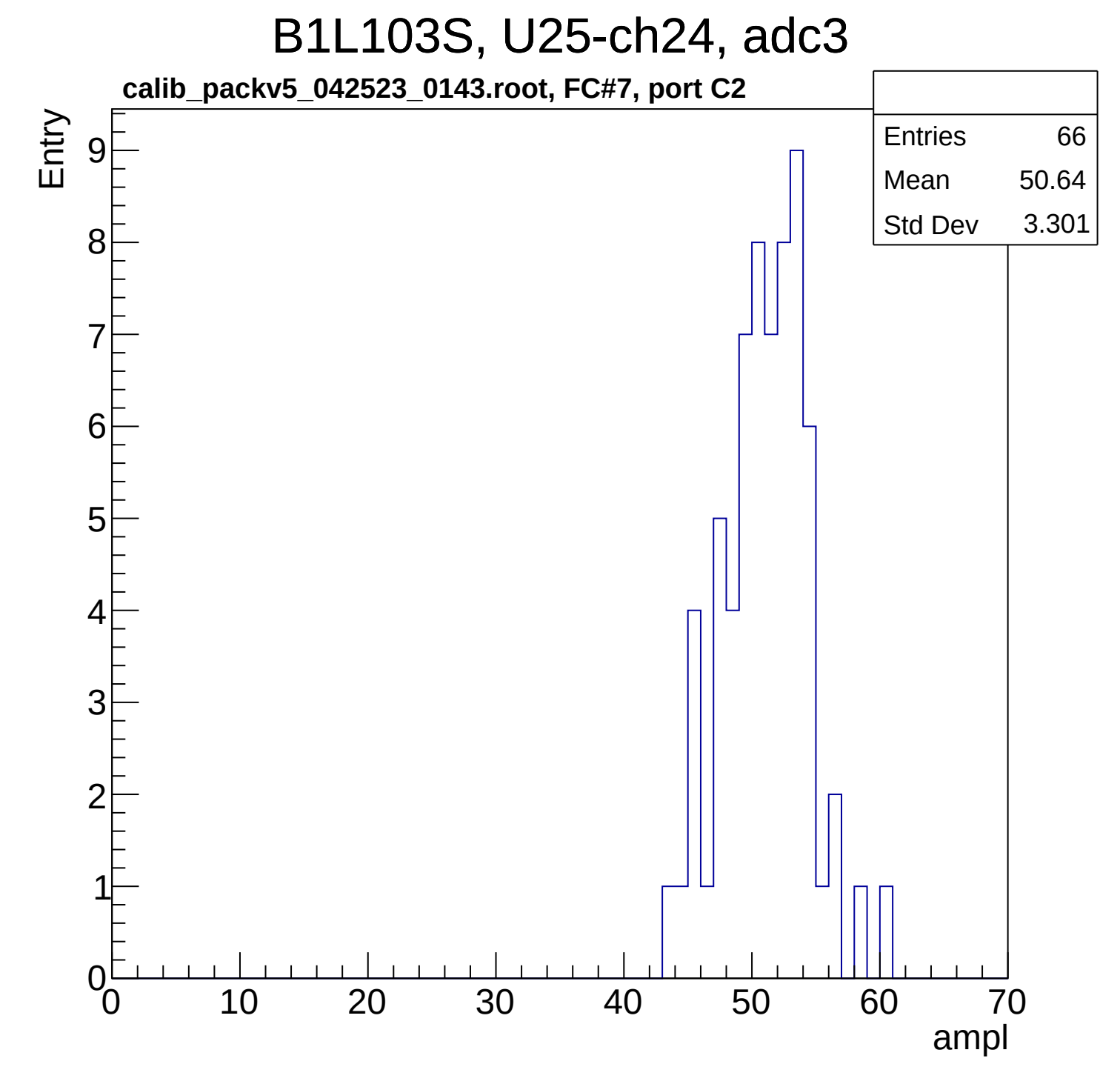

B1L103S, U25-ch24, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 44.47
Std Dev 3.294
Gaus mean : 45.1592
Gaus Width: 3.2873
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch24, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 50.64
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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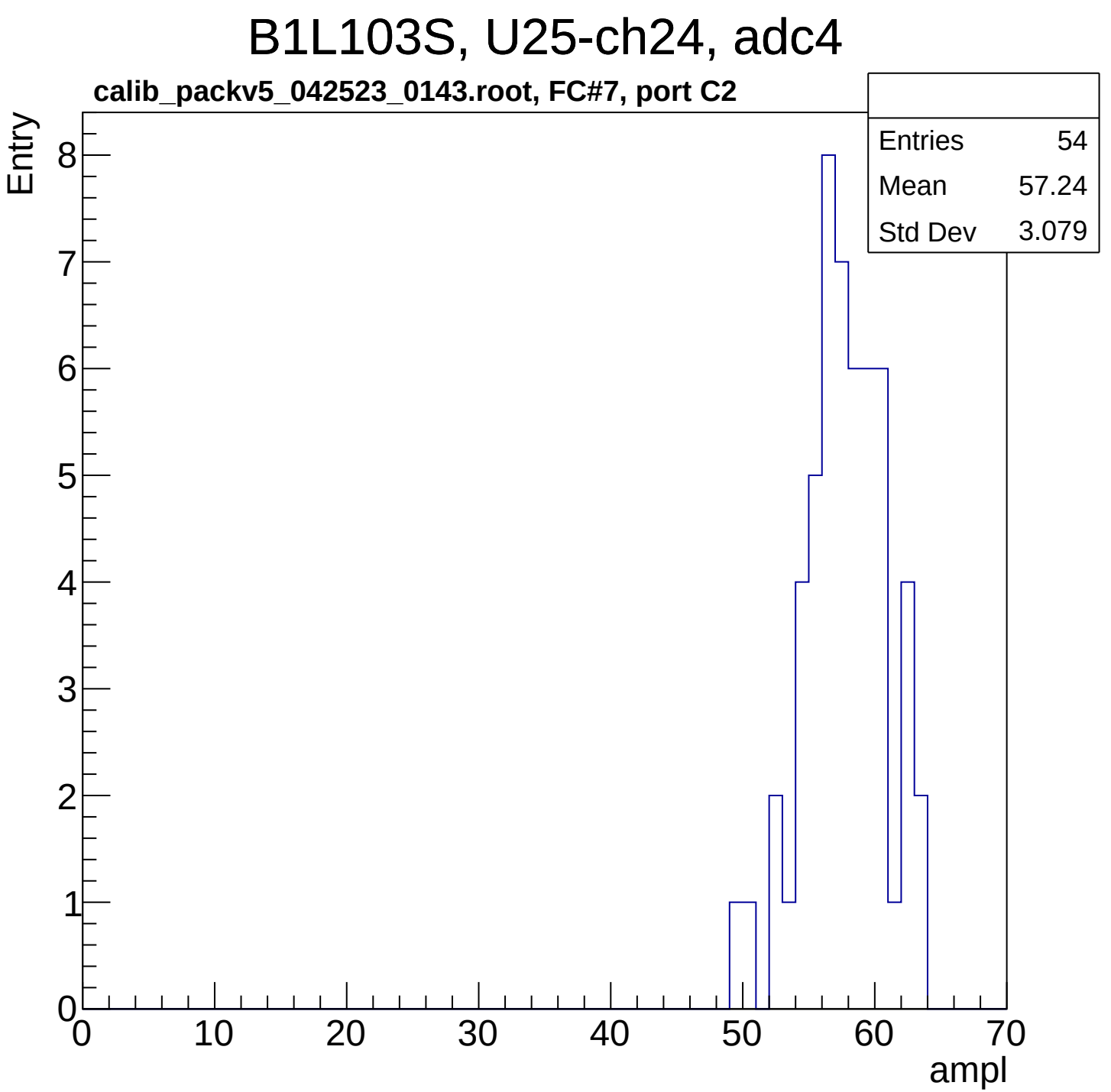
B1L103S, U25-ch24, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.24
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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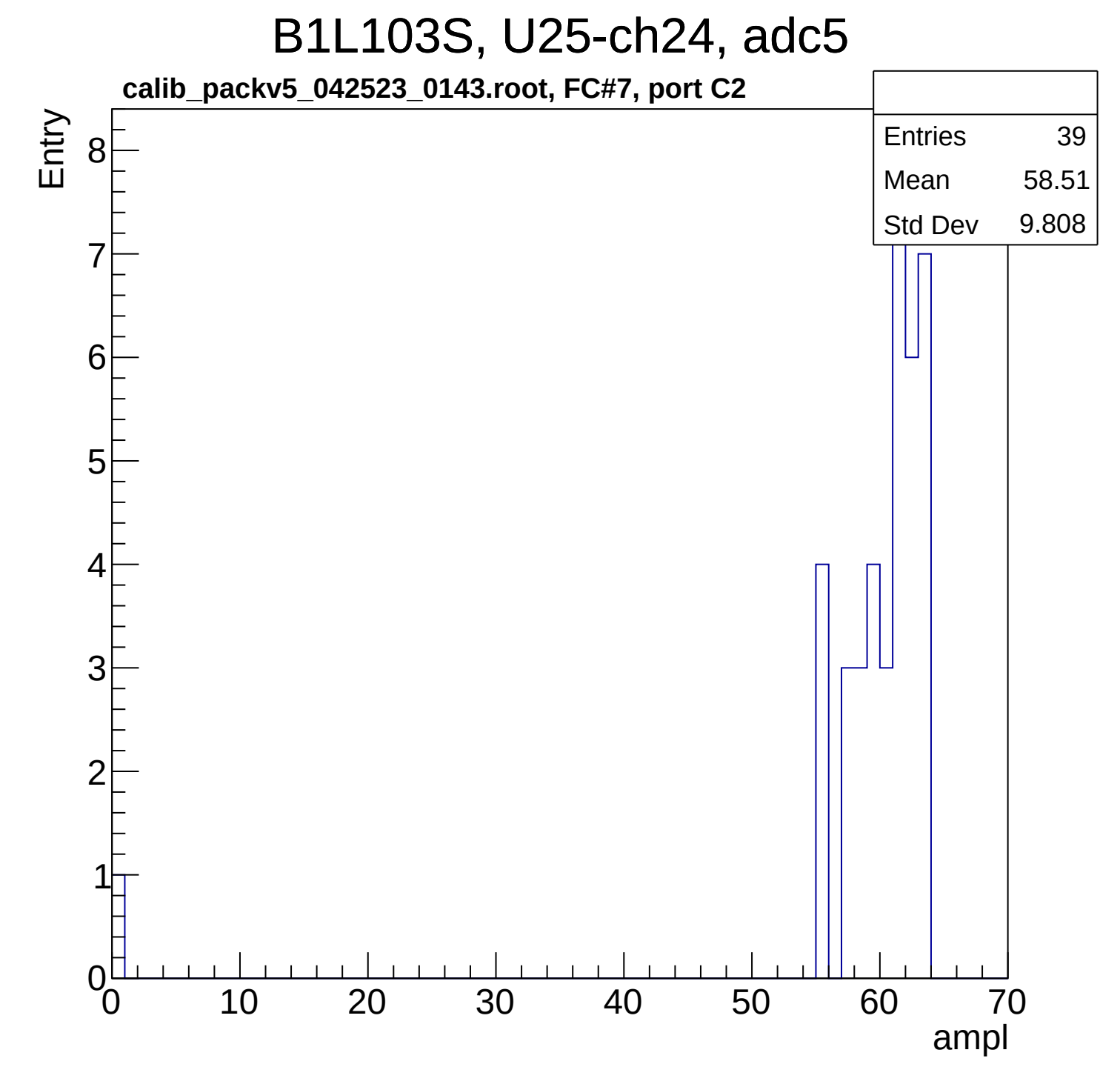
B1L103S, U25-ch24, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.51
Std Dev 9.808
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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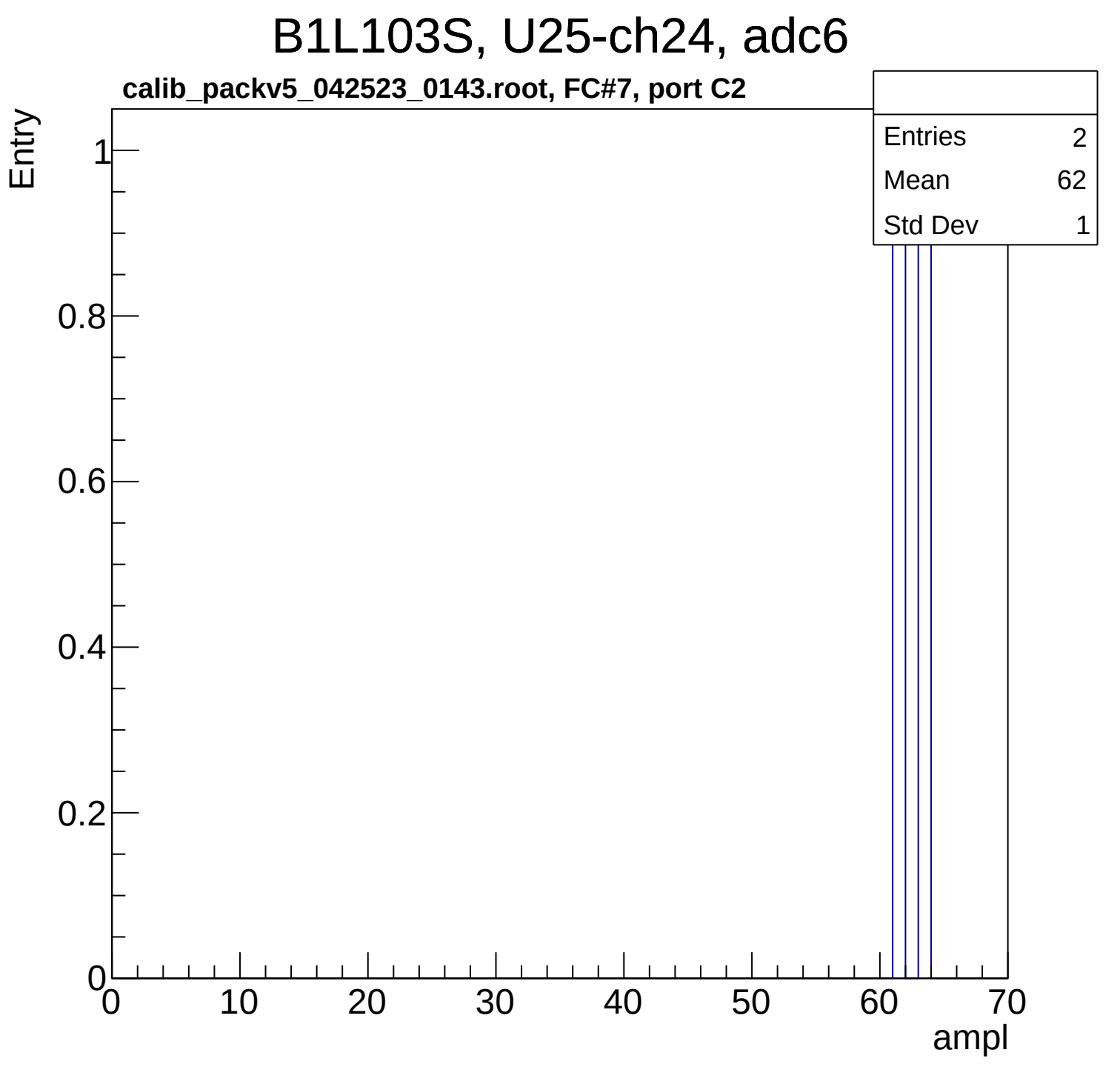
B1L103S, U25-ch24, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
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0.4
0.2
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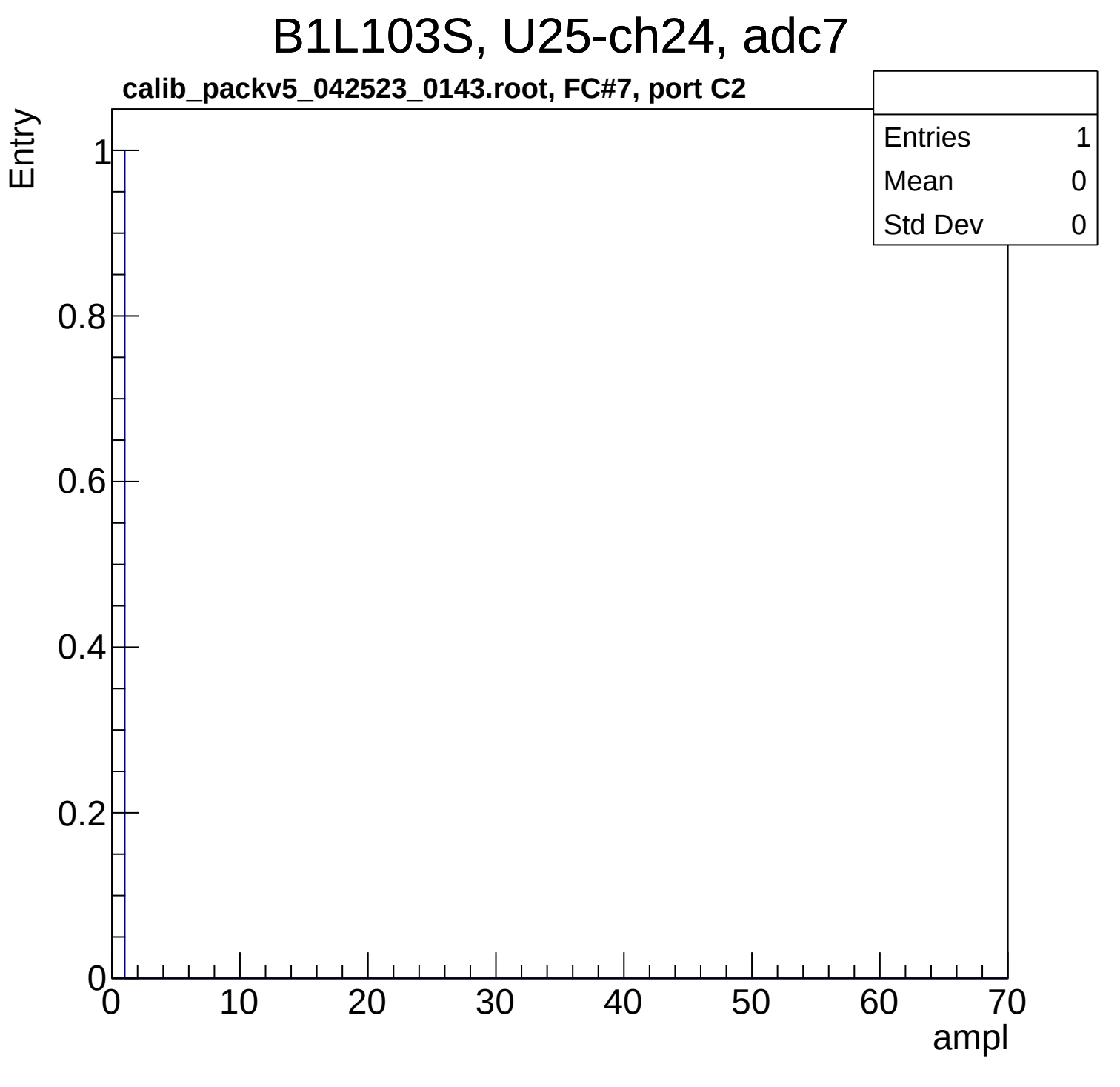
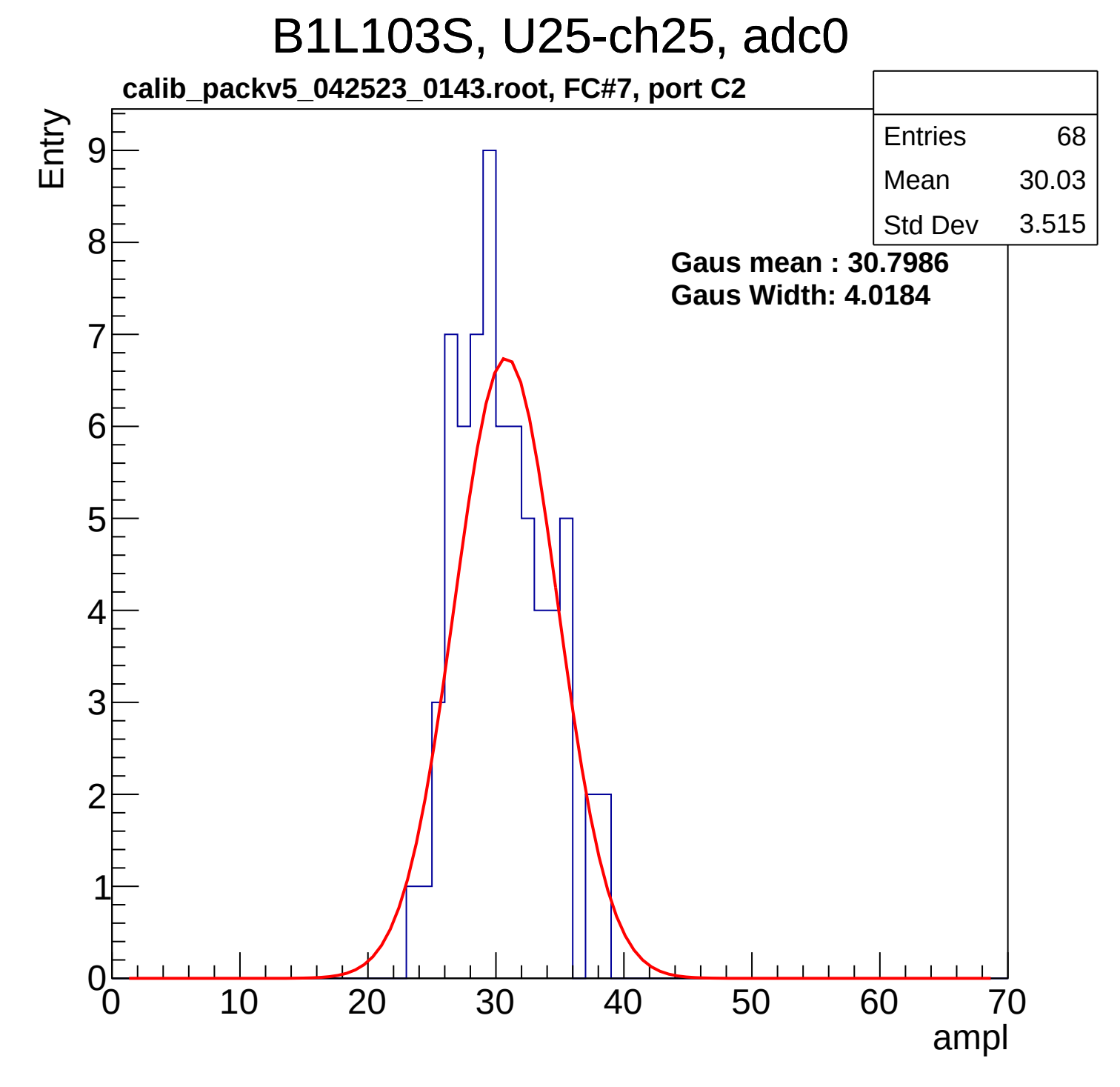
50
60
70
ampl


B1L103S, U25-ch24, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch25, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 30.03
Std Dev 3.515
Gaus mean : 30.7986
Gaus Width: 4.0184
Entry
ampl
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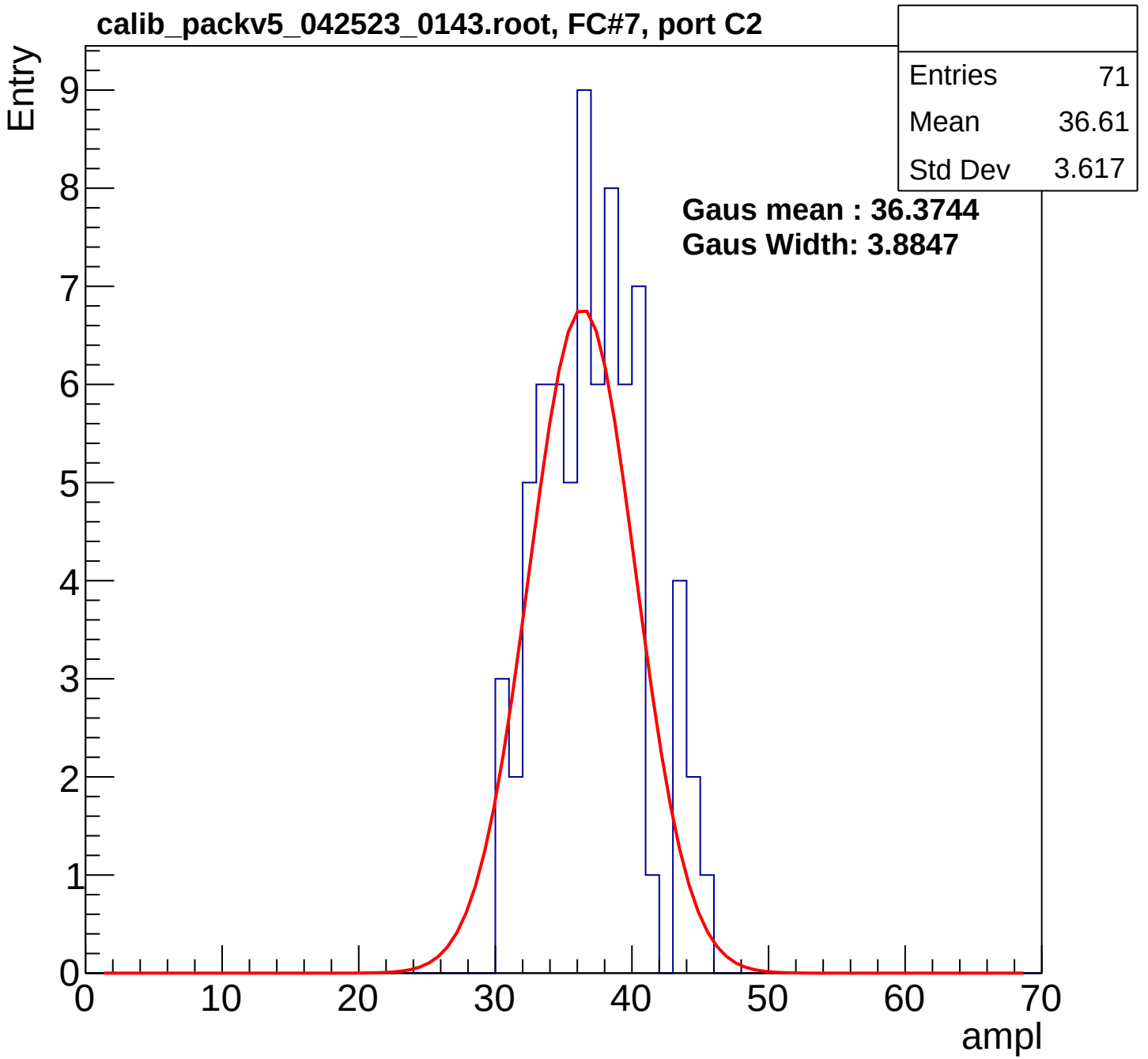
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch25, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.61
Std Dev 3.617
Gaus mean : 36.3744
Gaus Width: 3.8847
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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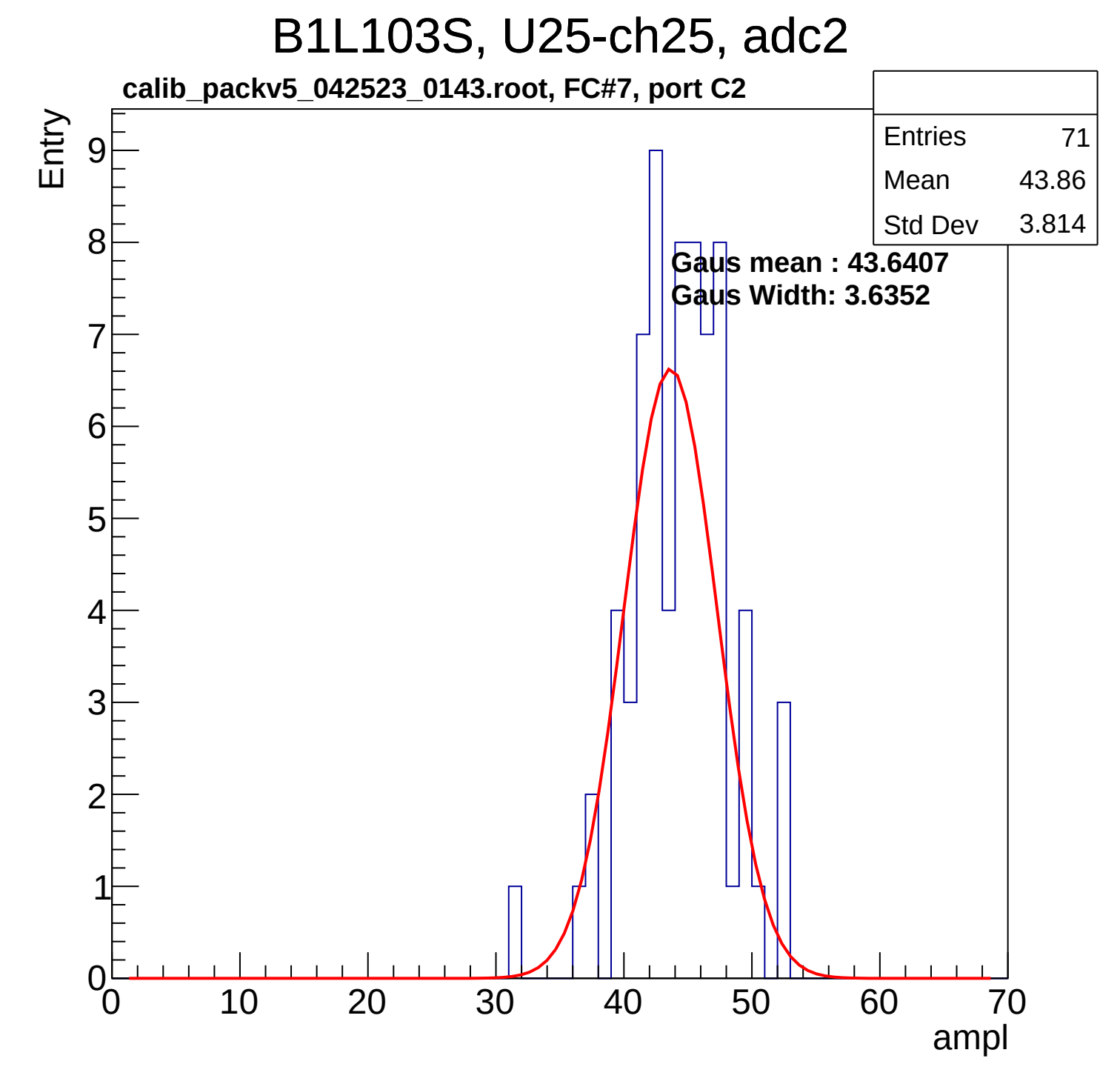
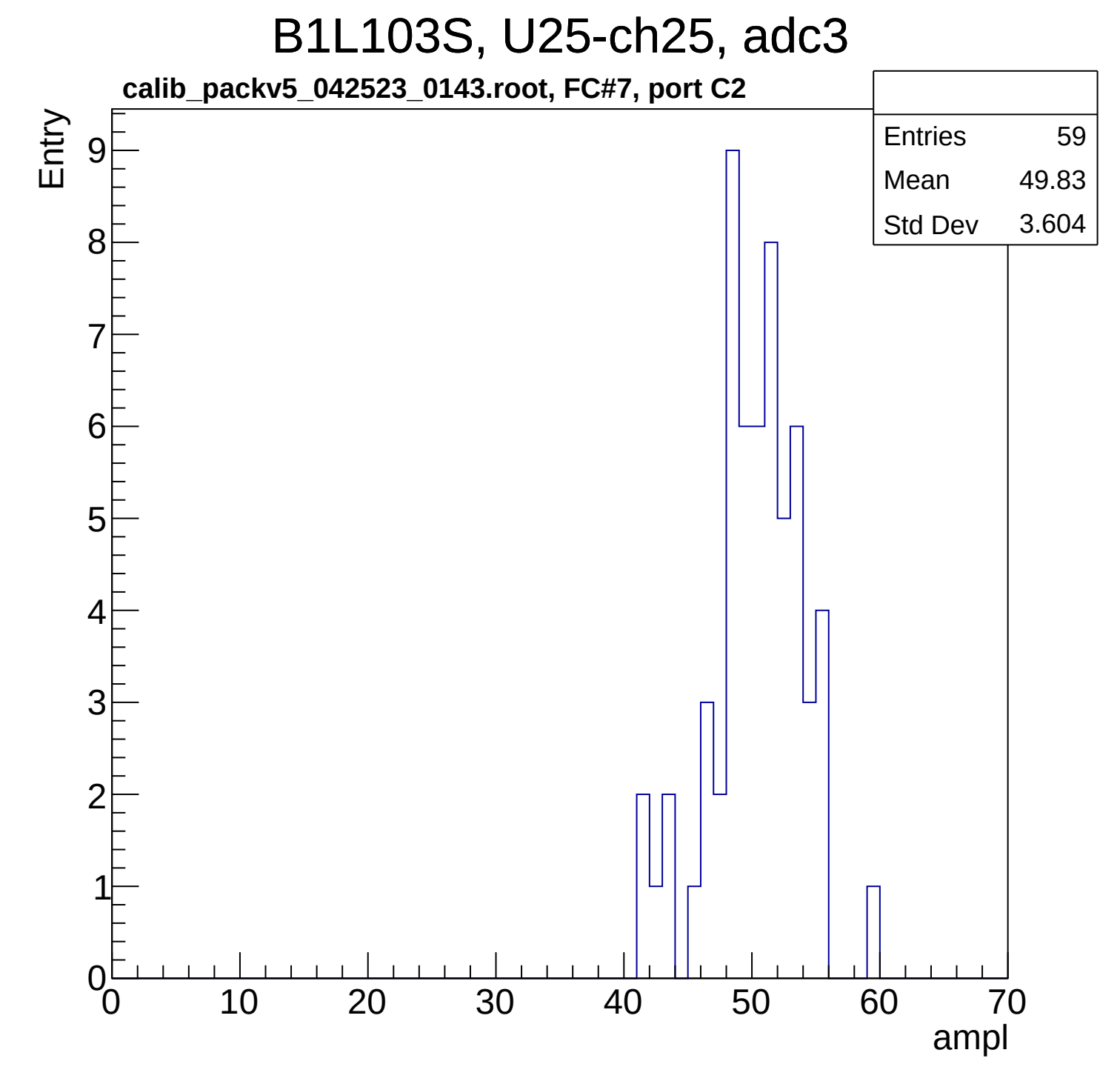

B1L103S, U25-ch25, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 43.86
Std Dev 3.814
Gaus mean : 43.6407
Gaus Width: 3.6352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch25, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 49.83
Std Dev 3.604
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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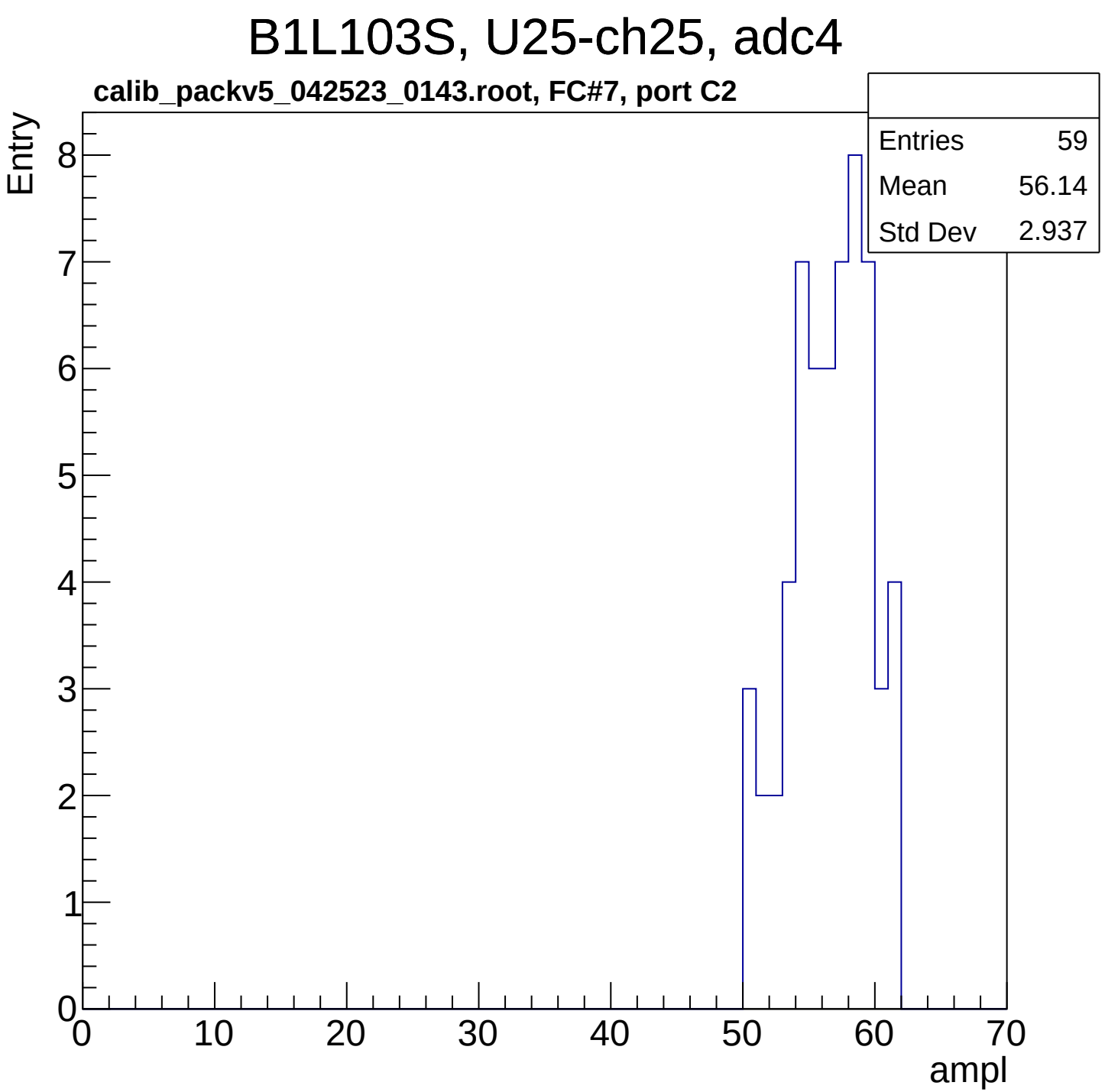
B1L103S, U25-ch25, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 56.14
Std Dev 2.937
Entry
ampl
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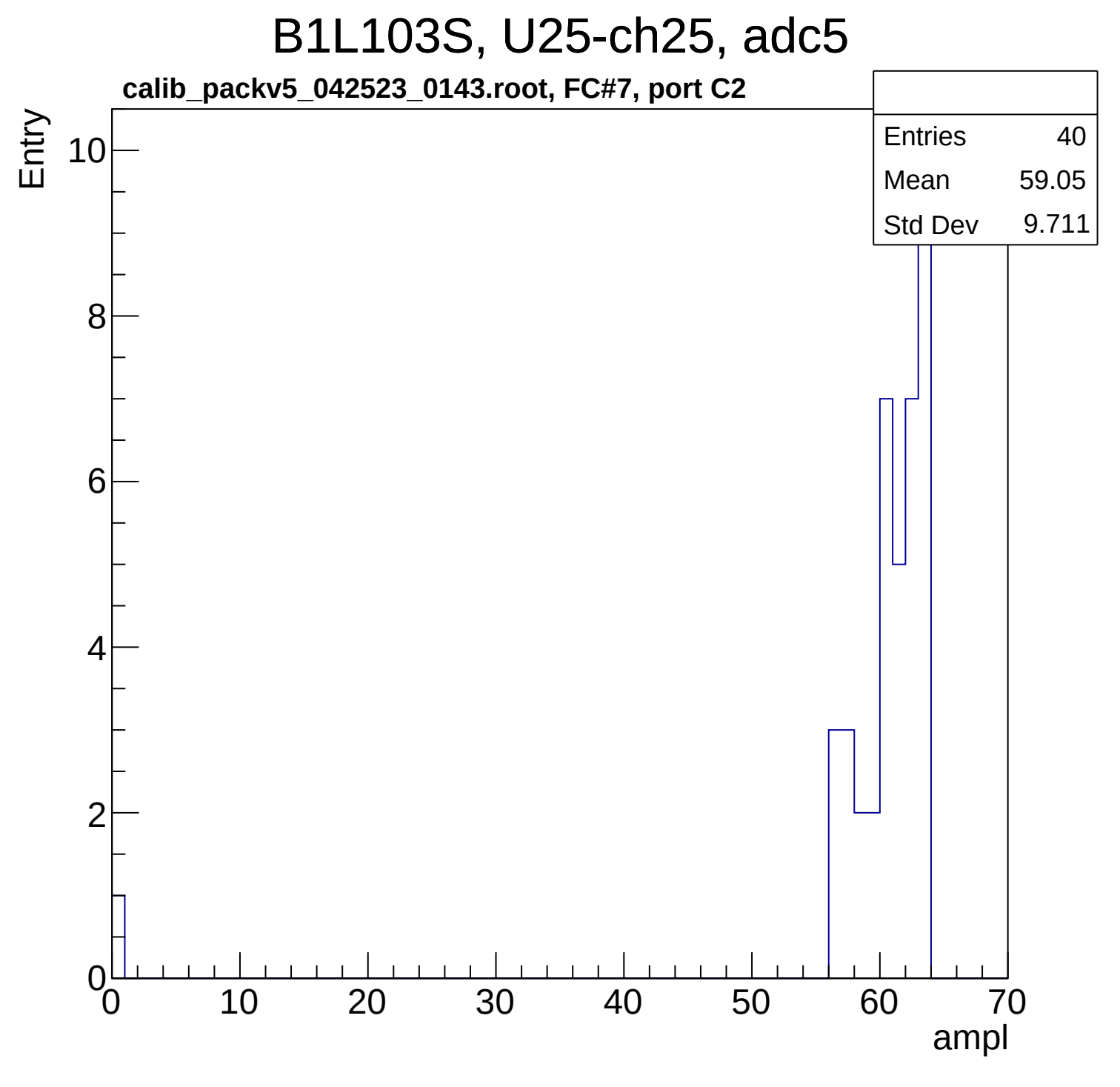
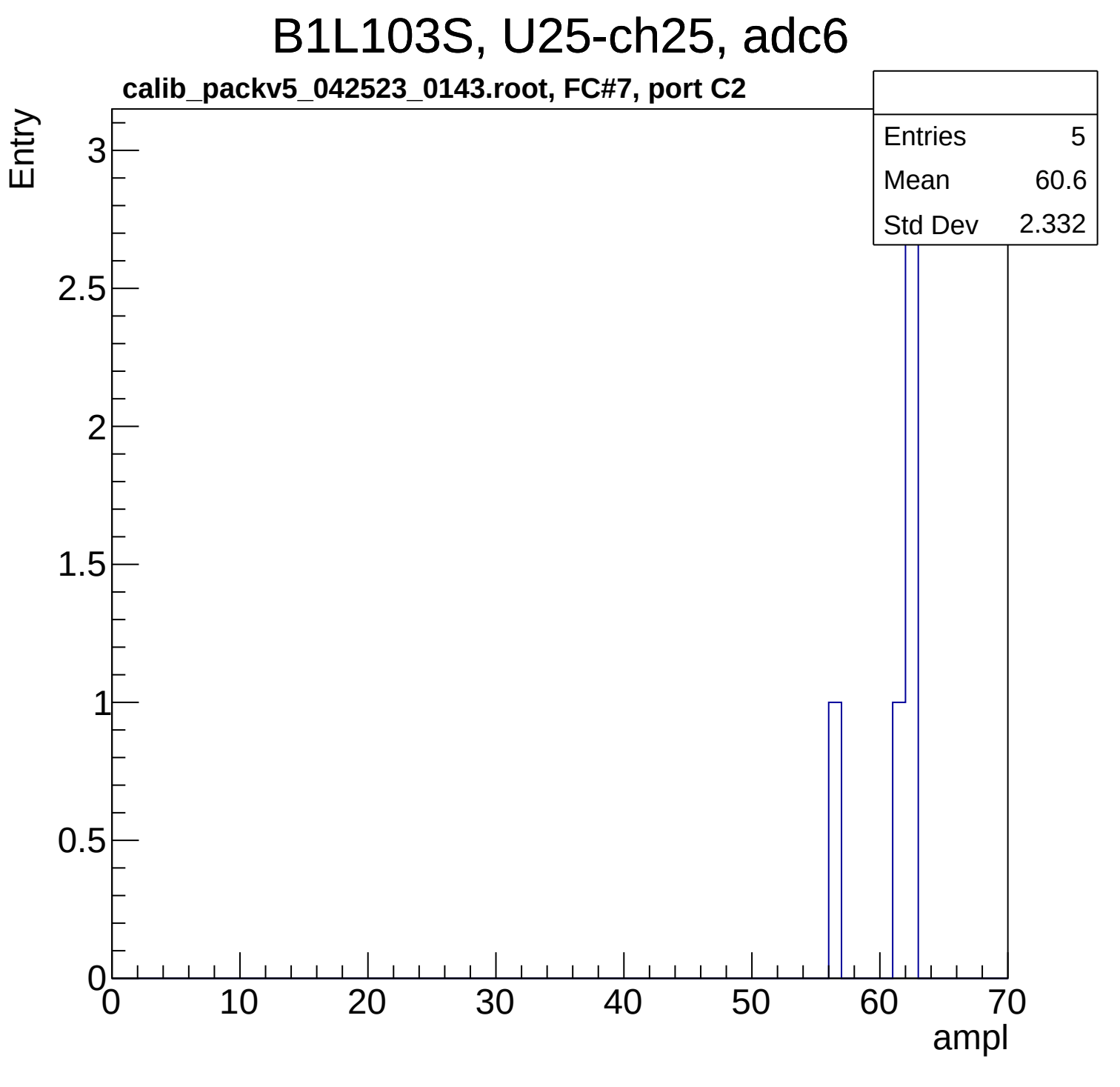
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch25, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 59.05
Std Dev 9.711
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch25, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 60.6
Std Dev 2.332
Entry
ampl
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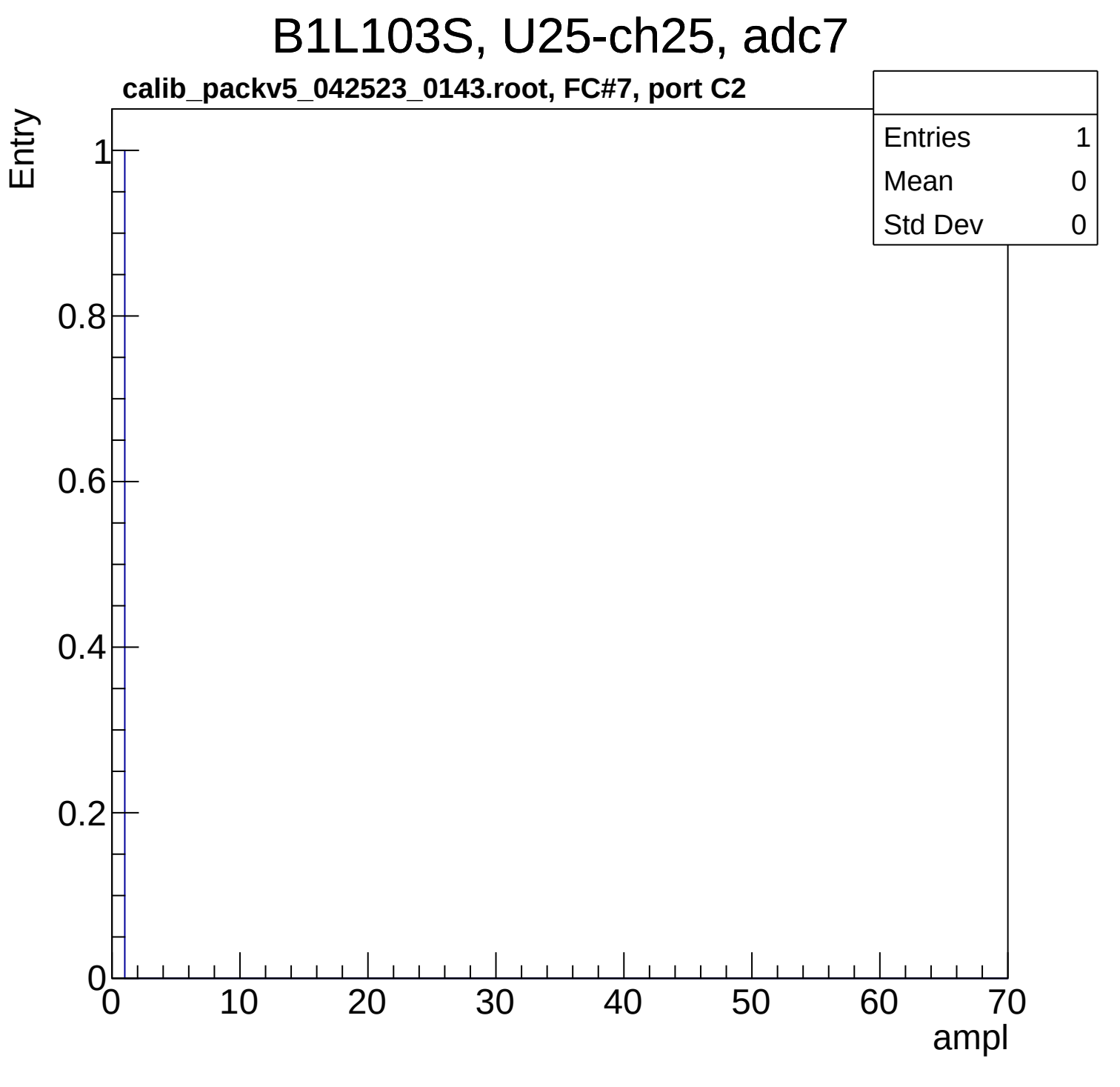
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch25, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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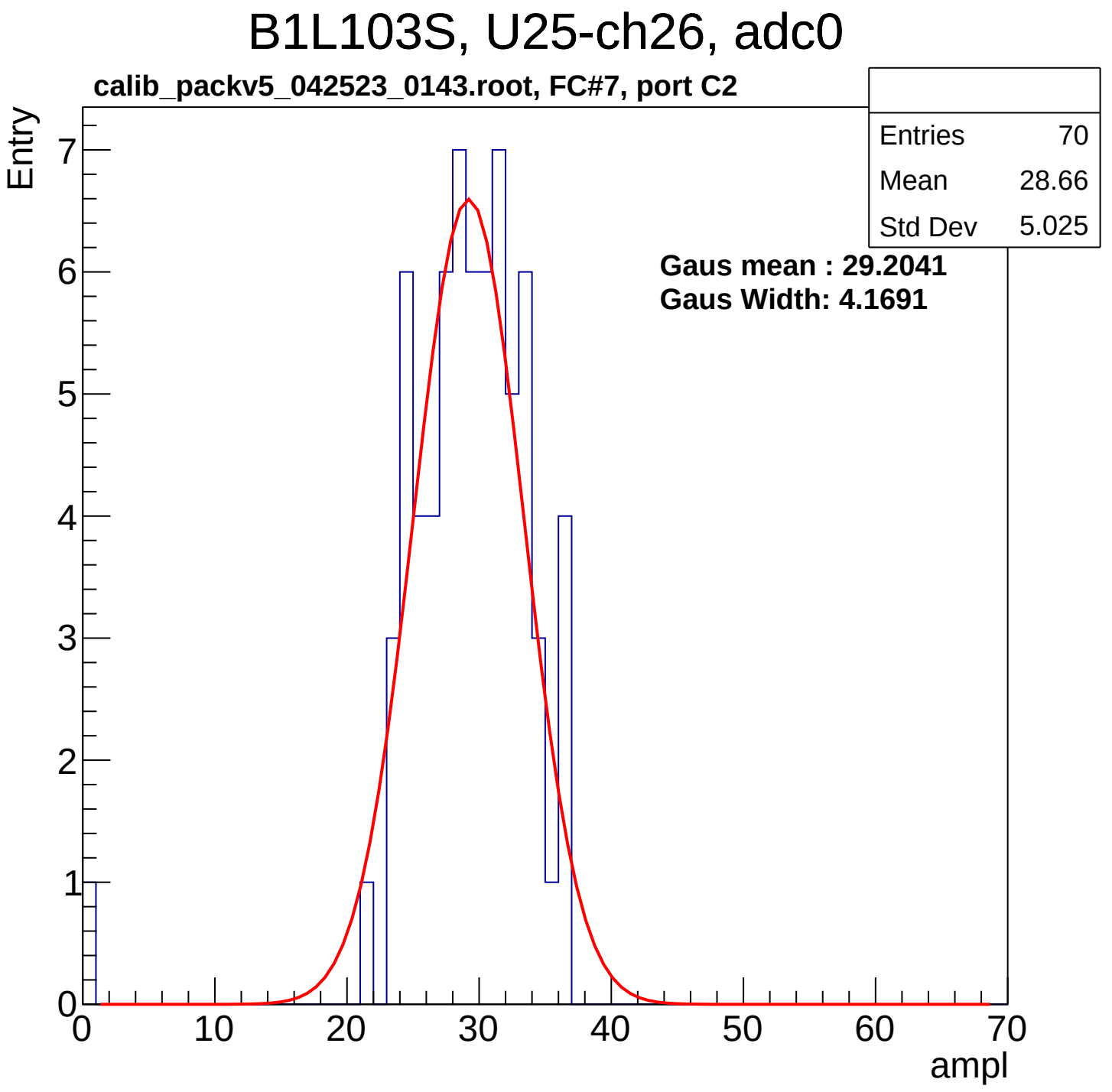
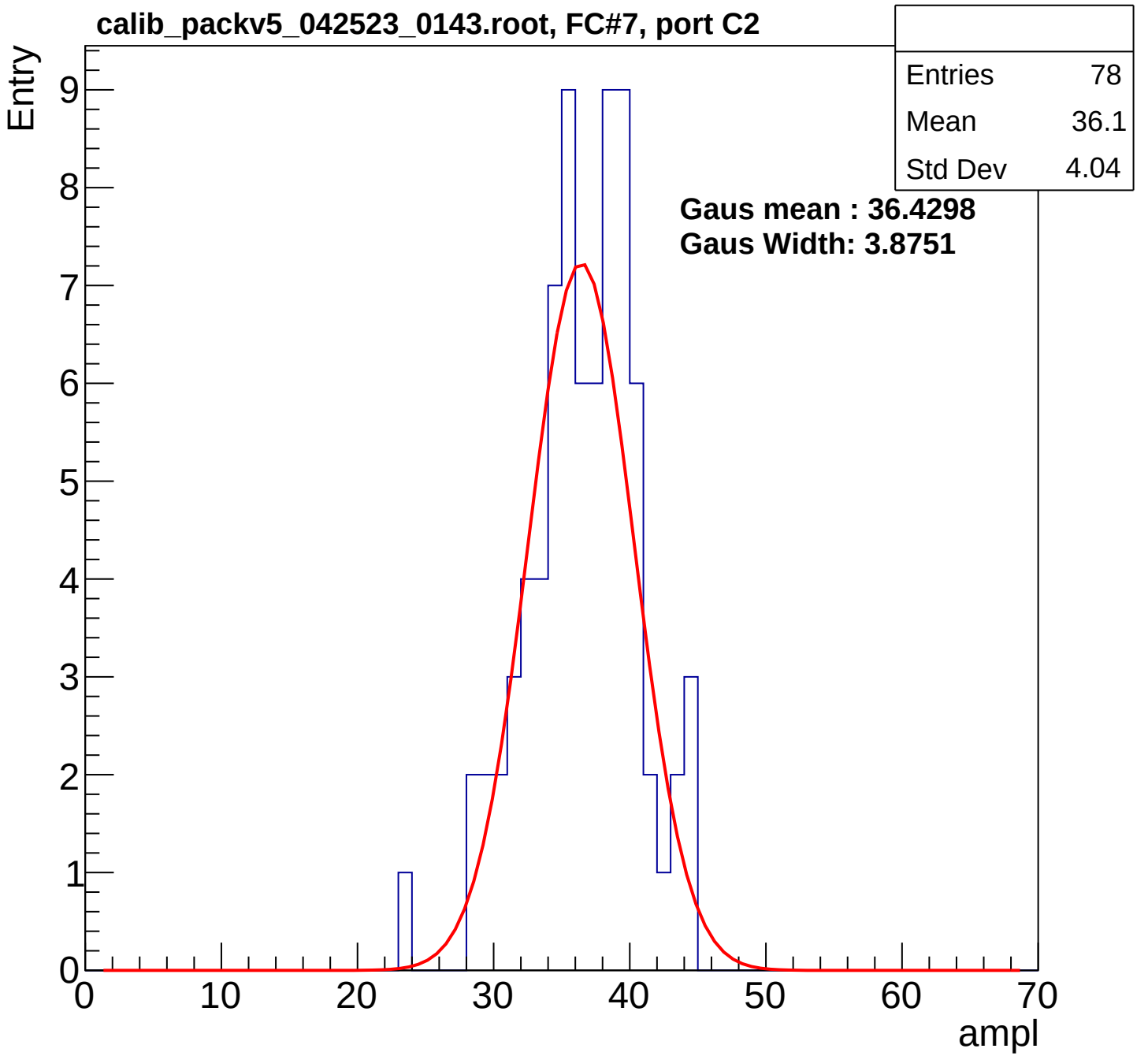
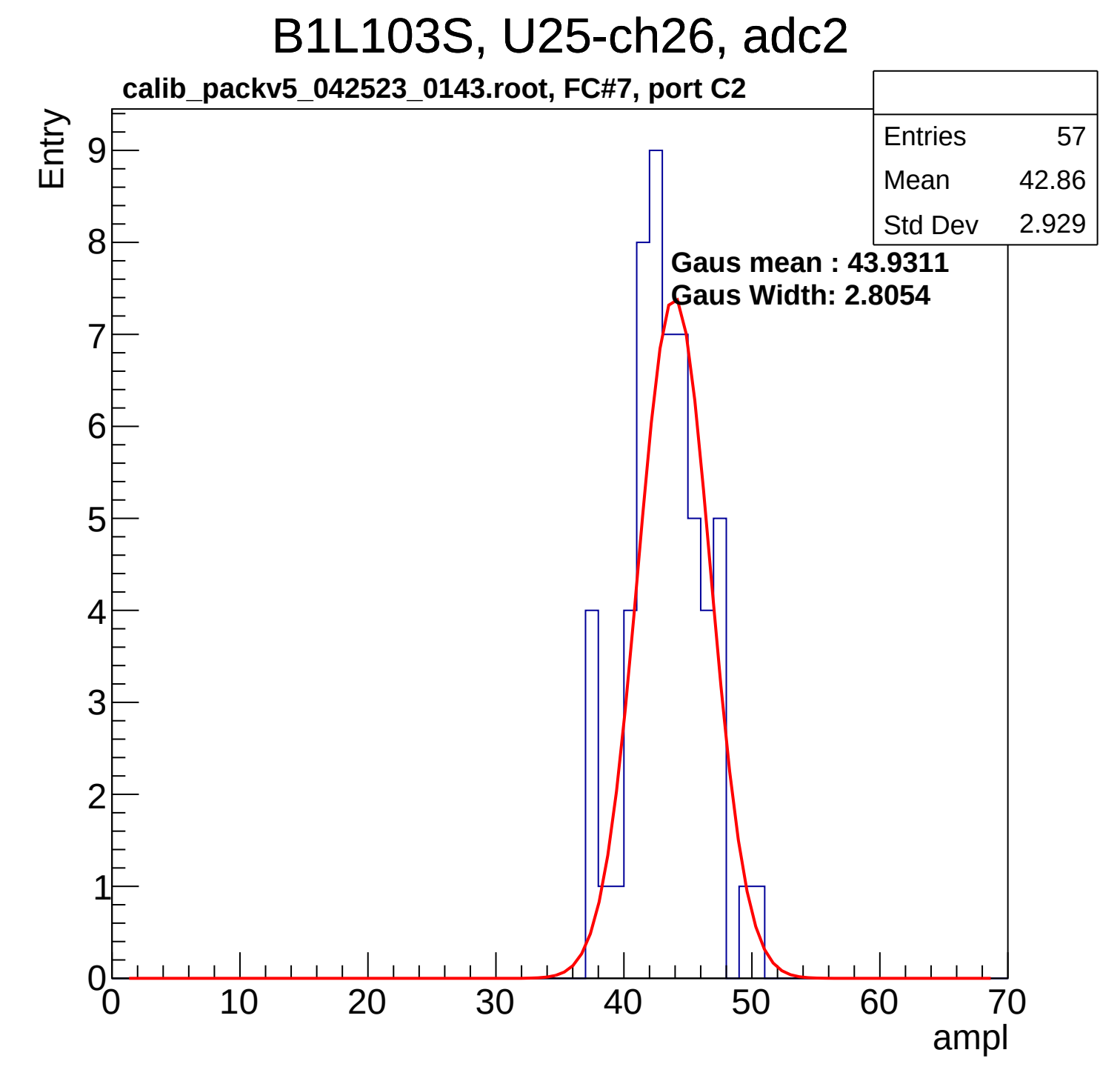
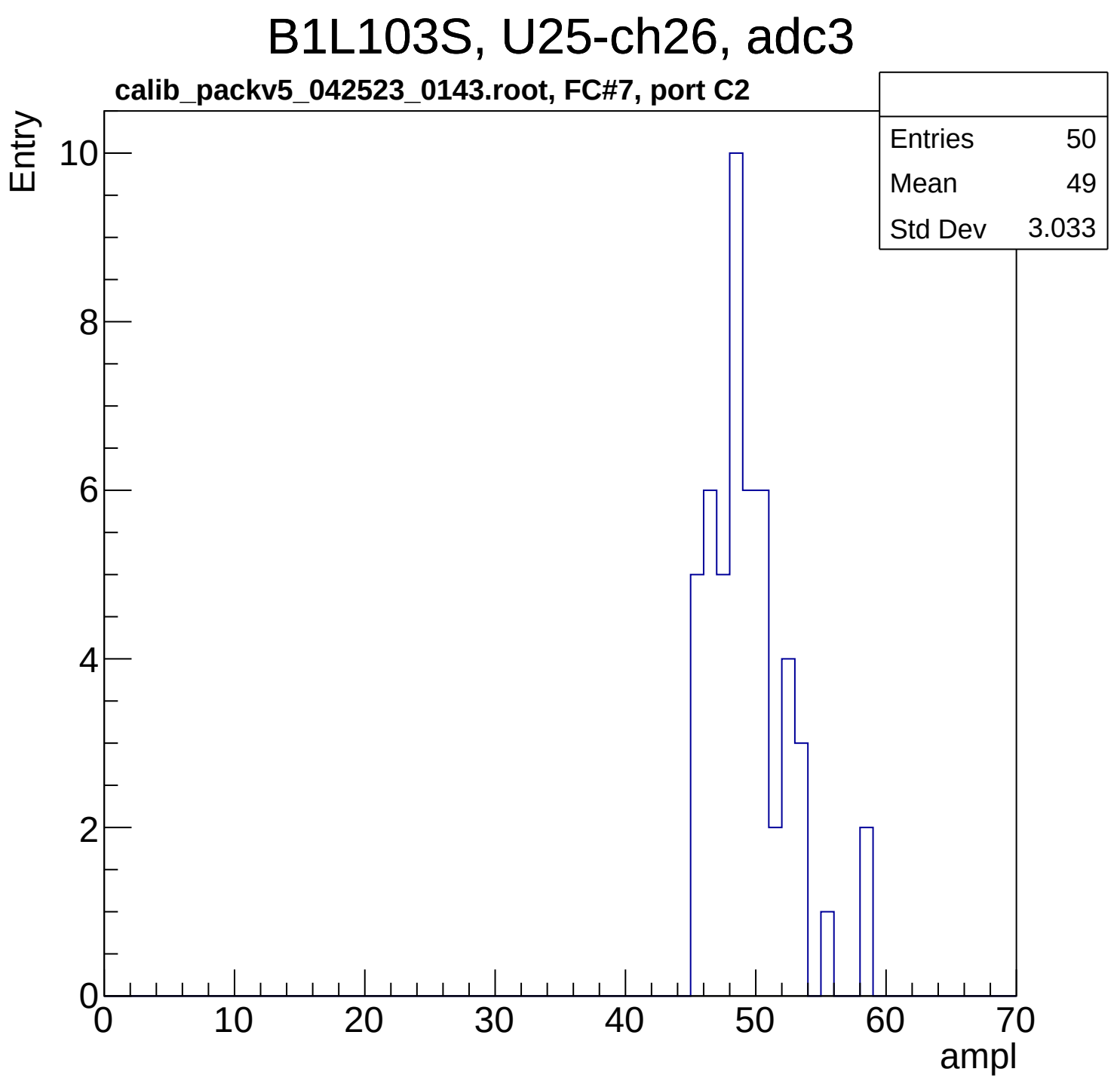
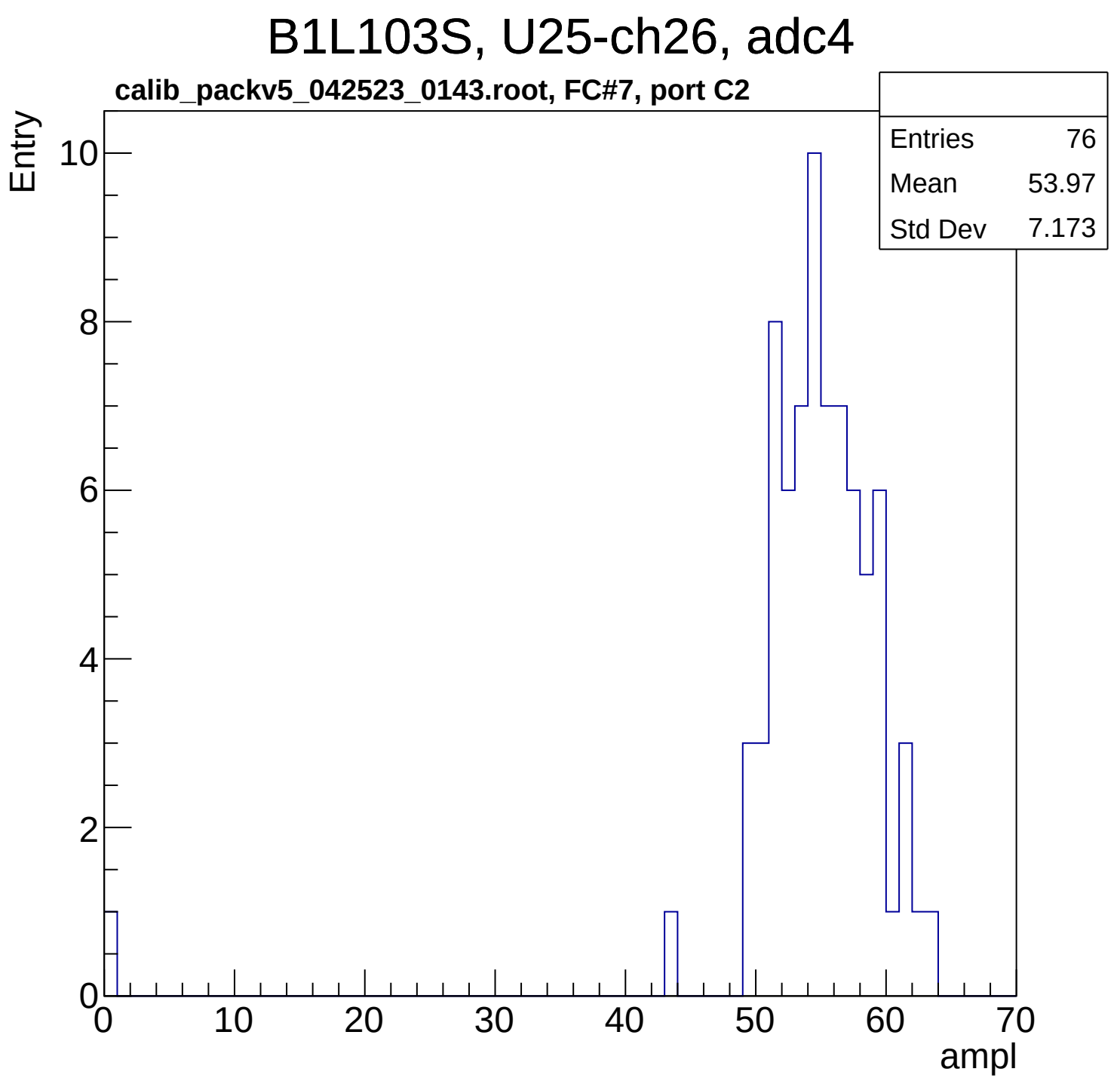
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

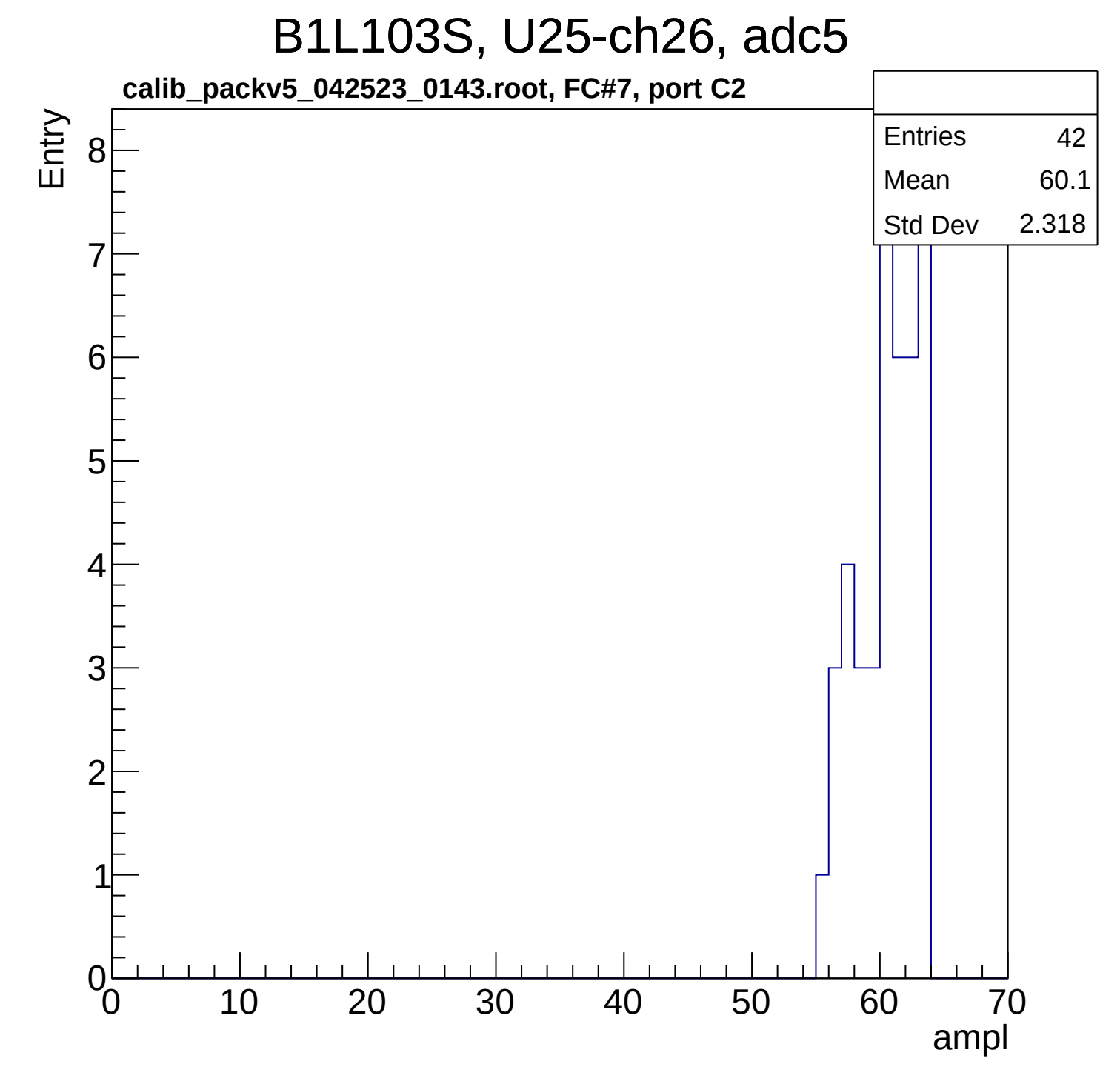

B1L103S, U25-ch26, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 28.66
Std Dev 5.025
Gaus mean : 29.2041
Gaus Width: 4.1691
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

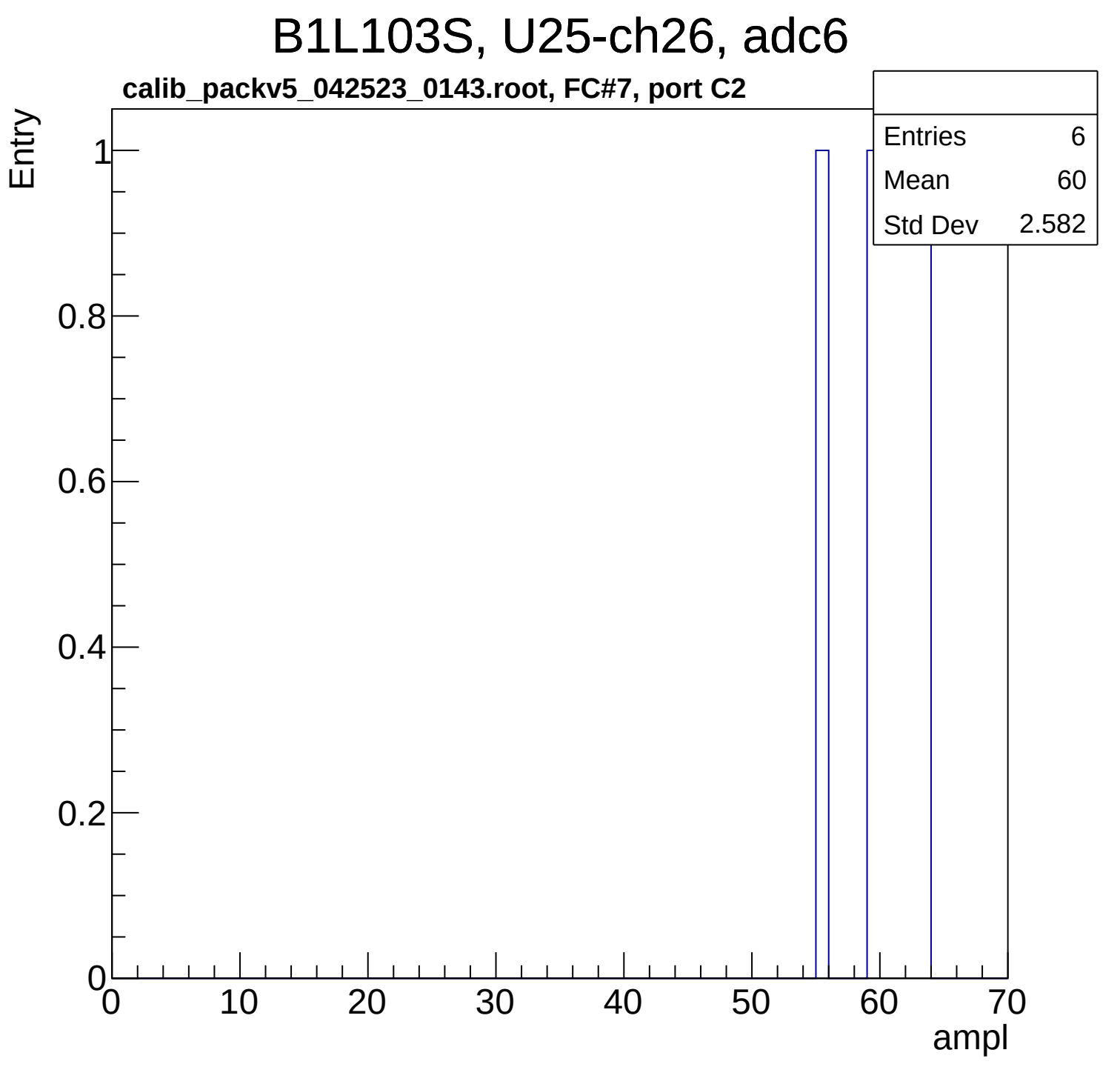

B1L103S, U25-ch26, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.1
Std Dev 4.04
Gaus mean : 36.4298
Gaus Width: 3.8751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch26, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 42.86
Std Dev 2.929
Gaus mean : 43.9311
Gaus Width: 2.8054
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch26, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 49
Std Dev 3.033
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch26, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 53.97
Std Dev 7.173
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch26, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 60.1
Std Dev 2.318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch26, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 60
Std Dev 2.582
Entry
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0.6
0.4
0.2
0
0
10
20
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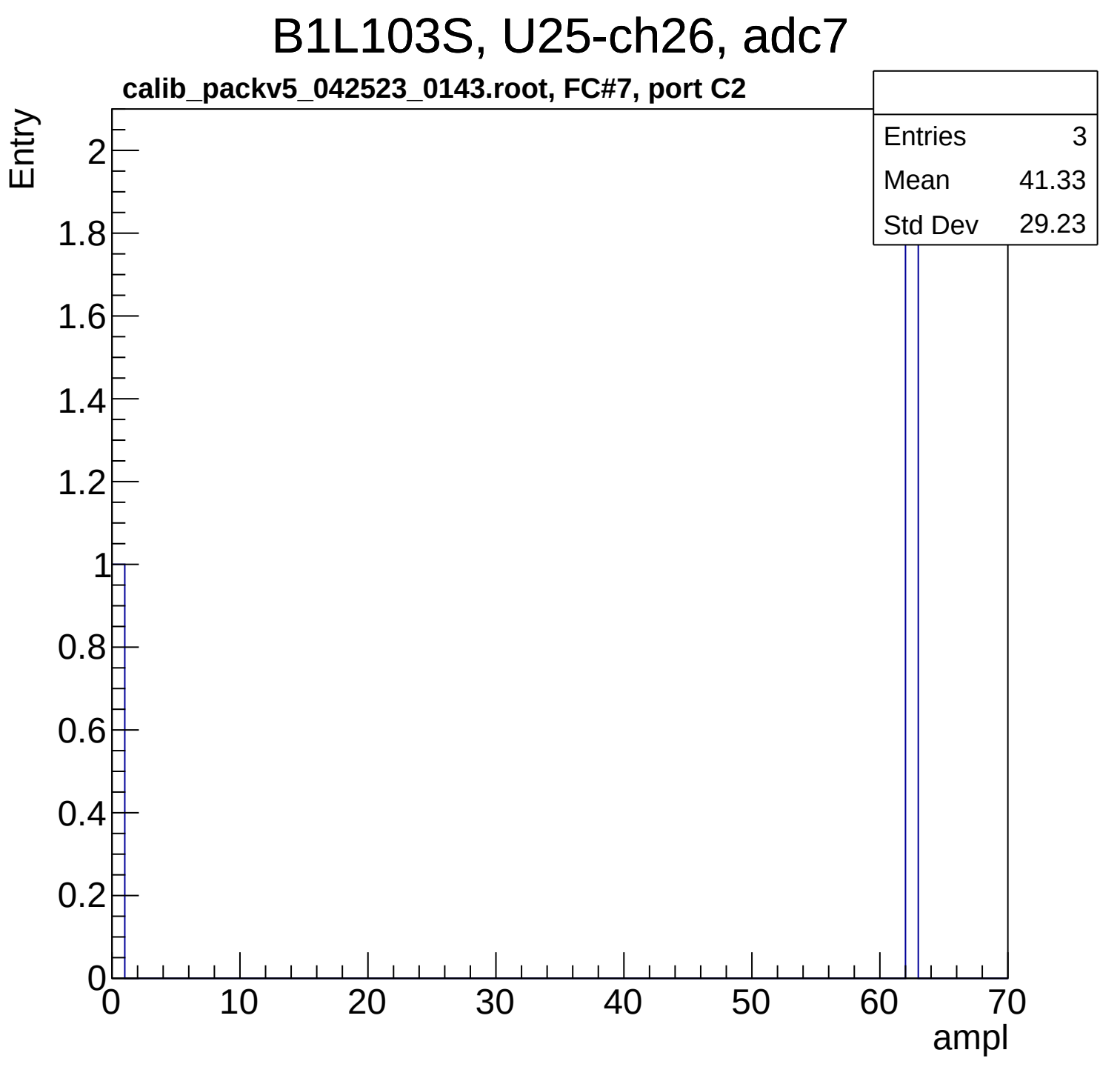
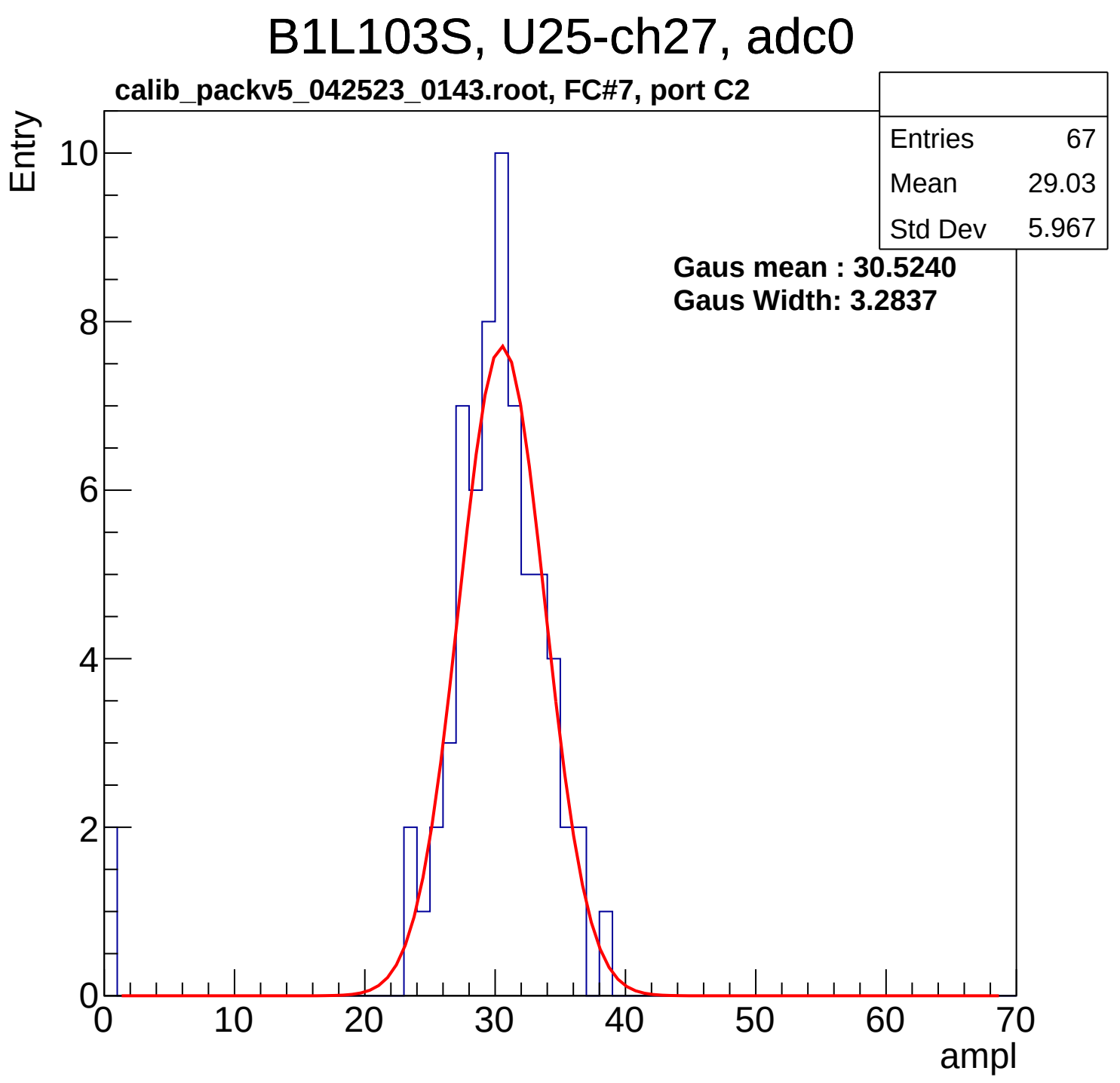
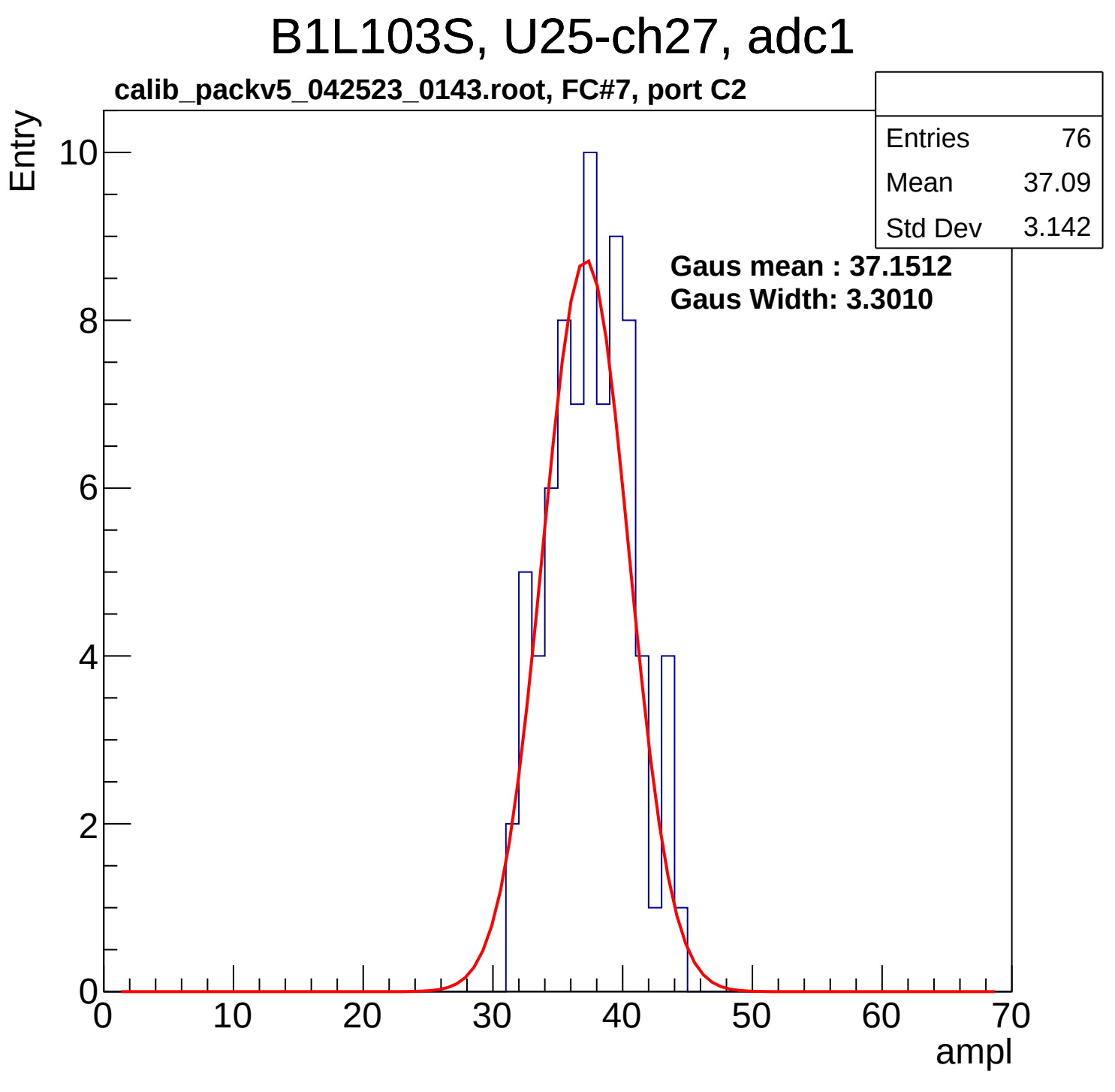
40
50
60
70
ampl


B1L103S, U25-ch26, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch27, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.03
Std Dev 5.967
Gaus mean : 30.5240
Gaus Width: 3.2837
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch27, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 37.09
Std Dev 3.142
Gaus mean : 37.1512
Gaus Width: 3.3010
Entry
ampl
0
2
4
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60
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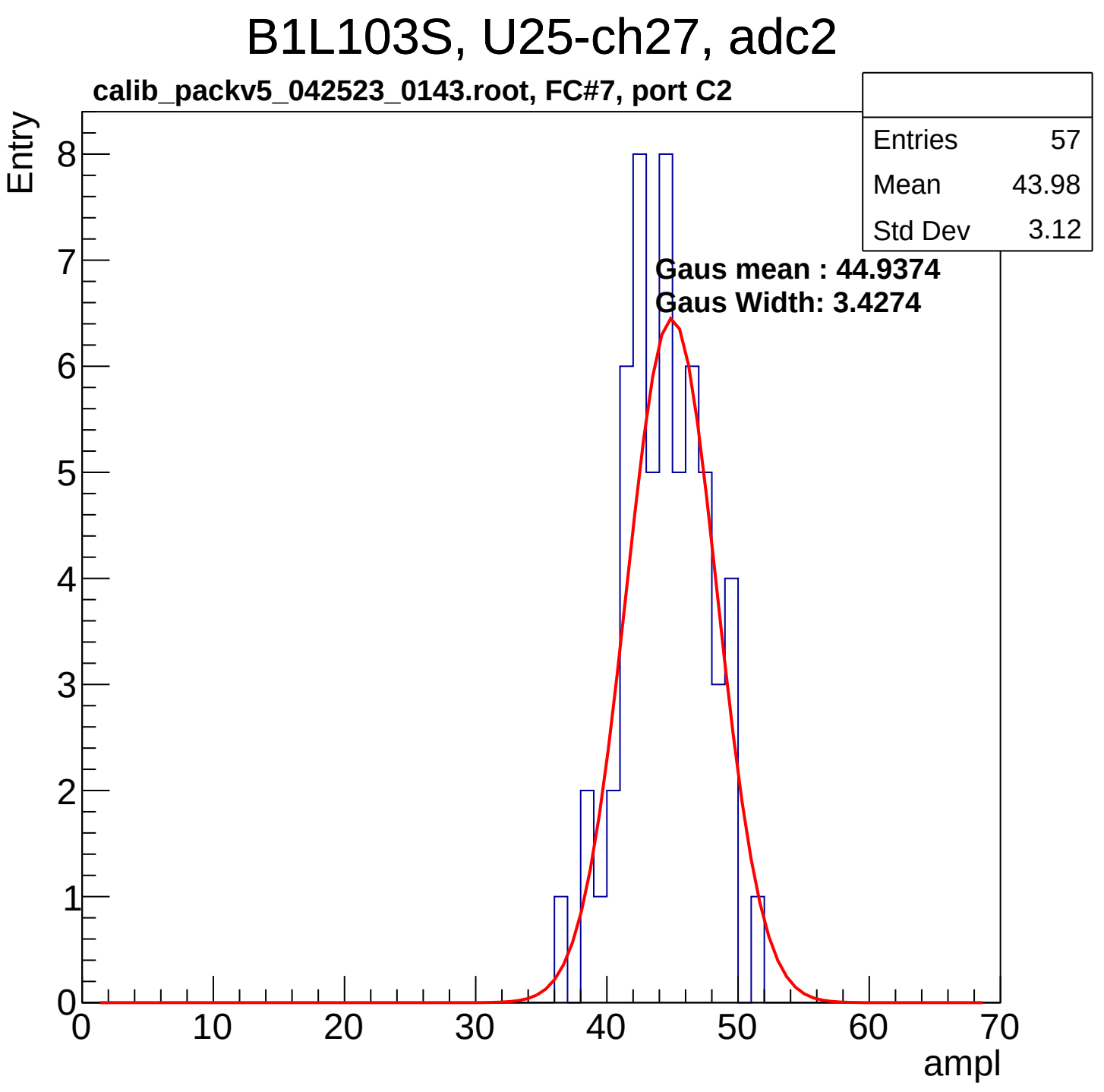
B1L103S, U25-ch27, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.98
Std Dev 3.12
Gaus mean : 44.9374
Gaus Width: 3.4274
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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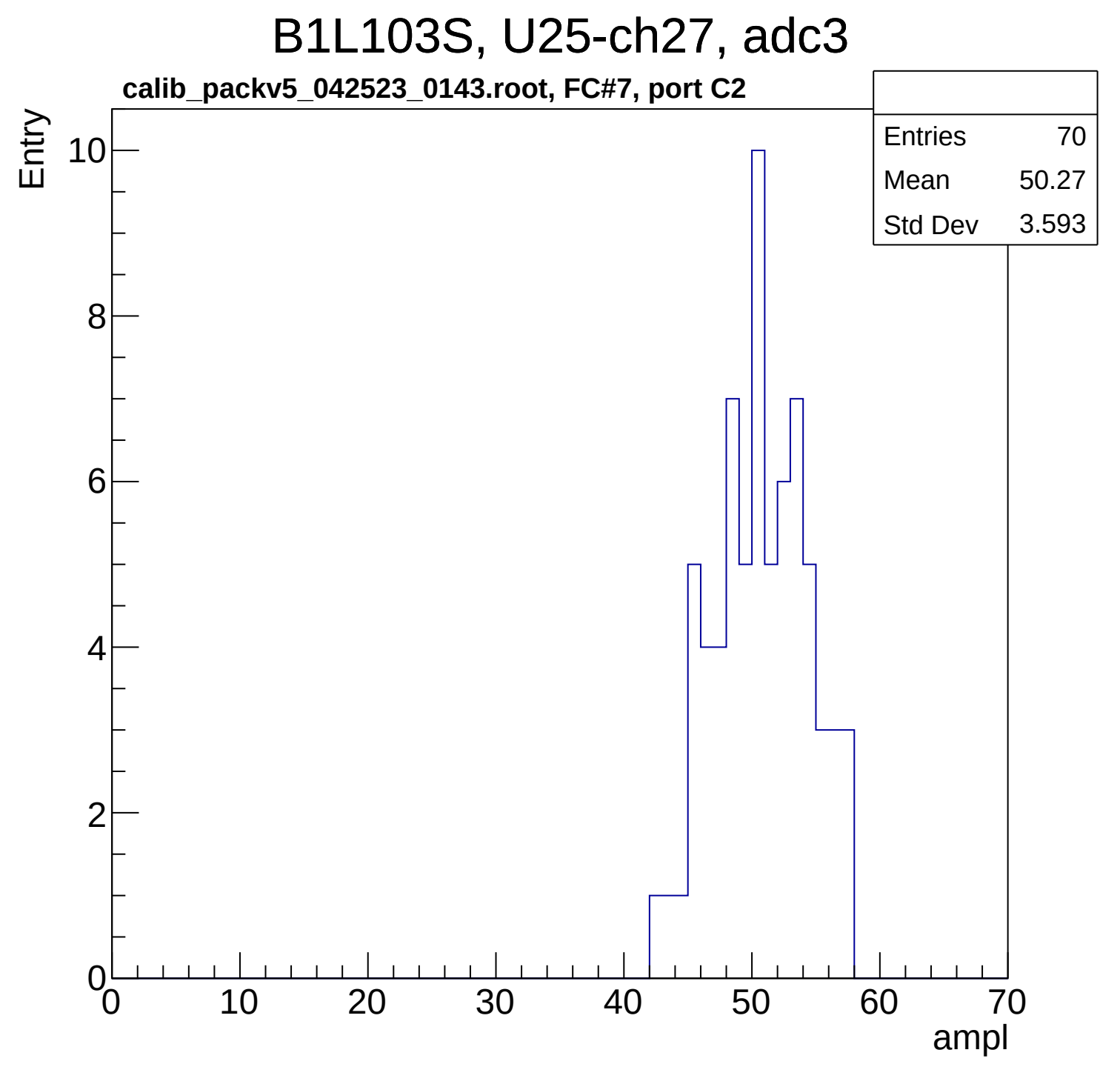
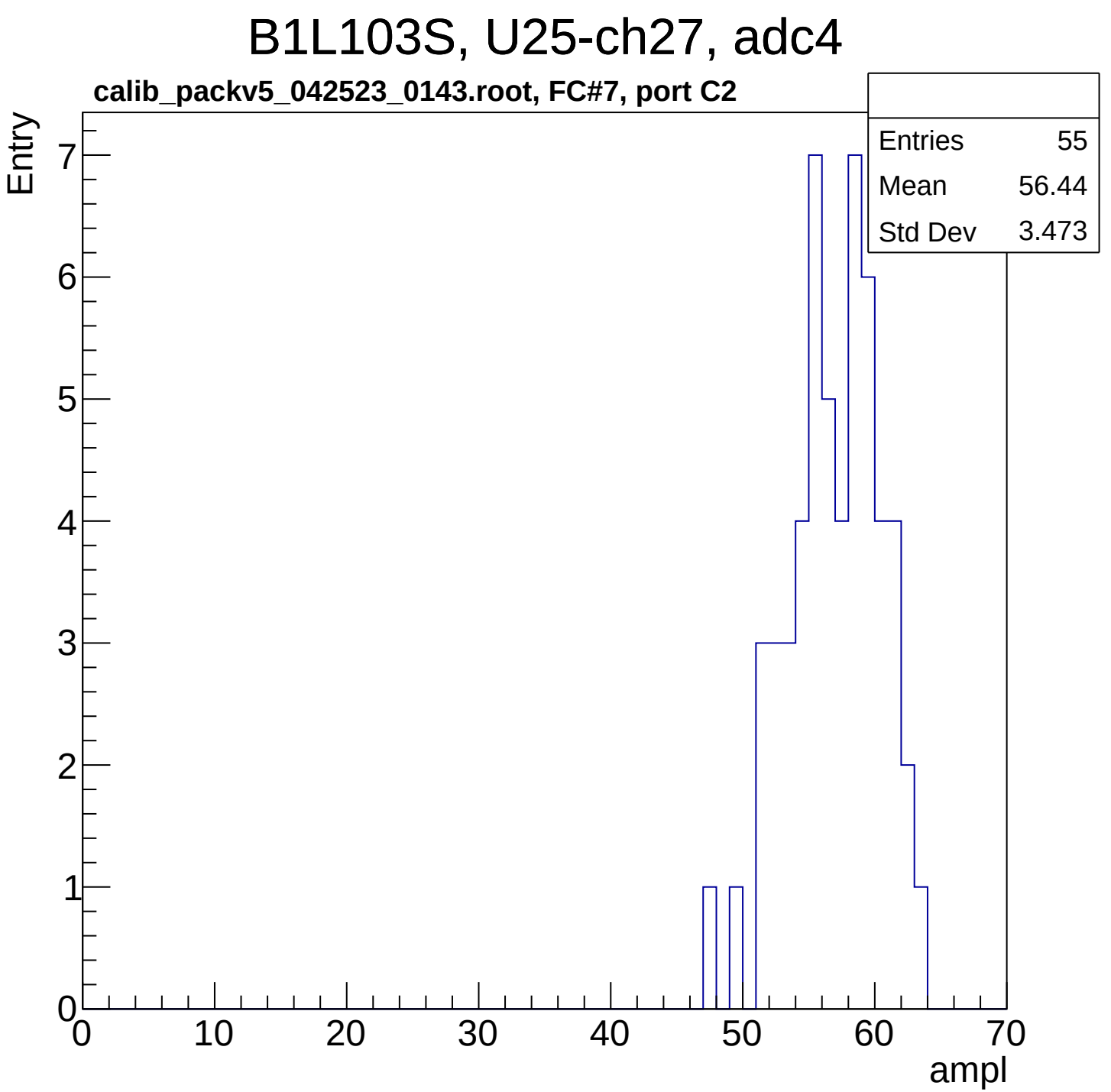

B1L103S, U25-ch27, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 50.27
Std Dev 3.593
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch27, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 56.44
Std Dev 3.473
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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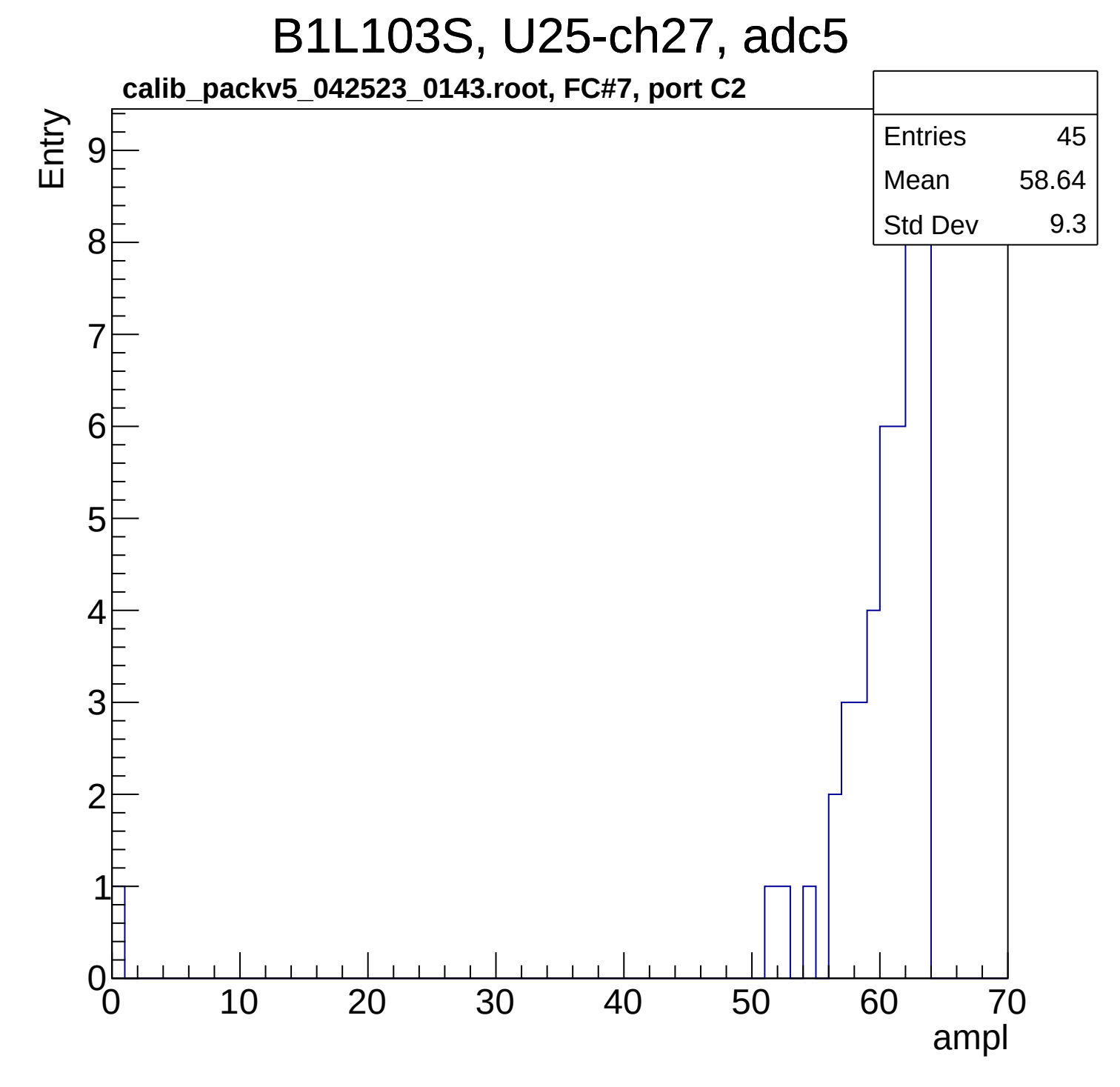
B1L103S, U25-ch27, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.64
Std Dev 9.3
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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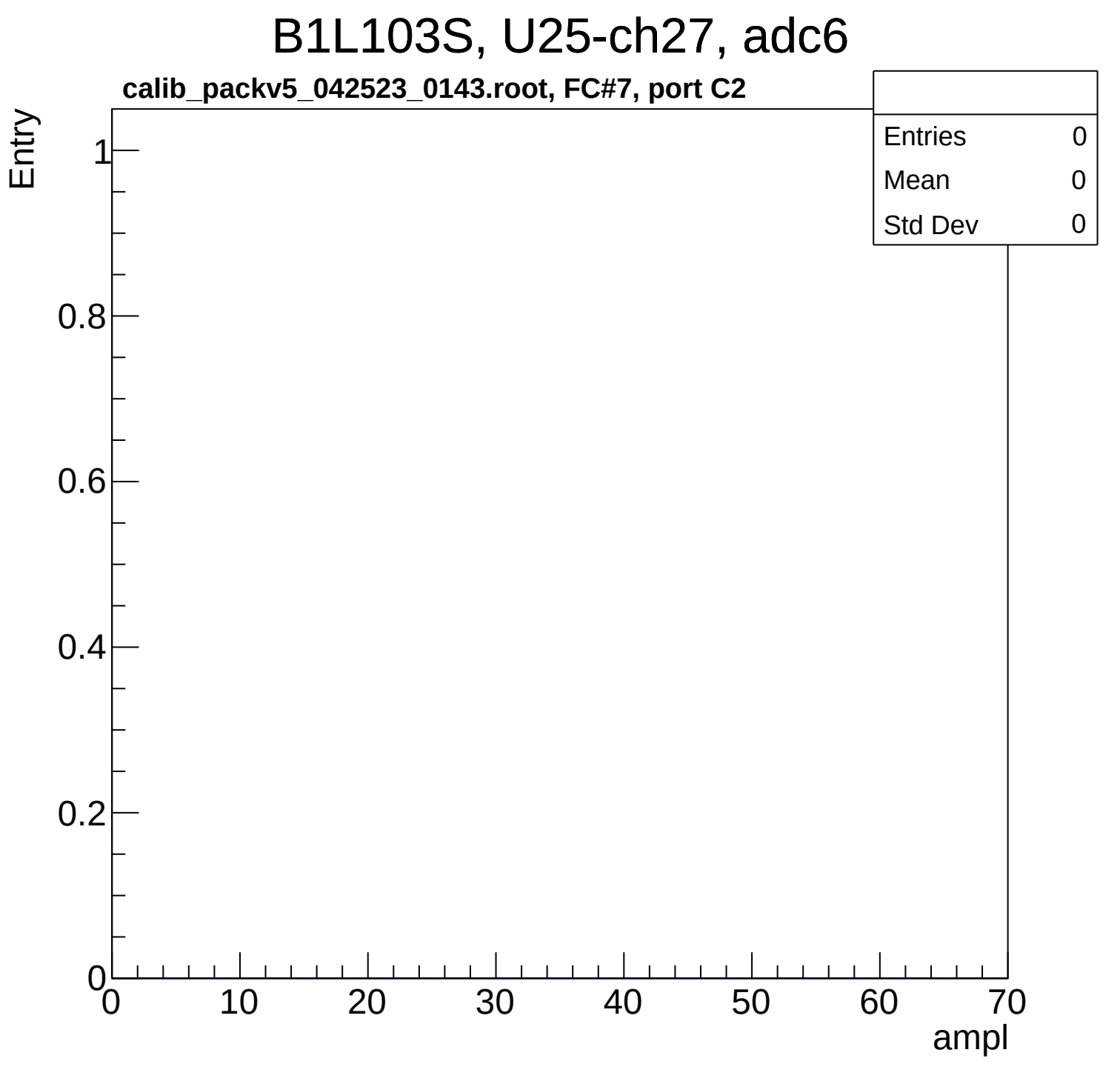
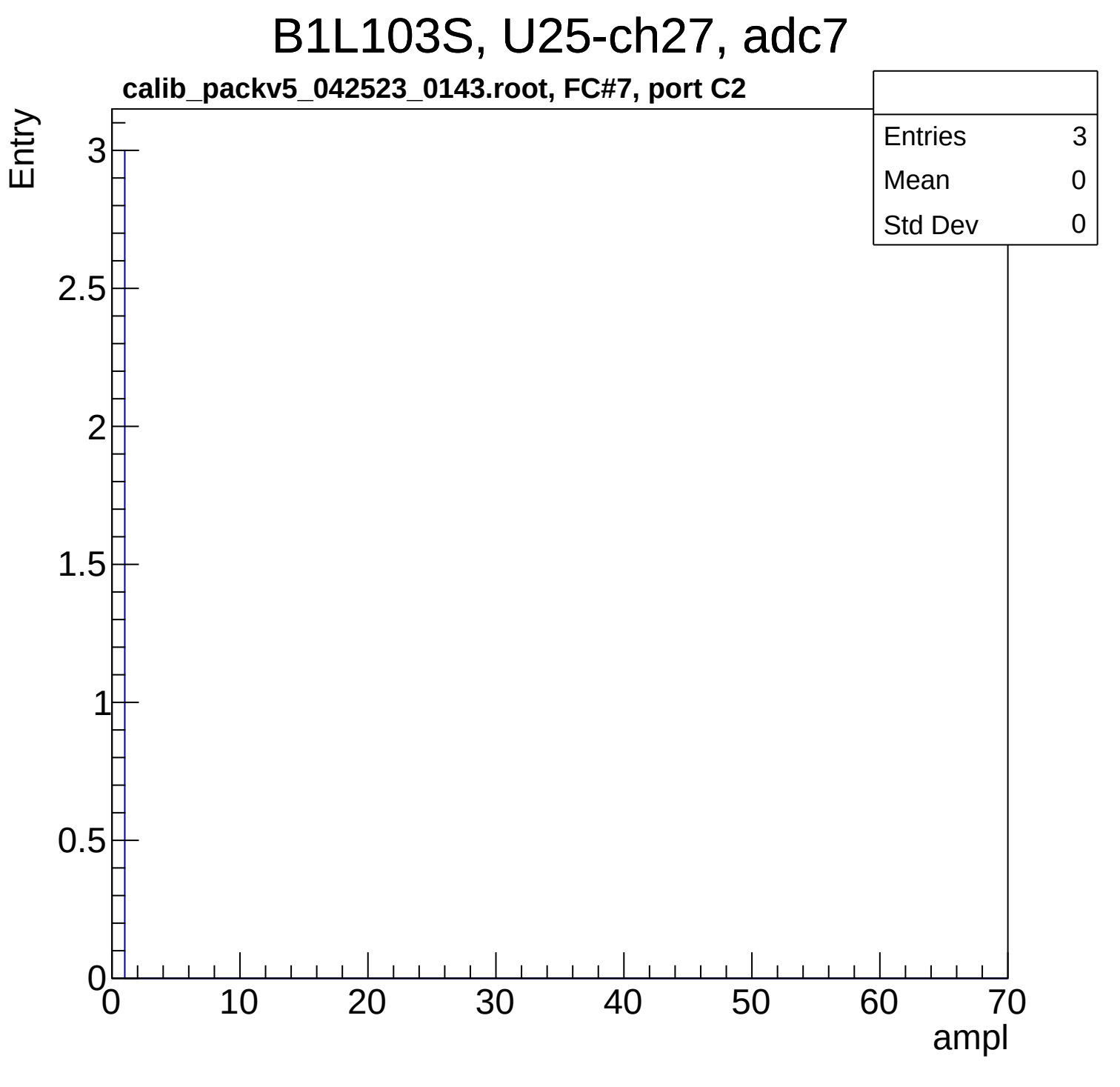

B1L103S, U25-ch27, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch27, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
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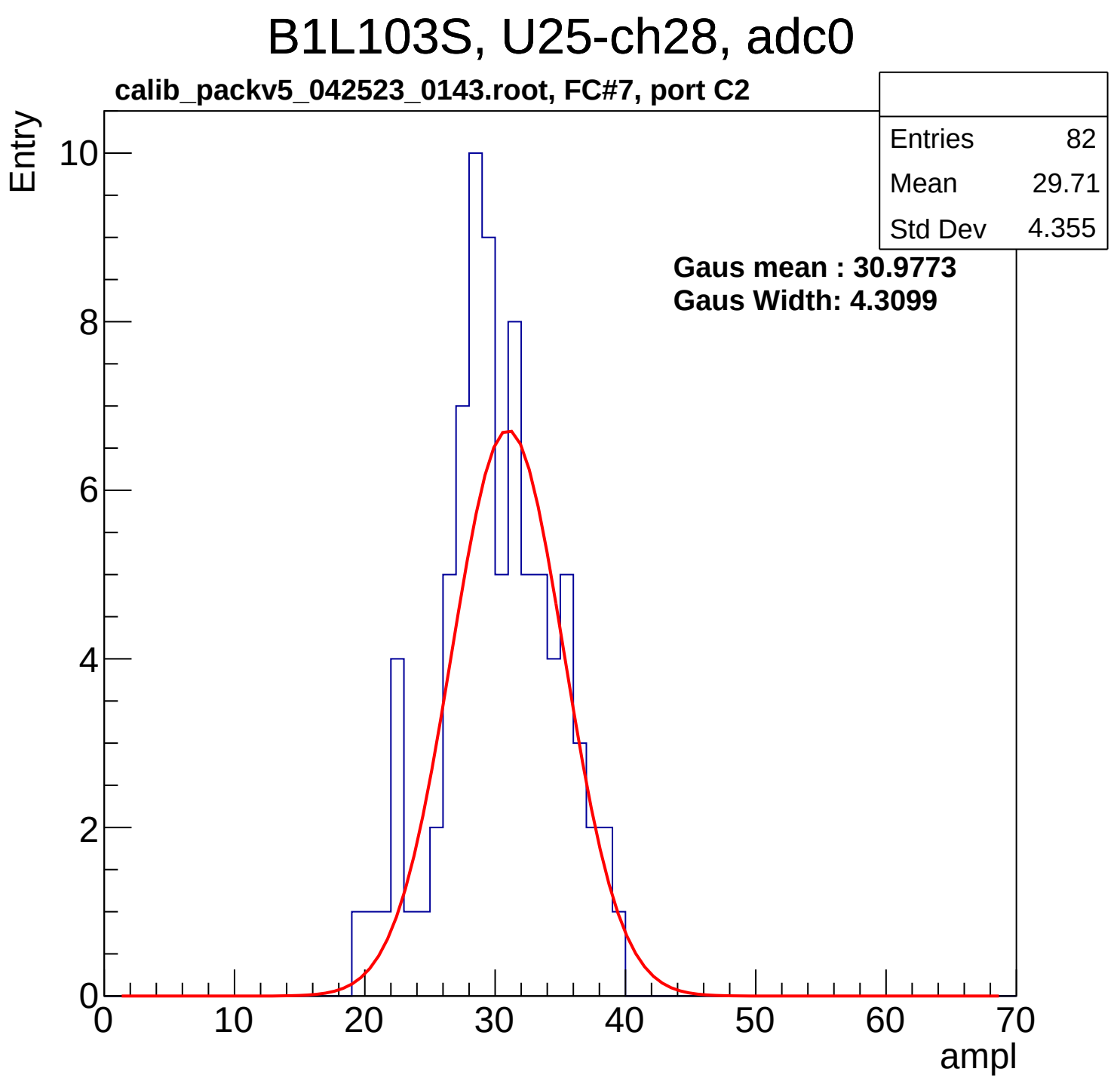
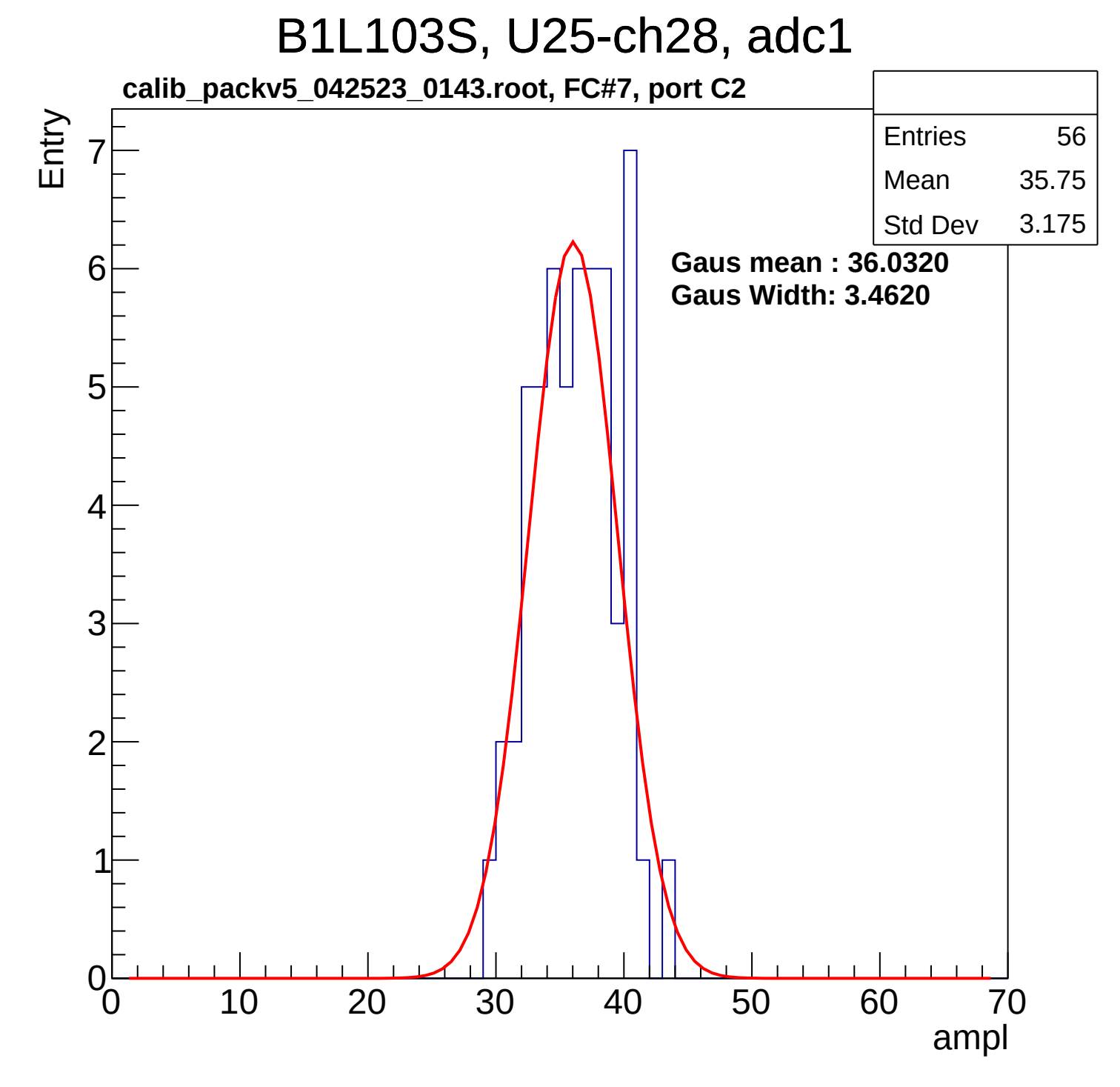
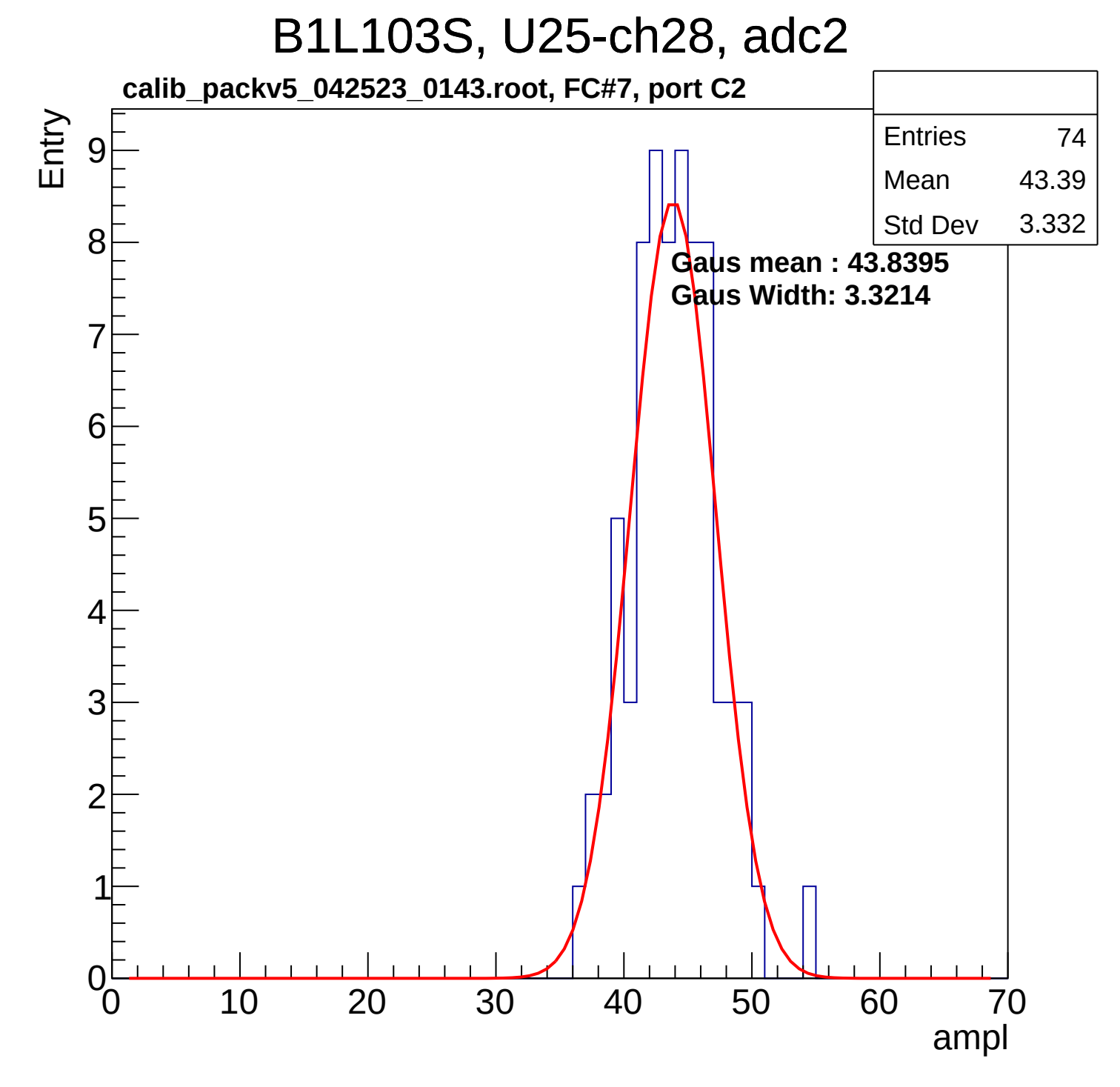
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch28, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 29.71
Std Dev 4.355
Gaus mean : 30.9773
Gaus Width: 4.3099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch28, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 35.75
Std Dev 3.175
Gaus mean : 36.0320
Gaus Width: 3.4620
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch28, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 43.39
Std Dev 3.332
Gaus mean : 43.8395
Gaus Width: 3.3214
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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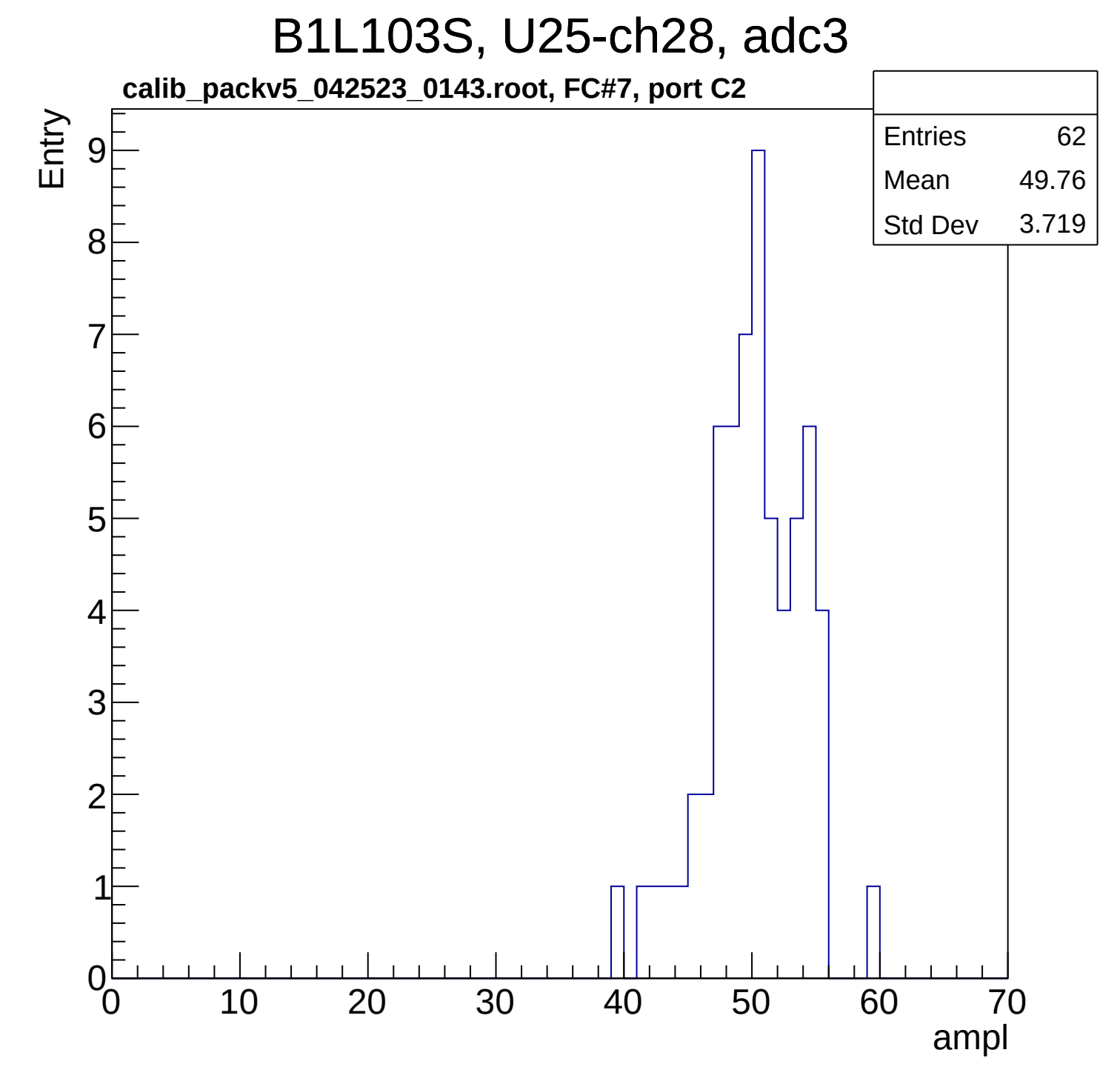
B1L103S, U25-ch28, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 49.76
Std Dev 3.719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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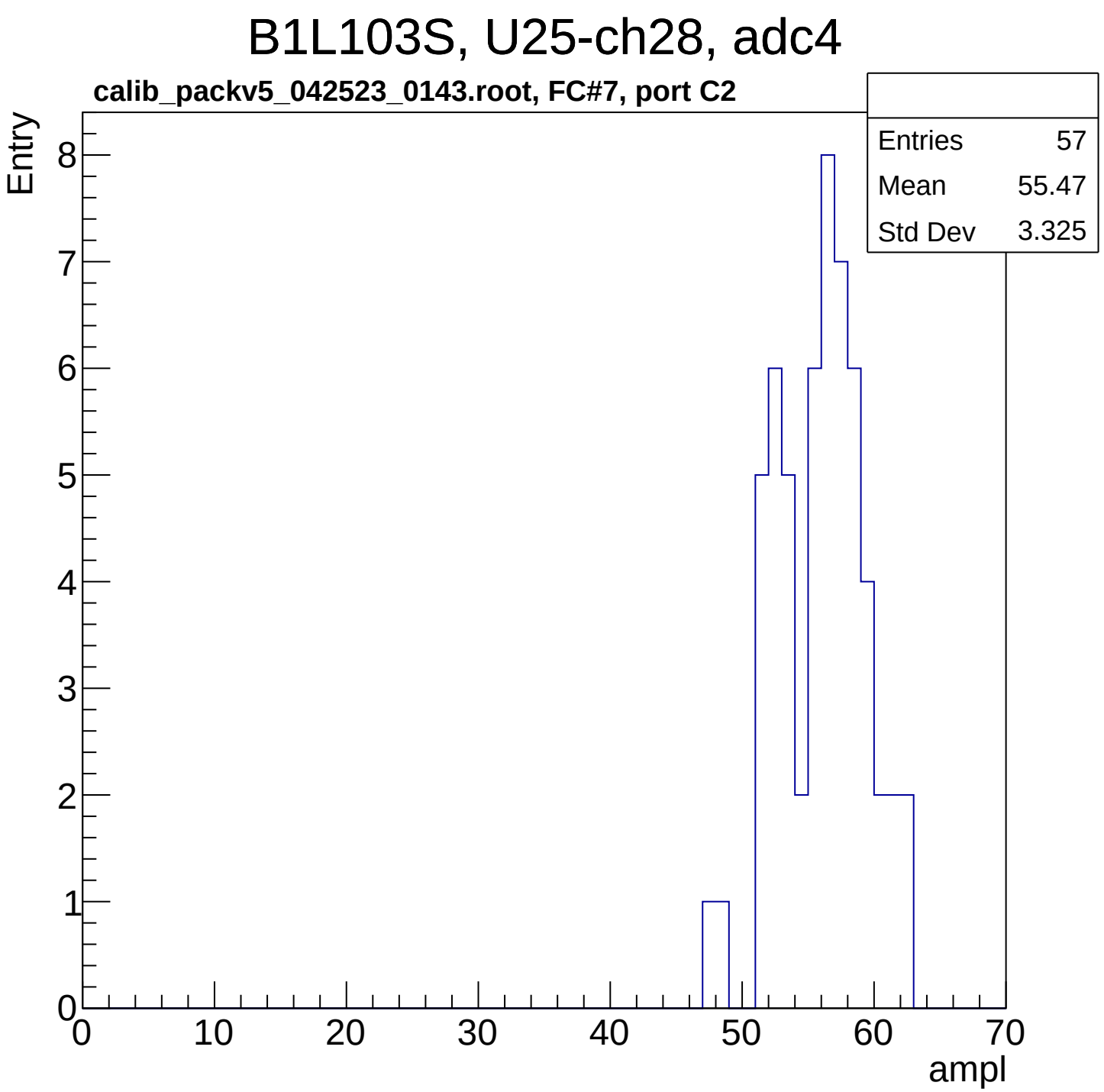
B1L103S, U25-ch28, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.47
Std Dev 3.325
Entry
ampl
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3
4
5
6
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0
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20
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40
50
60
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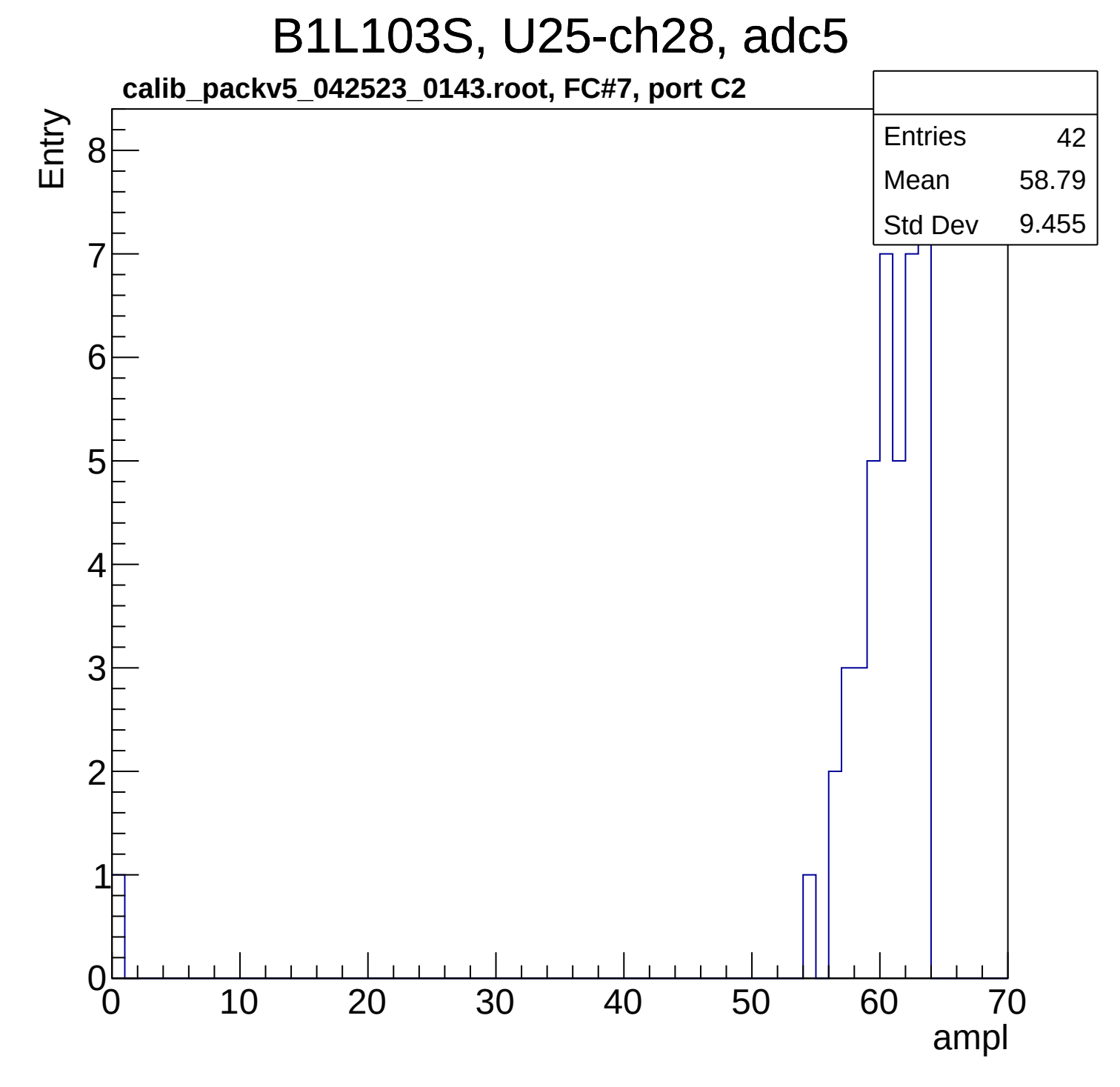
B1L103S, U25-ch28, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.79
Std Dev 9.455
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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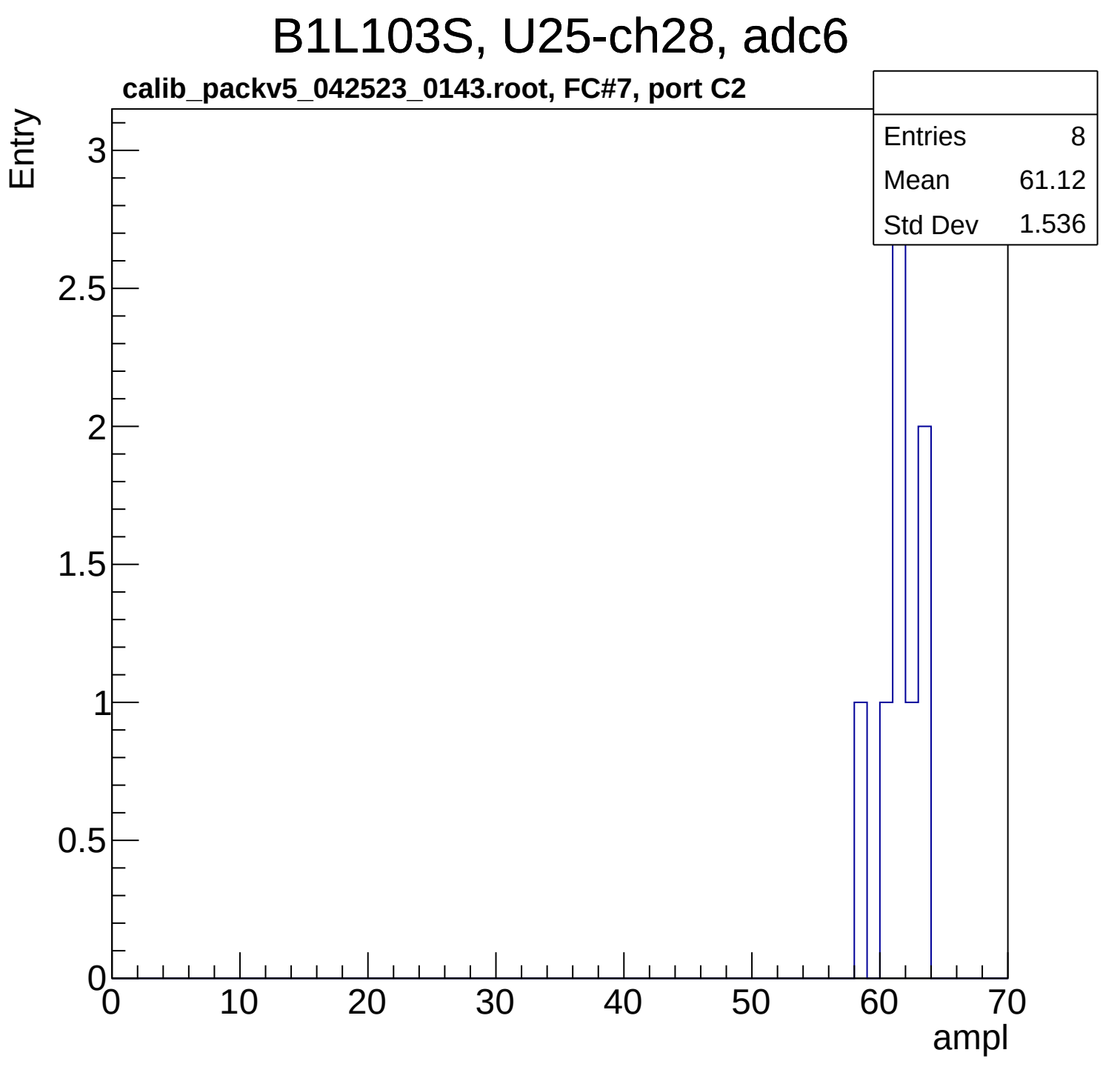
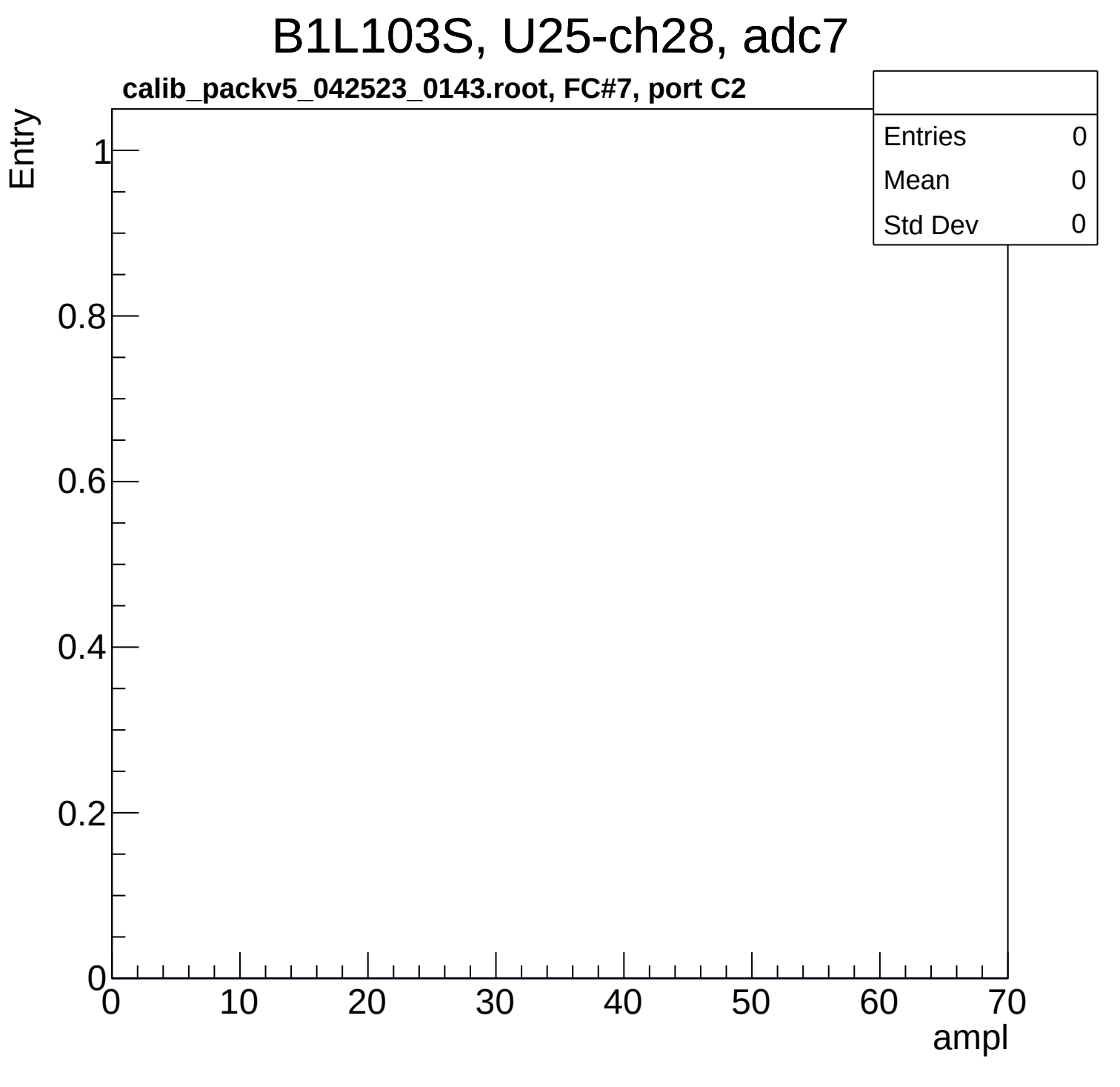
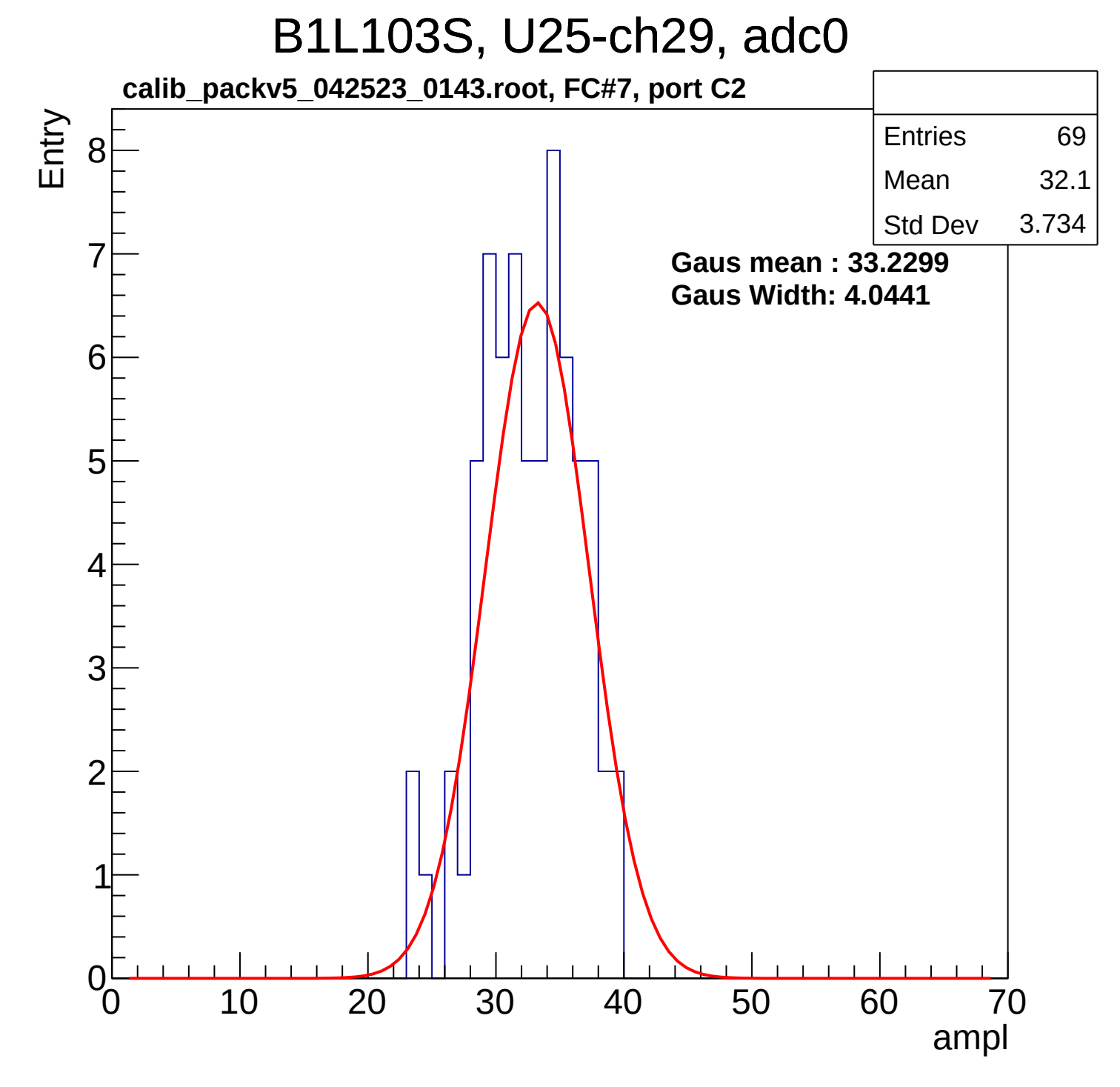

B1L103S, U25-ch28, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.12
Std Dev 1.536
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch28, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch29, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 32.1
Std Dev 3.734
Gaus mean : 33.2299
Gaus Width: 4.0441
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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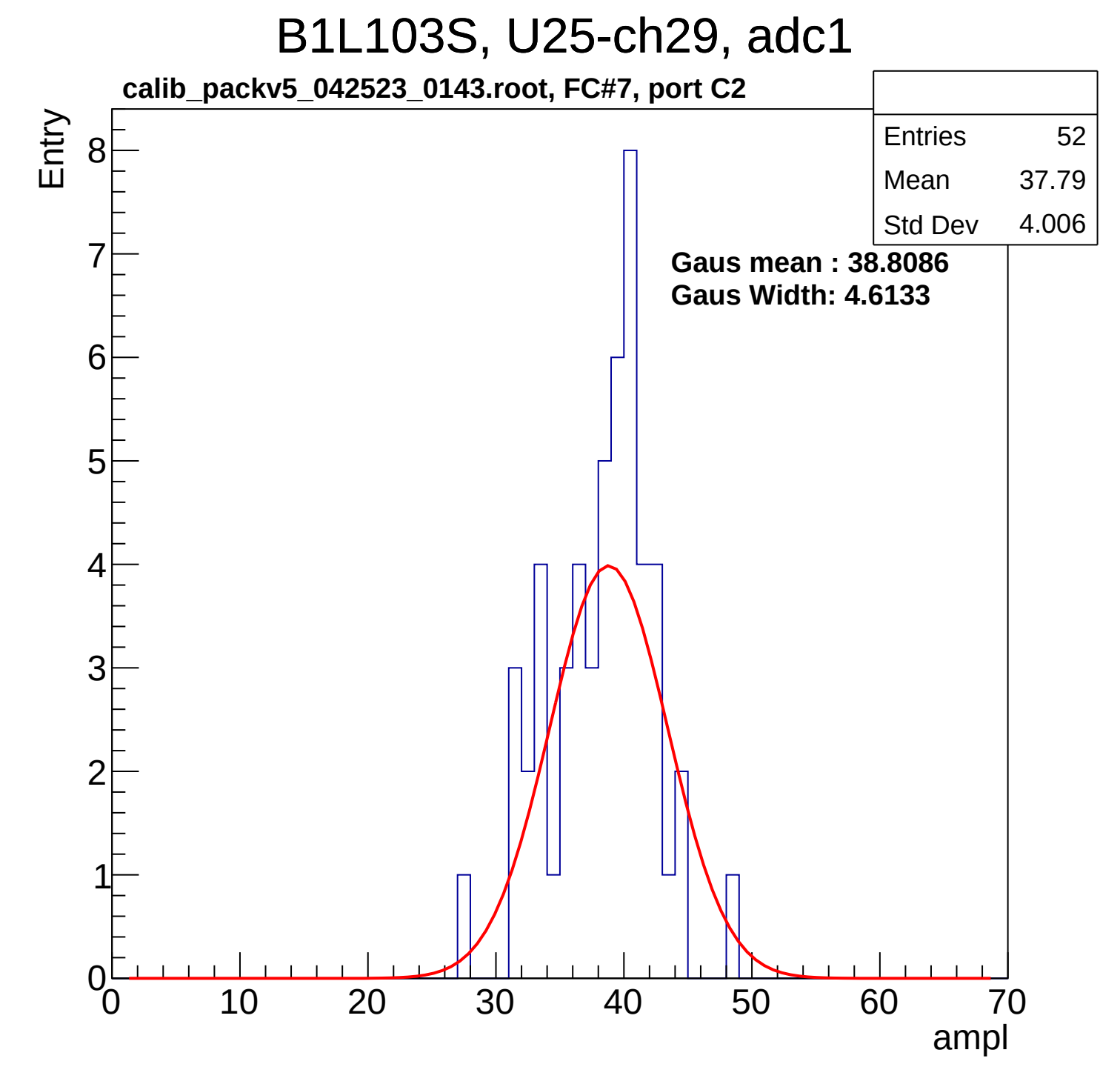
B1L103S, U25-ch29, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 37.79
Std Dev 4.006
Gaus mean : 38.8086
Gaus Width: 4.6133
Entry
ampl
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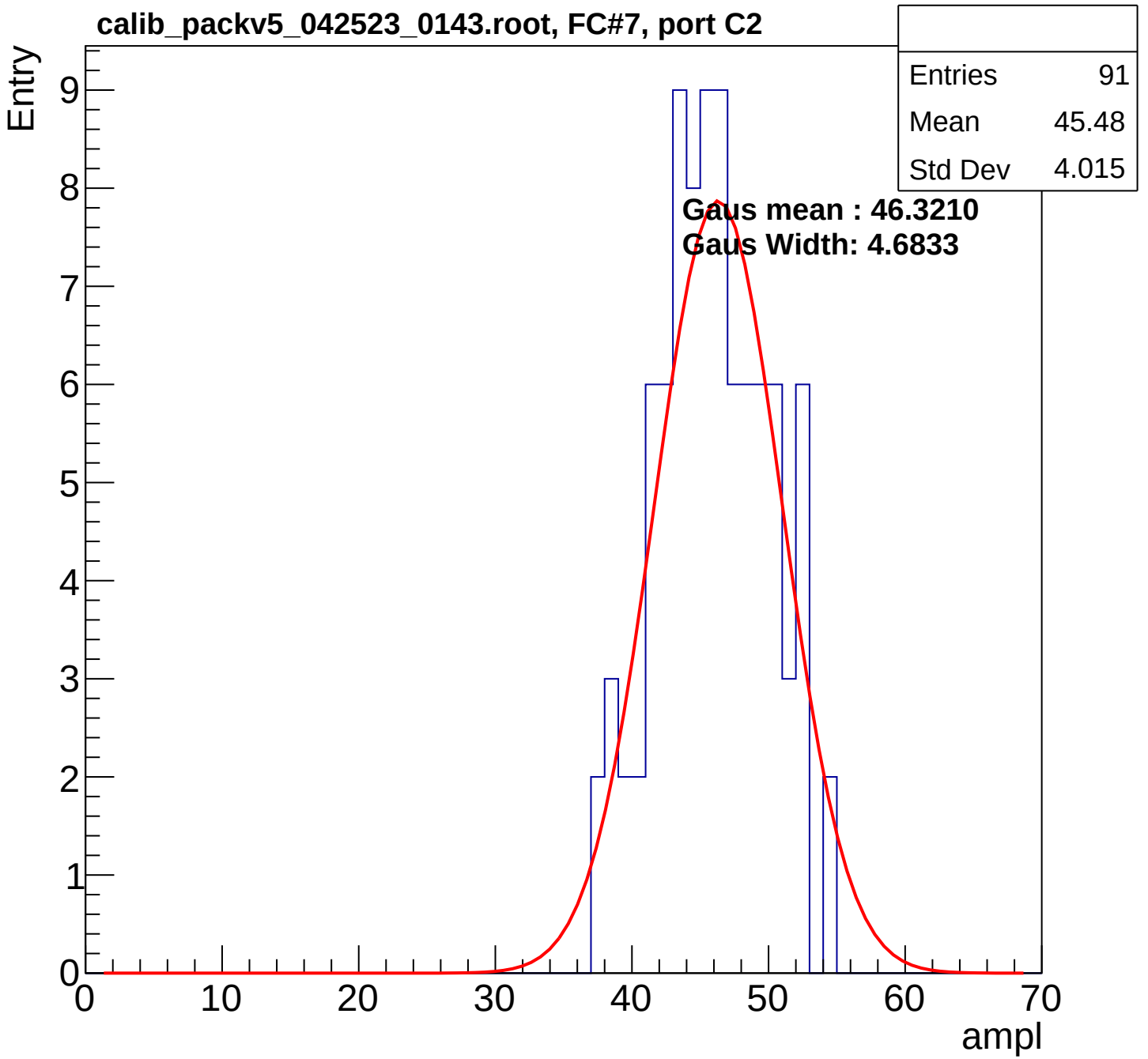
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch29, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 45.48
Std Dev 4.015
Gaus mean : 46.3210
Gaus Width: 4.6833
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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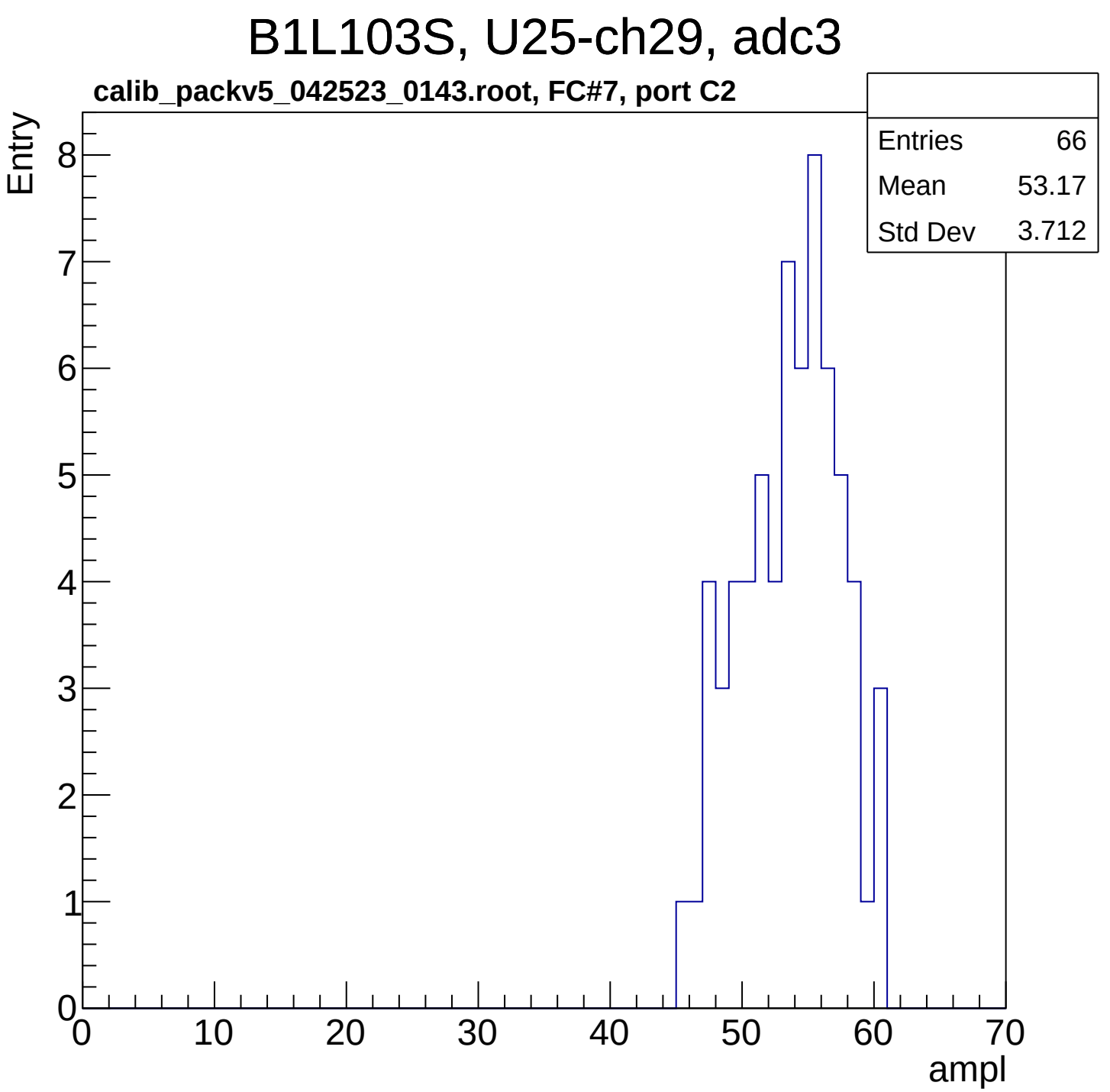
B1L103S, U25-ch29, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 53.17
Std Dev 3.712
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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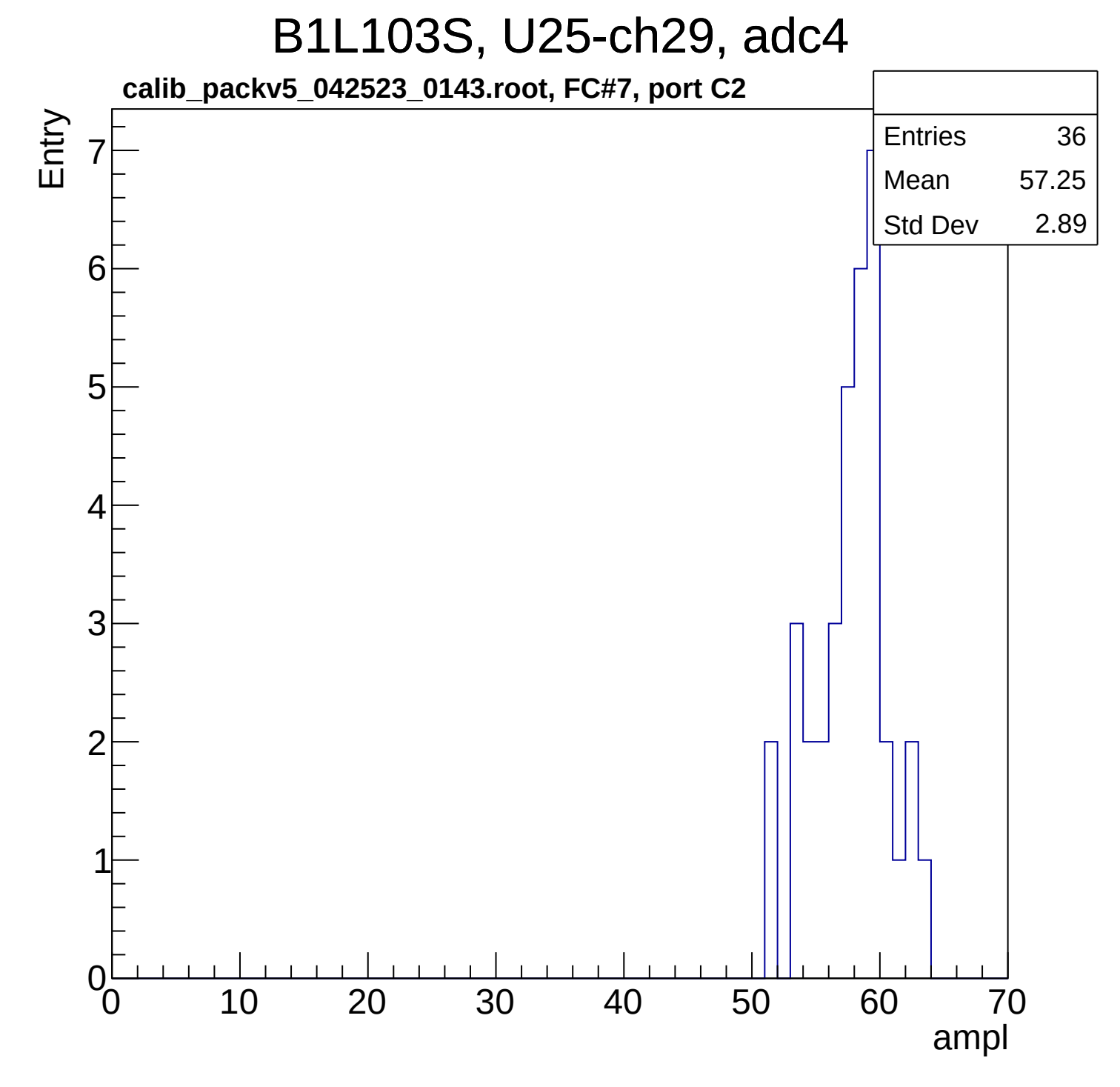
B1L103S, U25-ch29, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 57.25
Std Dev 2.89
Entry
ampl
0
1
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4
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0
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40
50
60
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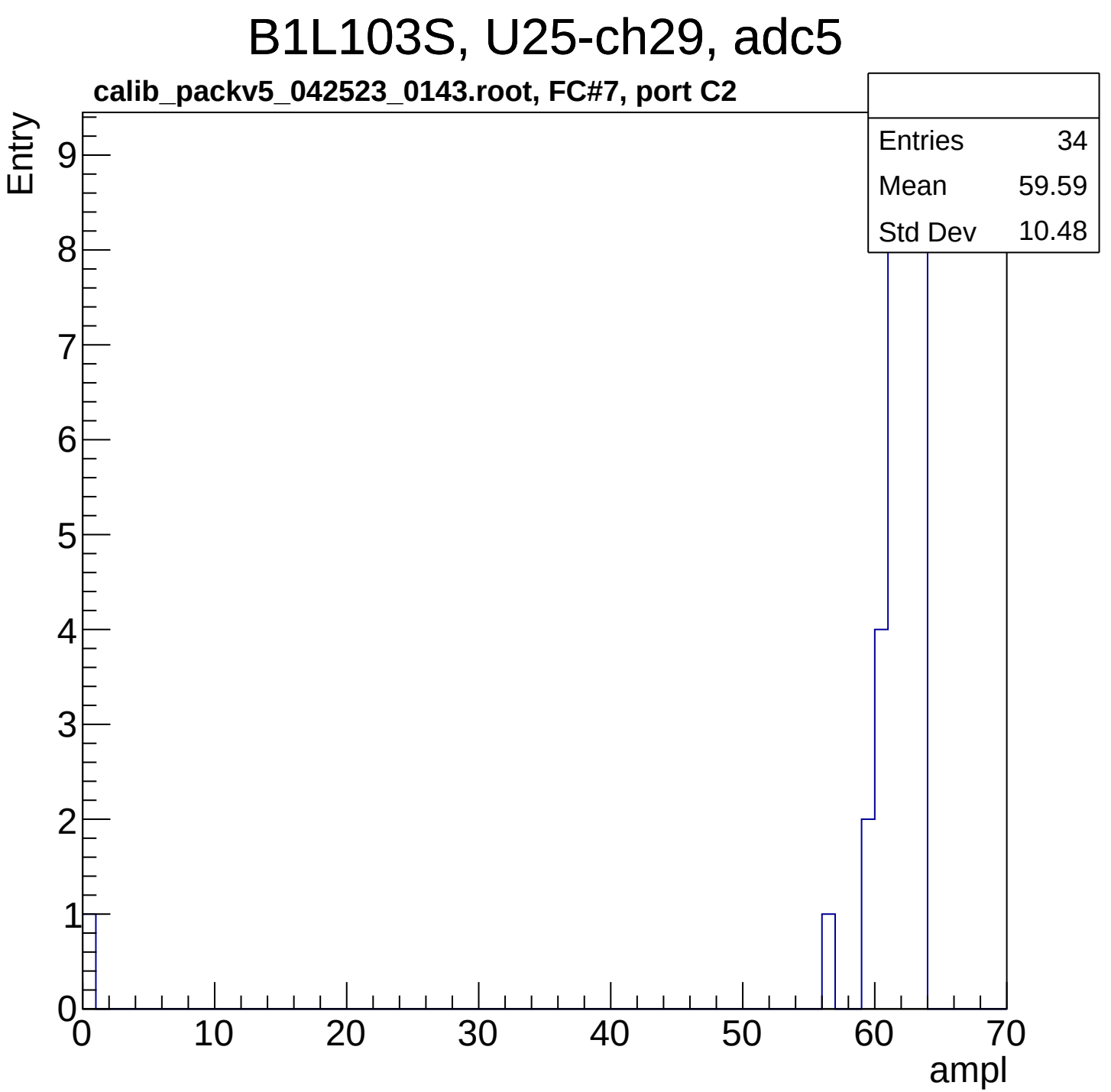
B1L103S, U25-ch29, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59.59
Std Dev 10.48
Entry
ampl
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6
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0
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40
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60
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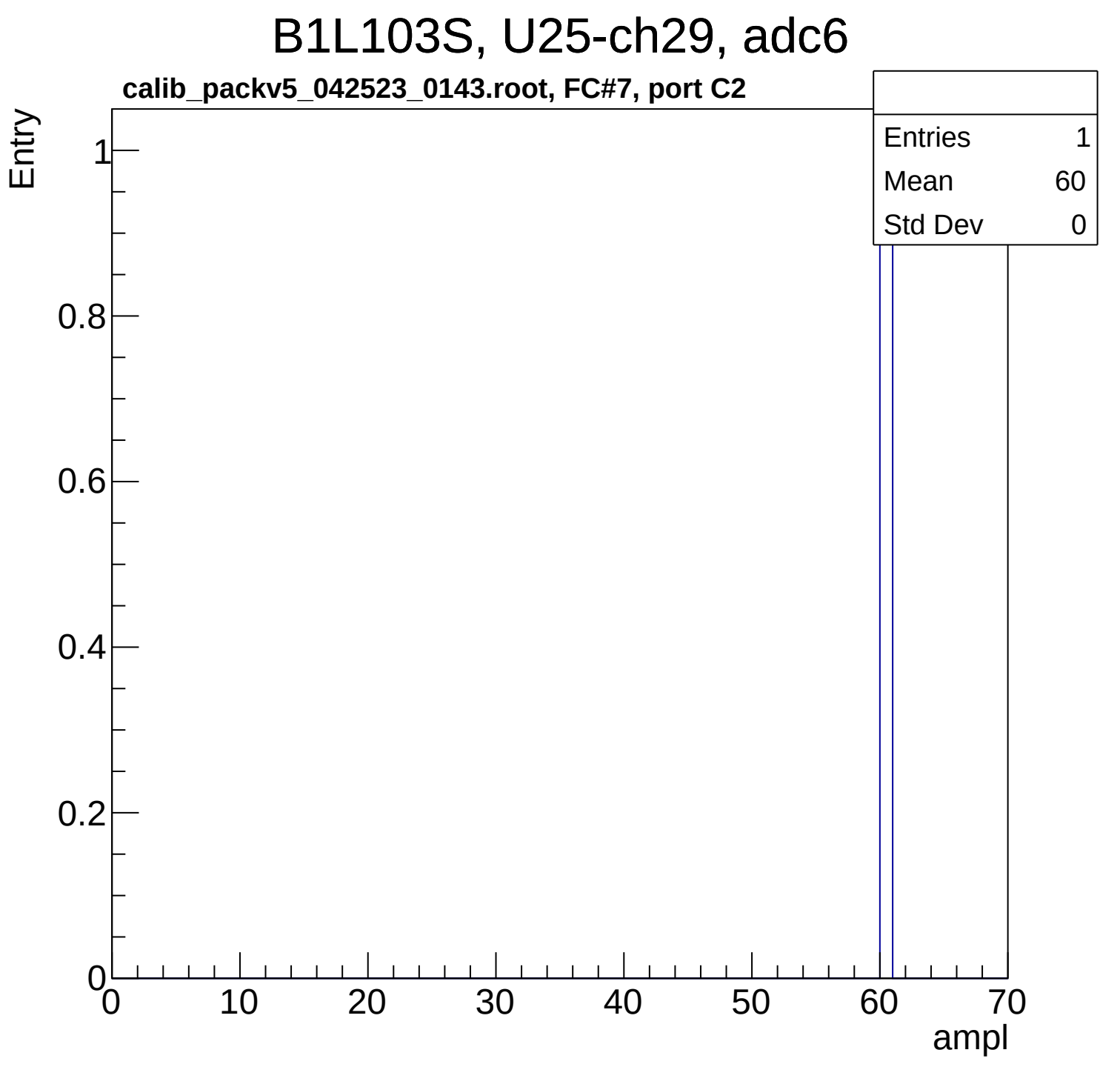
B1L103S, U25-ch29, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 60
Std Dev 0
Entry
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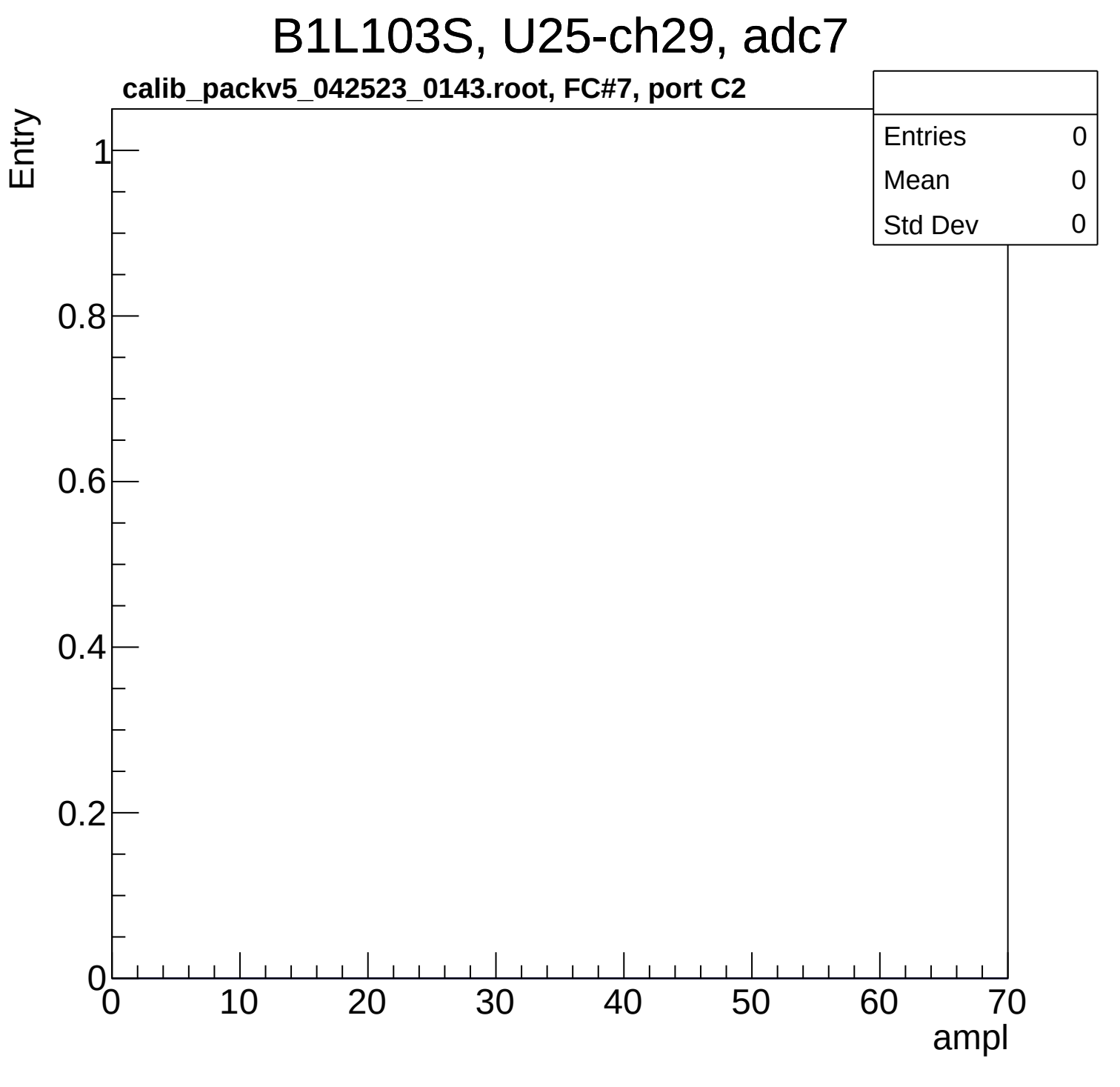
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch29, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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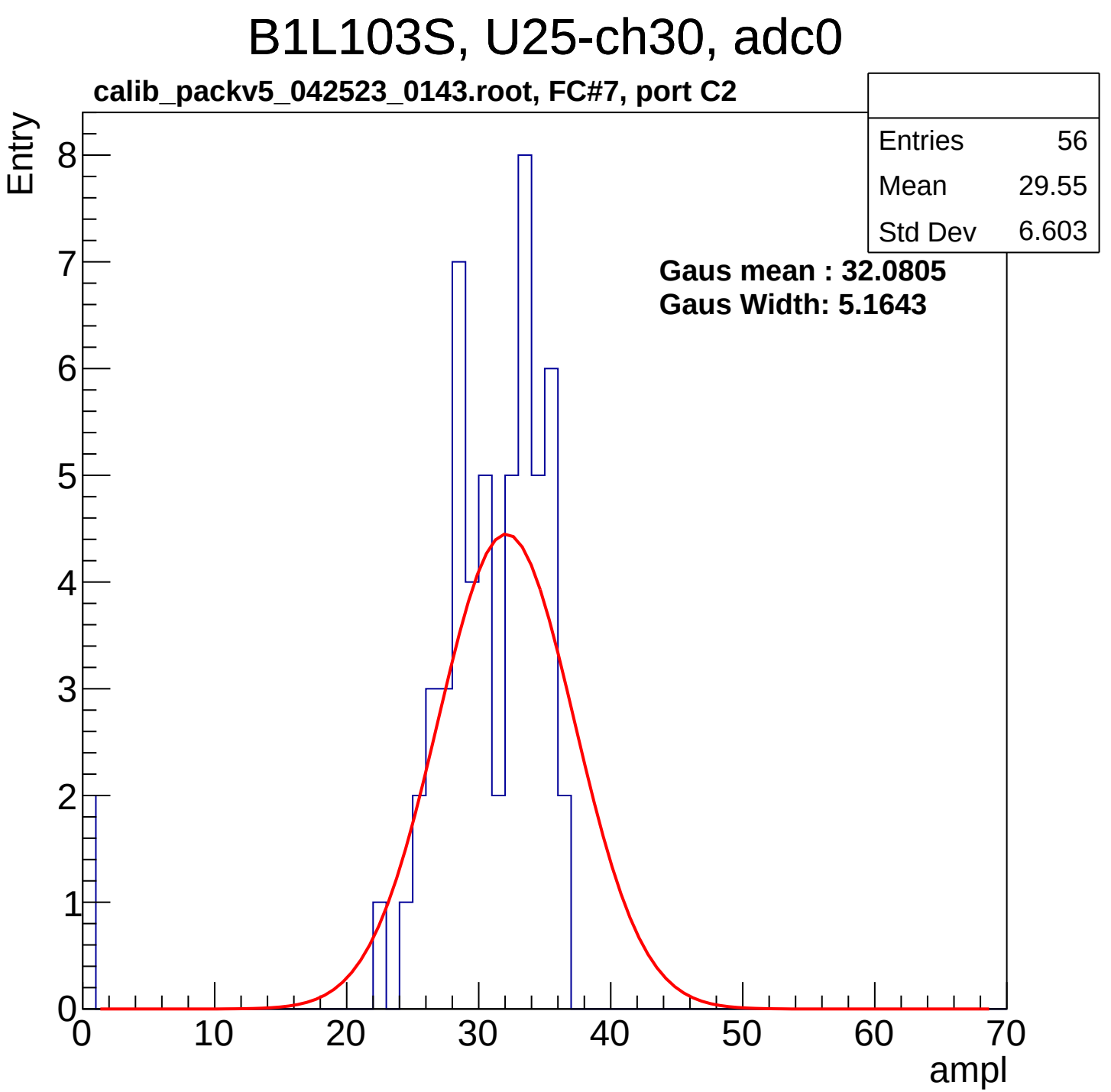
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch30, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 29.55
Std Dev 6.603
Gaus mean : 32.0805
Gaus Width: 5.1643
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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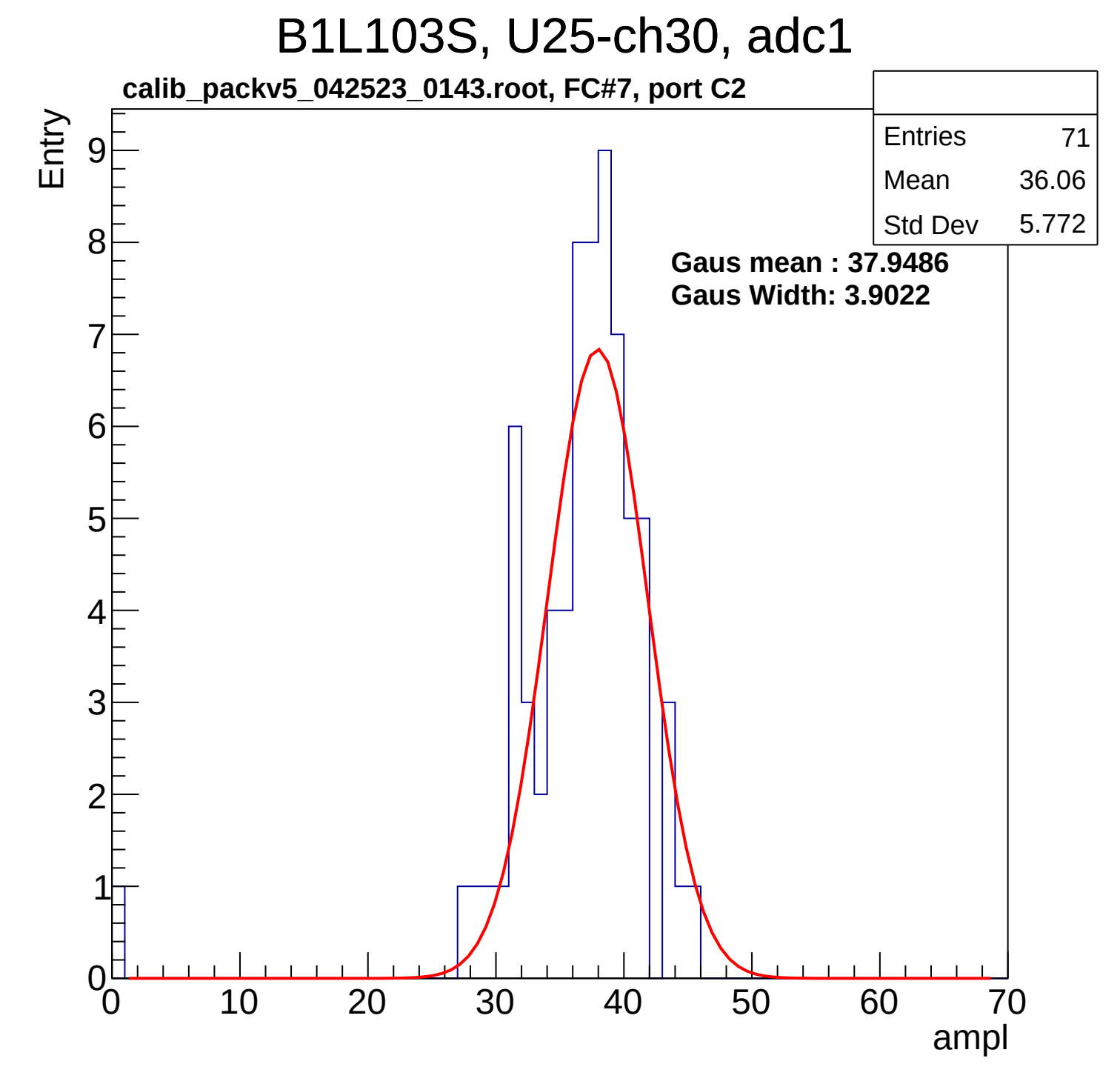
B1L103S, U25-ch30, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.06
Std Dev 5.772
Gaus mean : 37.9486
Gaus Width: 3.9022
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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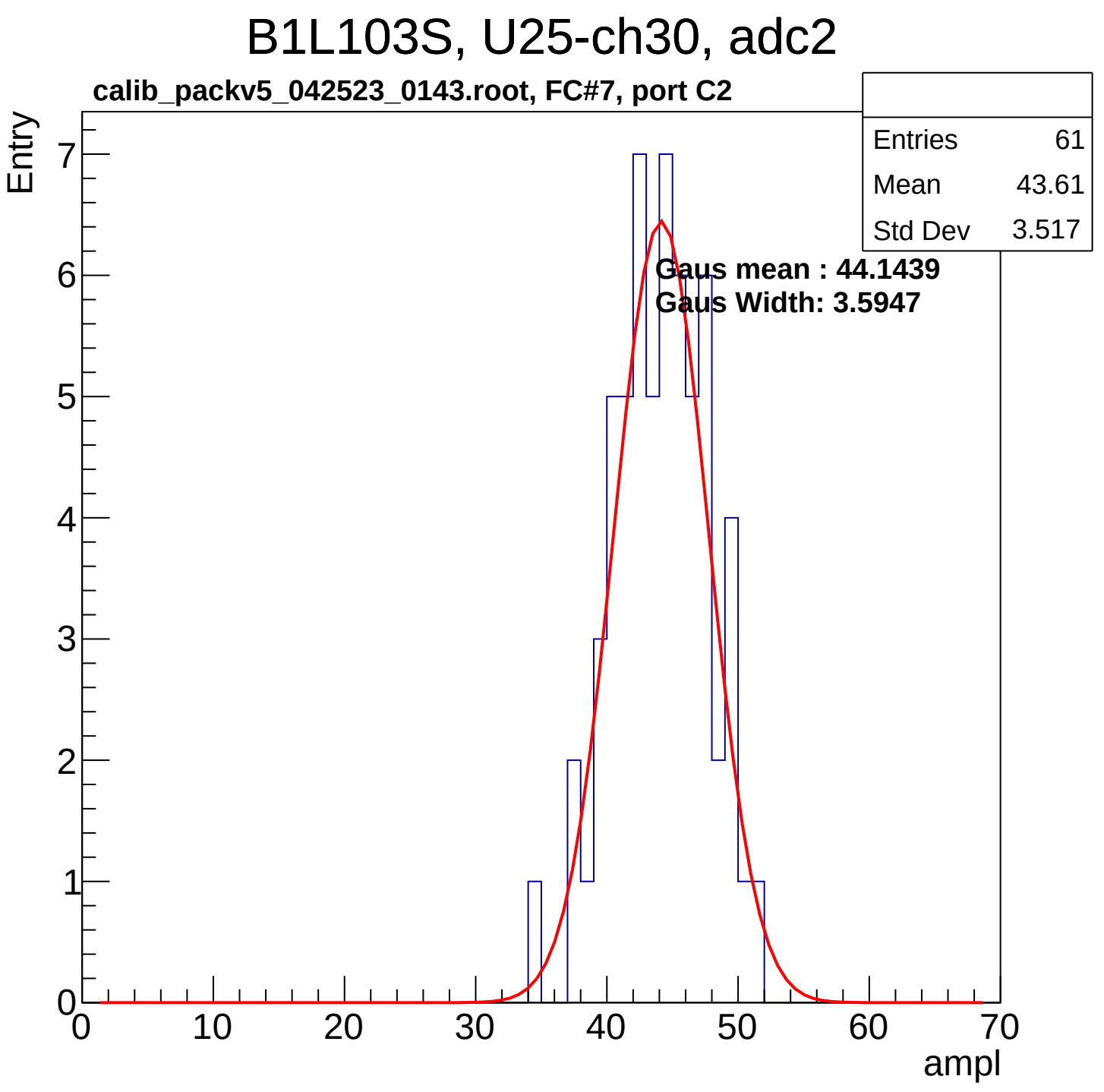
B1L103S, U25-ch30, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.61
Std Dev 3.517
Gaus mean : 44.1439
Gaus Width: 3.5947
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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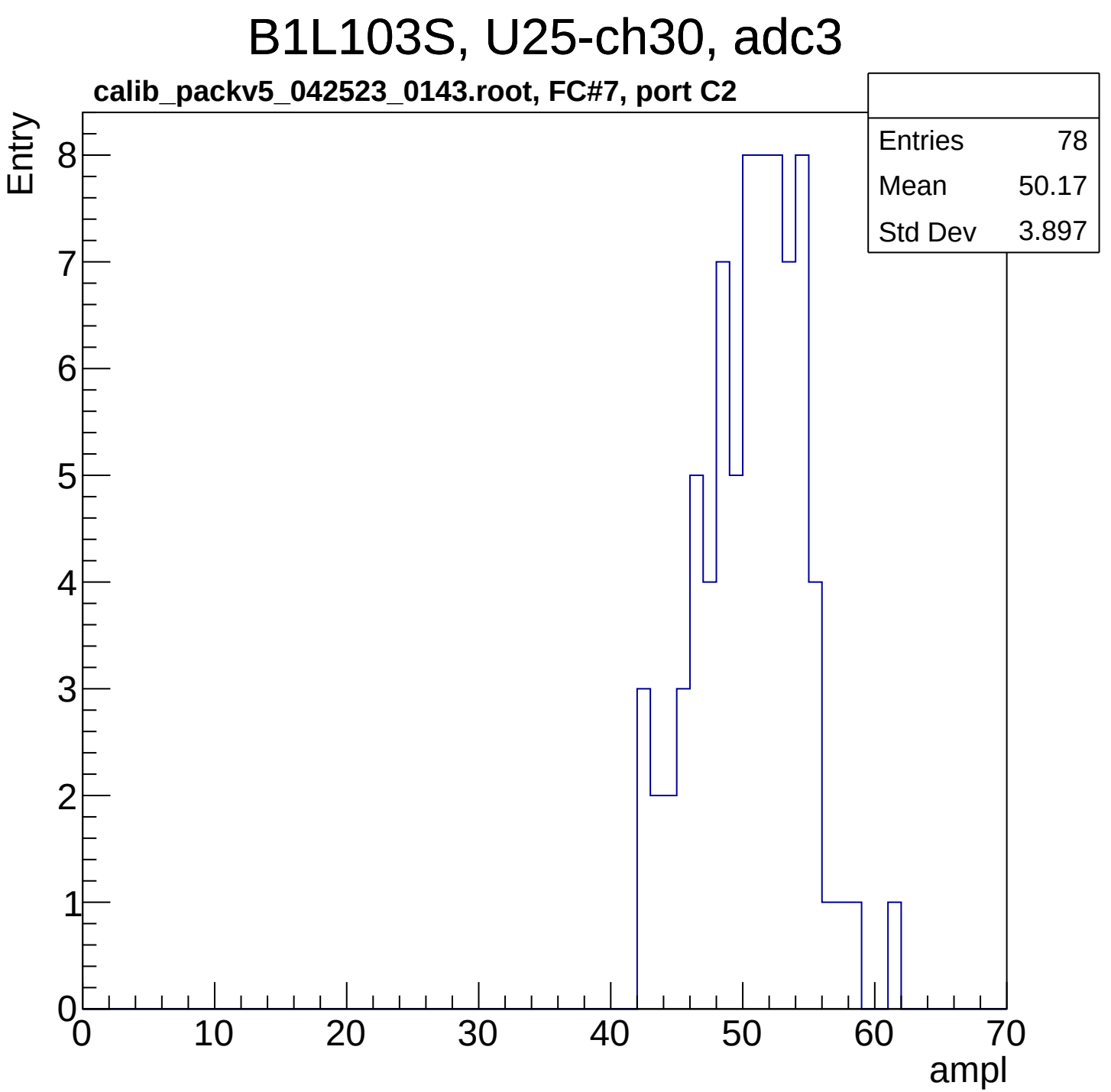
B1L103S, U25-ch30, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 50.17
Std Dev 3.897
Entry
ampl
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4
5
6
7
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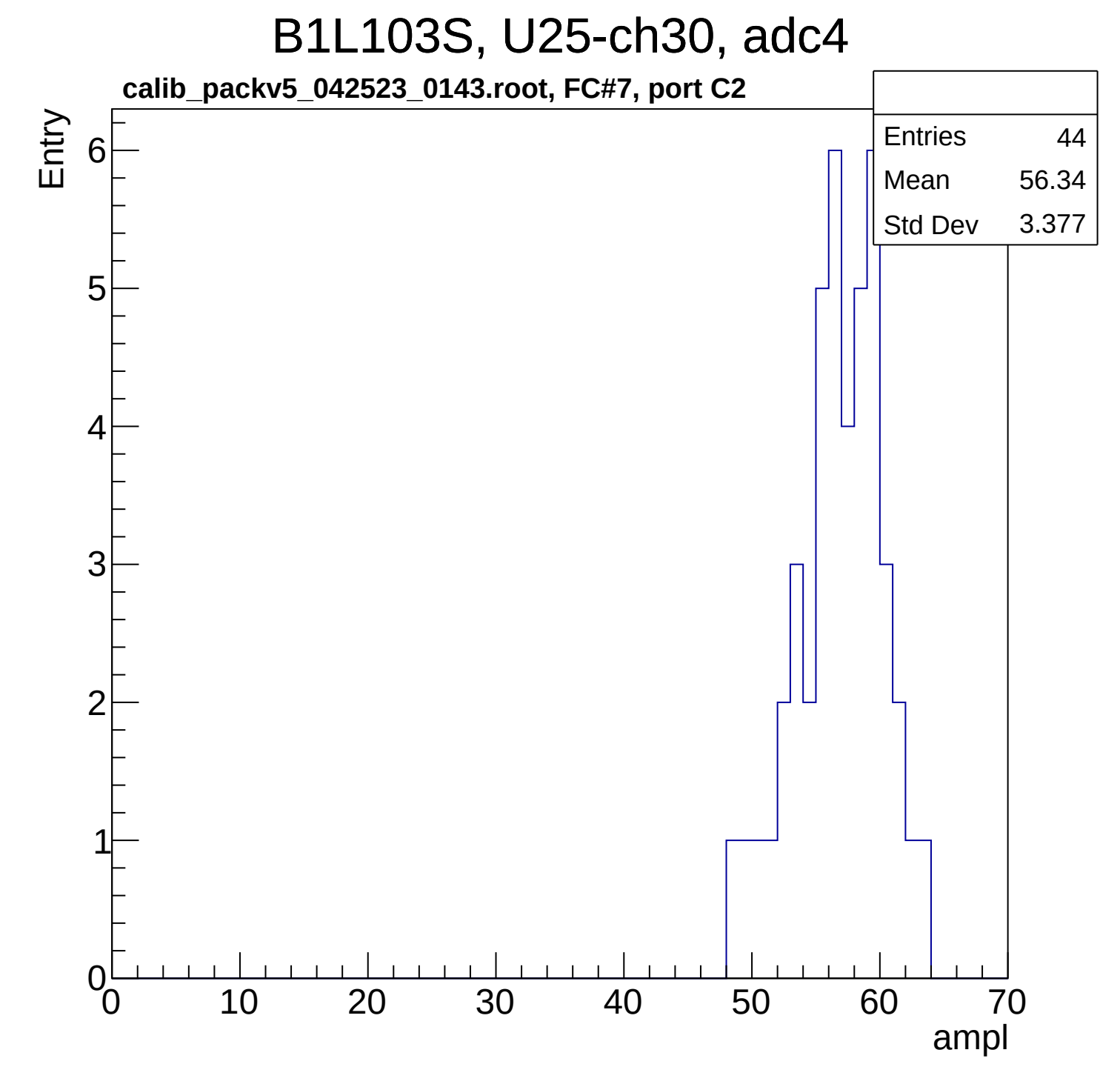
B1L103S, U25-ch30, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 56.34
Std Dev 3.377
Entry
ampl
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1
2
3
4
5
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0
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20
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40
50
60
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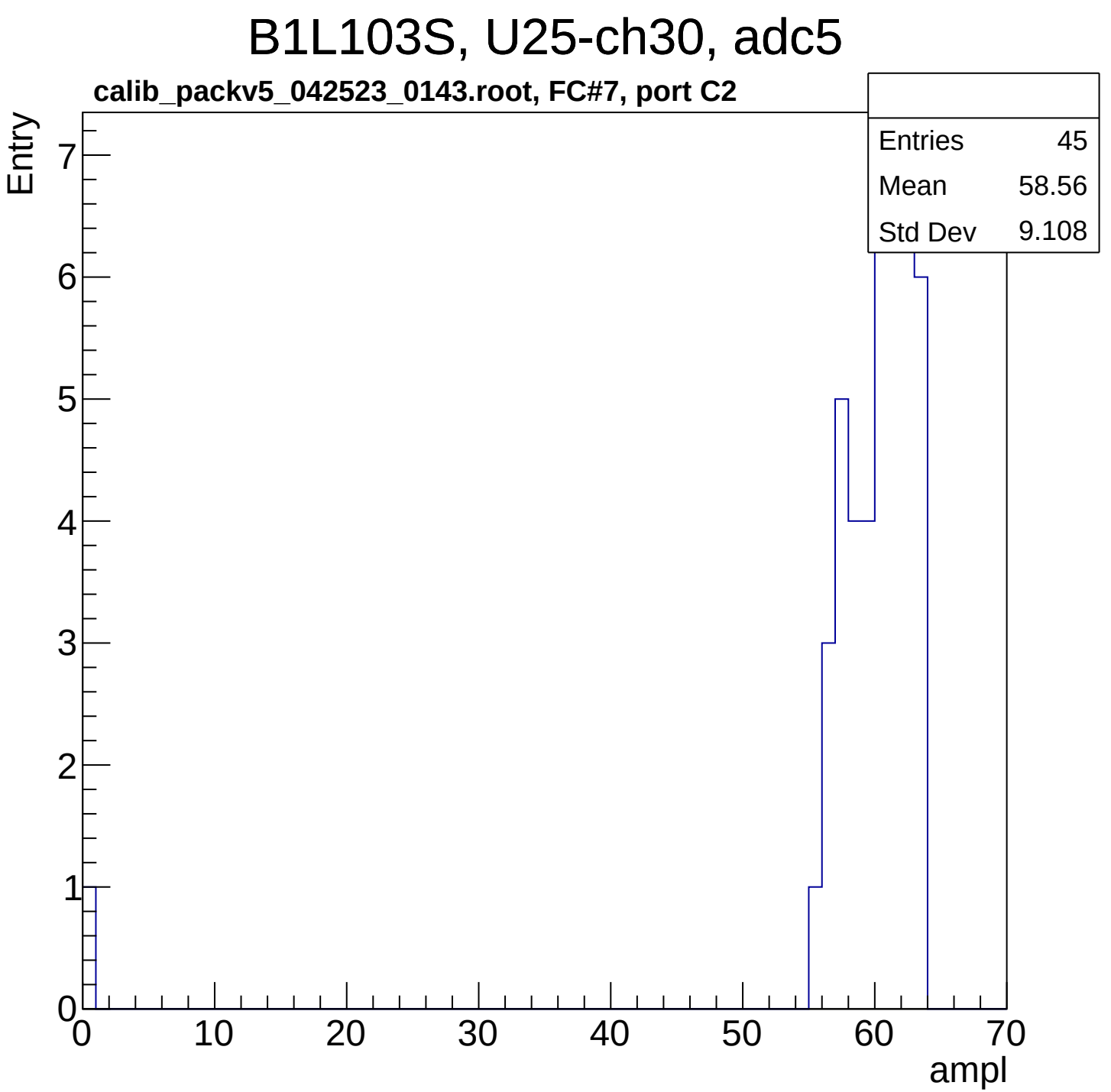
B1L103S, U25-ch30, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.56
Std Dev 9.108
Entry
ampl
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3
4
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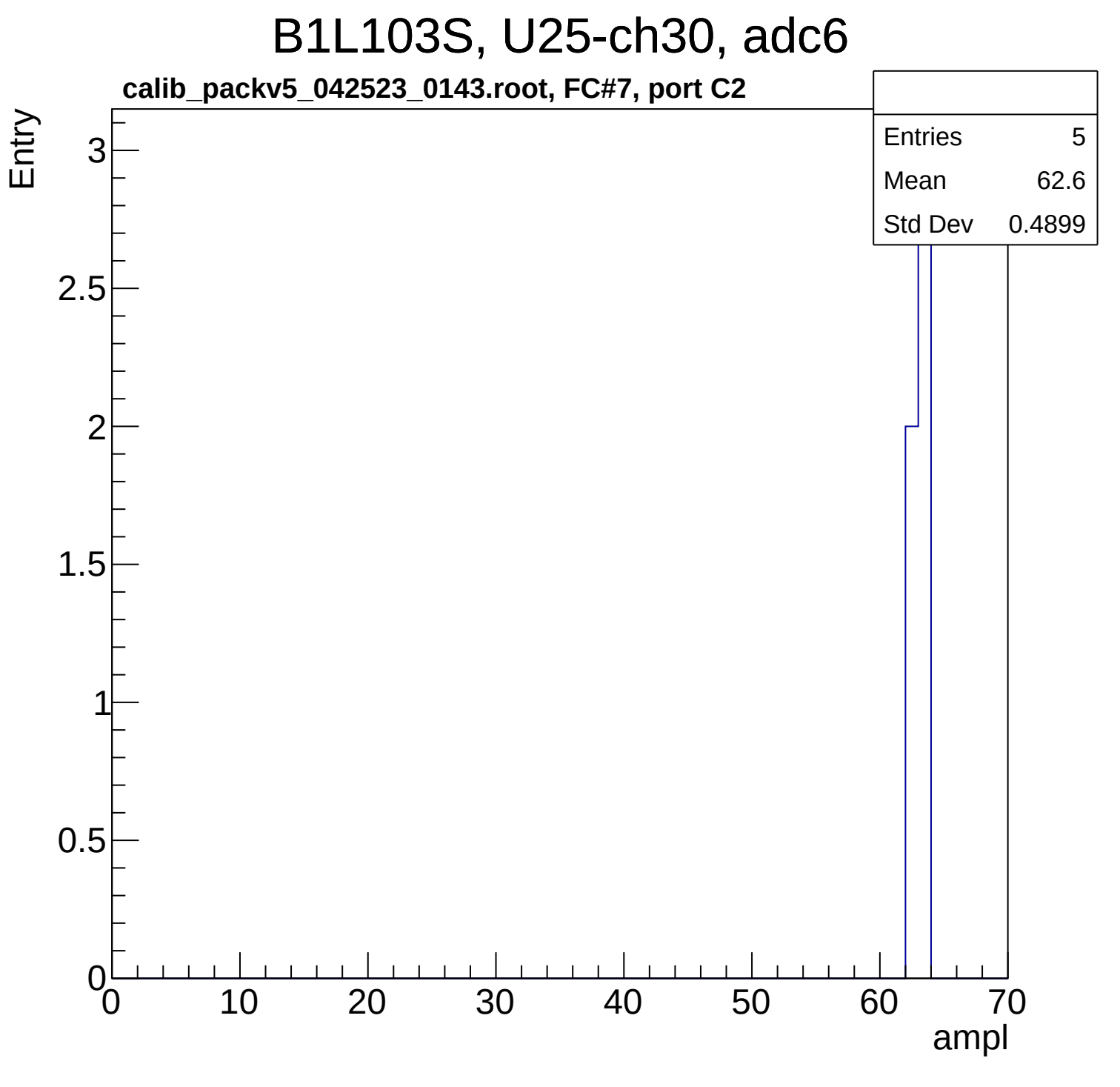
B1L103S, U25-ch30, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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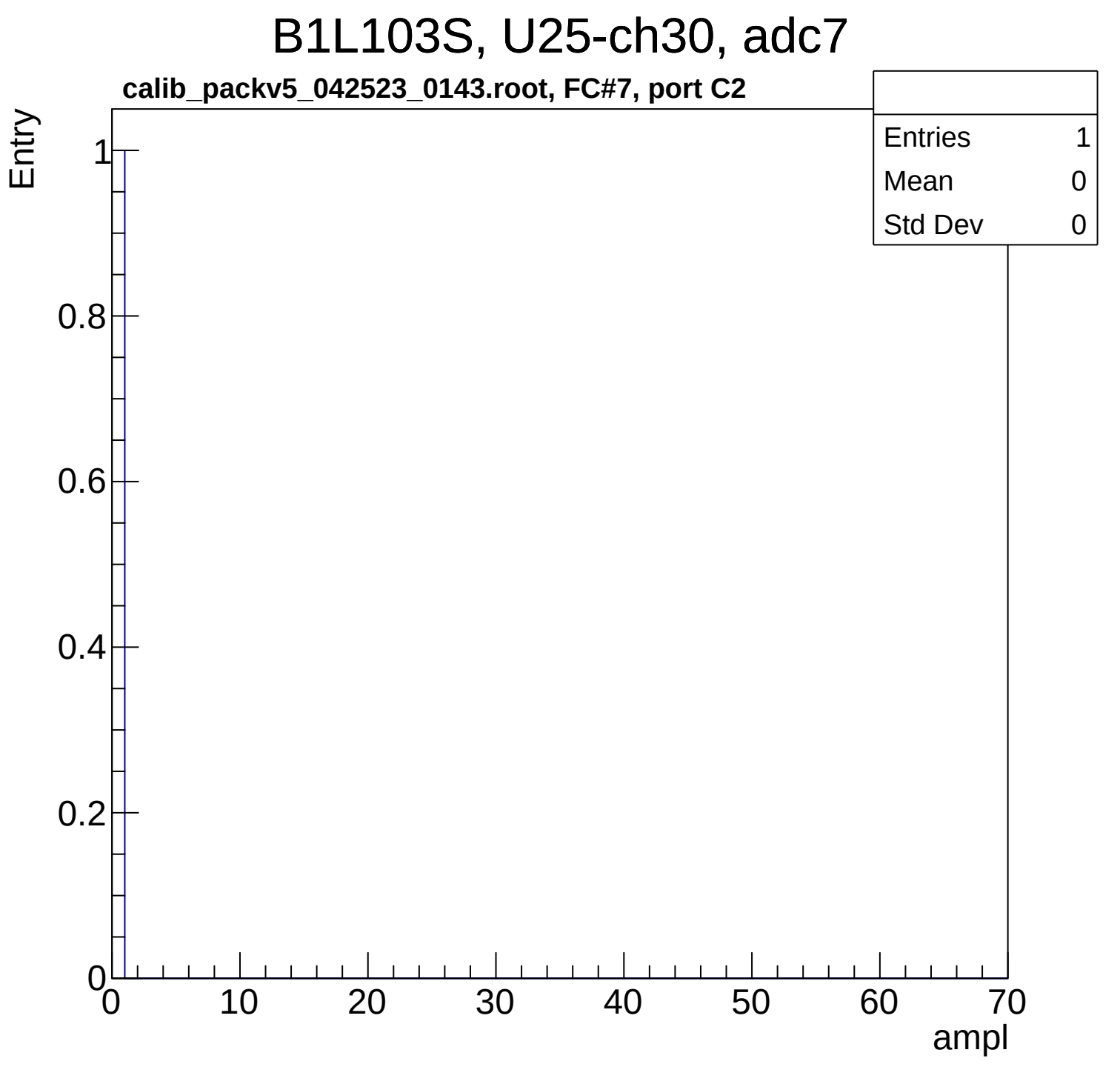
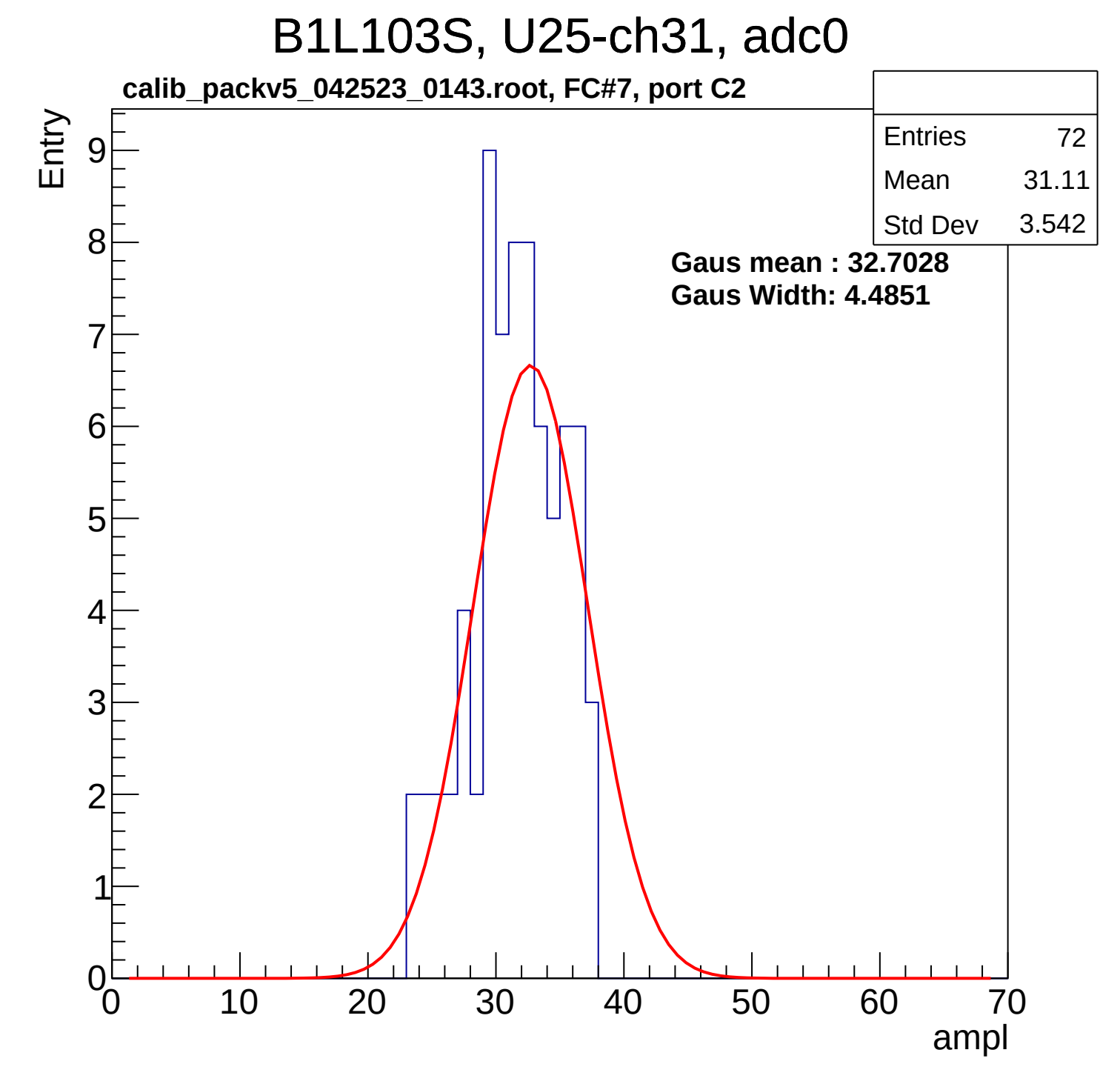
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch30, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch31, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 31.11
Std Dev 3.542
Gaus mean : 32.7028
Gaus Width: 4.4851
Entry
ampl
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2
3
4
5
6
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0
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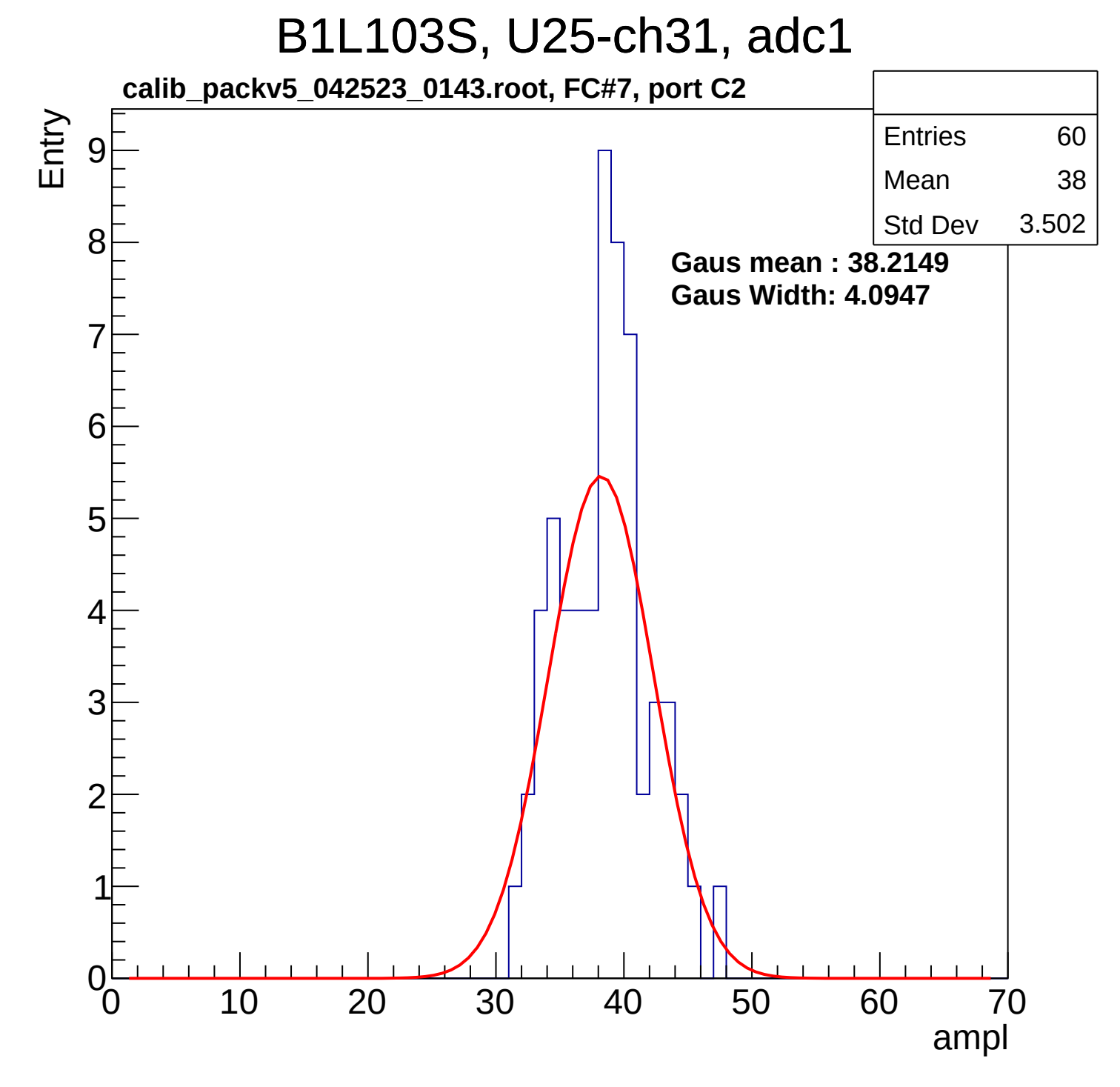
B1L103S, U25-ch31, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 38
Std Dev 3.502
Gaus mean : 38.2149
Gaus Width: 4.0947
Entry
ampl
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4
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6
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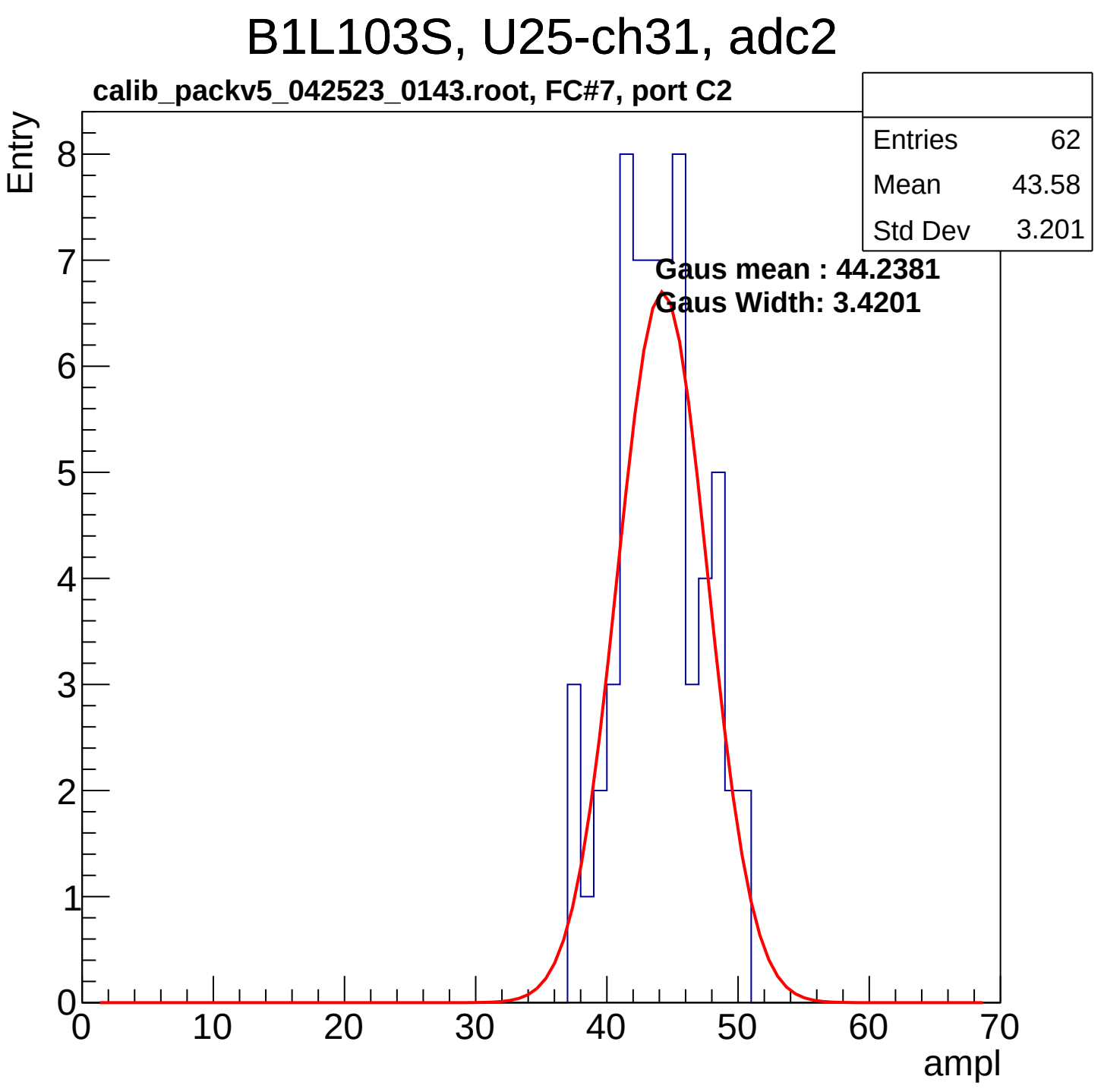
B1L103S, U25-ch31, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 43.58
Std Dev 3.201
Gaus mean : 44.2381
Gaus Width: 3.4201
Entry
ampl
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2
3
4
5
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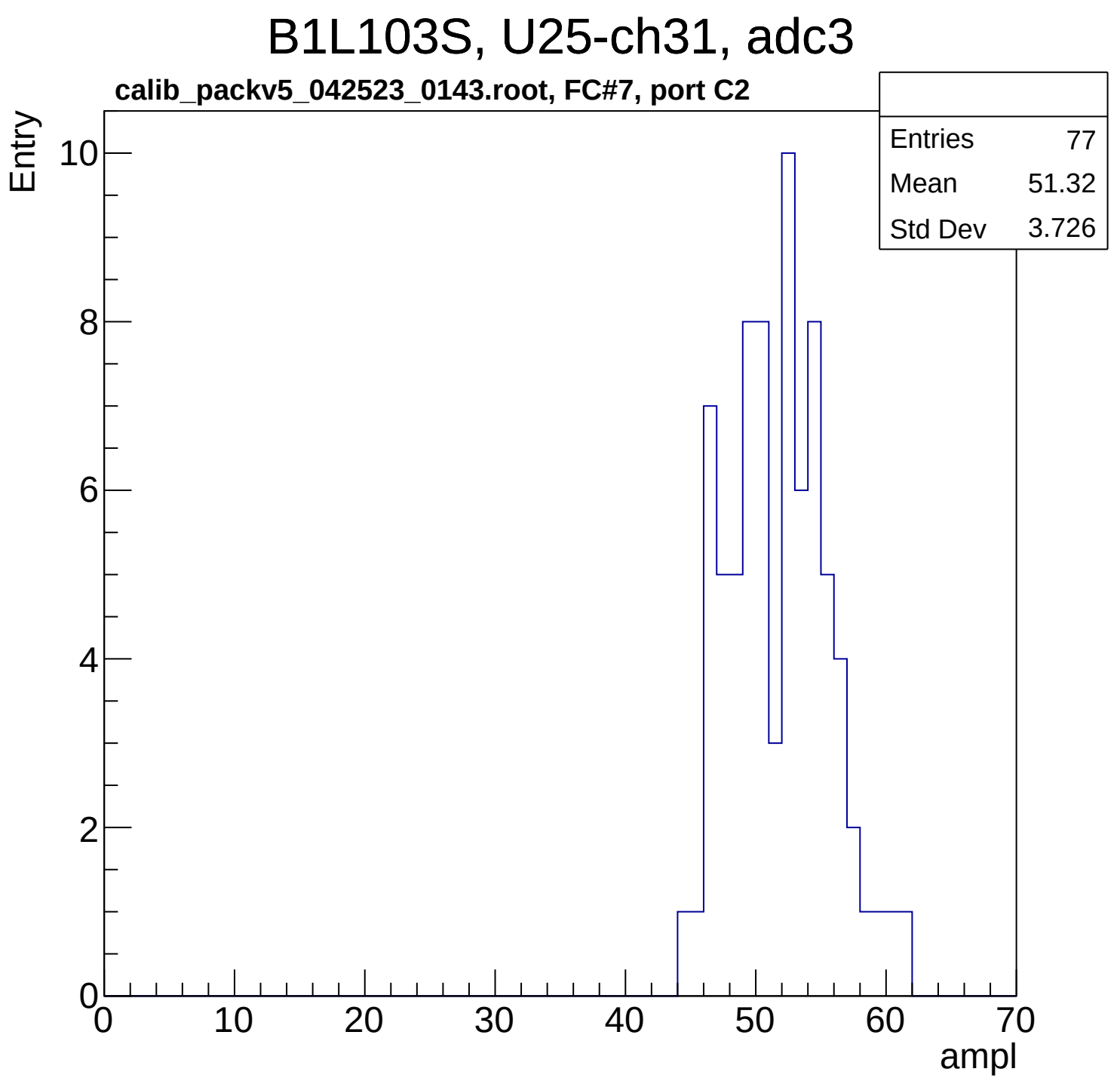
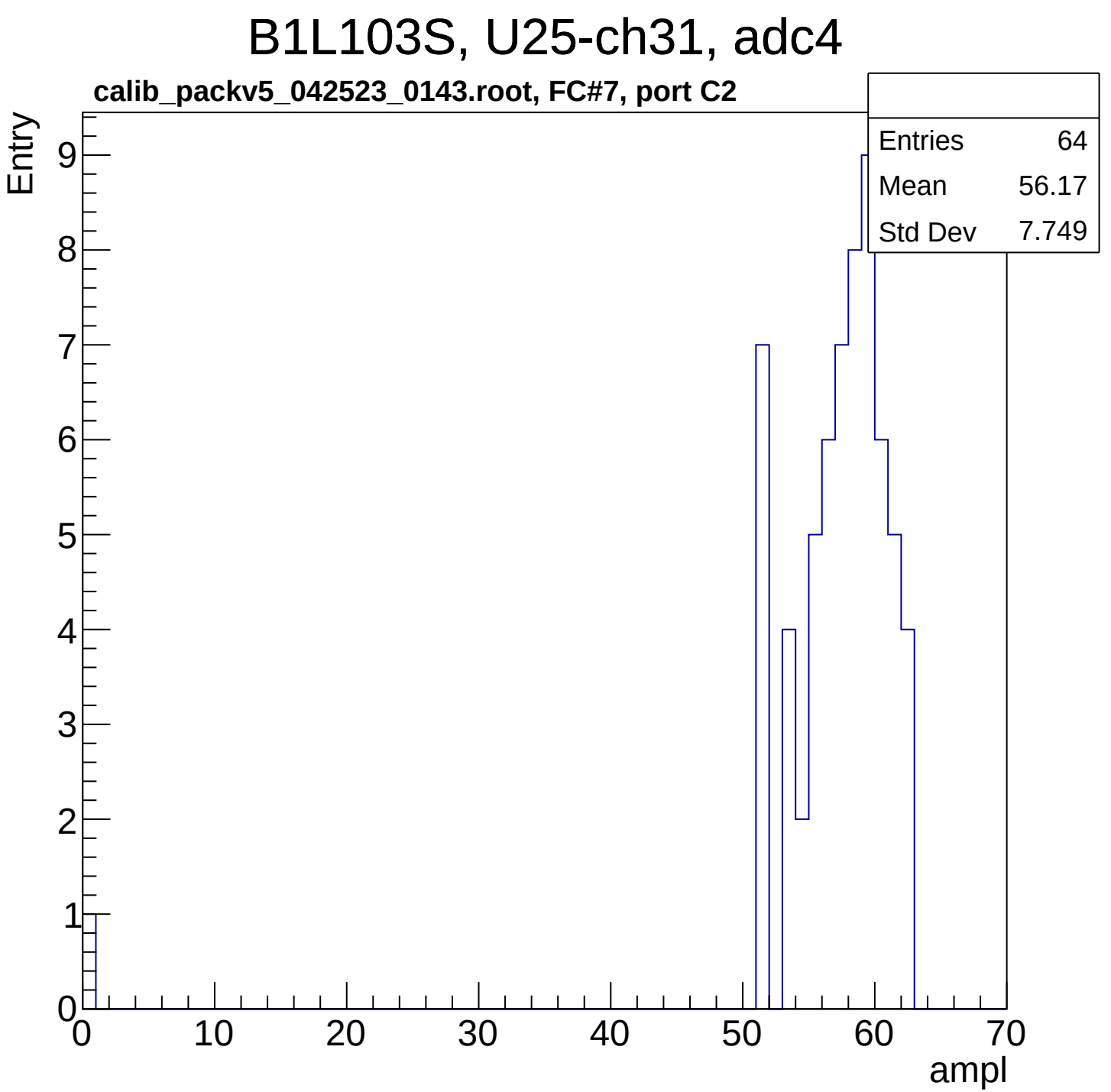

B1L103S, U25-ch31, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 51.32
Std Dev 3.726
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch31, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 56.17
Std Dev 7.749
Entry
ampl
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2
3
4
5
6
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0
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60
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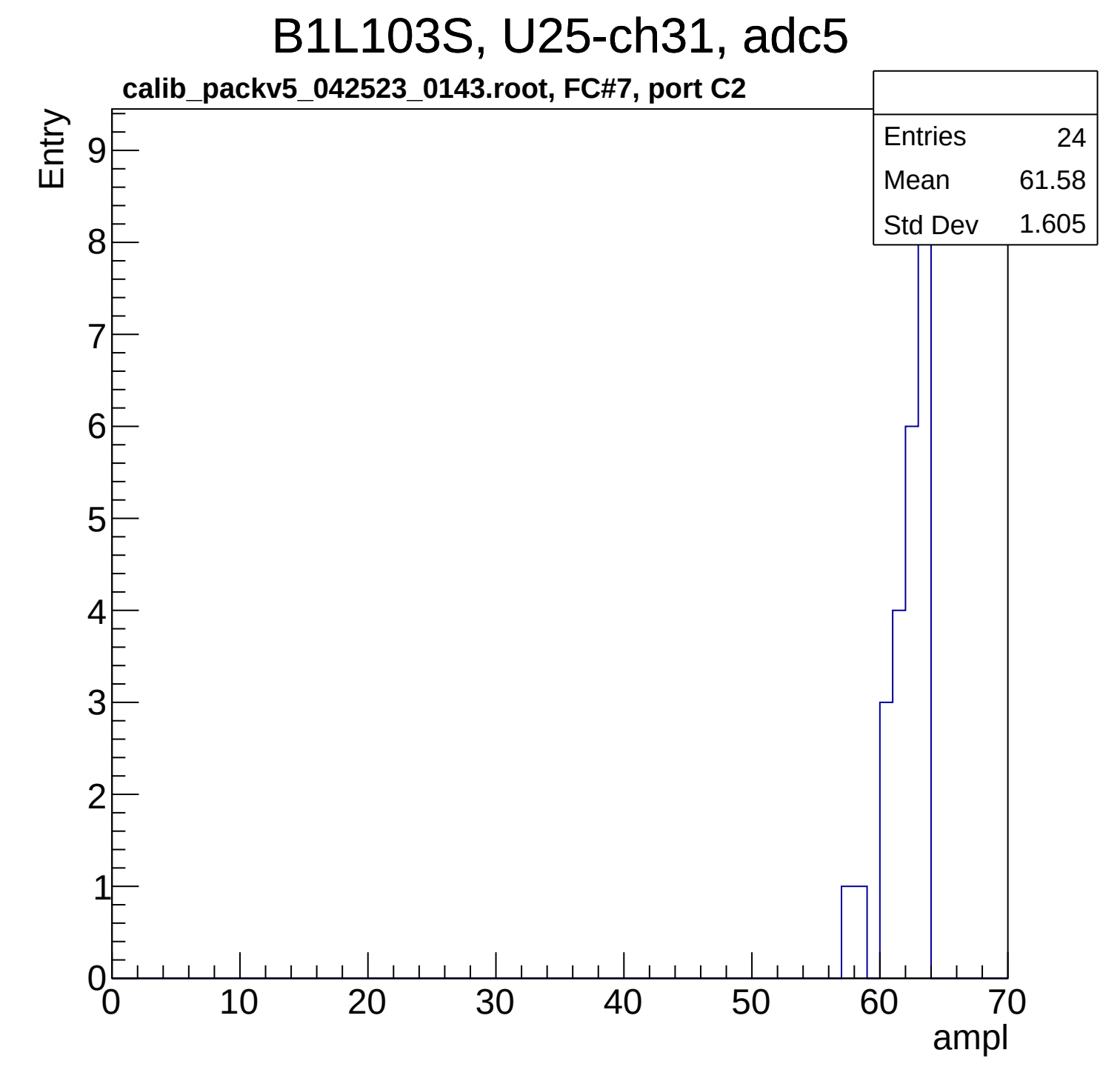
B1L103S, U25-ch31, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 24
Mean 61.58
Std Dev 1.605
Entry
ampl
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2
3
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6
7
8
9
0
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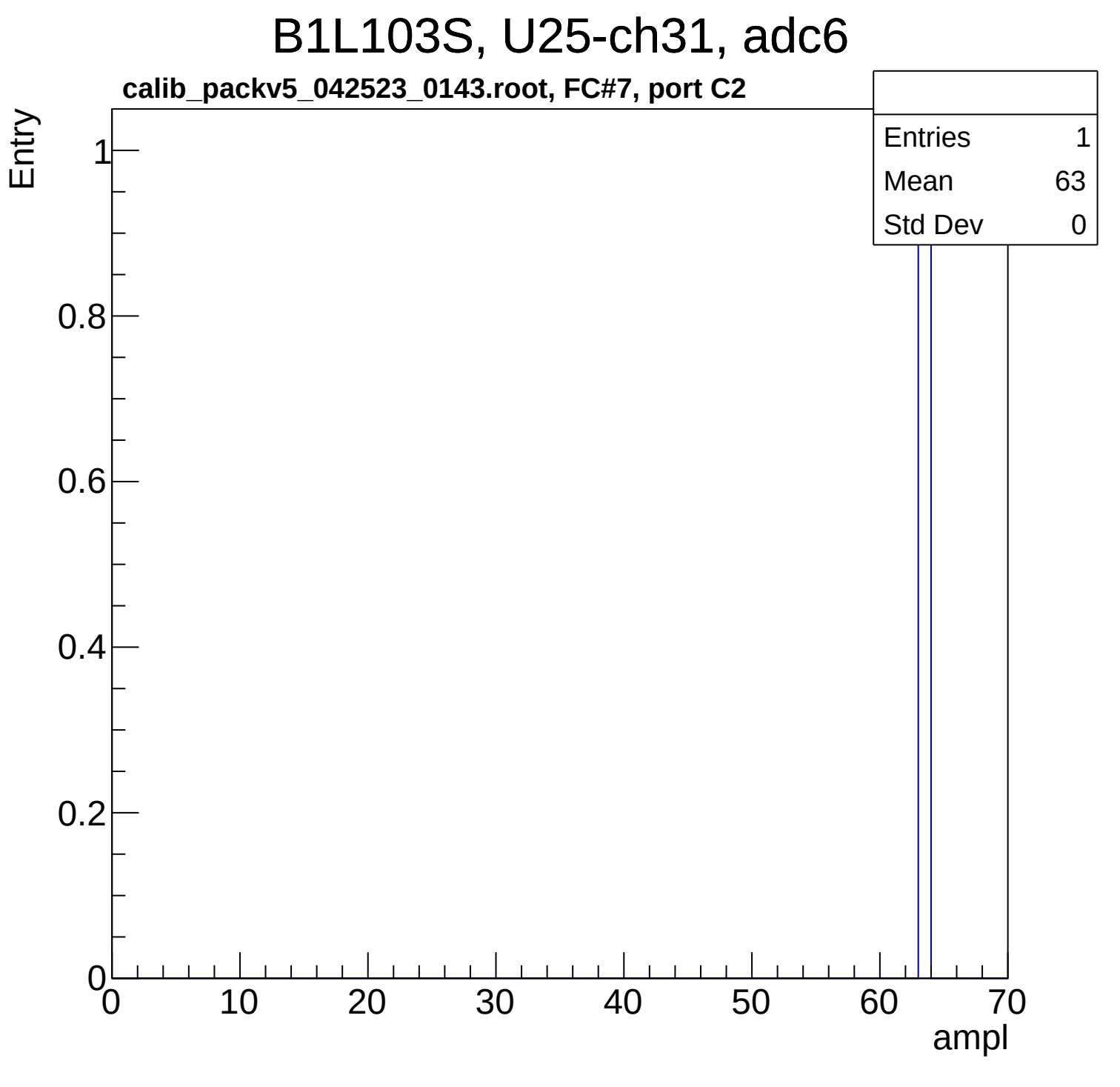
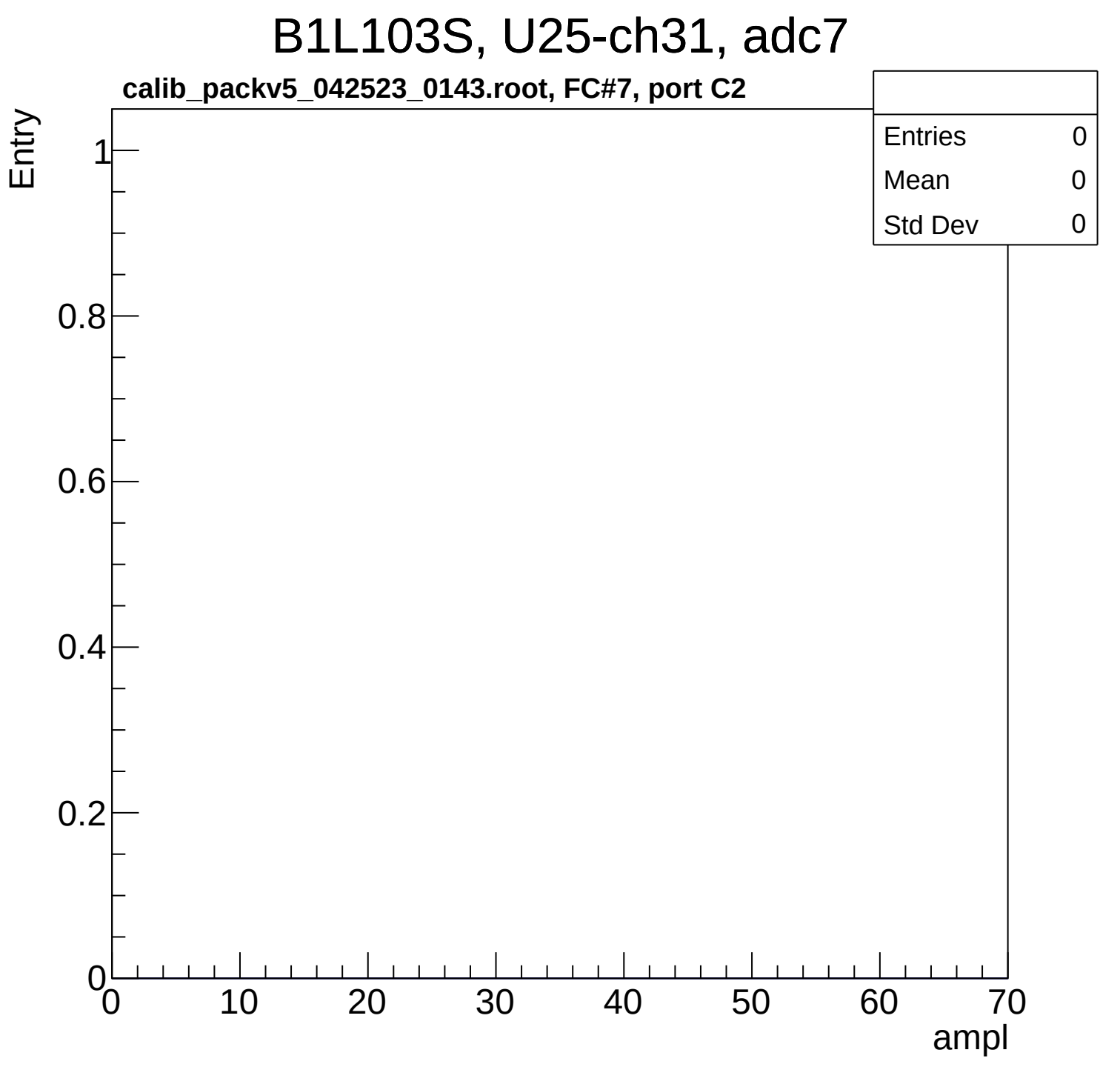
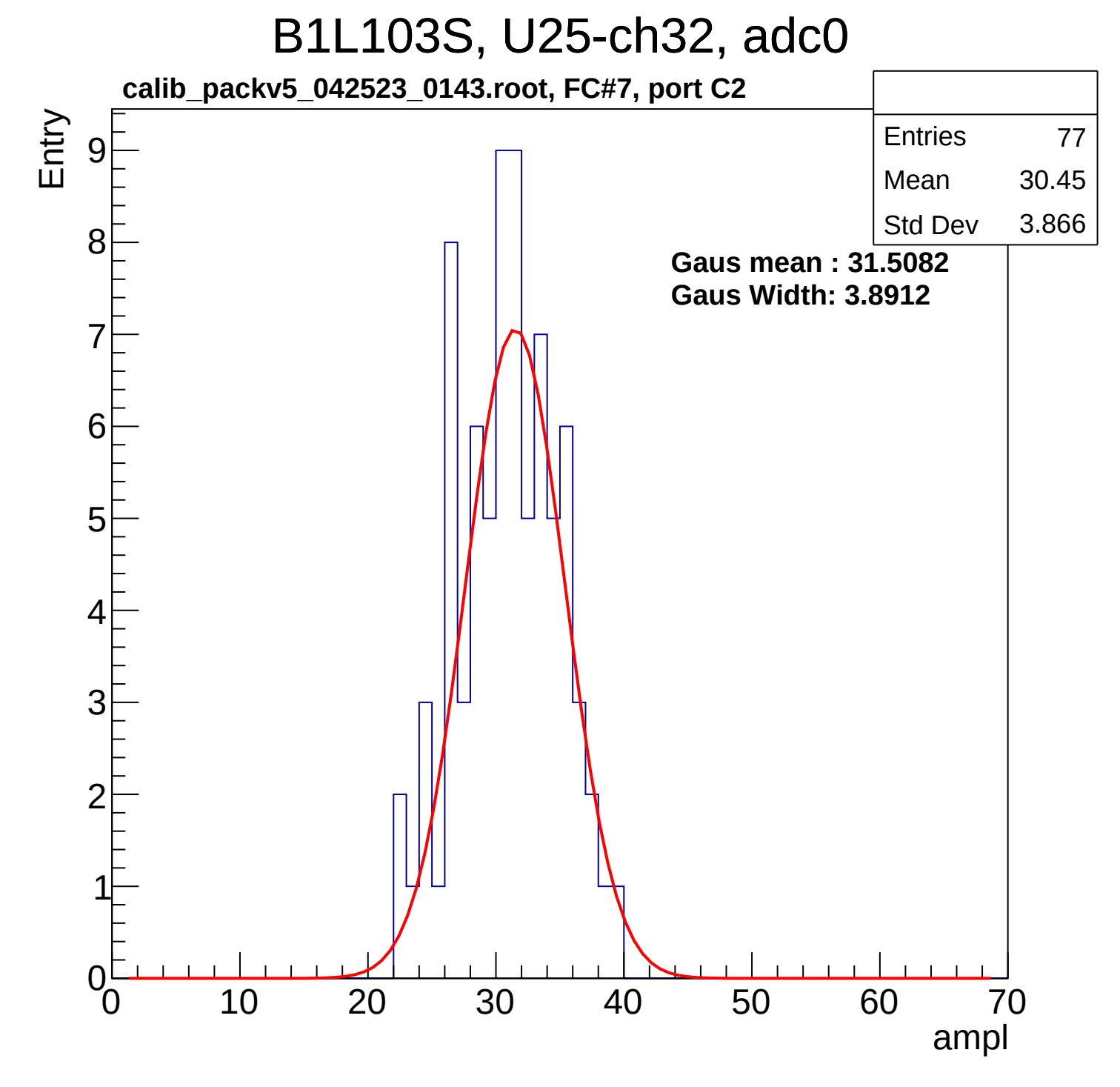

B1L103S, U25-ch31, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch31, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch32, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 30.45
Std Dev 3.866
Gaus mean : 31.5082
Gaus Width: 3.8912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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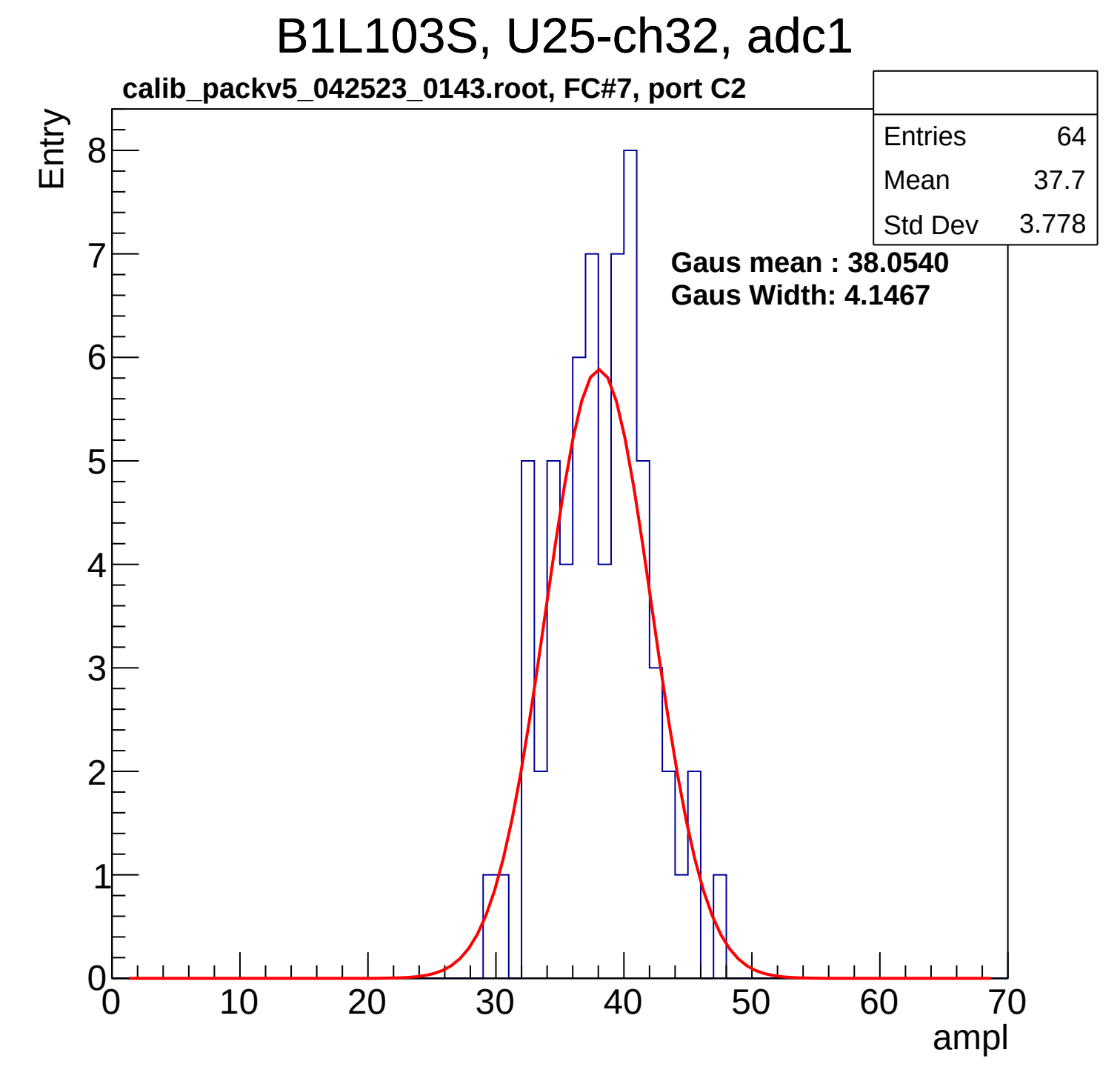
B1L103S, U25-ch32, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 37.7
Std Dev 3.778
Gaus mean : 38.0540
Gaus Width: 4.1467
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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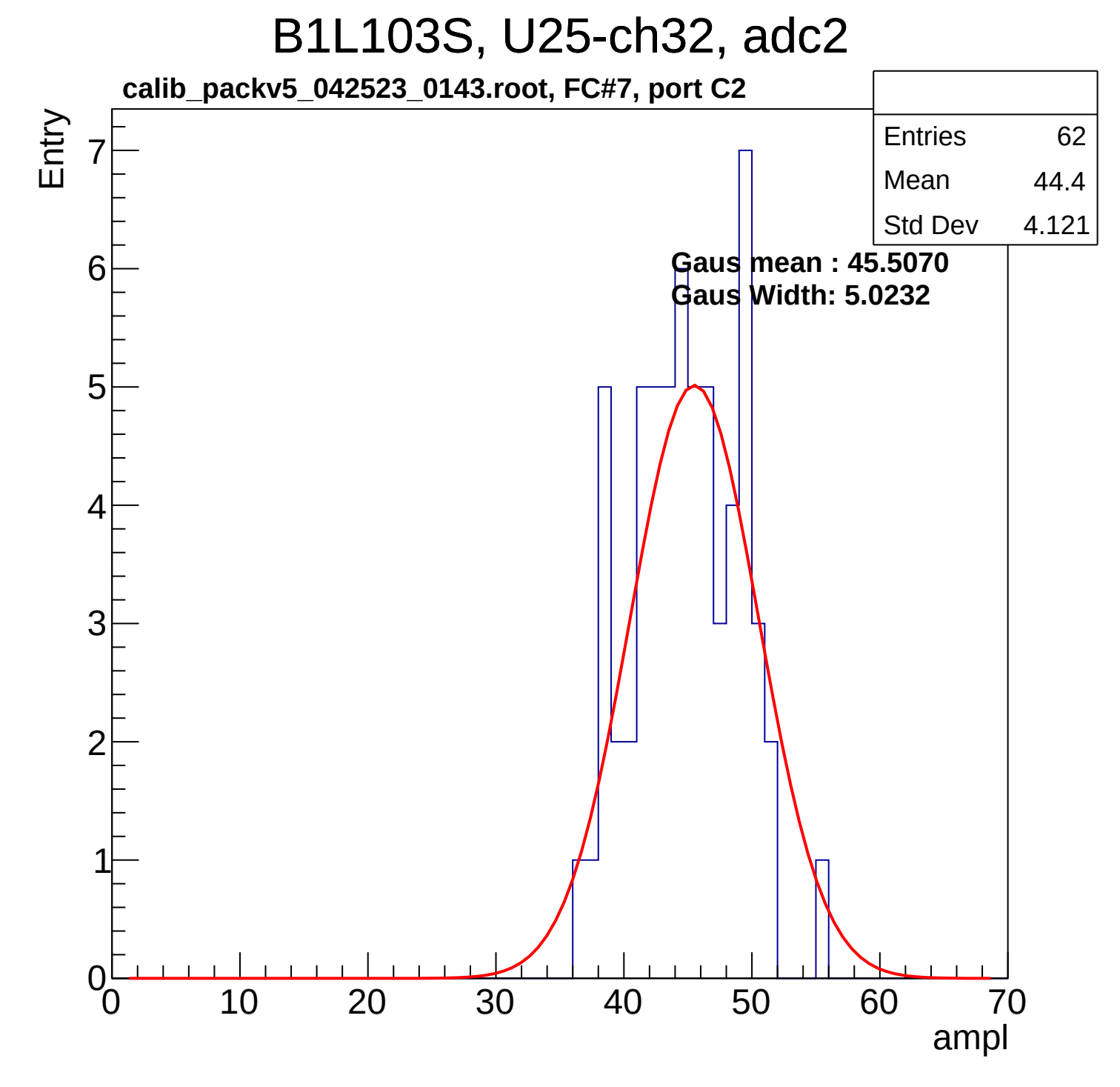
B1L103S, U25-ch32, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 44.4
Std Dev 4.121
Gaus mean : 45.5070
Gaus Width: 5.0232
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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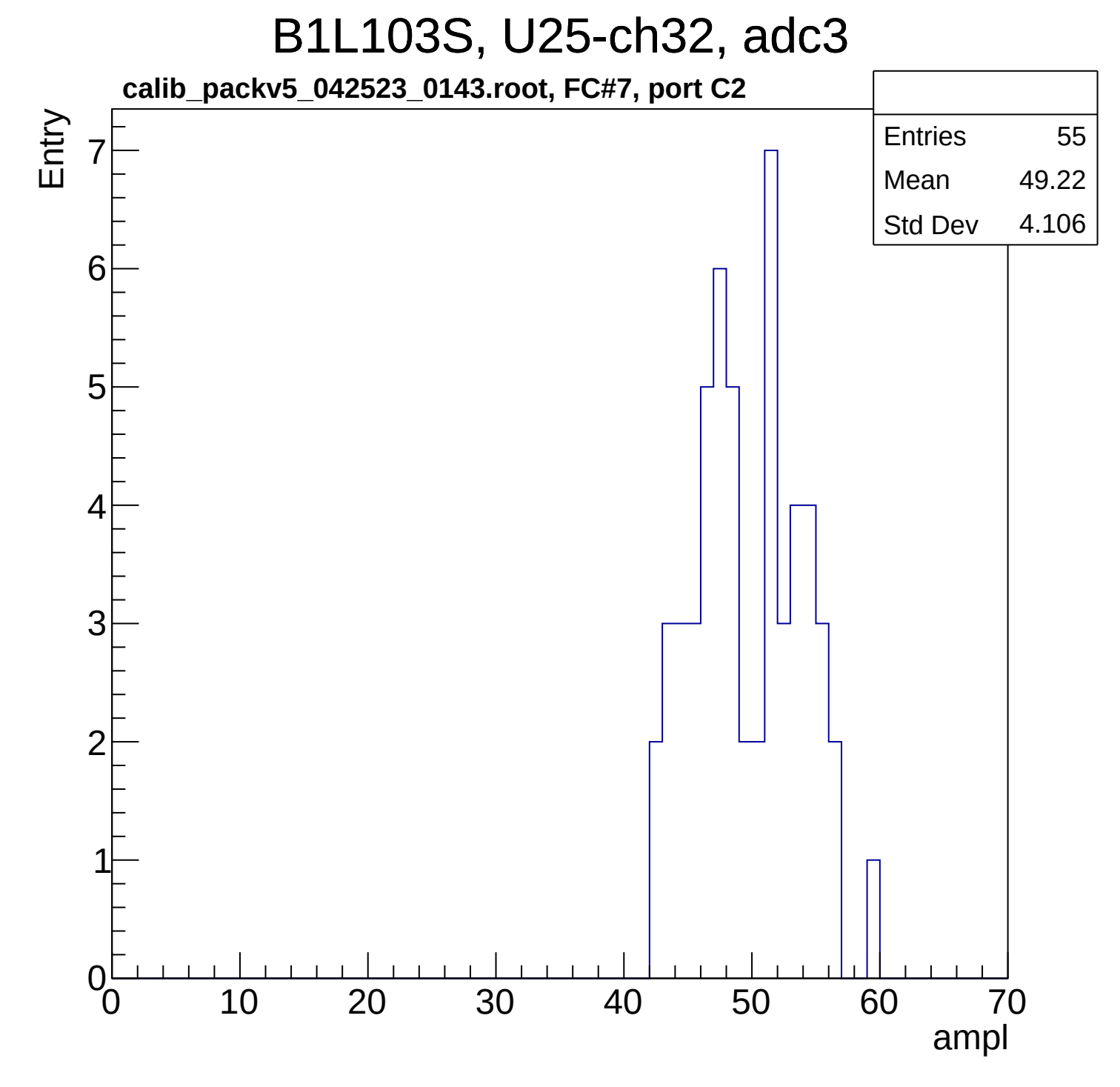
B1L103S, U25-ch32, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 49.22
Std Dev 4.106
Entry
ampl
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2
3
4
5
6
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0
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20
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60
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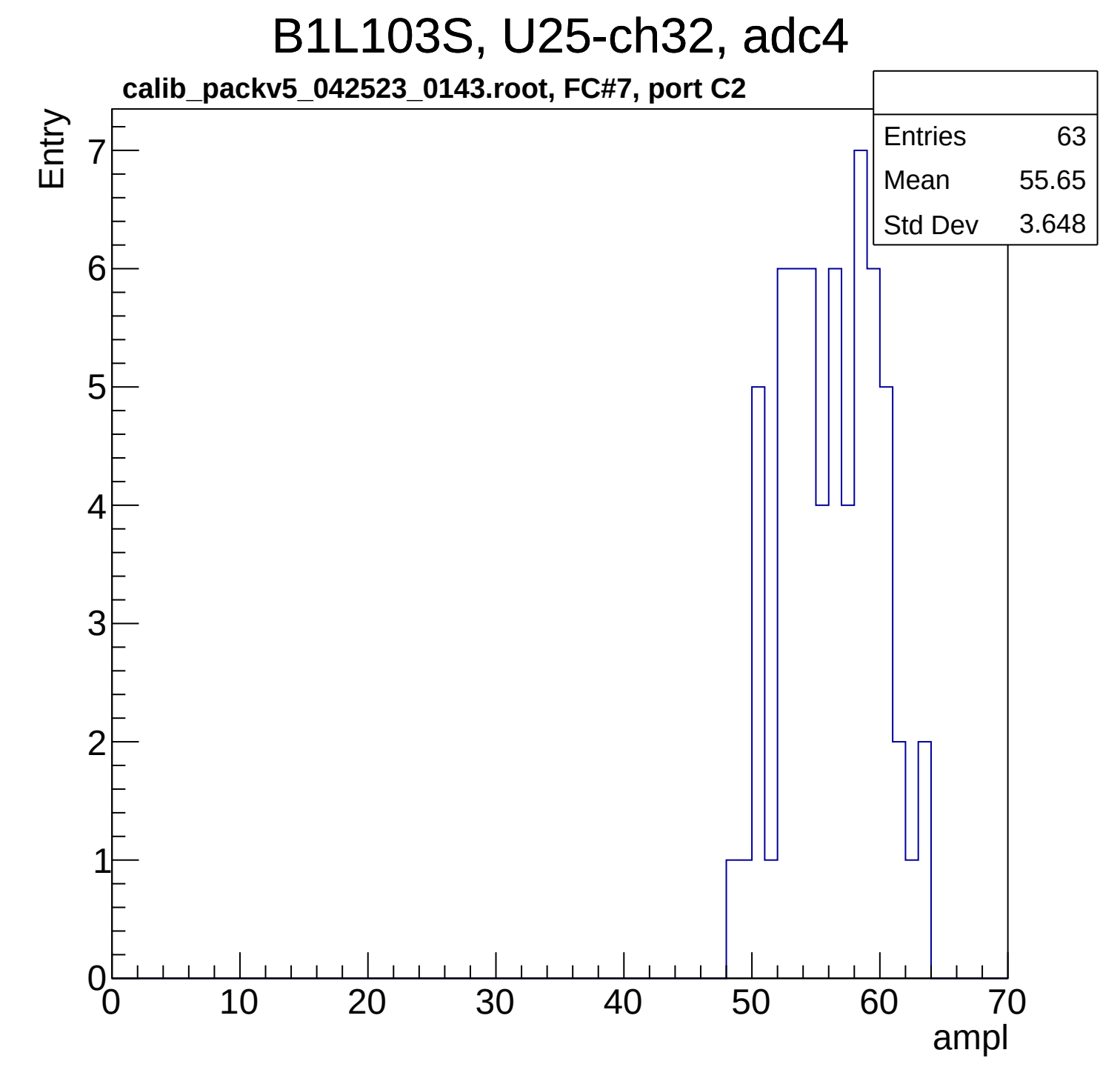
B1L103S, U25-ch32, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 55.65
Std Dev 3.648
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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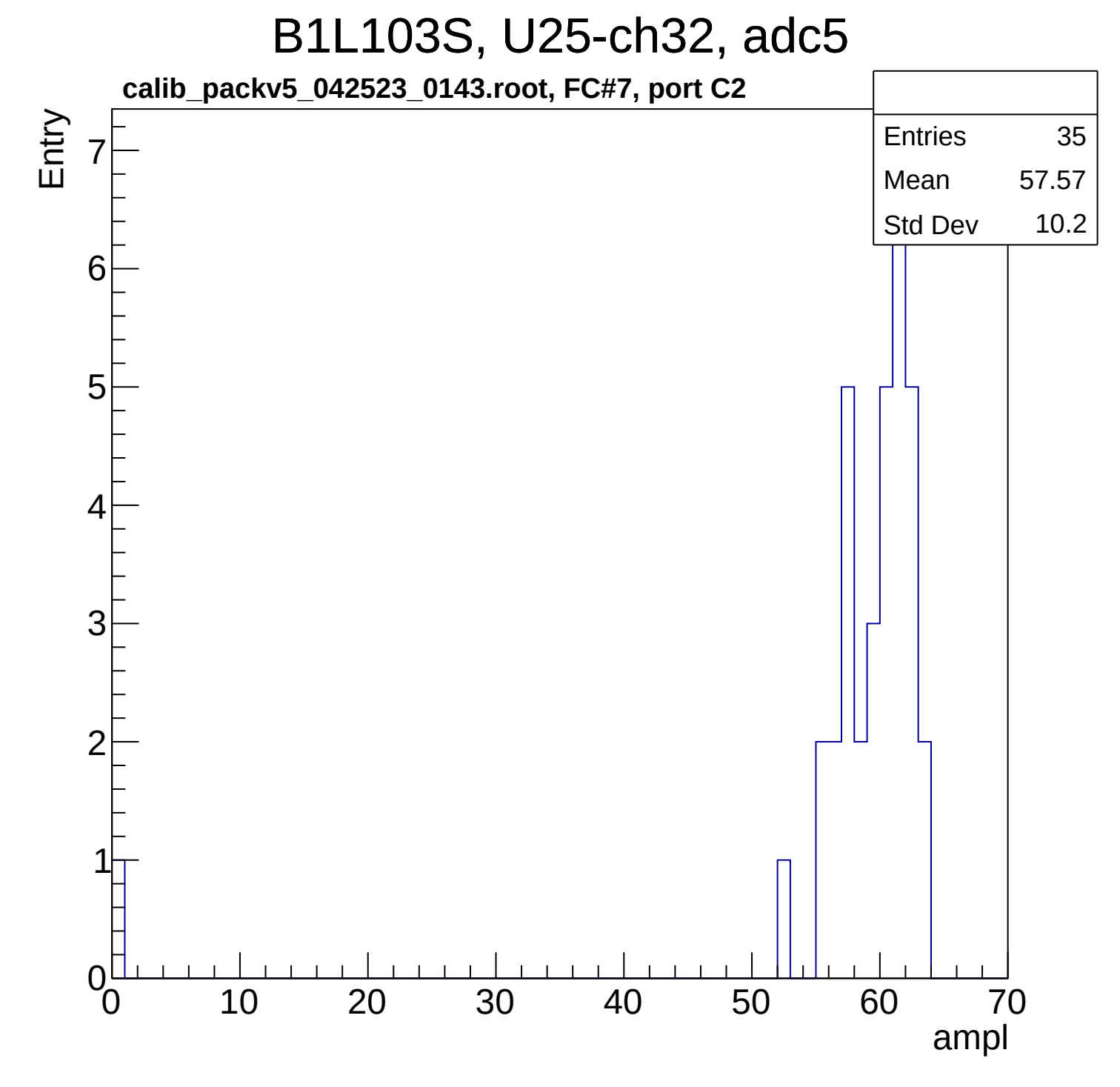
40
50
60
70


B1L103S, U25-ch32, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 57.57
Std Dev 10.2
Entry
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2
3
4
5
6
7
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10
20
30
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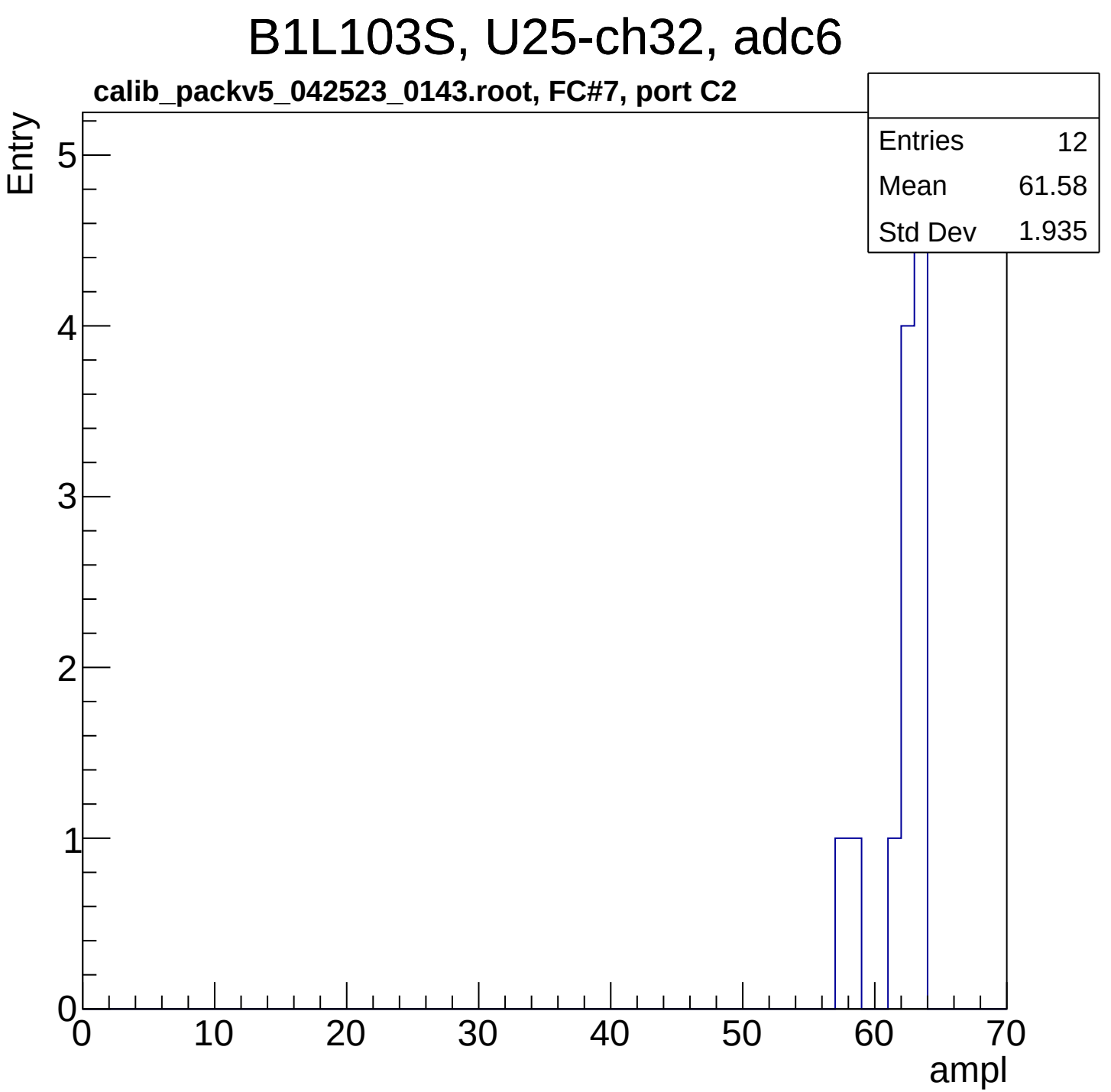
50
60
70
ampl


B1L103S, U25-ch32, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.58
Std Dev 1.935
Entry
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1
2
3
4
5
0
10
20
30
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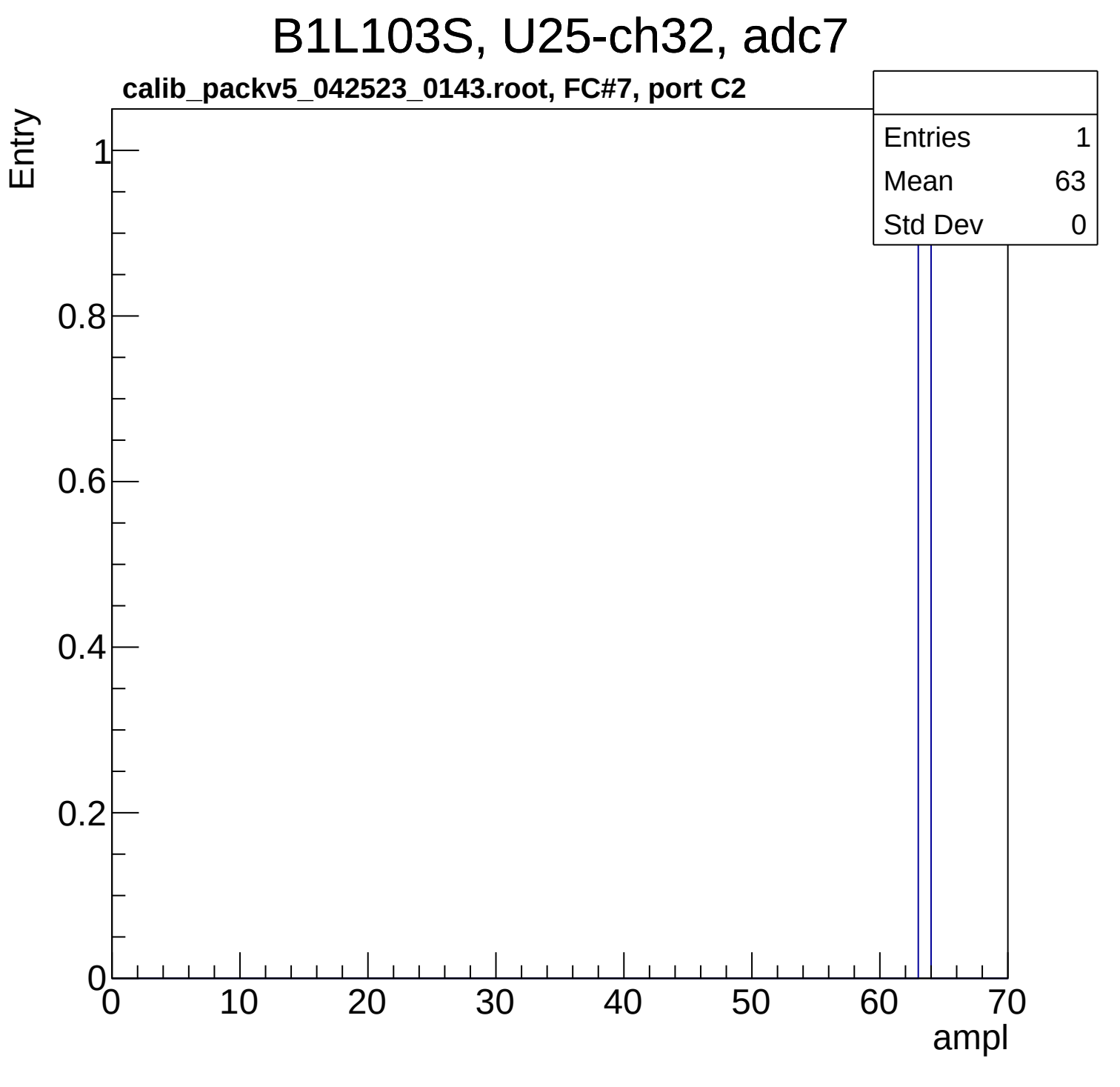
50
60
70
ampl


B1L103S, U25-ch32, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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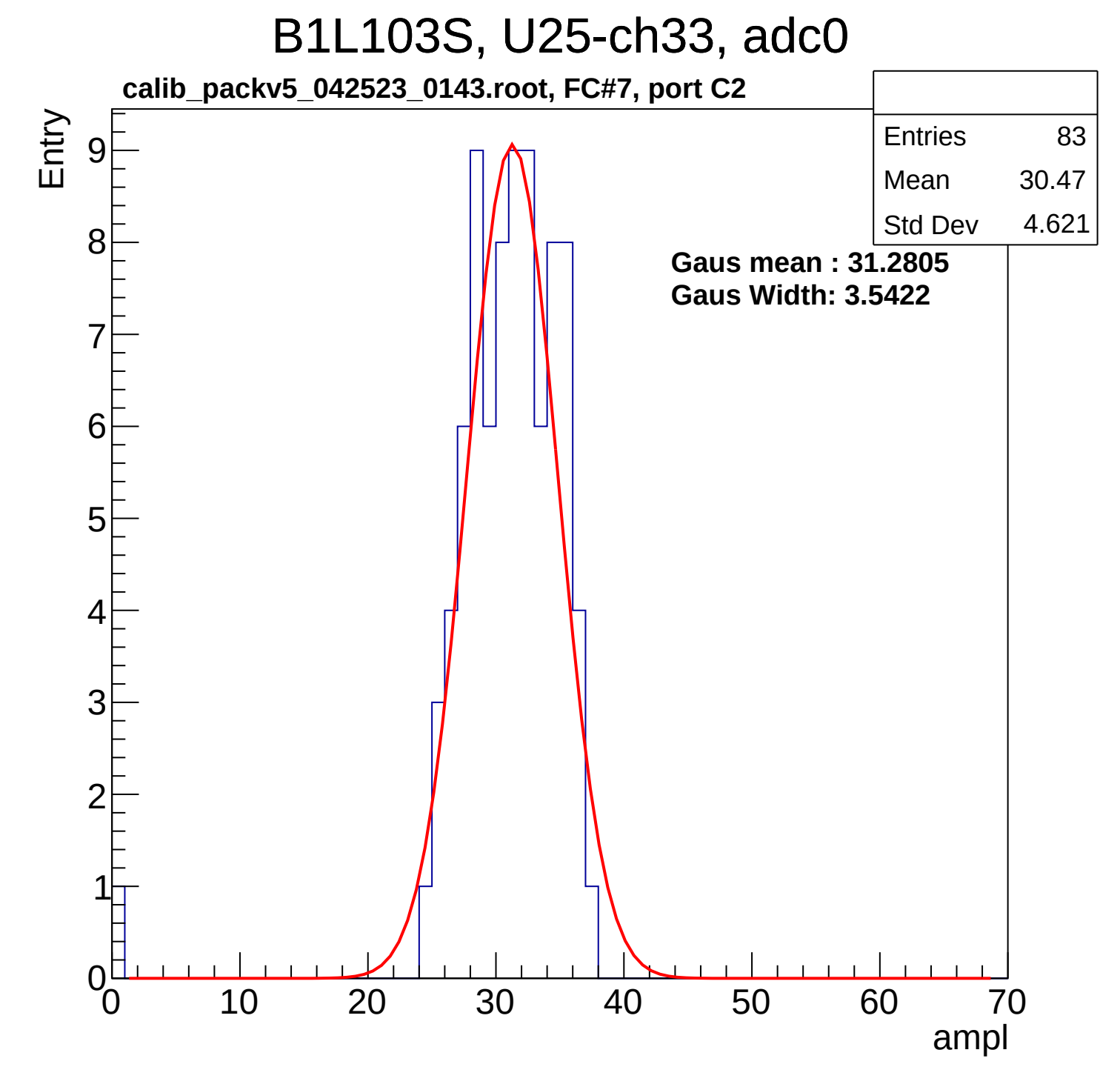
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch33, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 30.47
Std Dev 4.621
Gaus mean : 31.2805
Gaus Width: 3.5422
Entry
ampl
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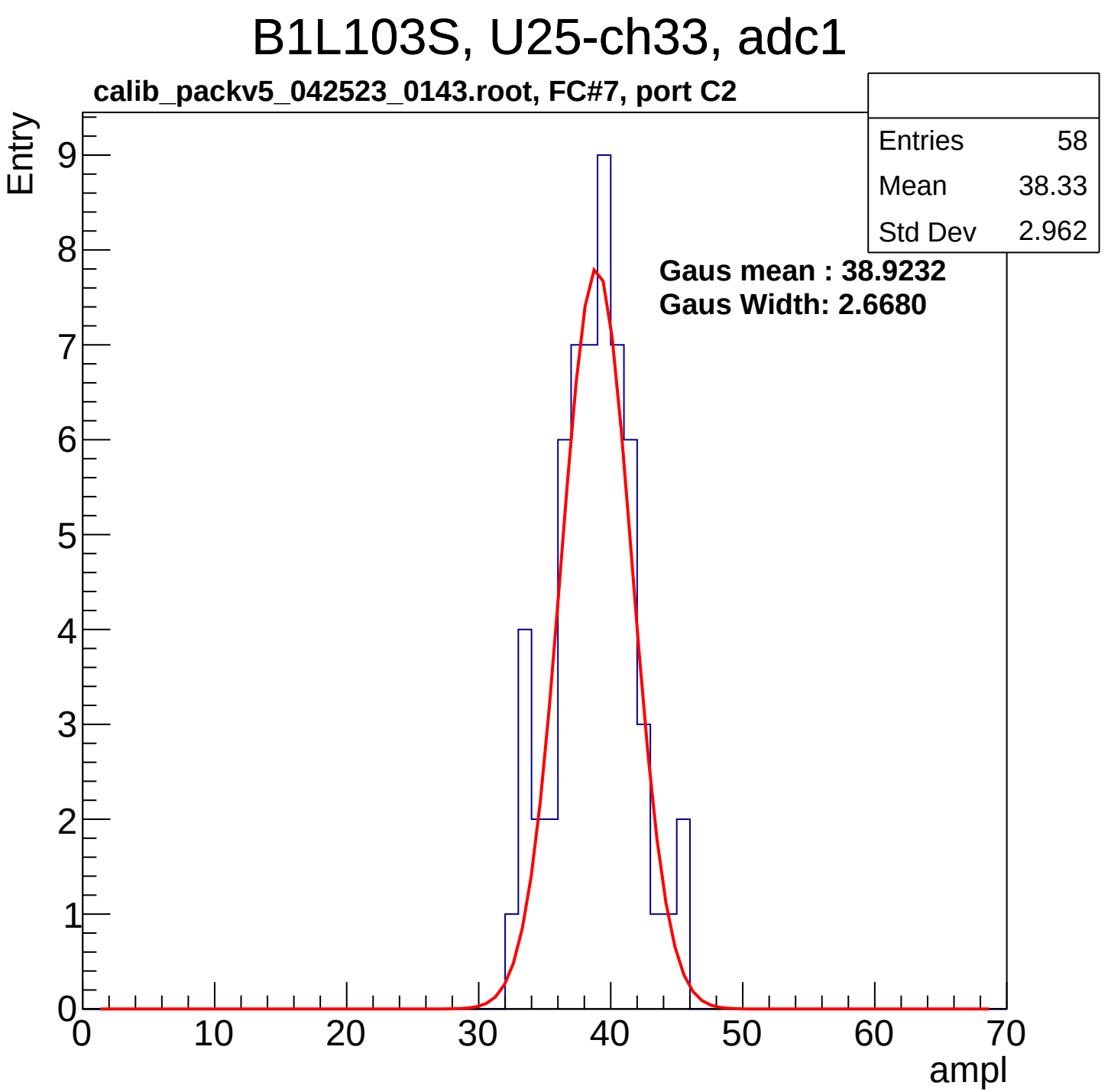
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch33, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 38.33
Std Dev 2.962
Gaus mean : 38.9232
Gaus Width: 2.6680
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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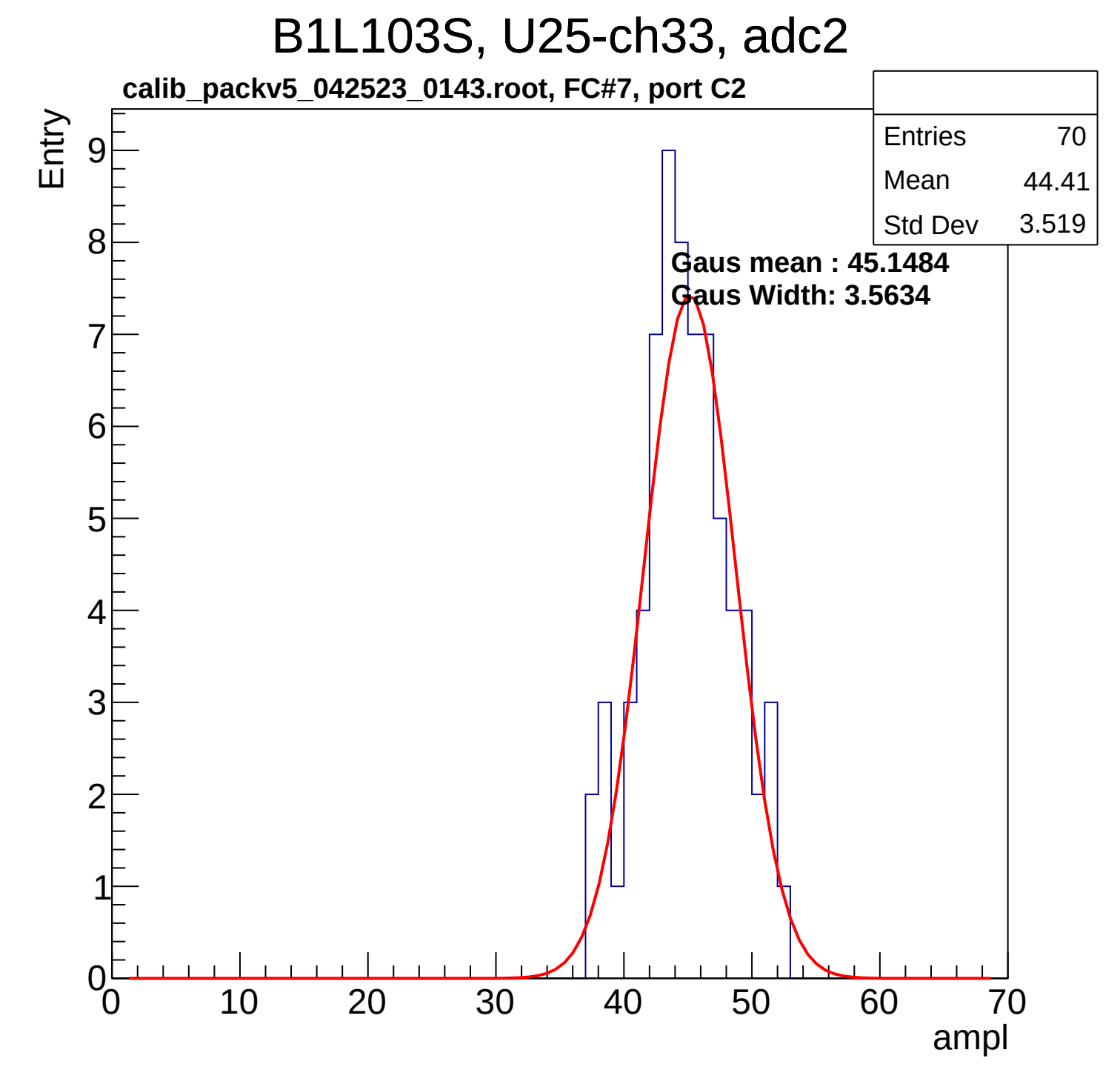
B1L103S, U25-ch33, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 44.41
Std Dev 3.519
Gaus mean : 45.1484
Gaus Width: 3.5634
Entry
ampl
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3
4
5
6
7
8
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0
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60
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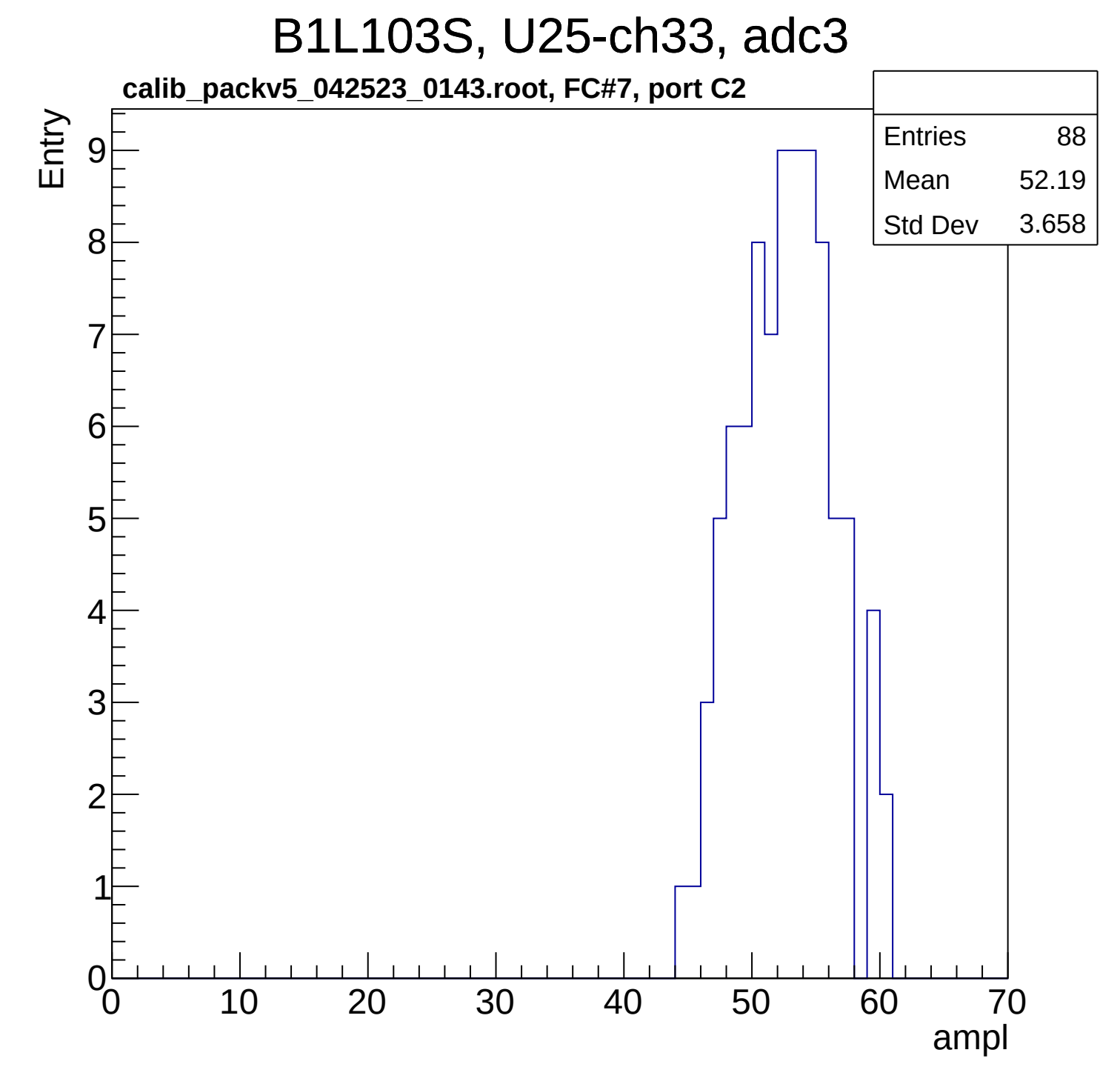
B1L103S, U25-ch33, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 52.19
Std Dev 3.658
Entry
ampl
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60
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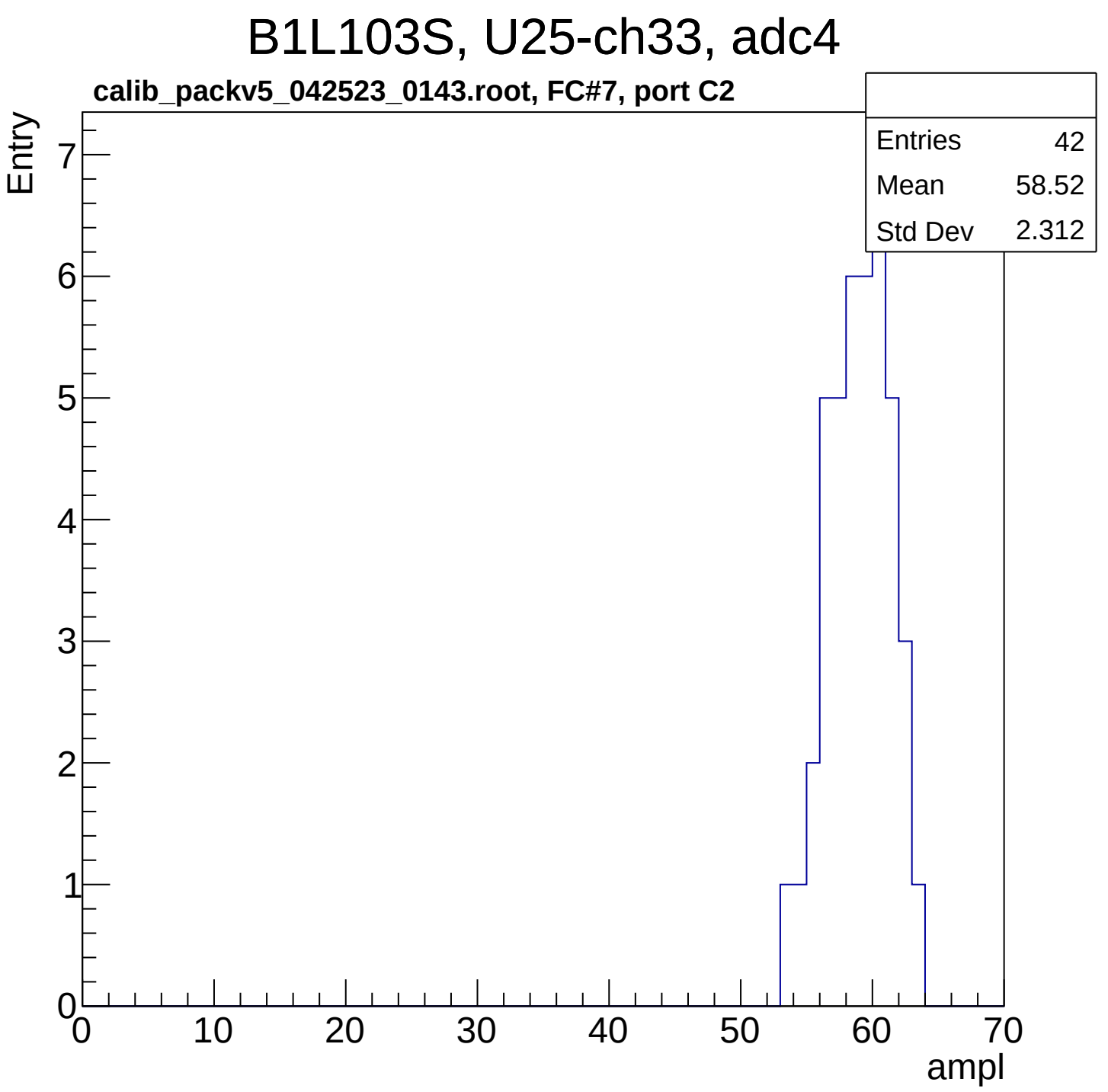
B1L103S, U25-ch33, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.52
Std Dev 2.312
Entry
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1
2
3
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5
6
7
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10
20
30
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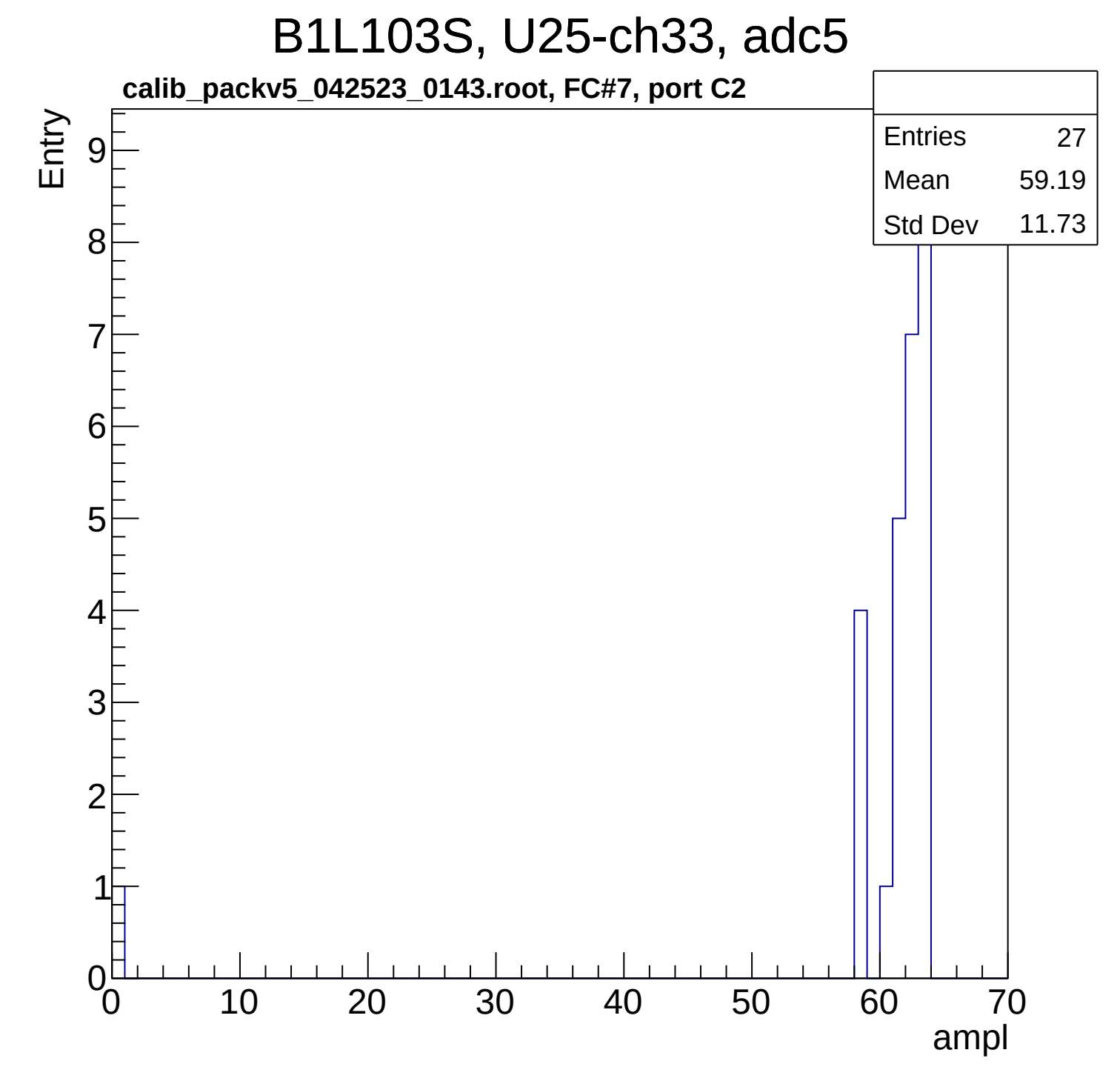
50
60
70
ampl


B1L103S, U25-ch33, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 27
Mean 59.19
Std Dev 11.73
Entry
ampl
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4
5
6
7
8
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0
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20
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40
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60
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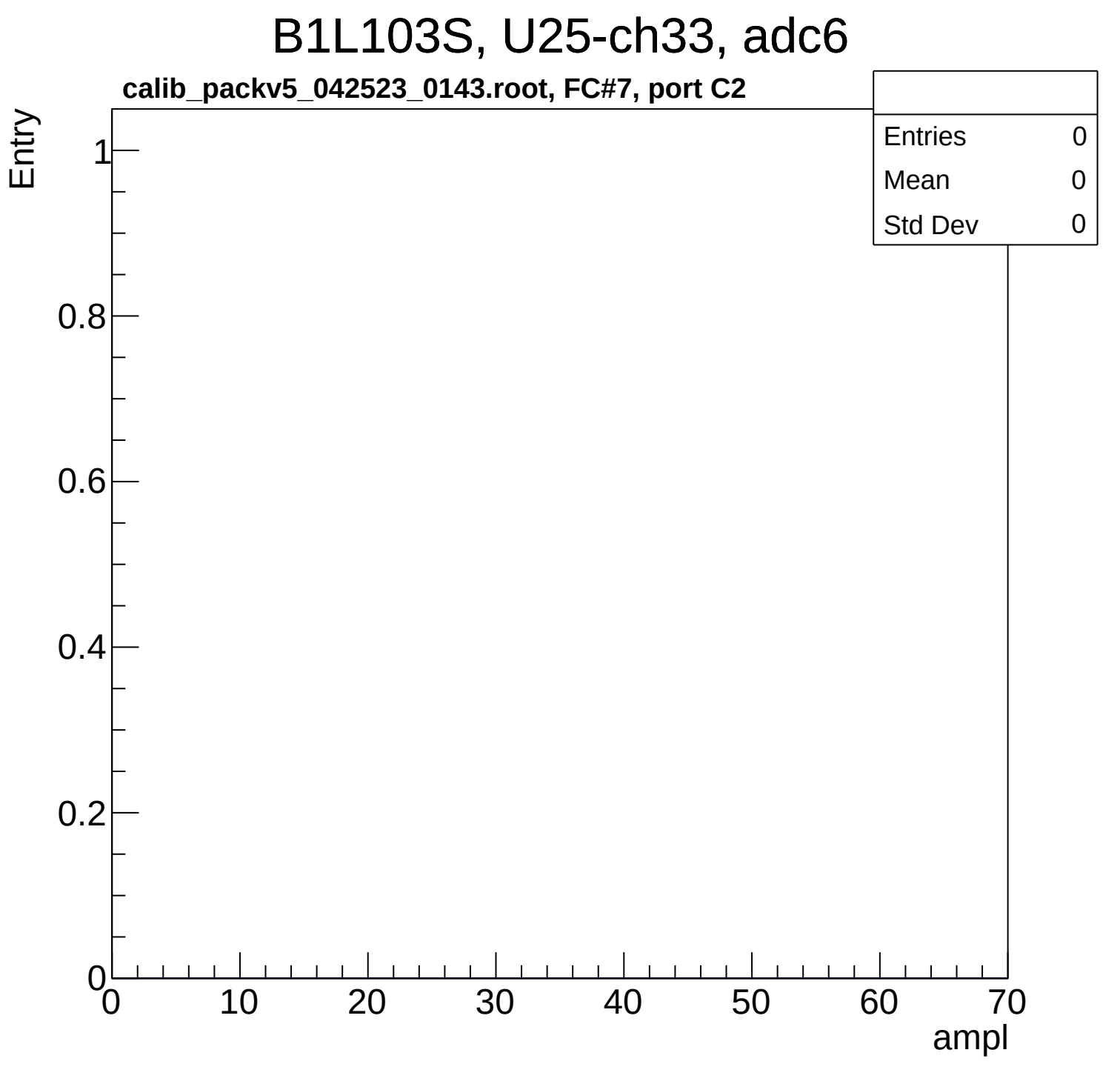
B1L103S, U25-ch33, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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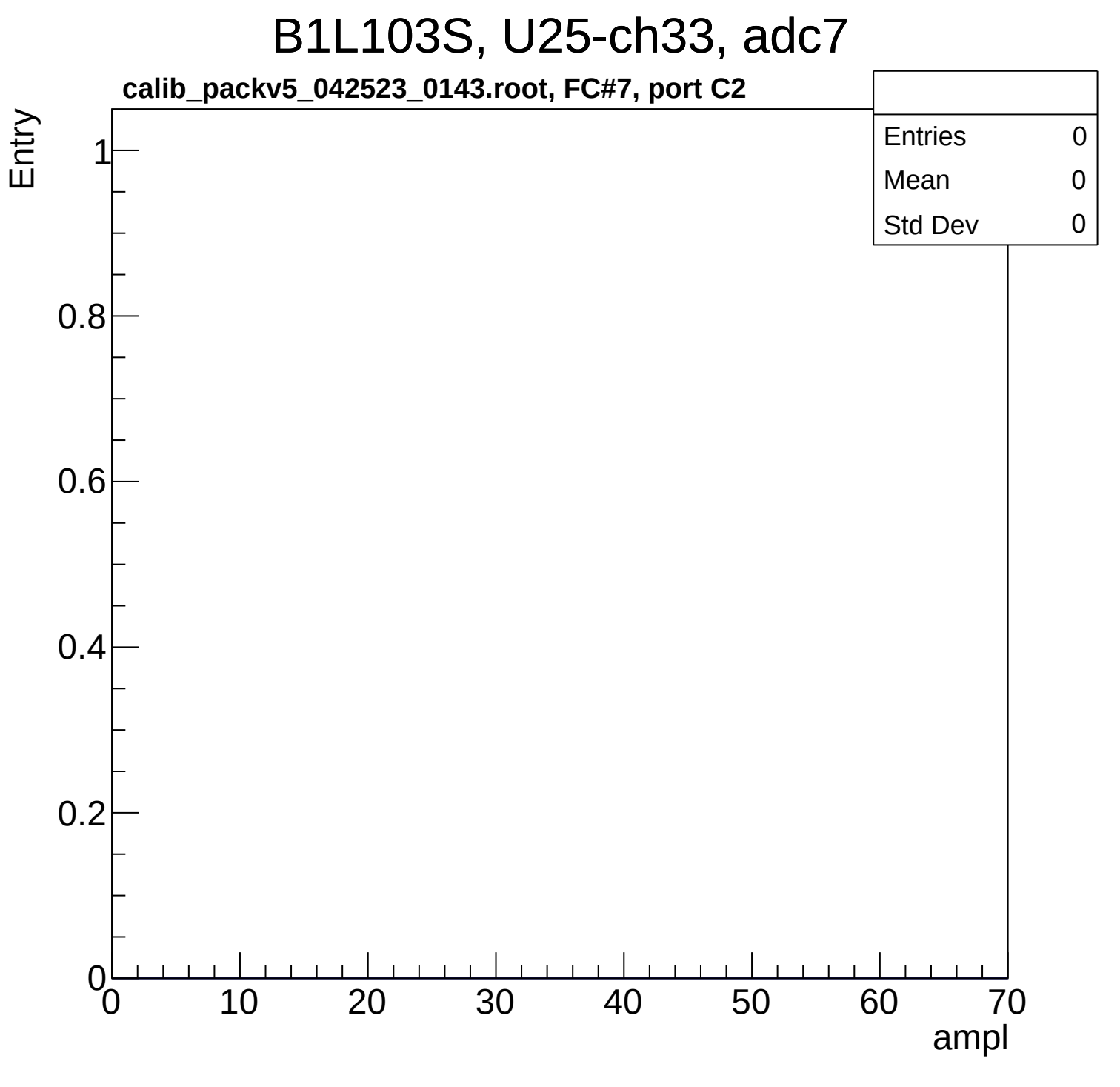
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch33, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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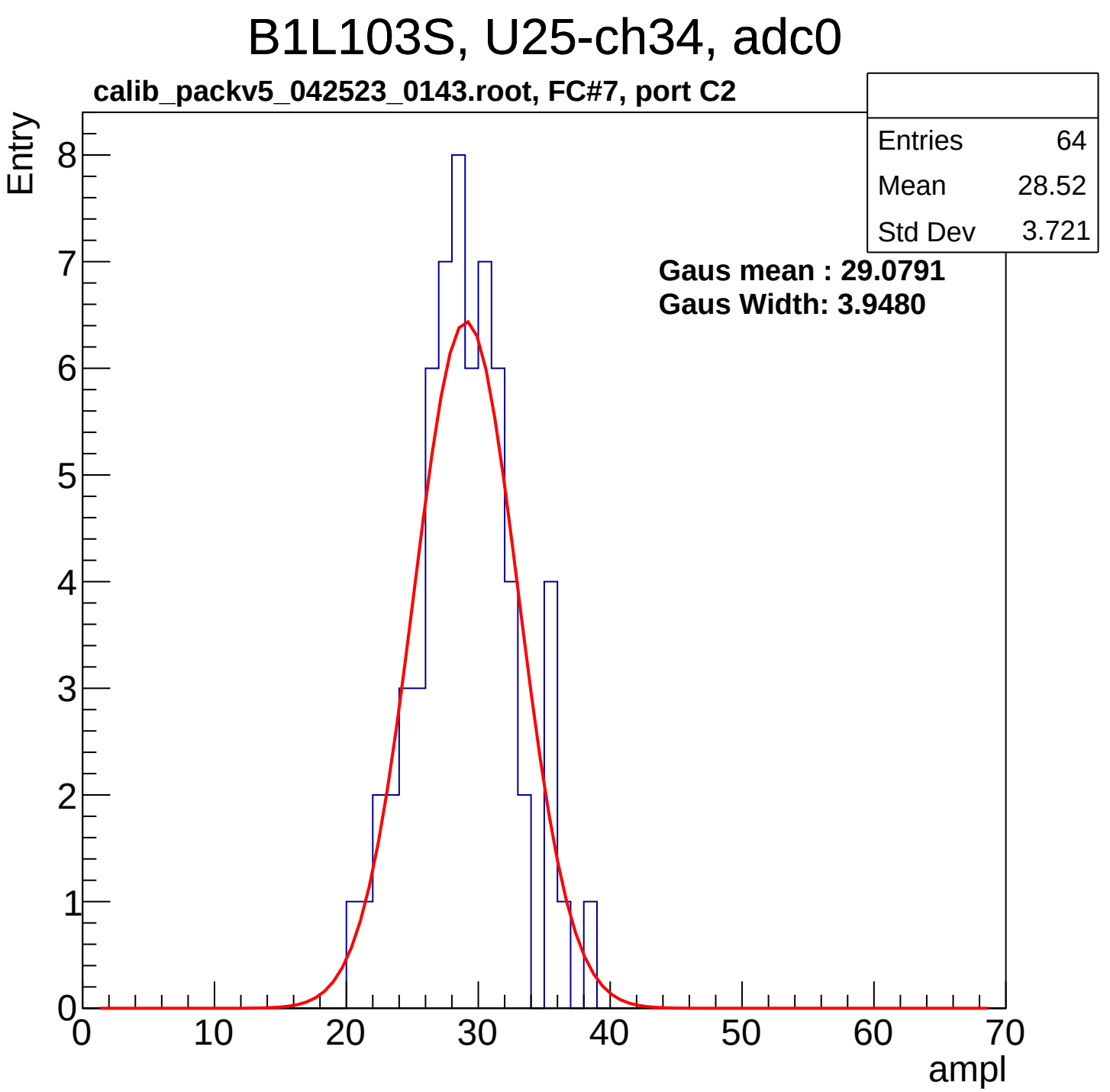
50
60
70
ampl


B1L103S, U25-ch34, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 28.52
Std Dev 3.721
Gaus mean : 29.0791
Gaus Width: 3.9480
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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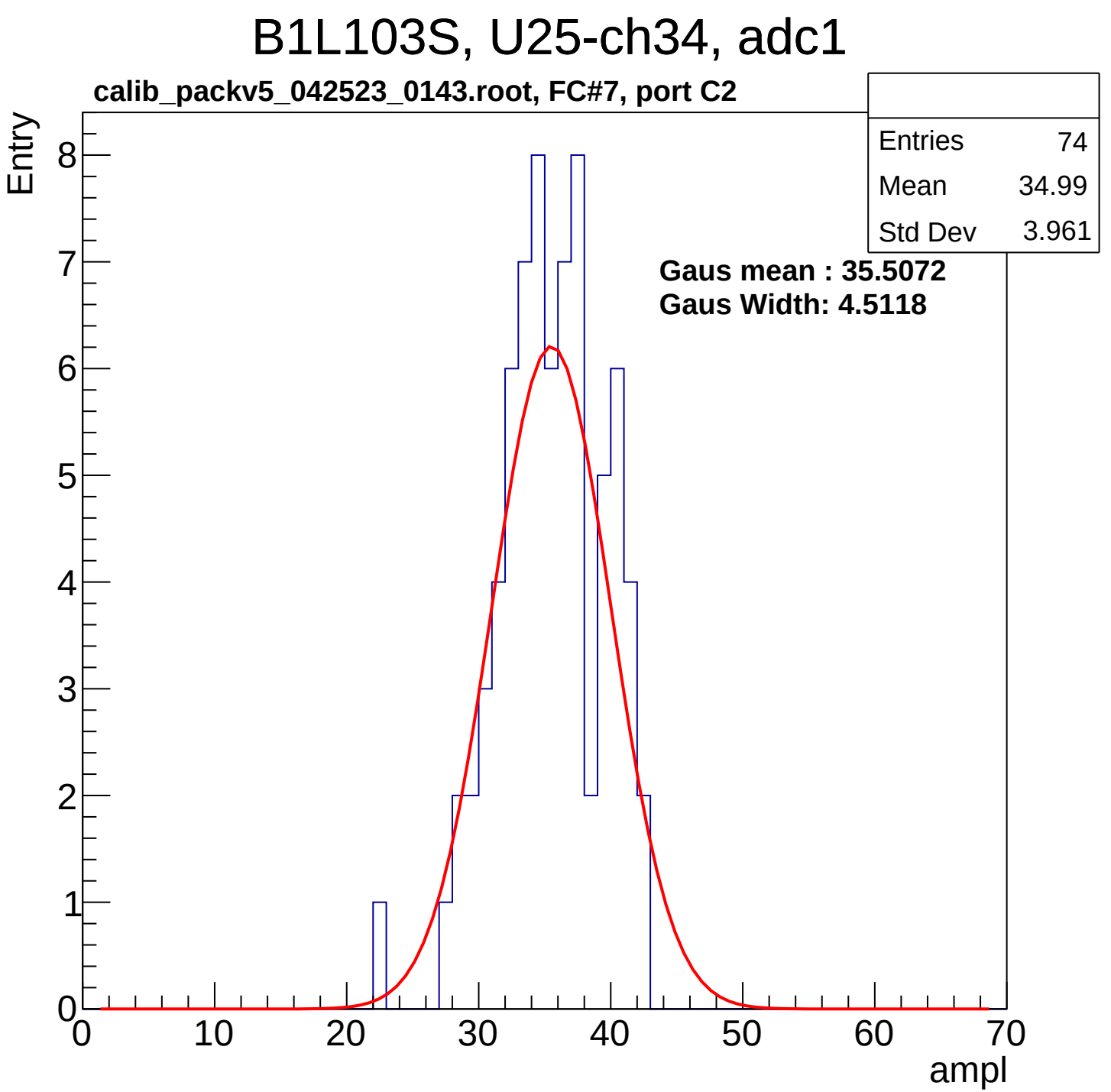
B1L103S, U25-ch34, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 34.99
Std Dev 3.961
Gaus mean : 35.5072
Gaus Width: 4.5118
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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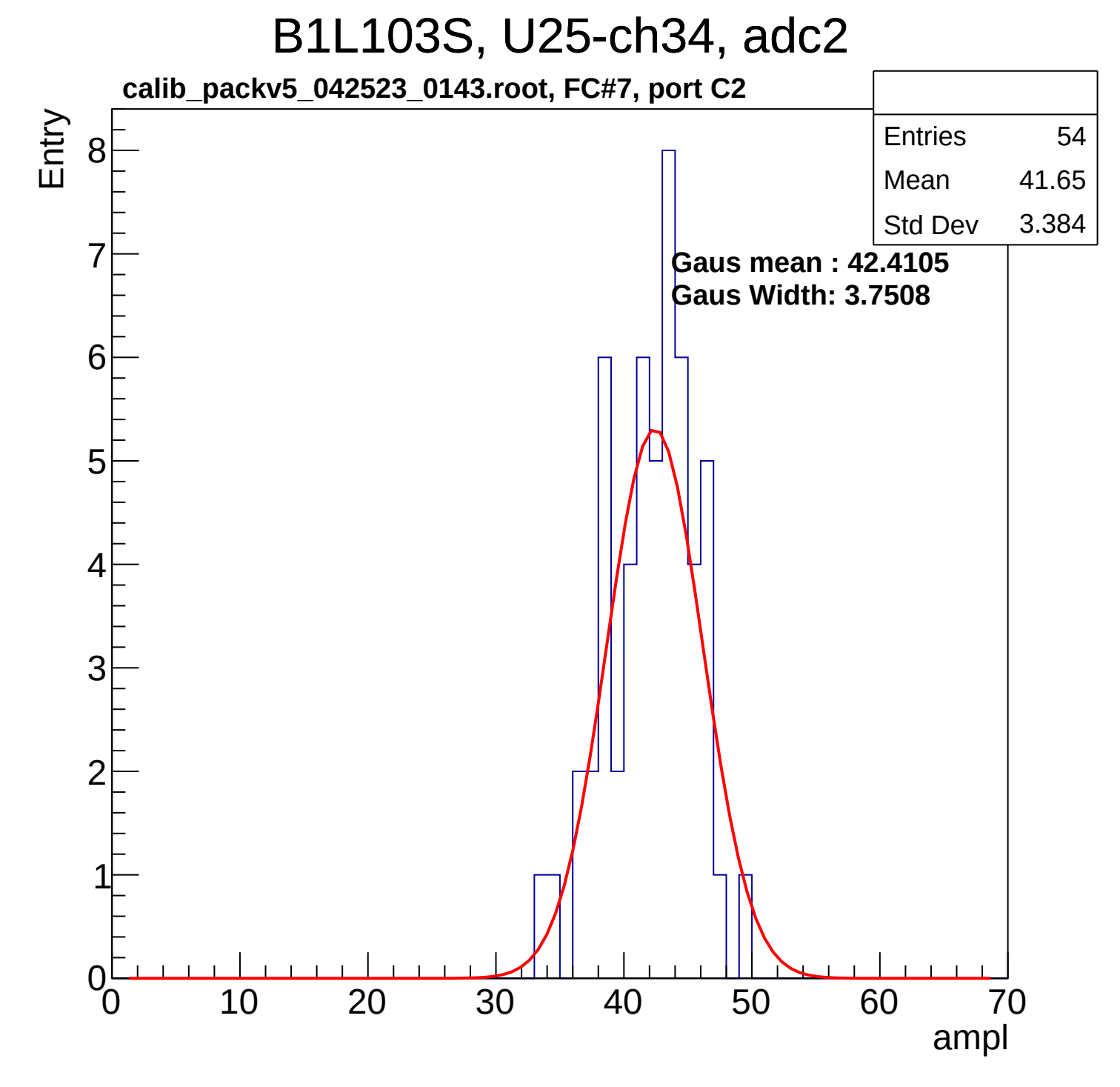
B1L103S, U25-ch34, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 41.65
Std Dev 3.384
Gaus mean : 42.4105
Gaus Width: 3.7508
Entry
ampl
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1
2
3
4
5
6
7
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0
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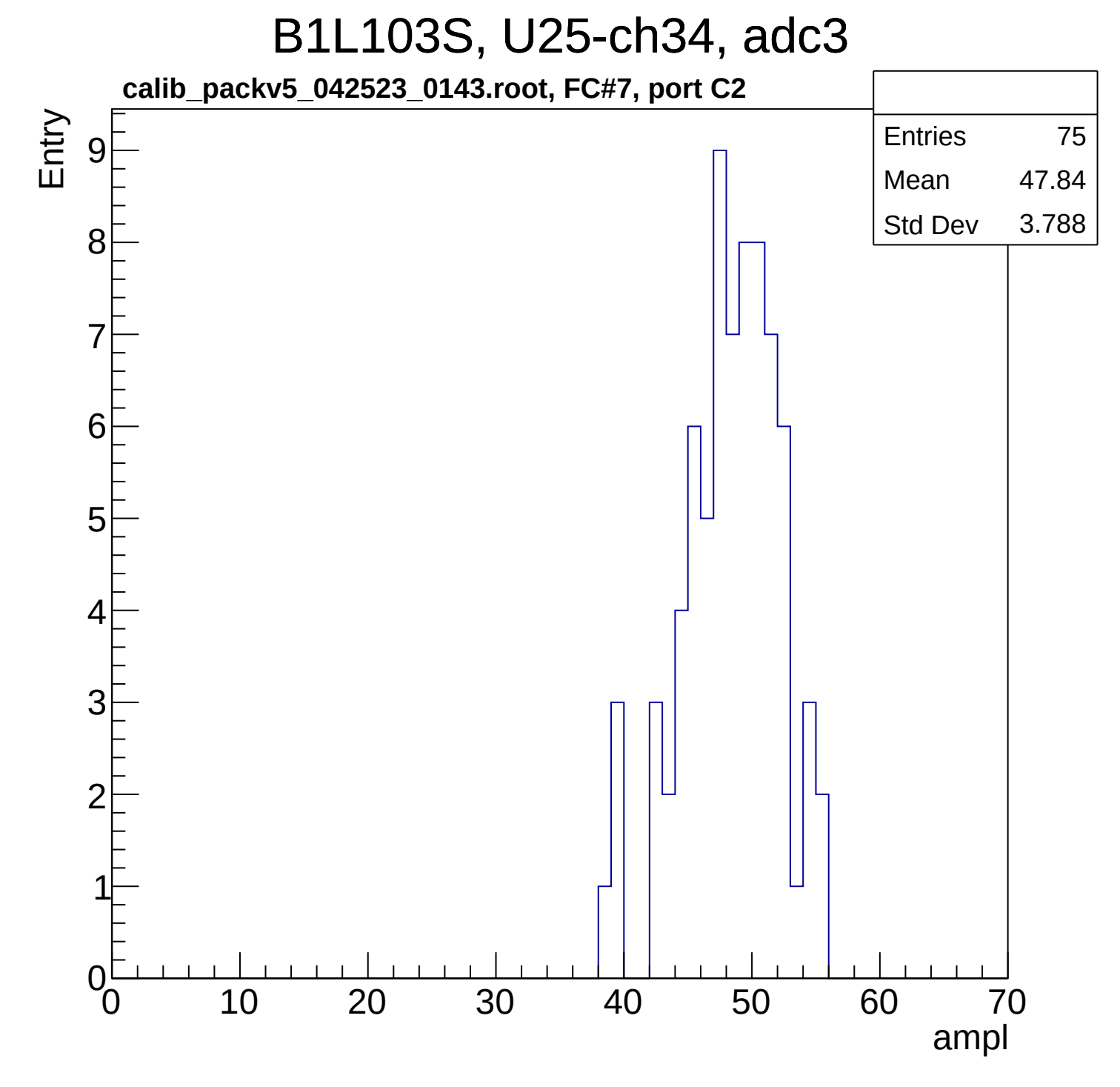
B1L103S, U25-ch34, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 47.84
Std Dev 3.788
Entry
ampl
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3
4
5
6
7
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0
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60
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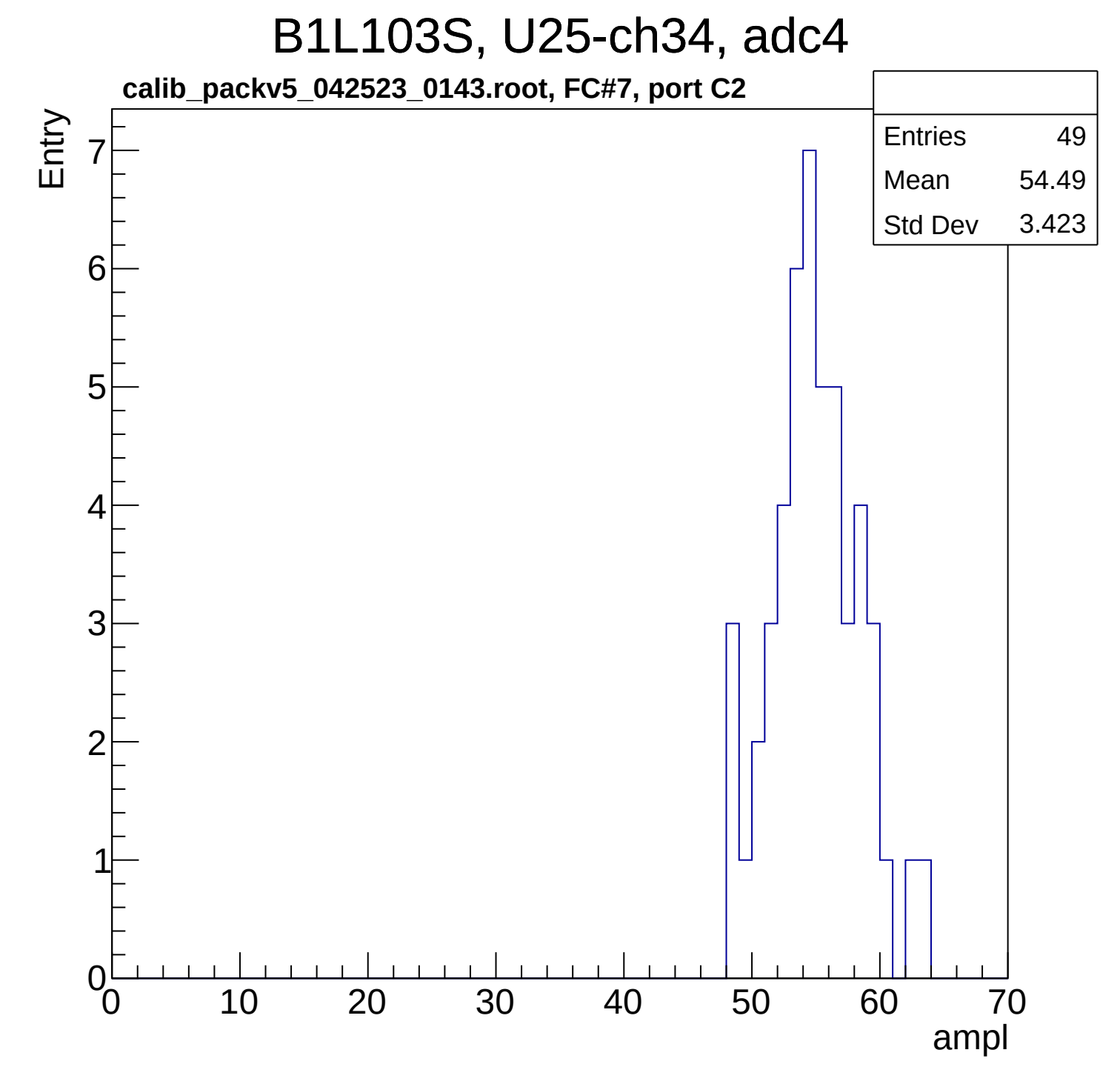
B1L103S, U25-ch34, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 54.49
Std Dev 3.423
Entry
ampl
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3
4
5
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0
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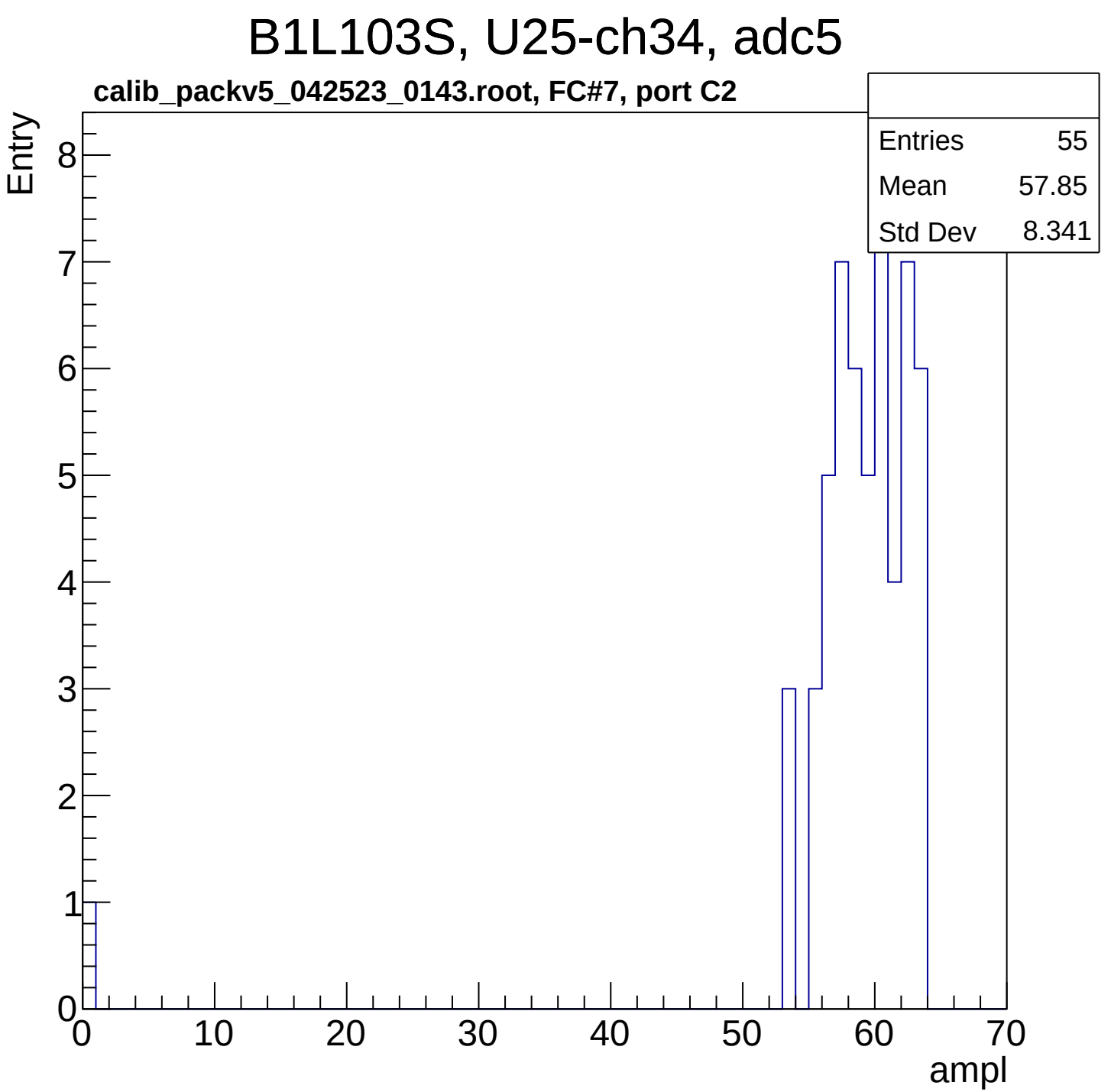
B1L103S, U25-ch34, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 57.85
Std Dev 8.341
Entry
ampl
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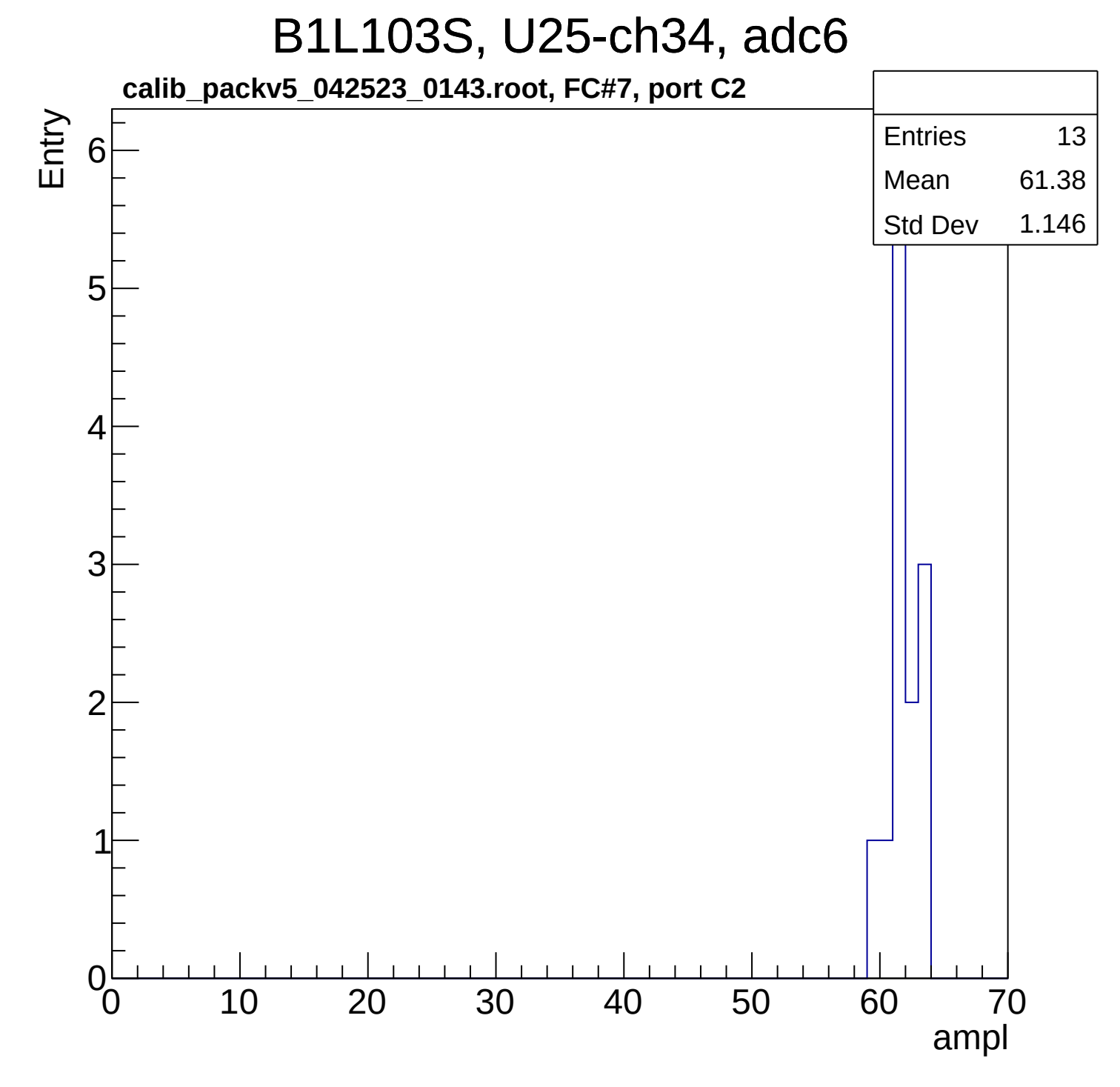
B1L103S, U25-ch34, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.38
Std Dev 1.146
Entry
ampl
0
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2
3
4
5
6
0
10
20
30
40
50
60
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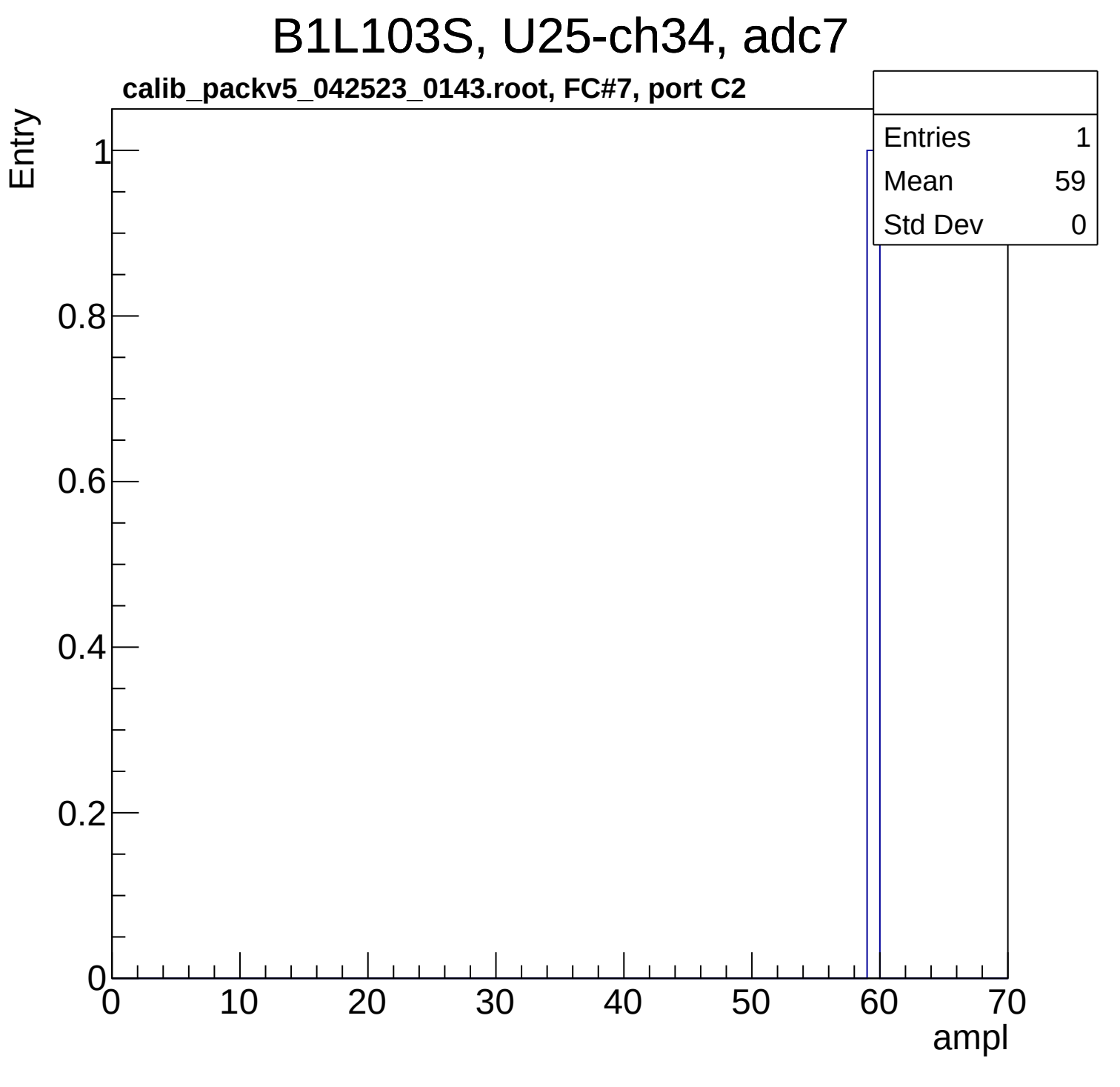
B1L103S, U25-ch34, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 59
Std Dev 0
Entry
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0.4
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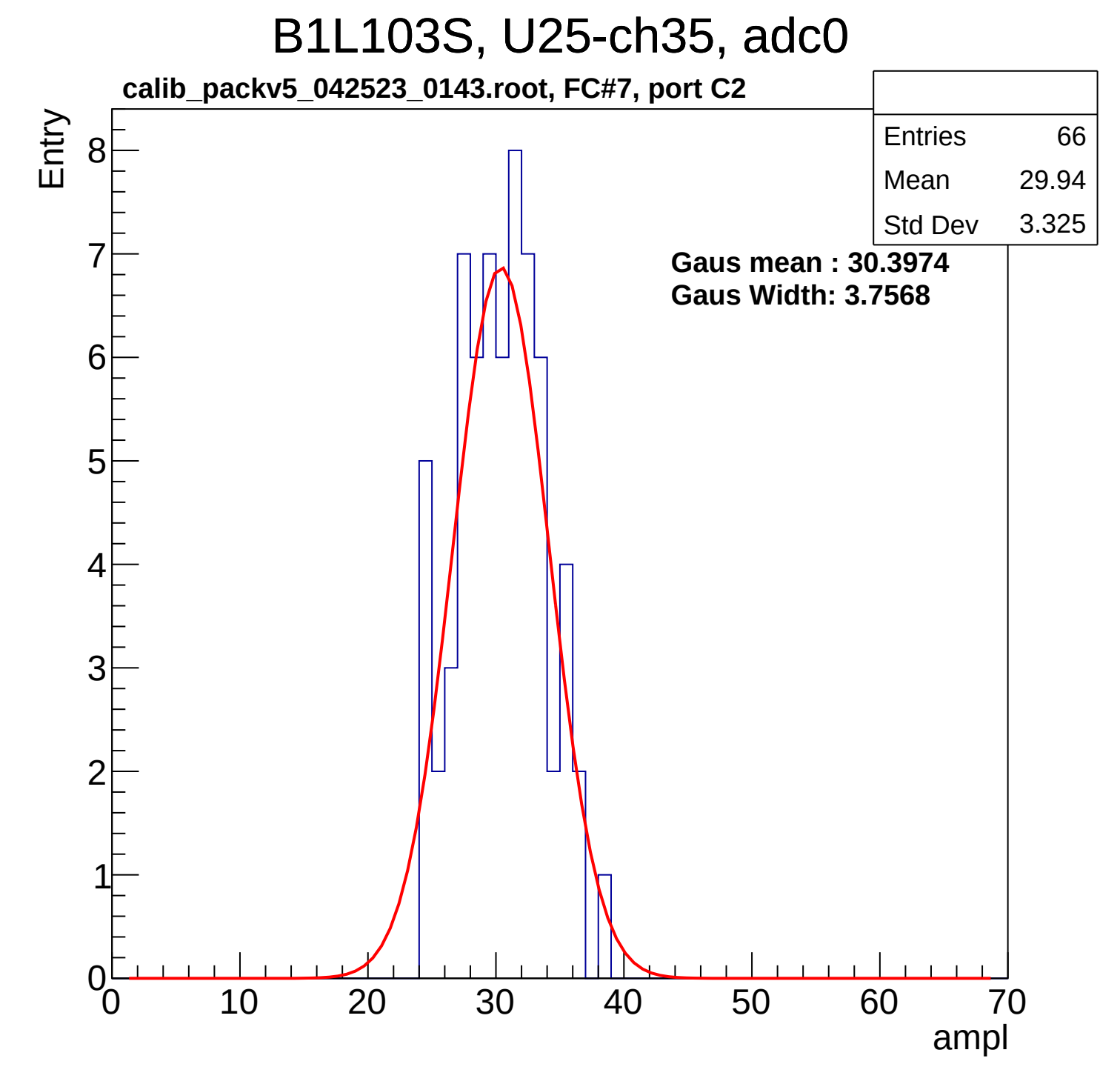
ampl


B1L103S, U25-ch35, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.94
Std Dev 3.325
Gaus mean : 30.3974
Gaus Width: 3.7568
Entry
ampl
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2
3
4
5
6
7
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0
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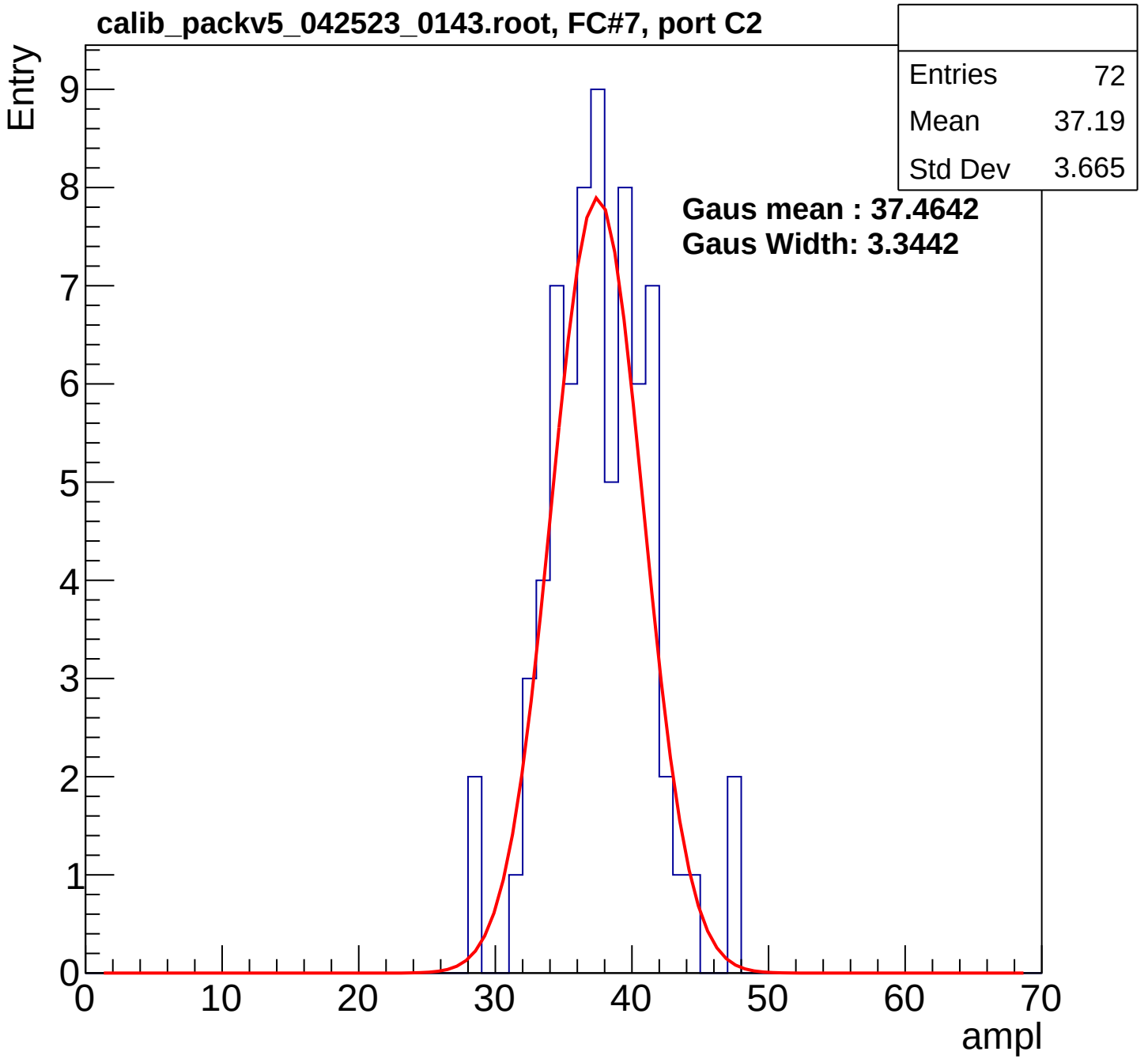
B1L103S, U25-ch35, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.19
Std Dev 3.665
Gaus mean : 37.4642
Gaus Width: 3.3442
Entry
ampl
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3
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5
6
7
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0
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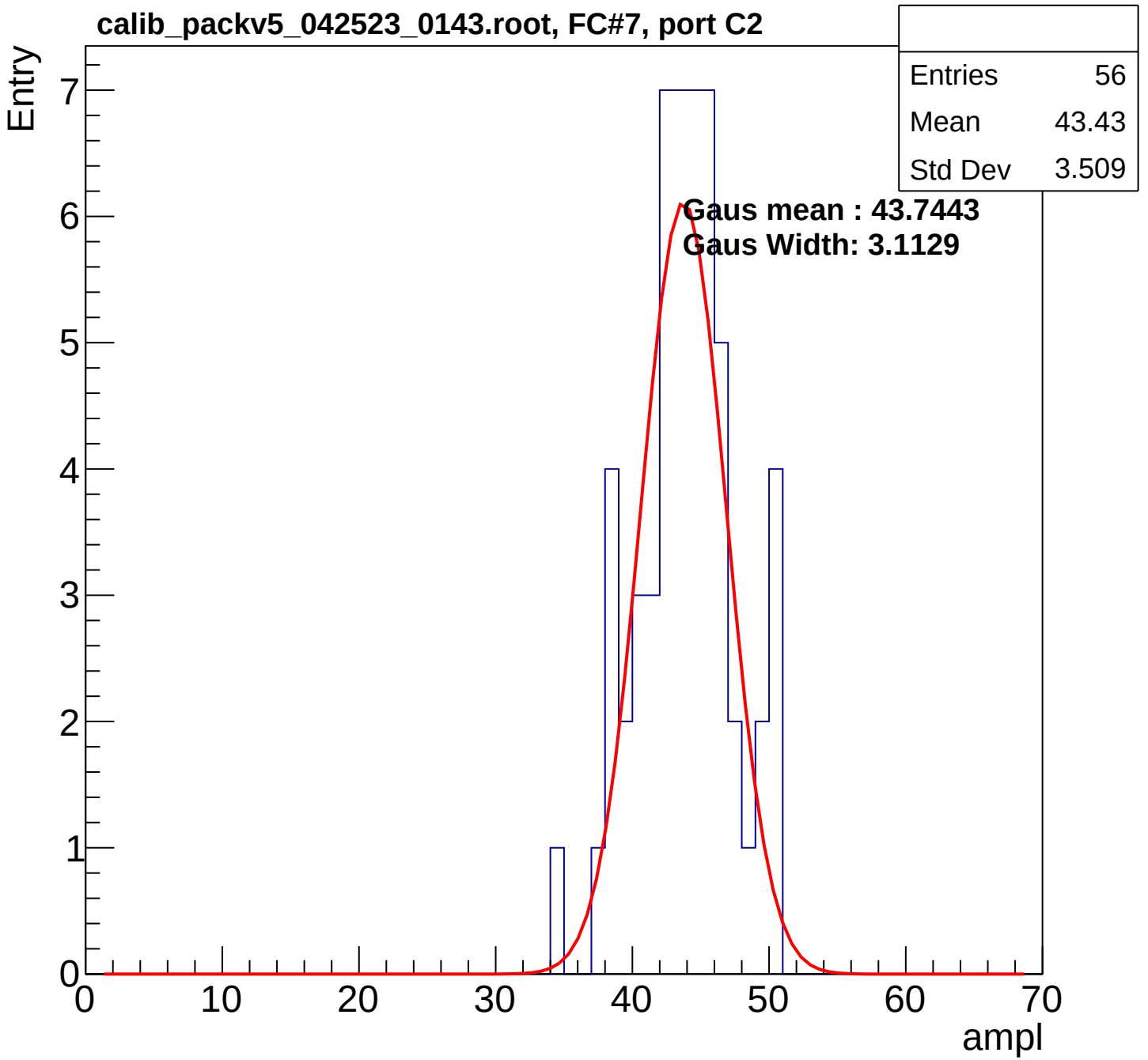
B1L103S, U25-ch35, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.43
Std Dev 3.509
Gaus mean : 43.7443
Gaus Width: 3.1129
Entry
ampl
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0
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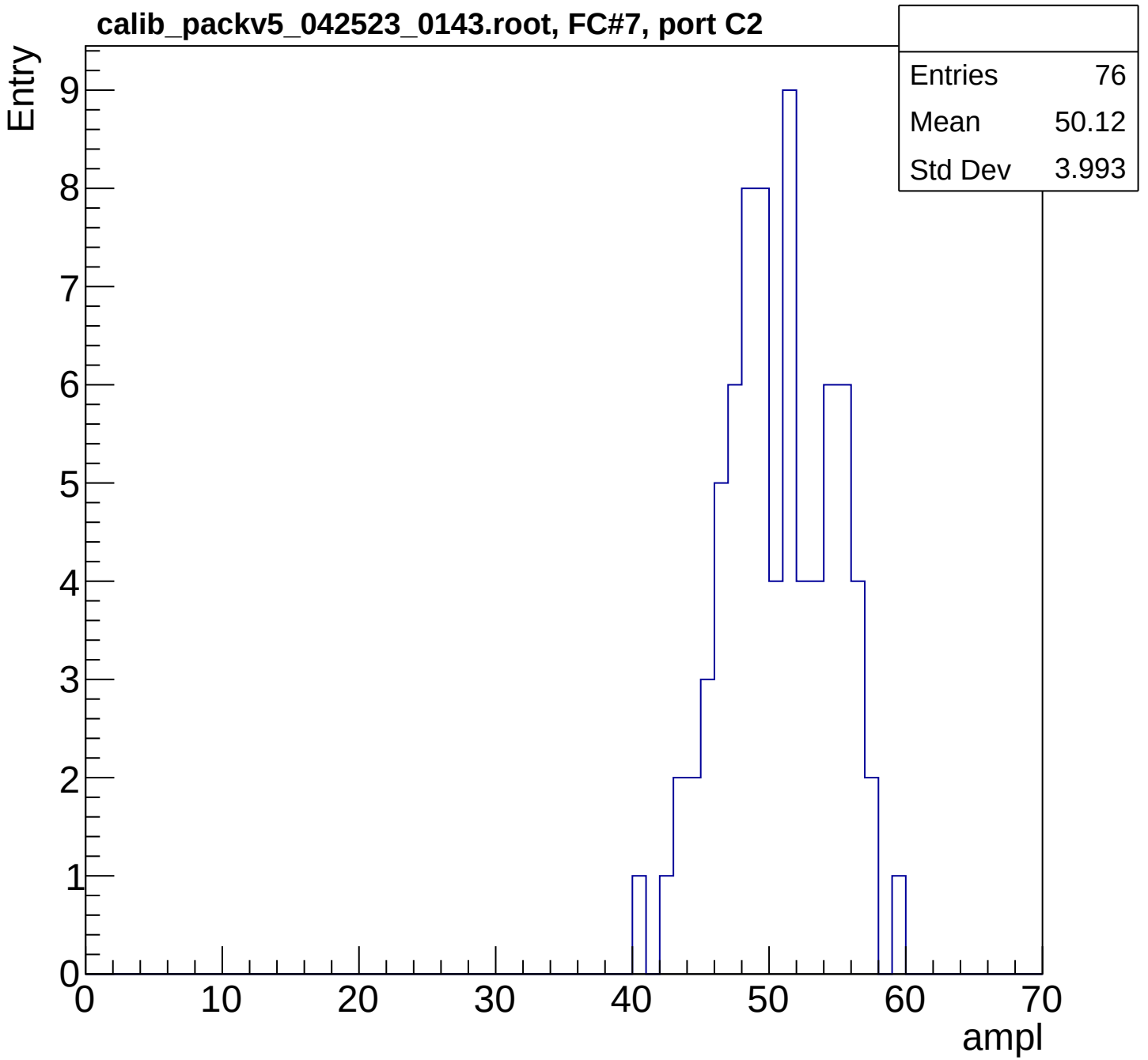
B1L103S, U25-ch35, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 50.12
Std Dev 3.993
Entry
ampl
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0
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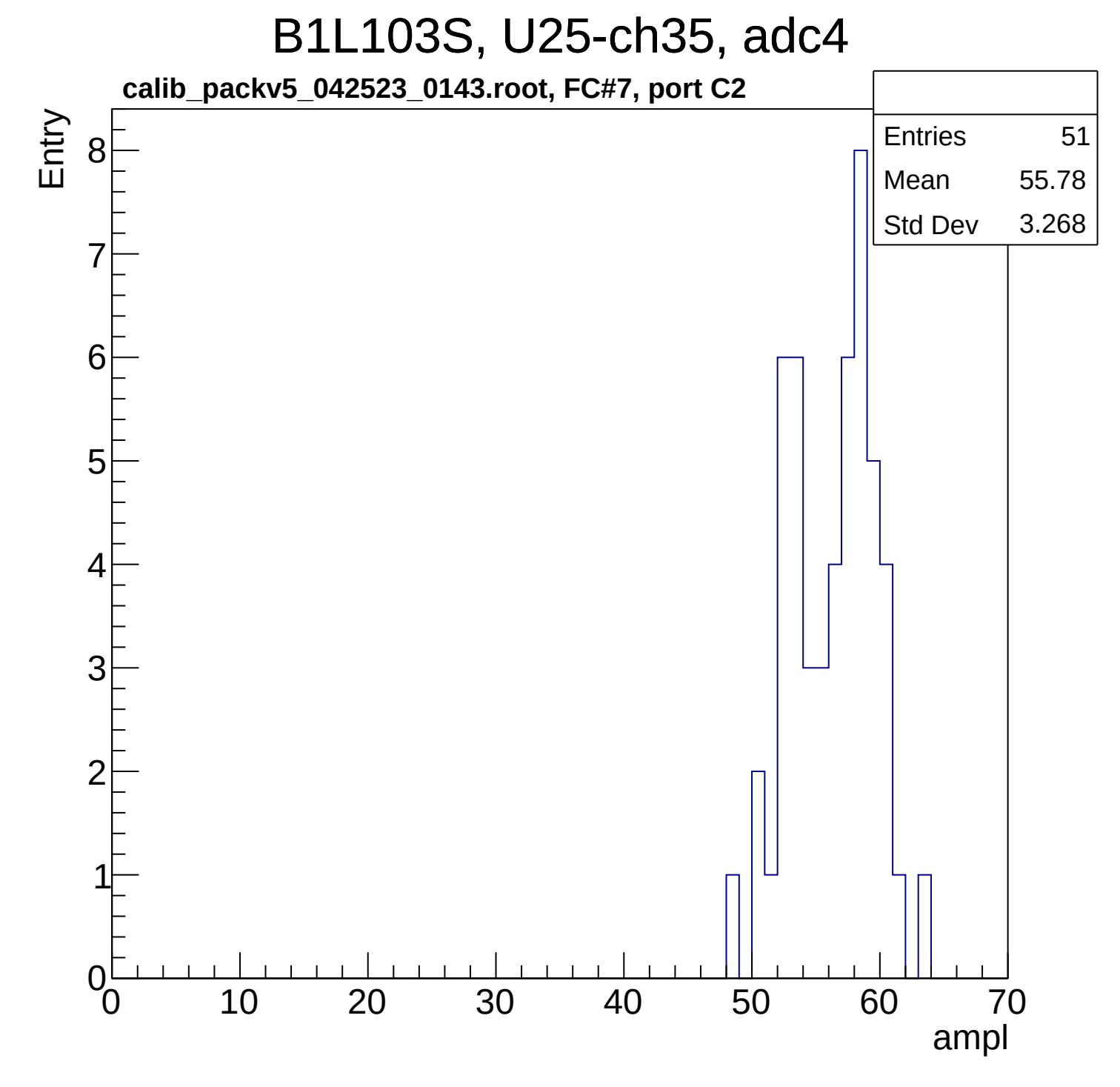
B1L103S, U25-ch35, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.78
Std Dev 3.268
Entry
ampl
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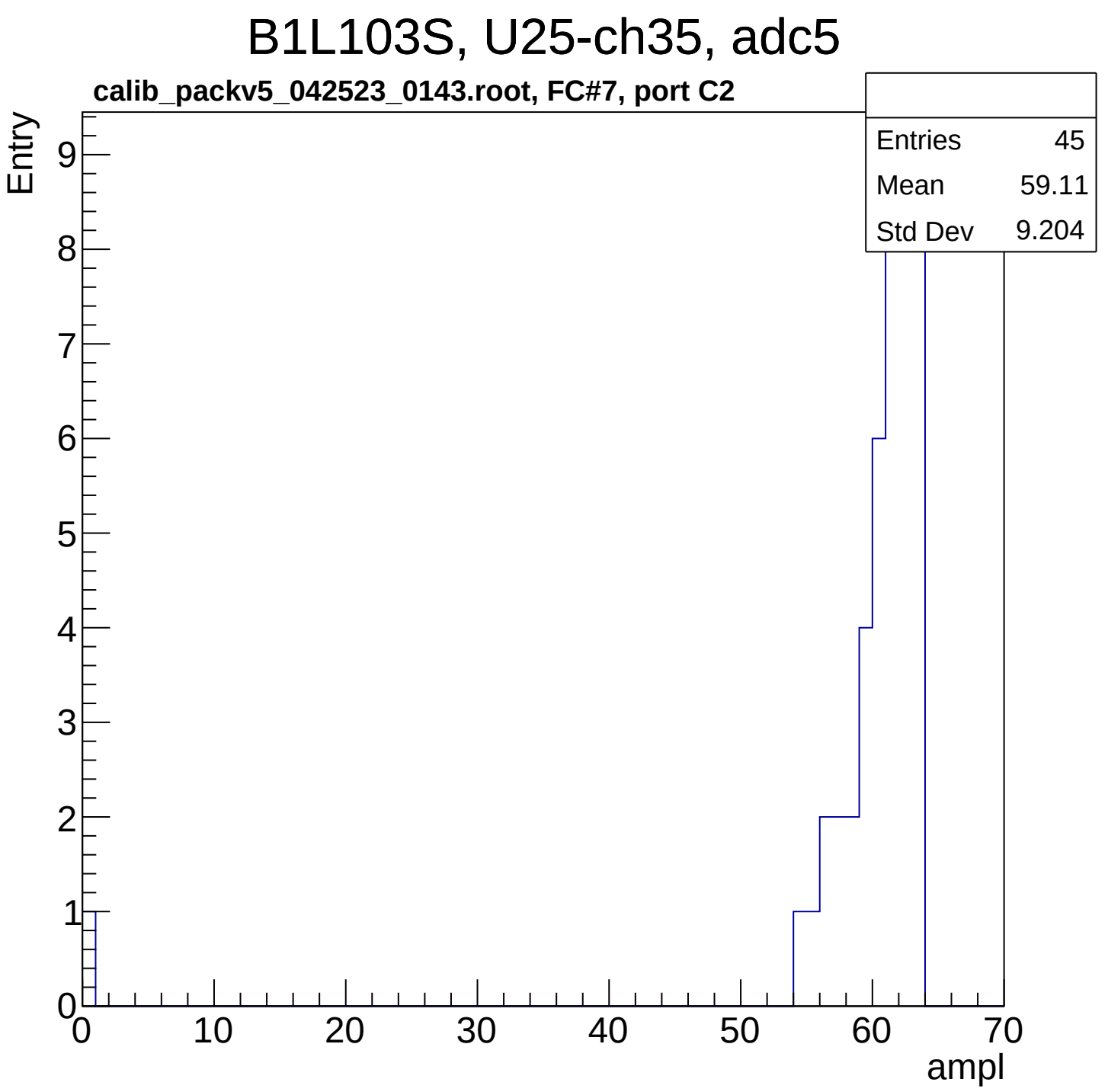
B1L103S, U25-ch35, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.11
Std Dev 9.204
Entry
ampl
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2
3
4
5
6
7
8
9
0
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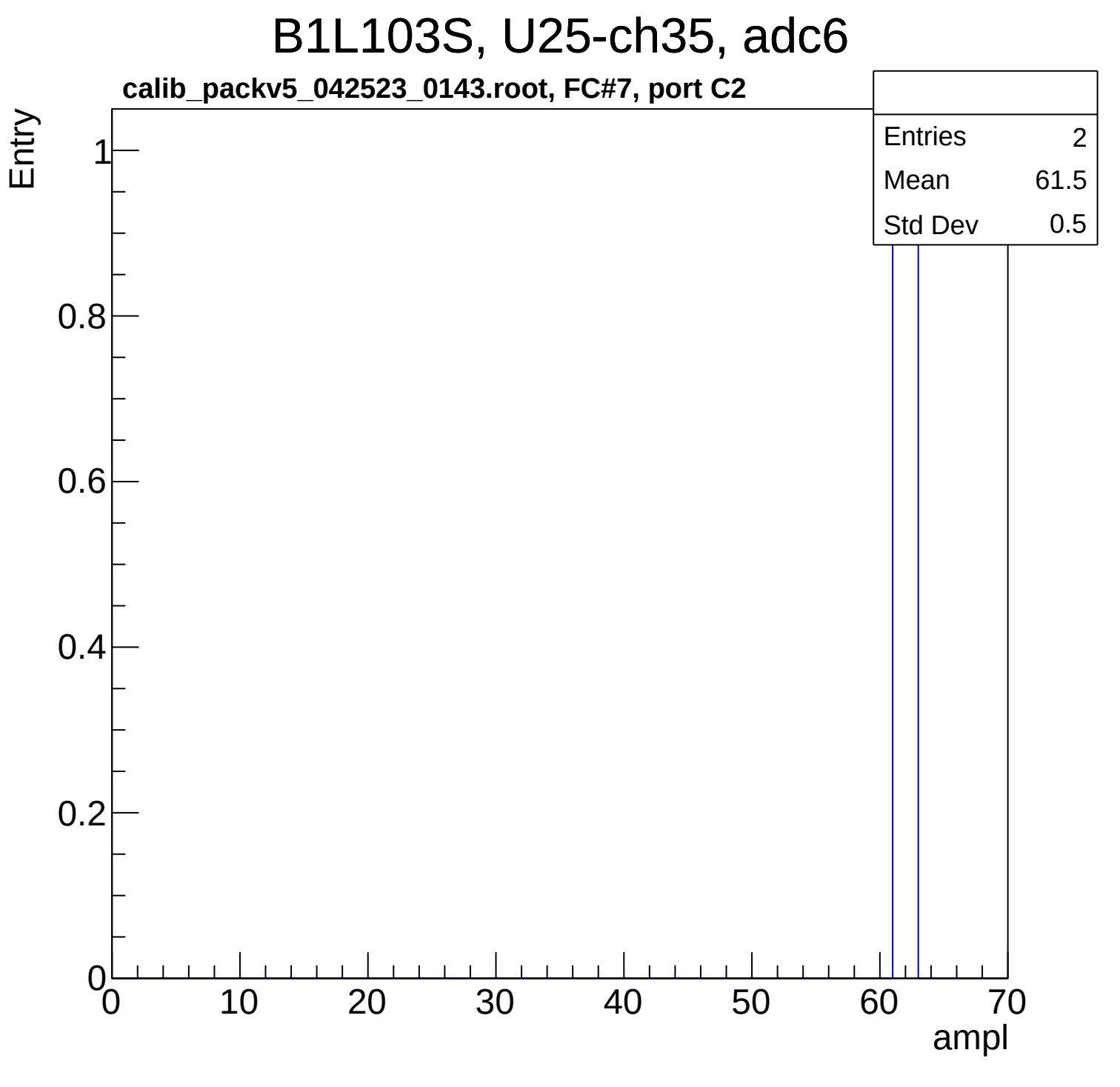
B1L103S, U25-ch35, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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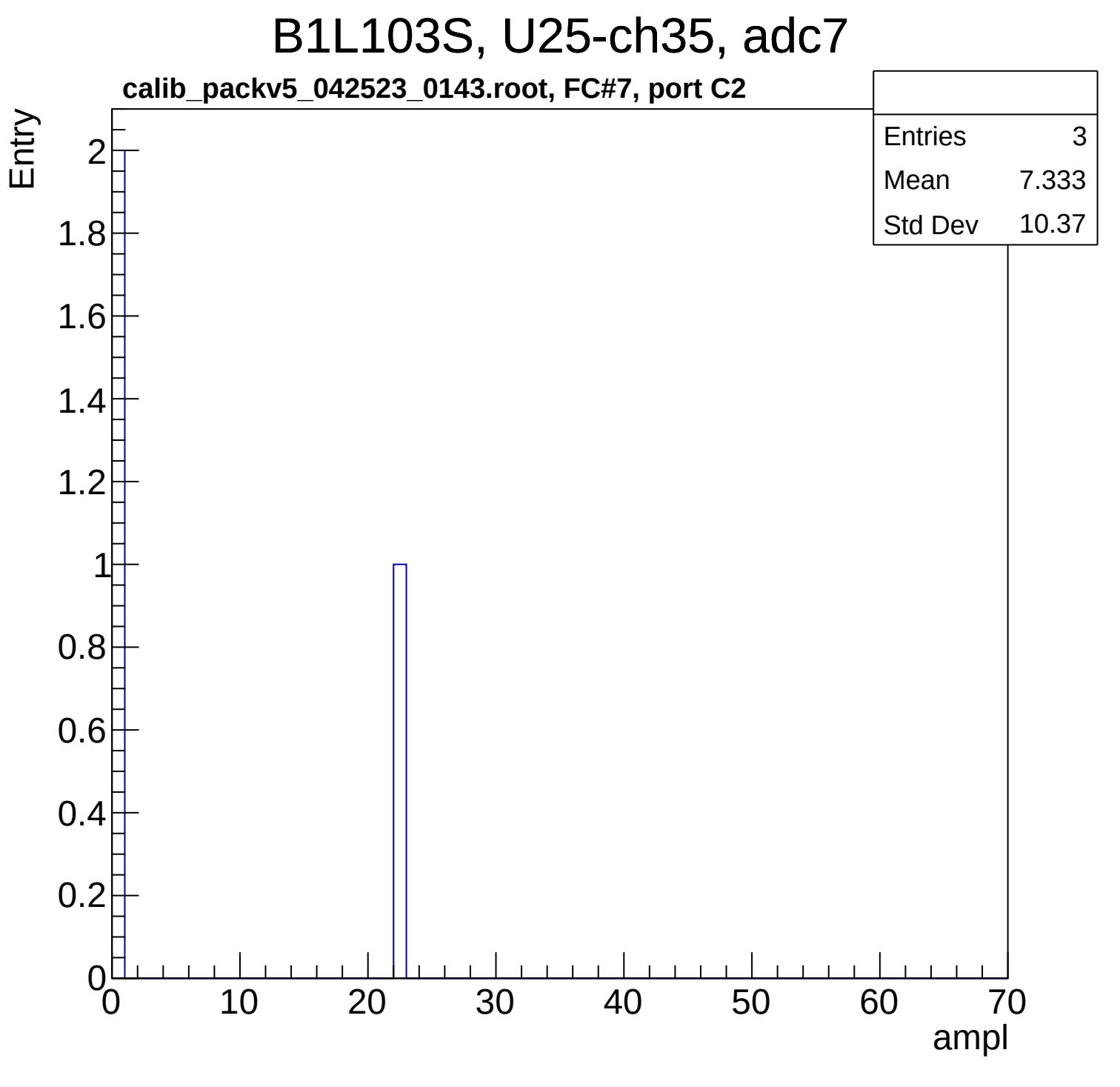
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch35, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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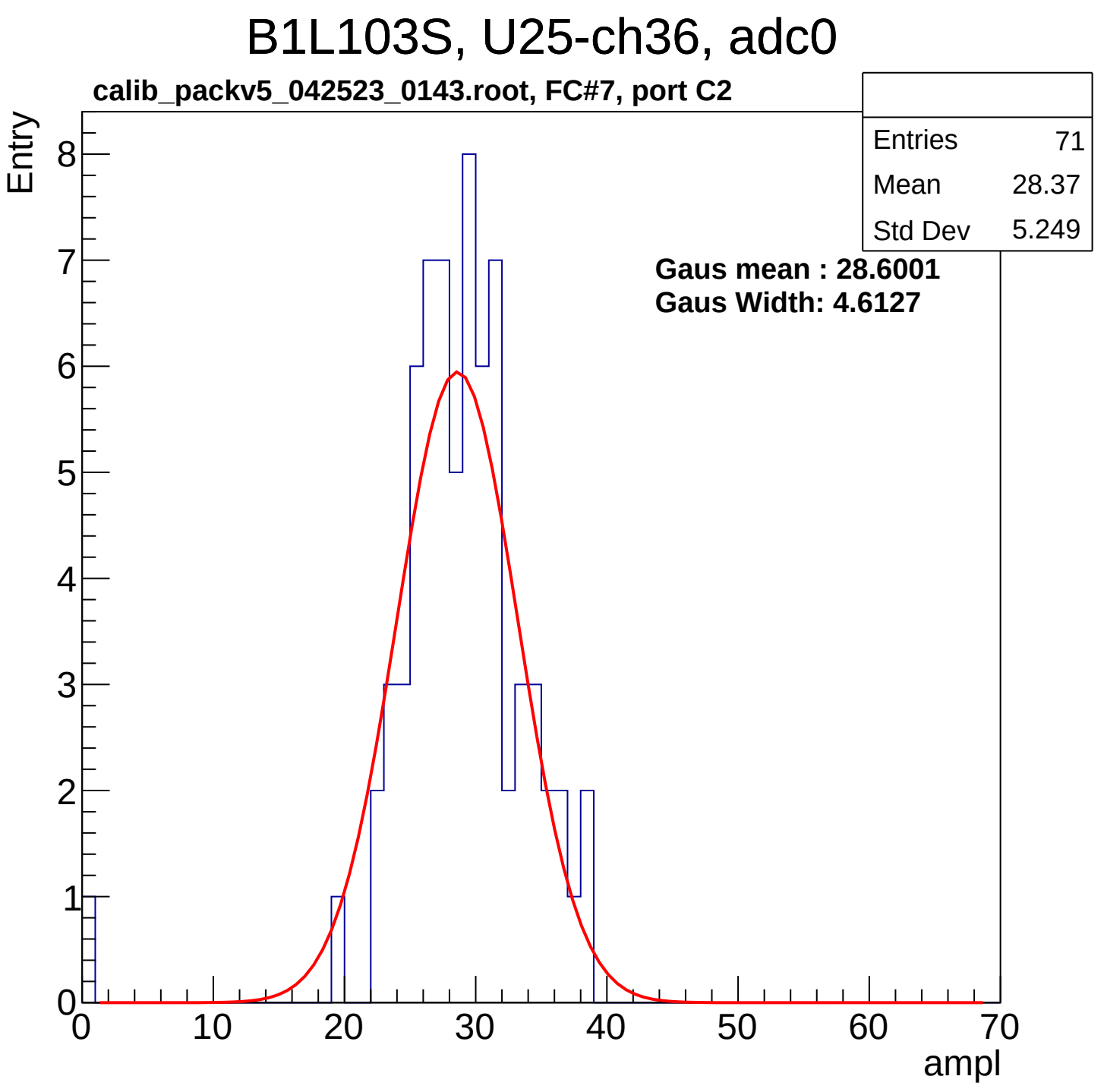
B1L103S, U25-ch36, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 28.37
Std Dev 5.249
Gaus mean : 28.6001
Gaus Width: 4.6127
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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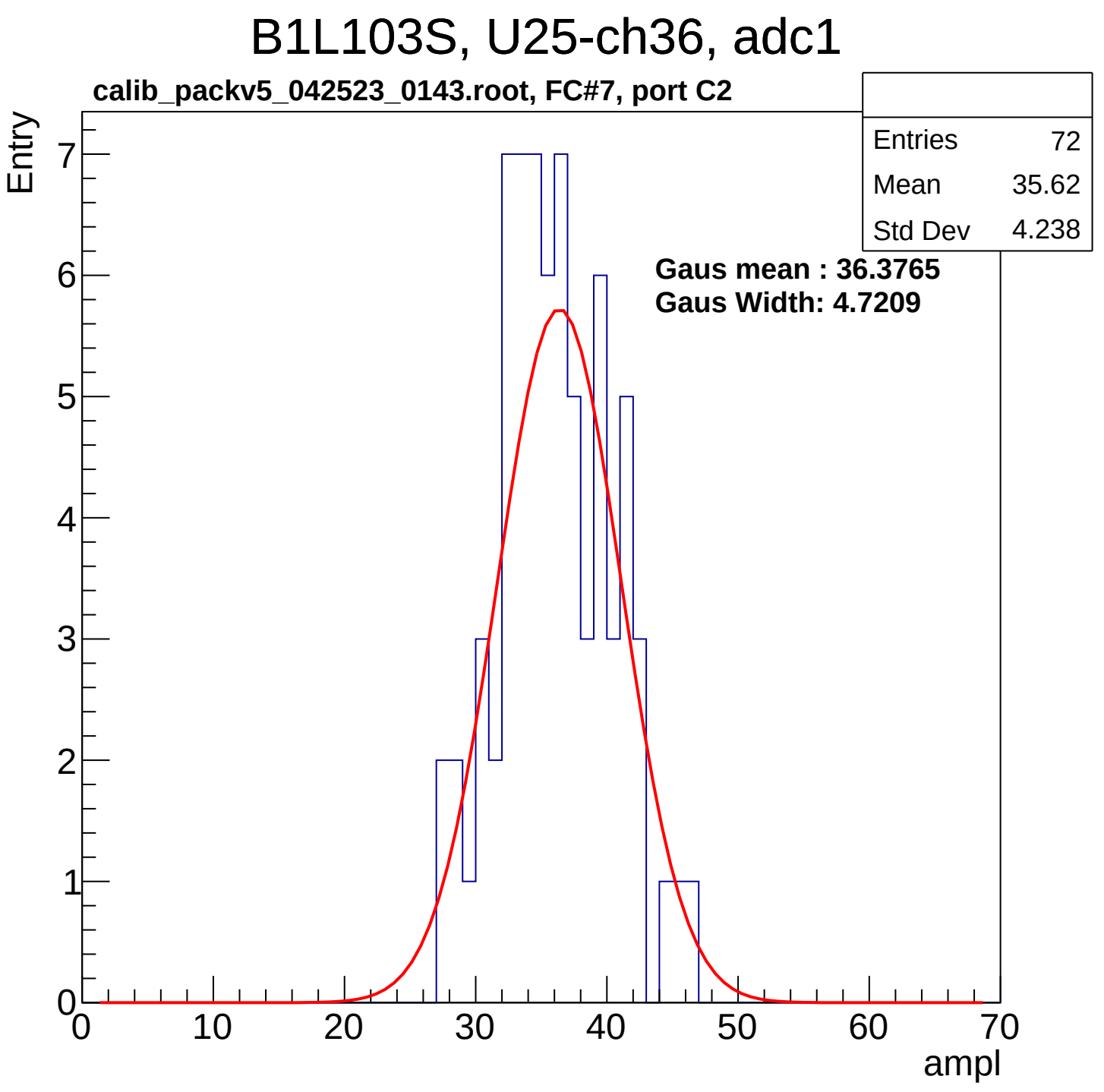
B1L103S, U25-ch36, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.62
Std Dev 4.238
Gaus mean : 36.3765
Gaus Width: 4.7209
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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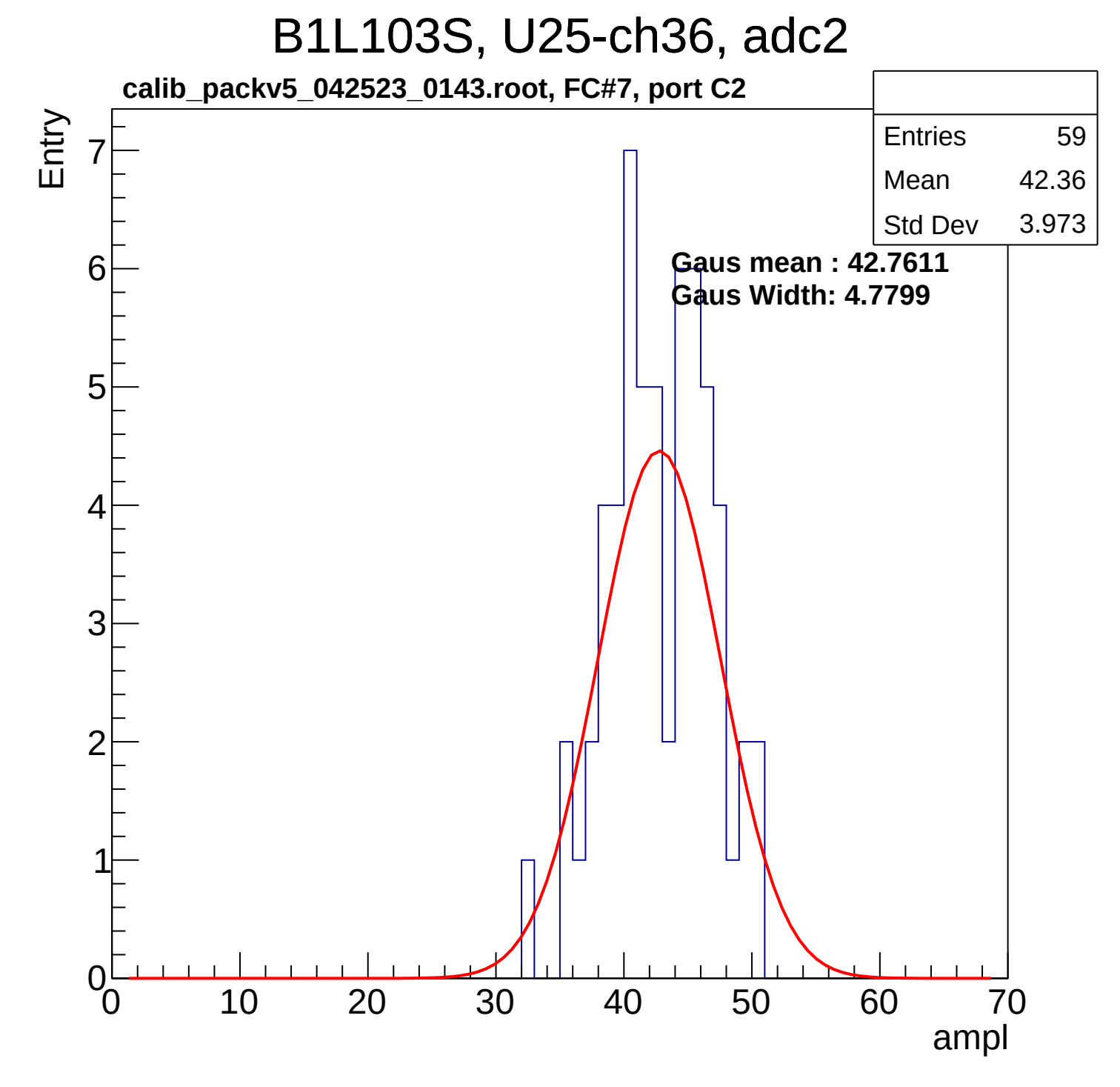
B1L103S, U25-ch36, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42.36
Std Dev 3.973
Gaus mean : 42.7611
Gaus Width: 4.7799
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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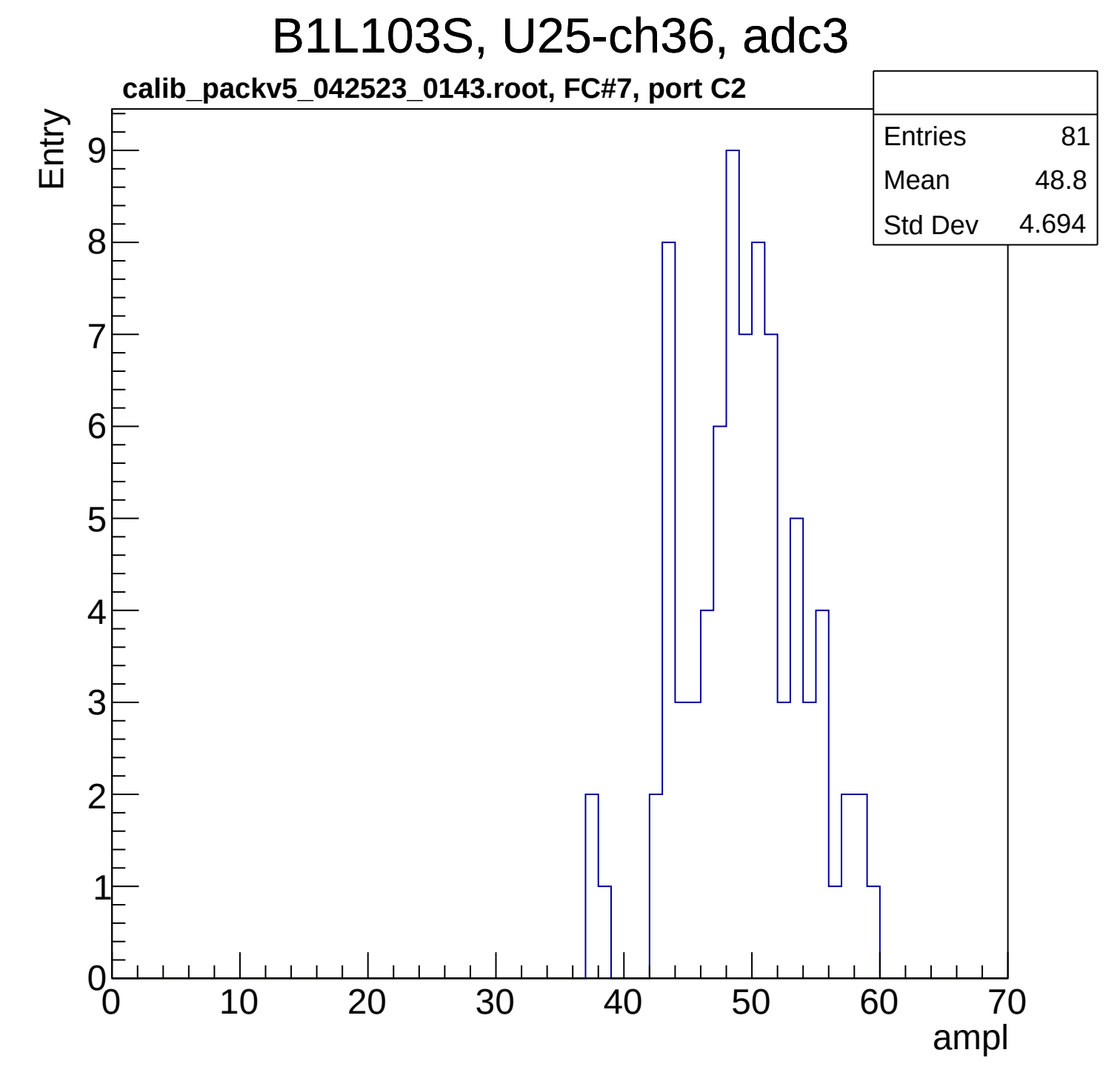
B1L103S, U25-ch36, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 48.8
Std Dev 4.694
Entry
ampl
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3
4
5
6
7
8
9
0
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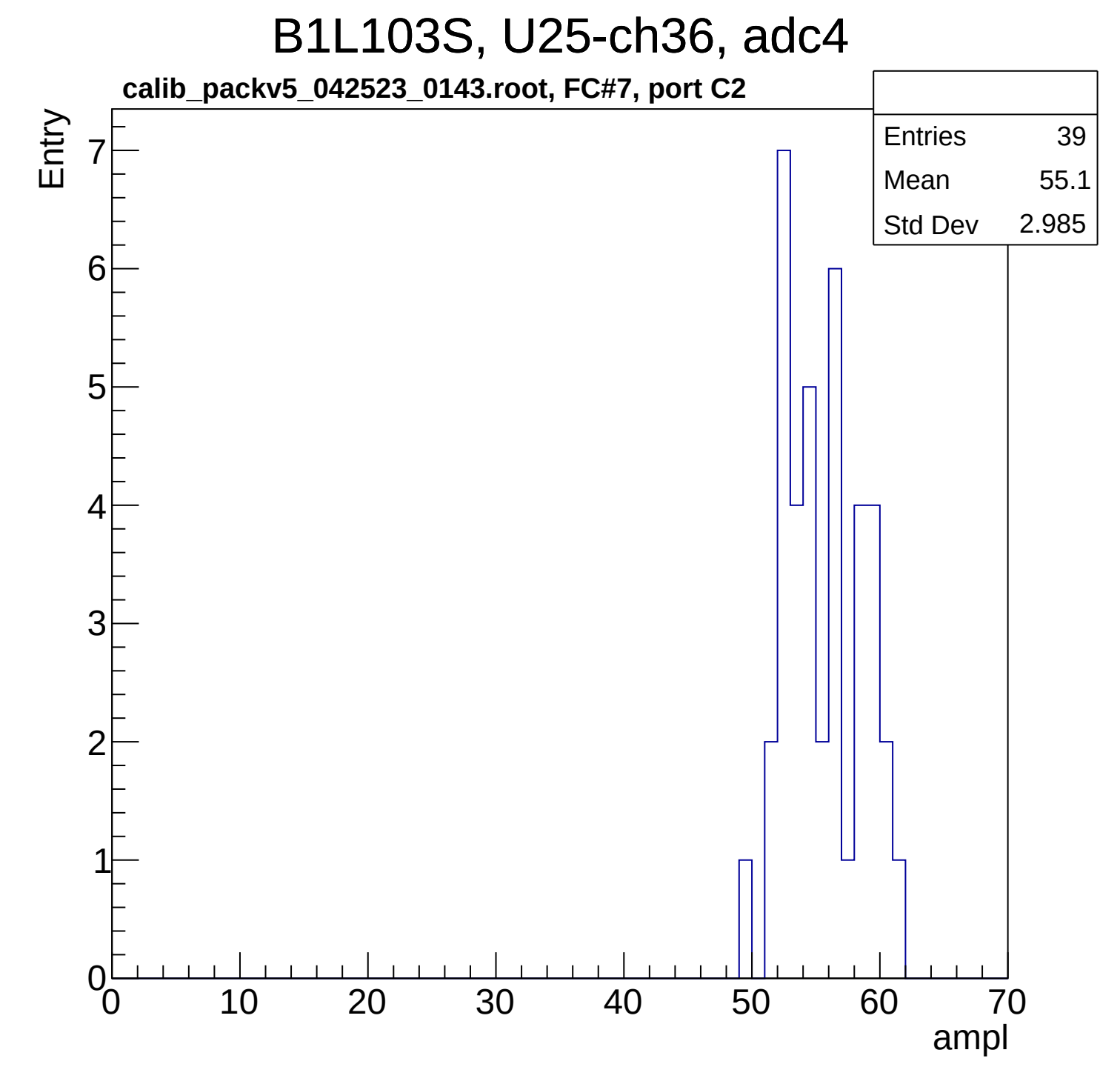
B1L103S, U25-ch36, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 55.1
Std Dev 2.985
Entry
ampl
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3
4
5
6
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0
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60
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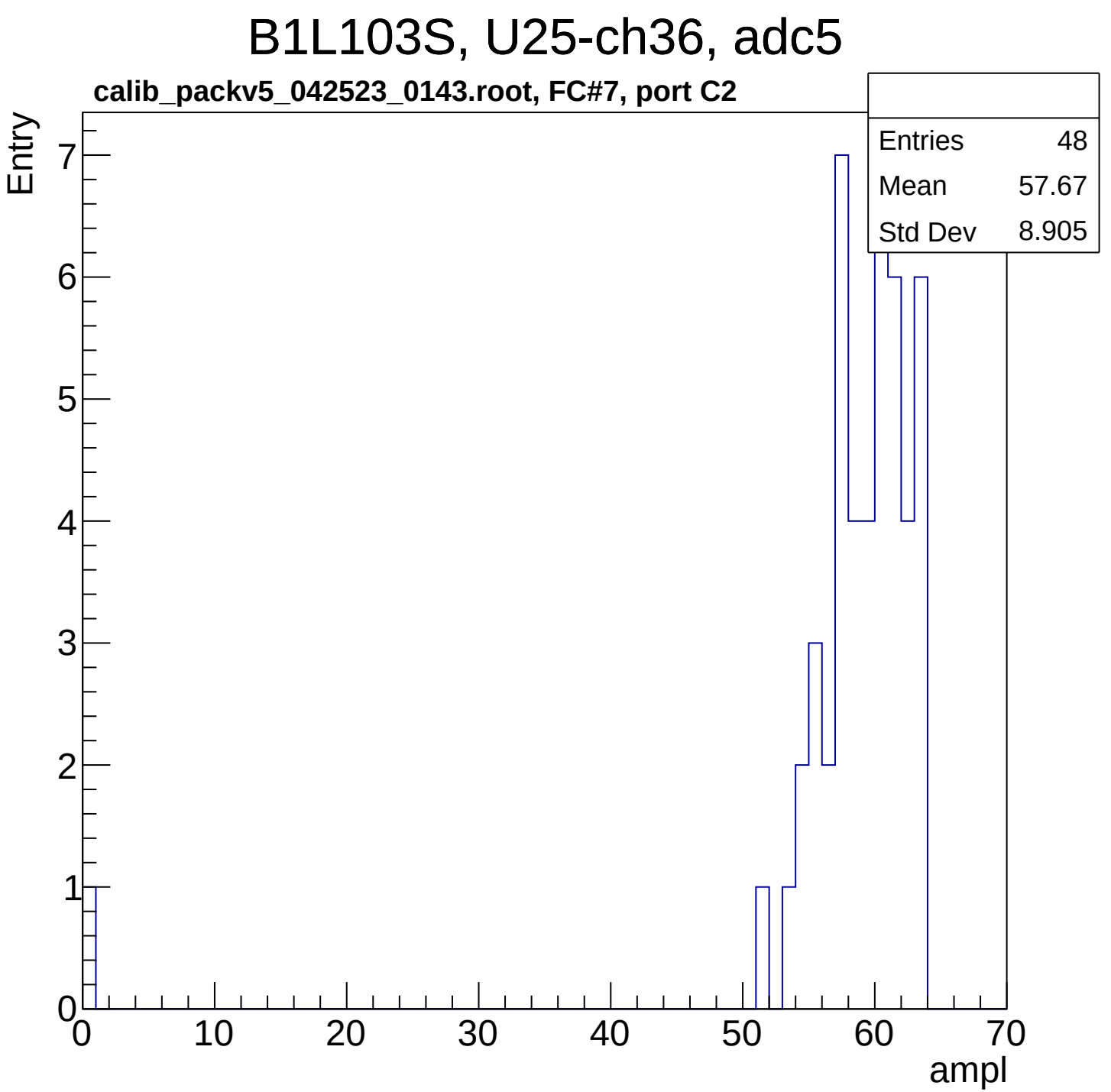
B1L103S, U25-ch36, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 57.67
Std Dev 8.905
Entry
ampl
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4
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0
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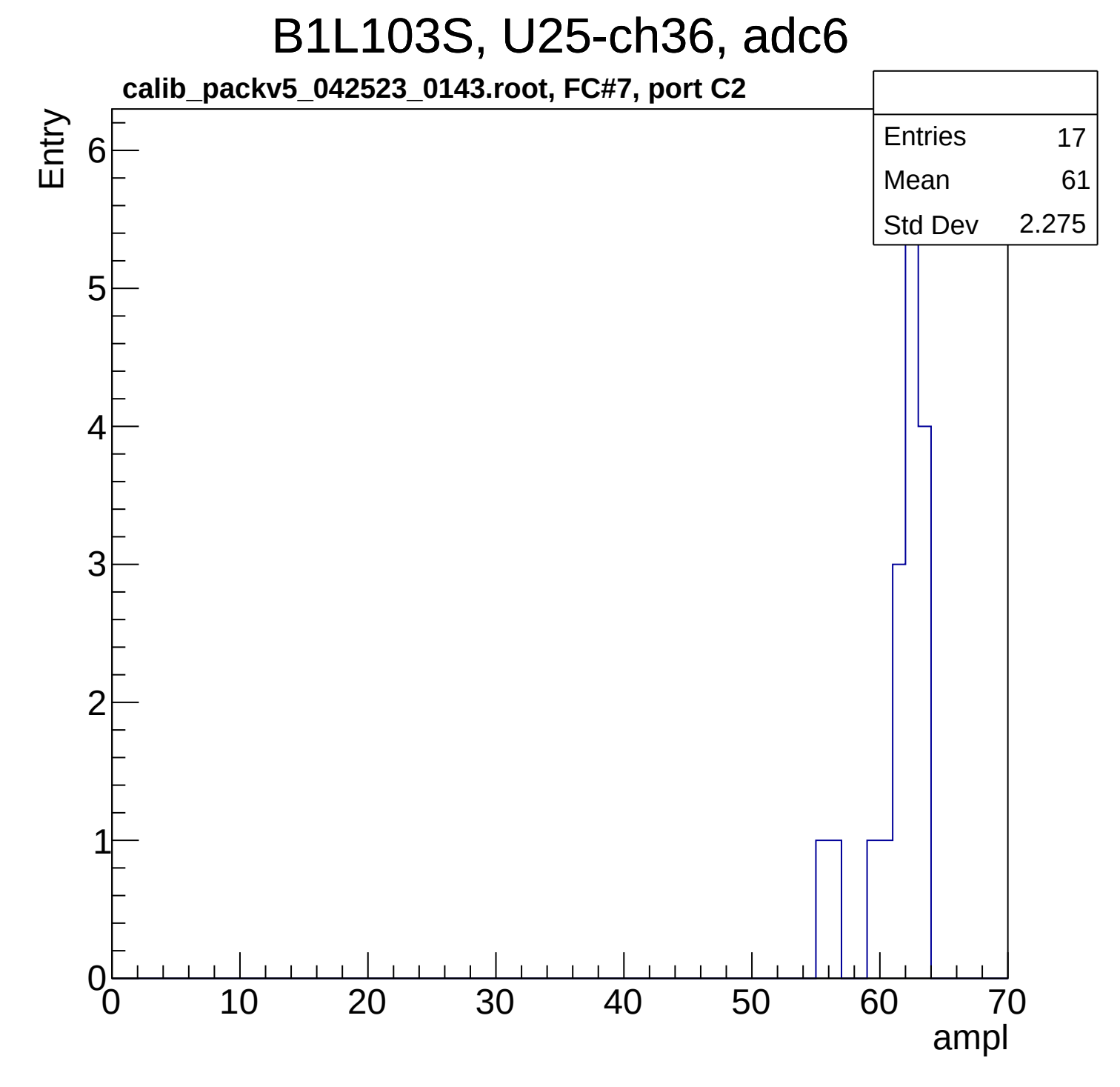
B1L103S, U25-ch36, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 61
Std Dev 2.275
Entry
ampl
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1
2
3
4
5
6
0
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20
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40
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60
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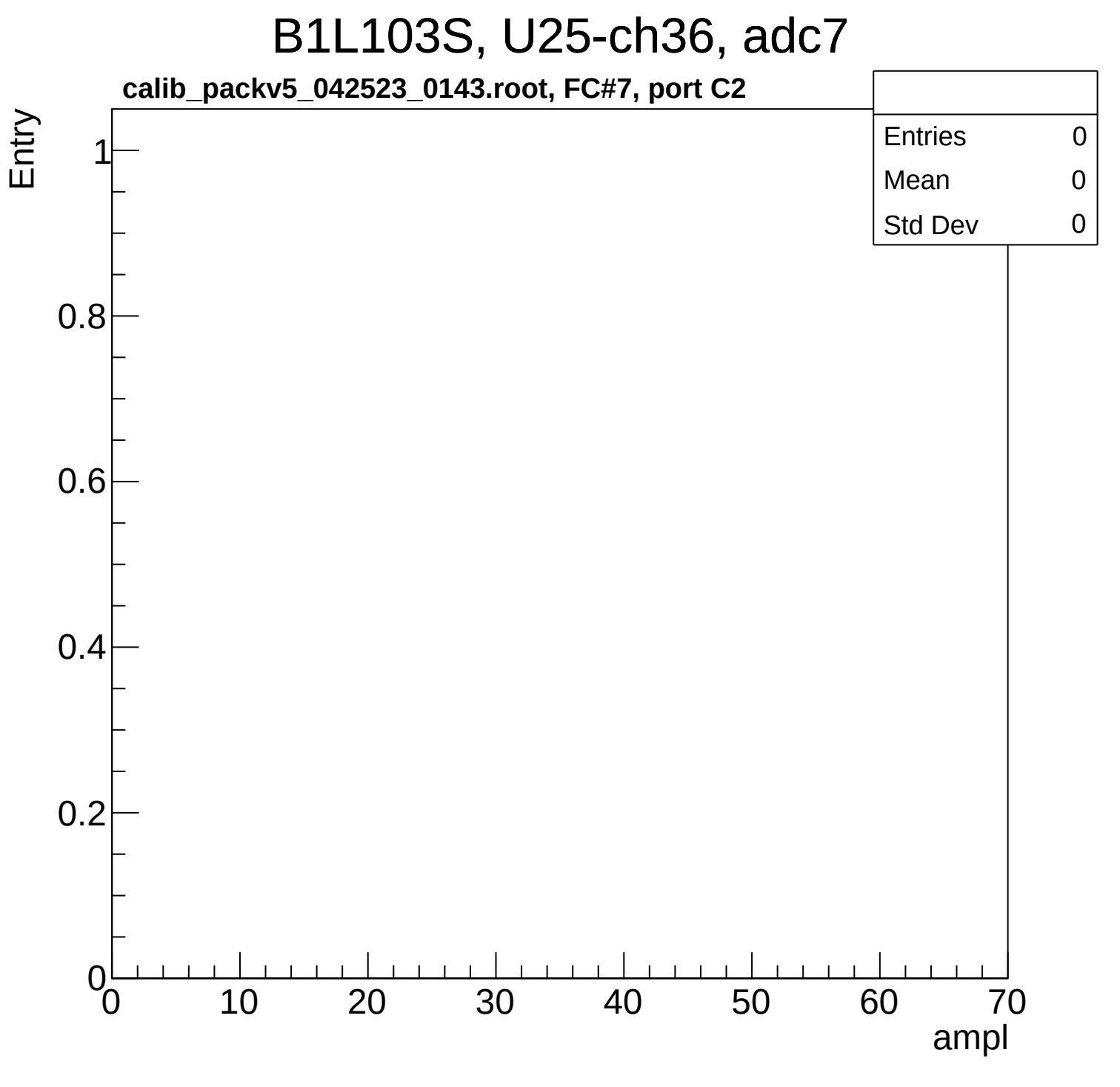
B1L103S, U25-ch36, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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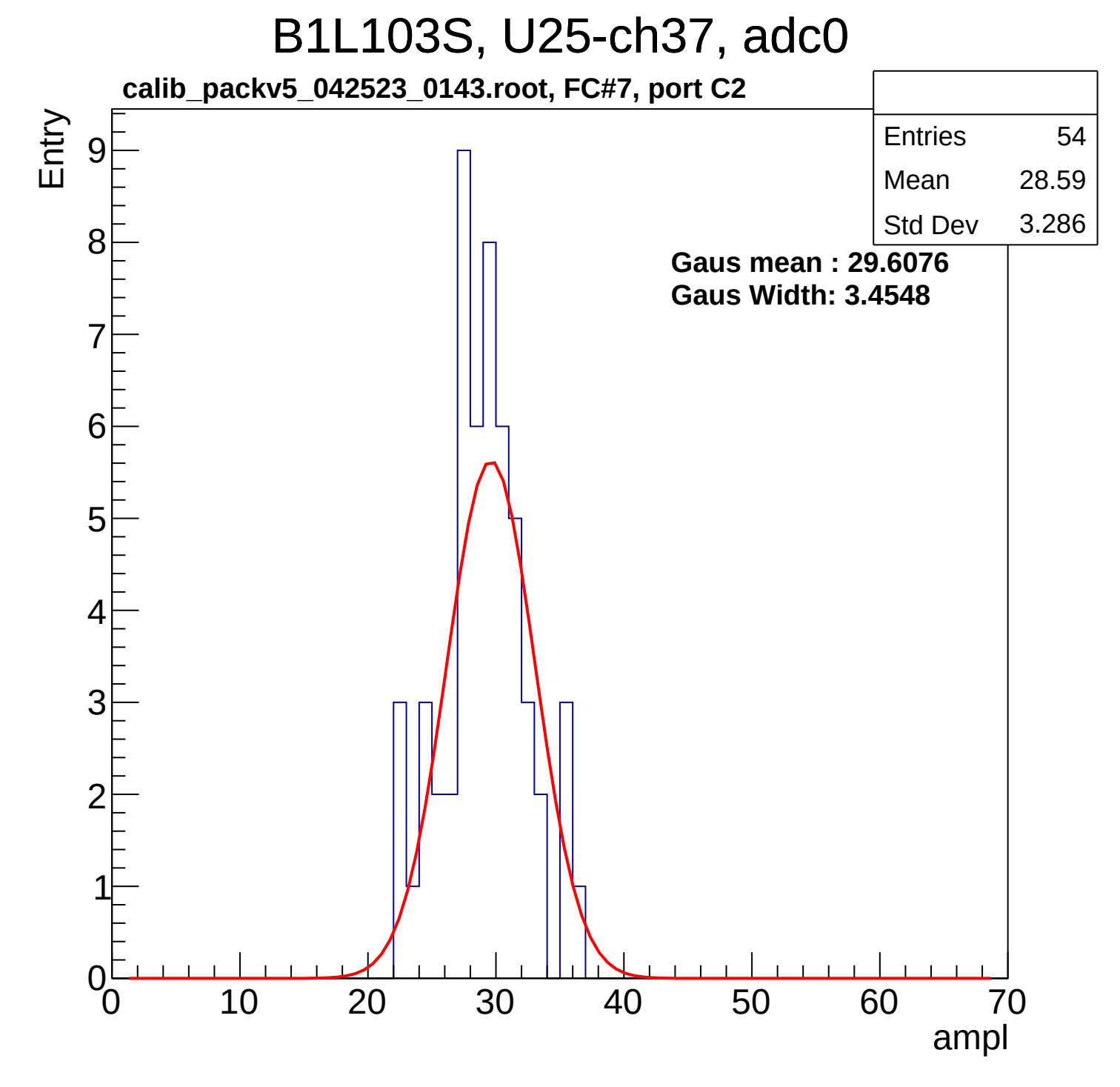
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch37, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 28.59
Std Dev 3.286
Gaus mean : 29.6076
Gaus Width: 3.4548
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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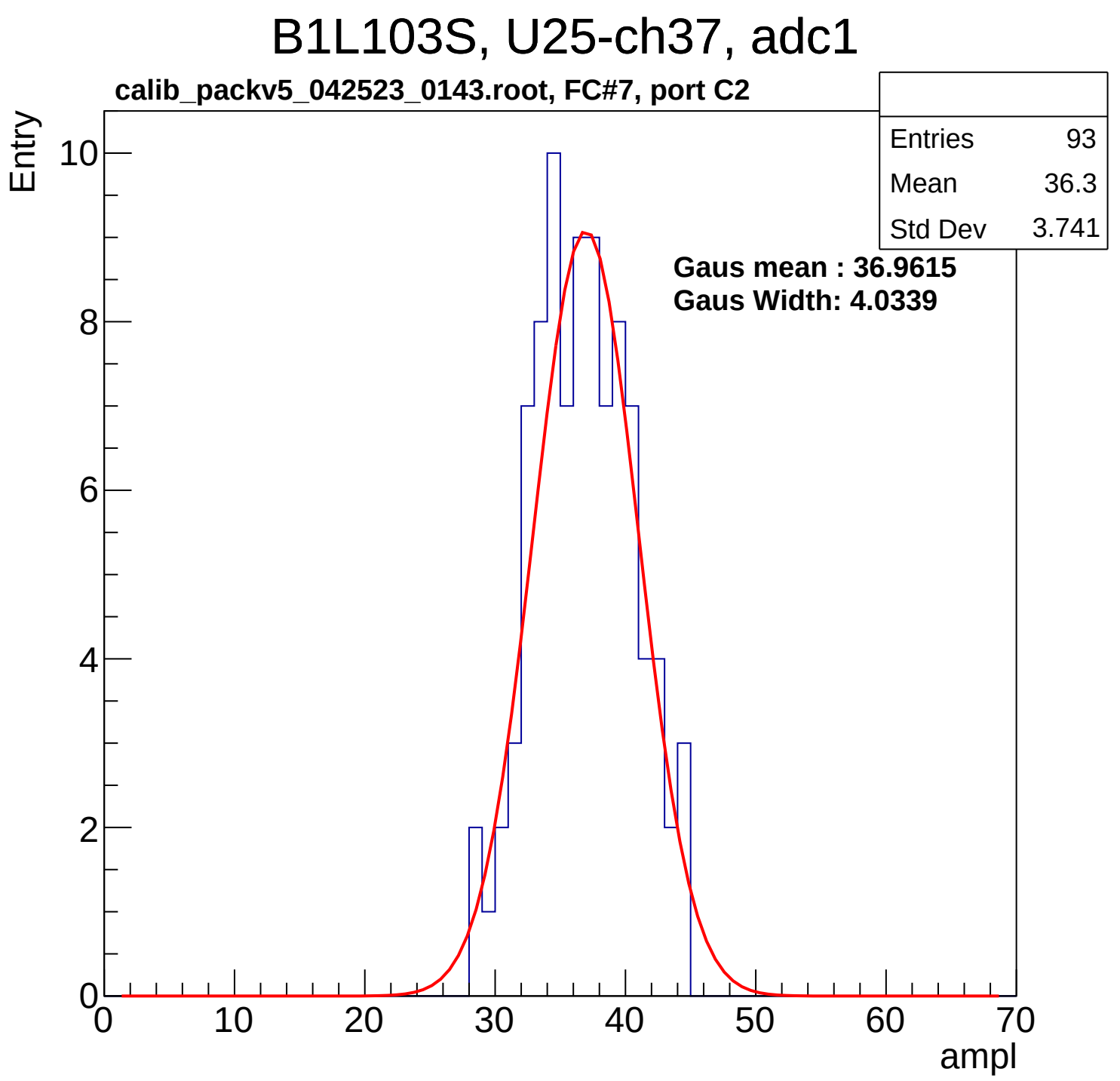
B1L103S, U25-ch37, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 93
Mean 36.3
Std Dev 3.741
Gaus mean : 36.9615
Gaus Width: 4.0339
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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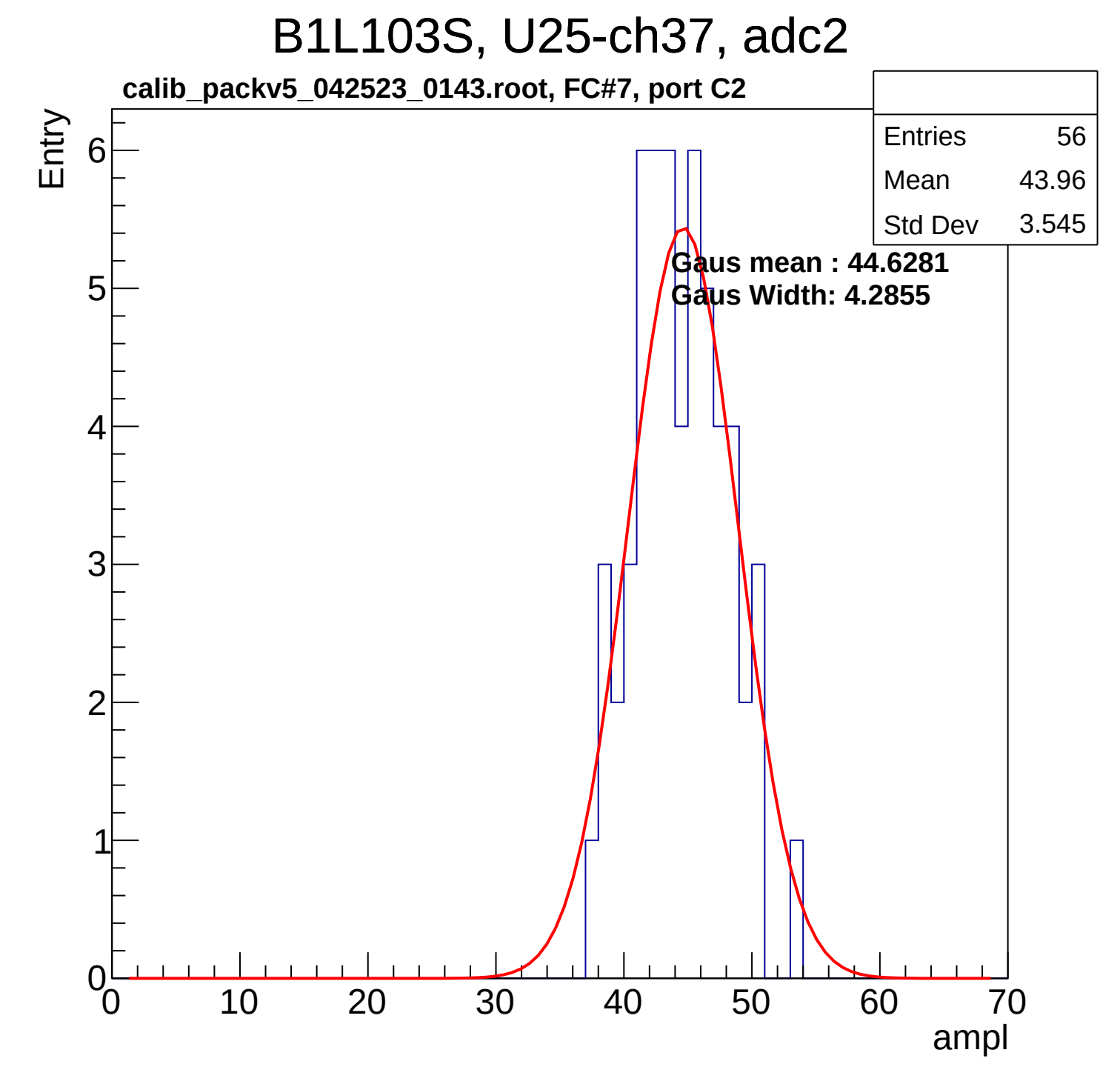
B1L103S, U25-ch37, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.96
Std Dev 3.545
Gaus mean : 44.6281
Gaus Width: 4.2855
Entry
ampl
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1
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3
4
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0
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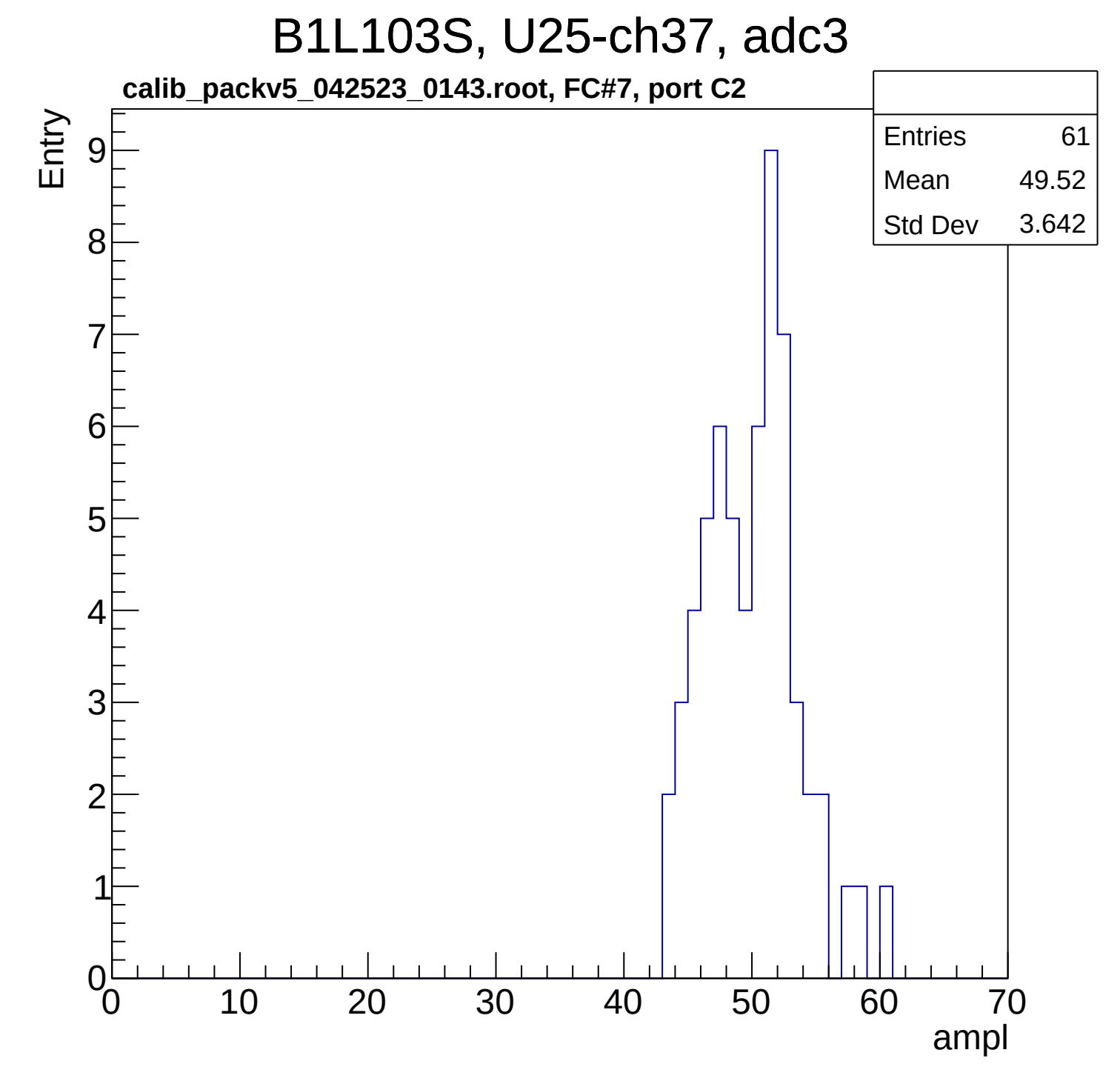
B1L103S, U25-ch37, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 49.52
Std Dev 3.642
Entry
ampl
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0
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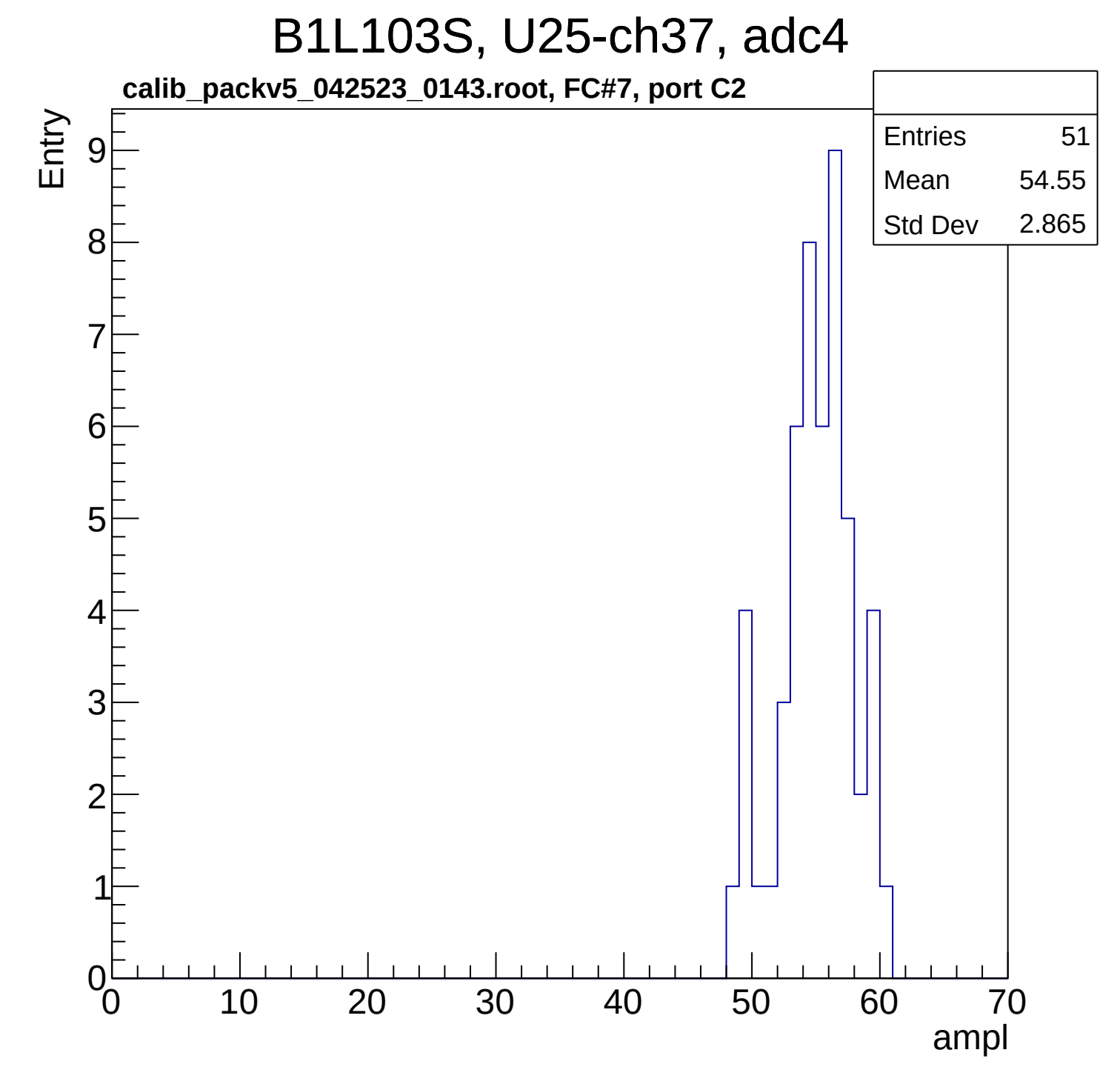
B1L103S, U25-ch37, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.55
Std Dev 2.865
Entry
ampl
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3
4
5
6
7
8
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0
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60
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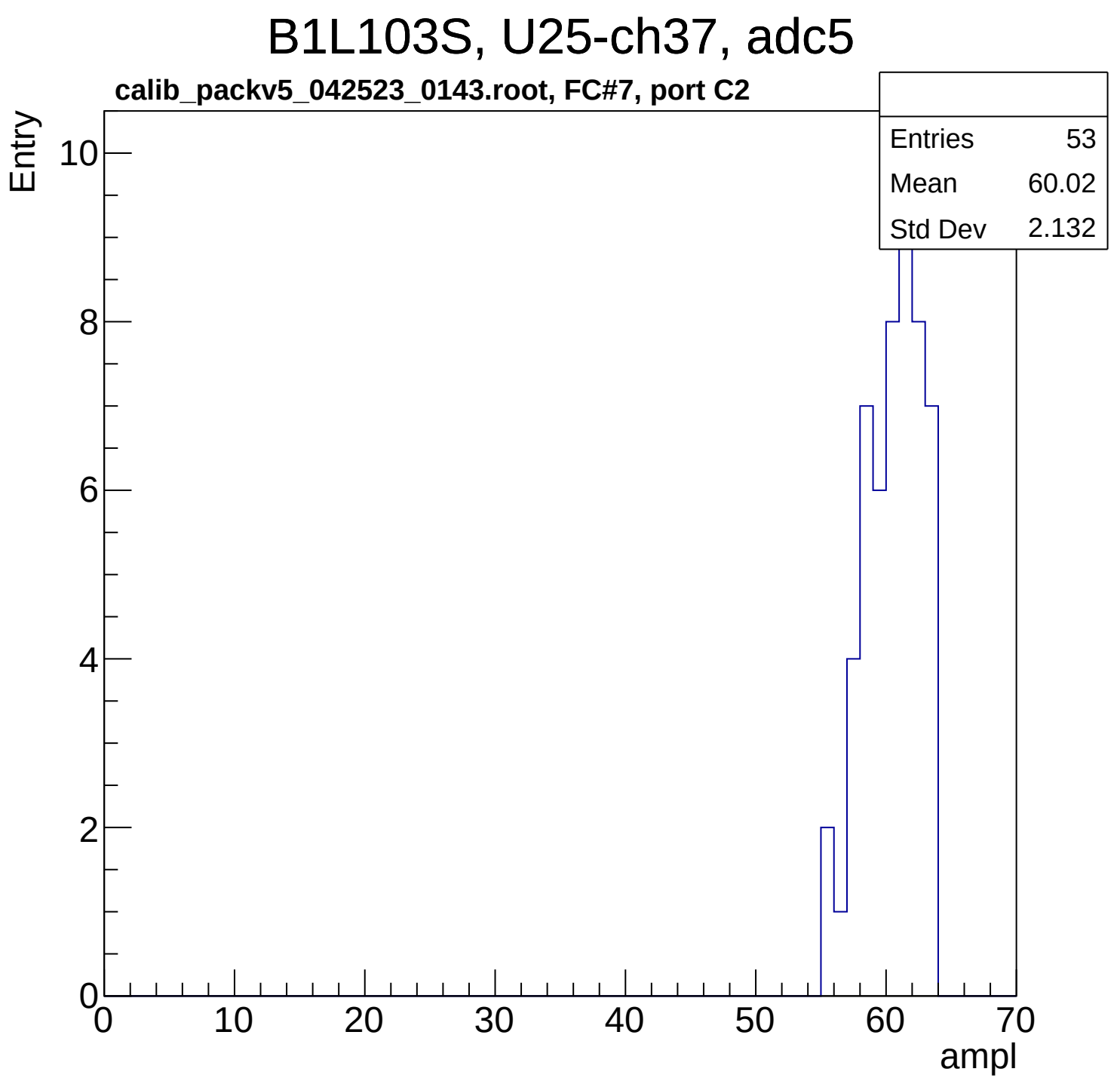
B1L103S, U25-ch37, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 60.02
Std Dev 2.132
Entry
ampl
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0
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60
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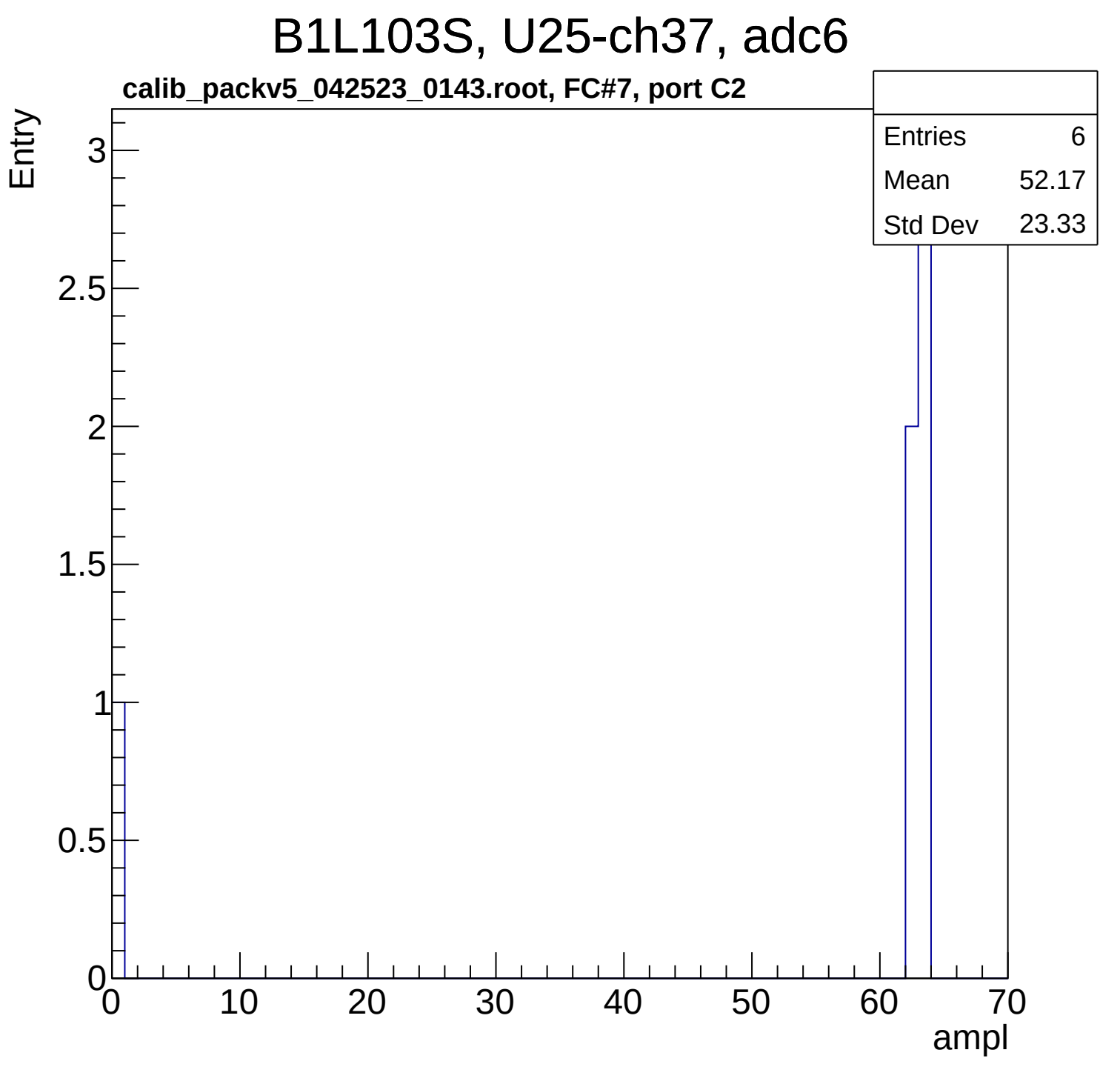
B1L103S, U25-ch37, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
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1.5
2
2.5
3
0
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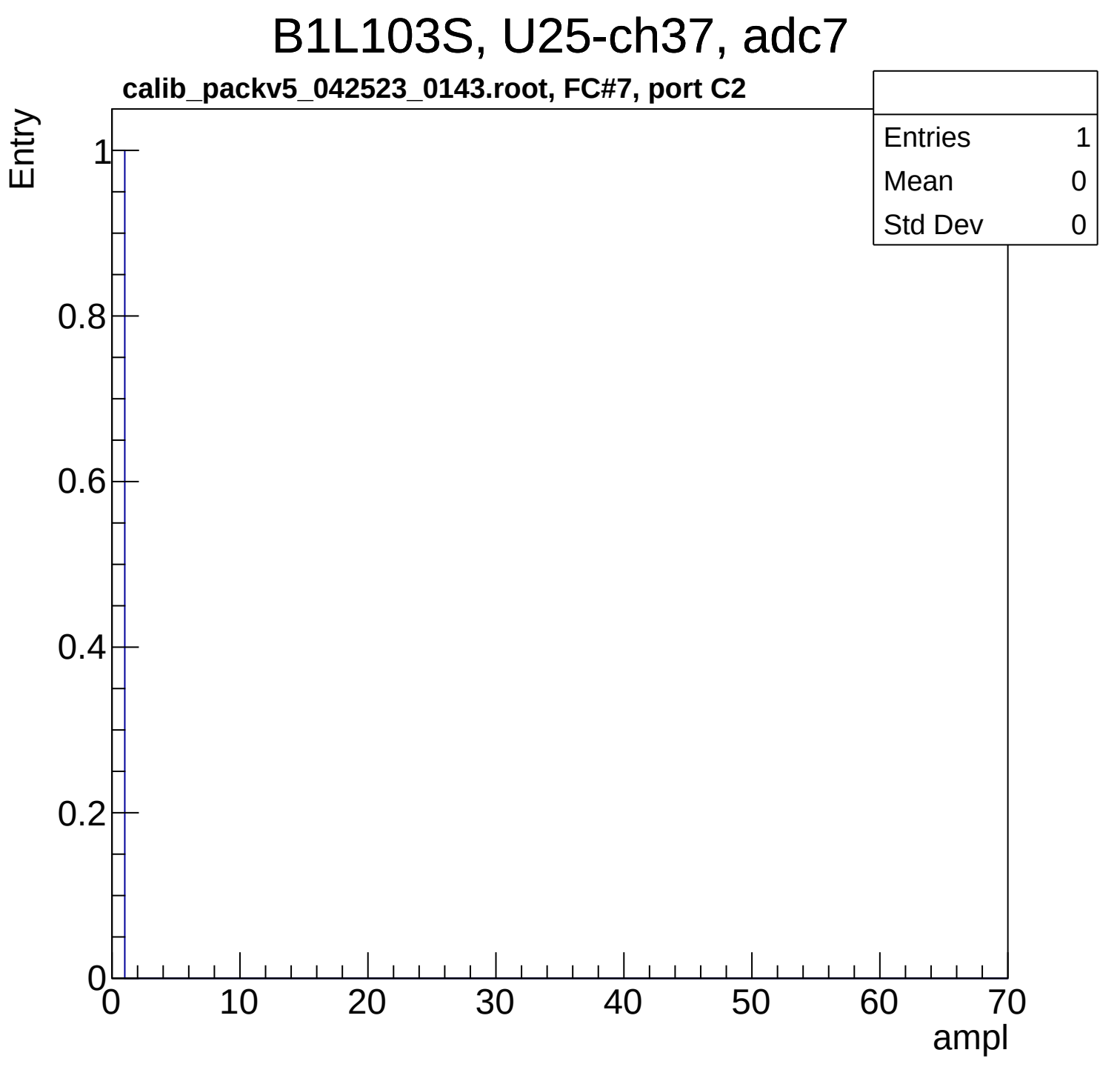
20
30
40
50
60
70


B1L103S, U25-ch37, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
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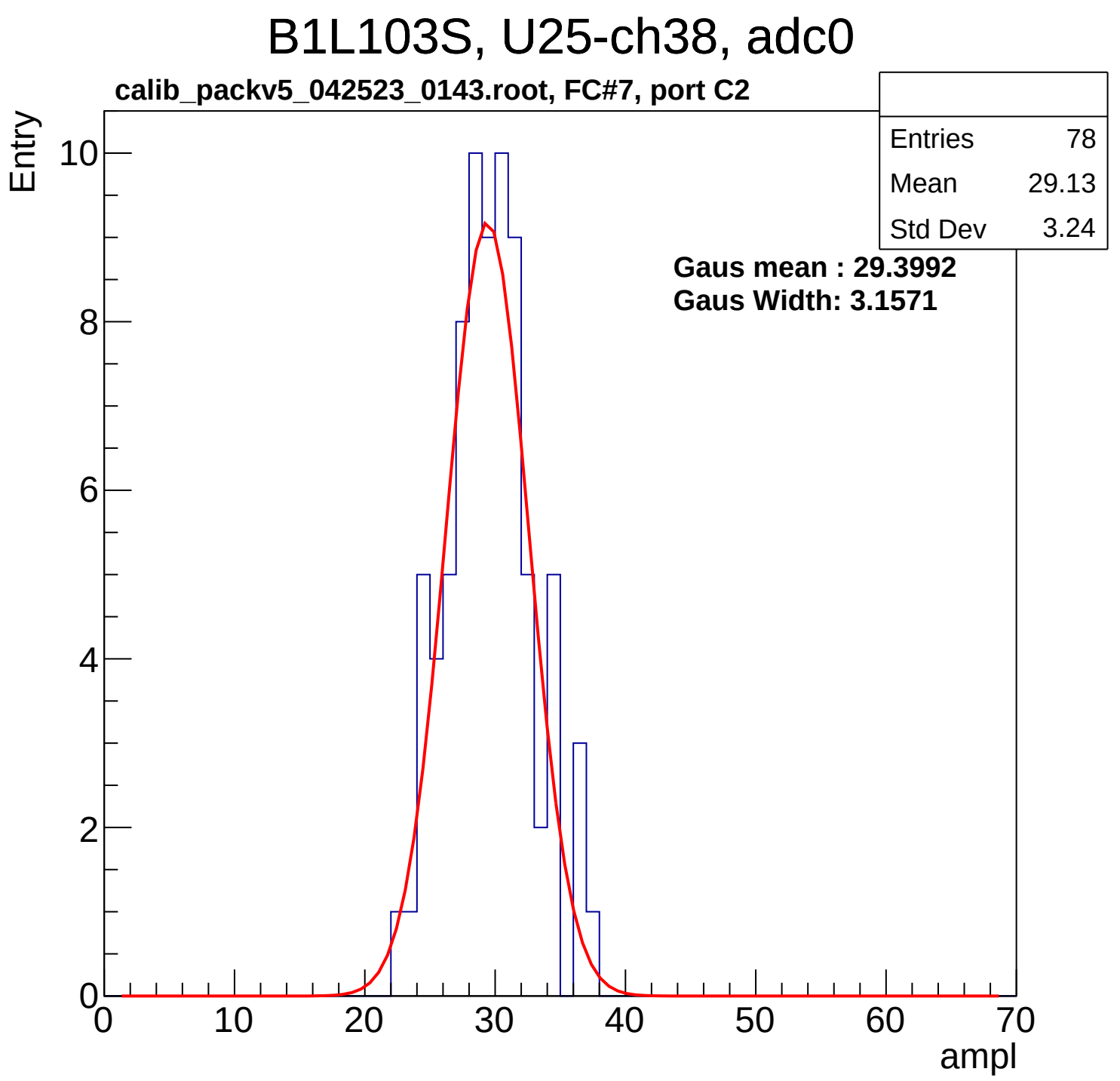
60
70
ampl


B1L103S, U25-ch38, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 29.13
Std Dev 3.24
Gaus mean : 29.3992
Gaus Width: 3.1571
Entry
ampl
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6
8
10
0
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20
30
40
50
60
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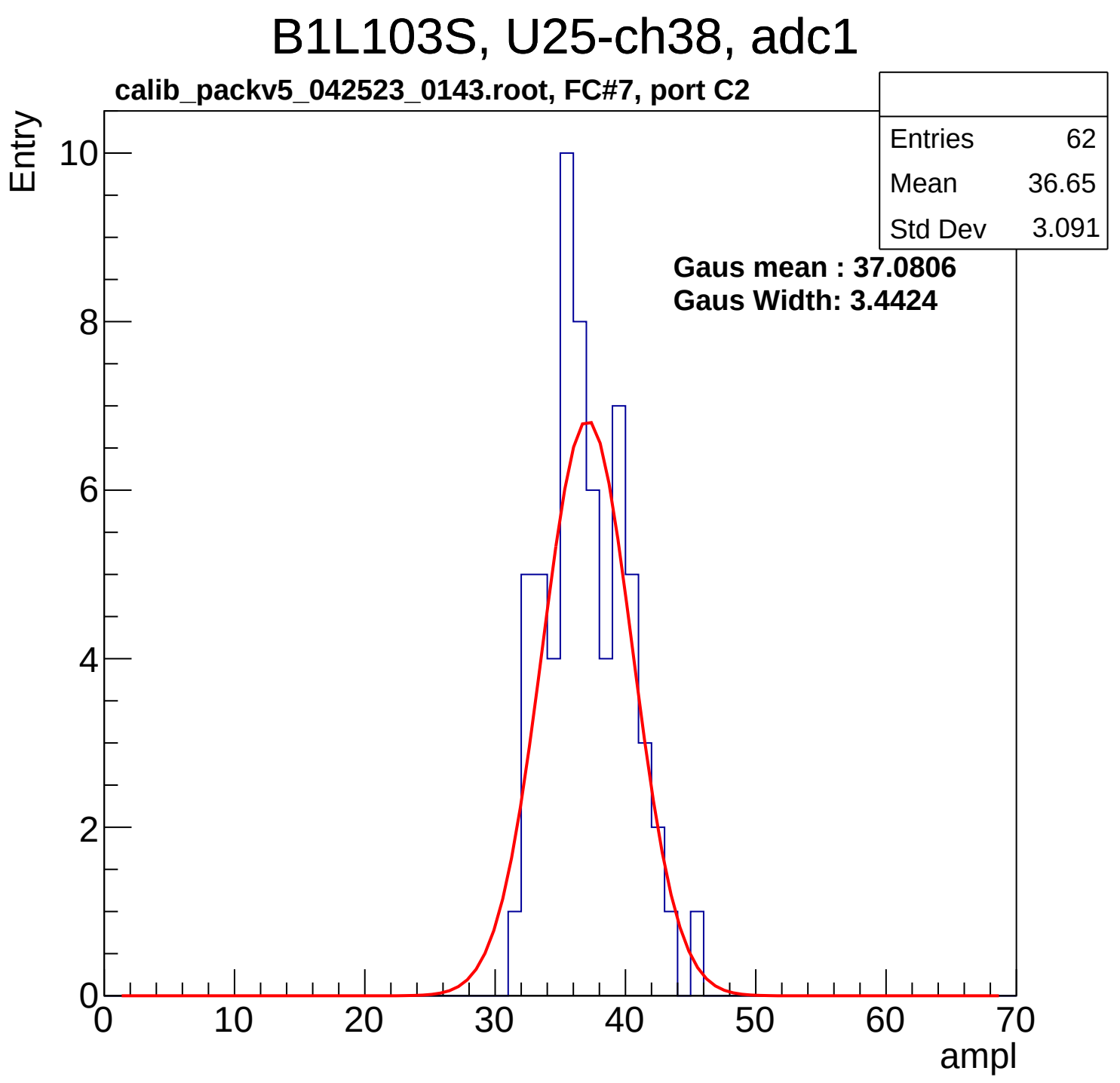
B1L103S, U25-ch38, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 36.65
Std Dev 3.091
Gaus mean : 37.0806
Gaus Width: 3.4424
Entry
ampl
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4
6
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0
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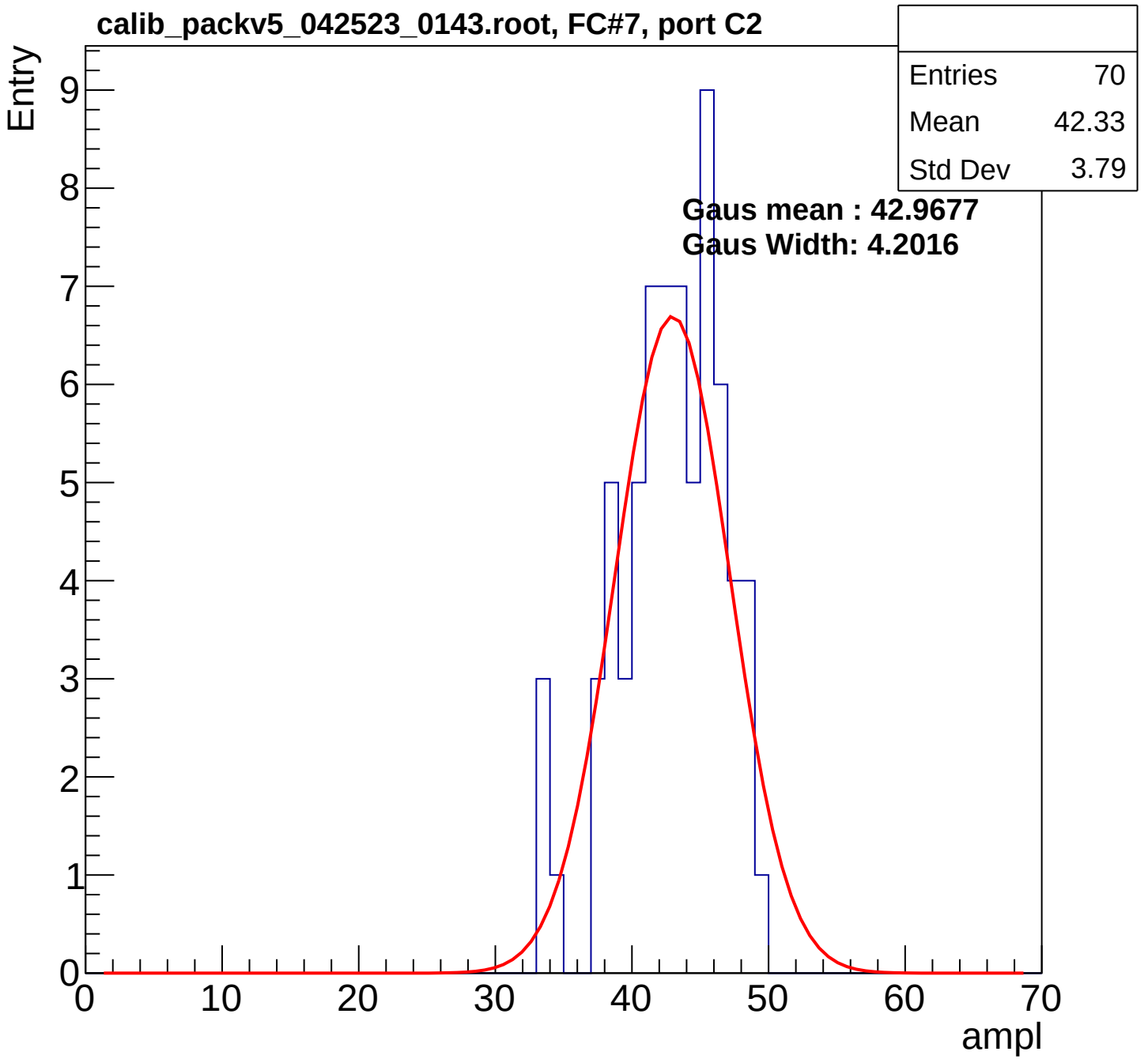
B1L103S, U25-ch38, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.33
Std Dev 3.79
Gaus mean : 42.9677
Gaus Width: 4.2016
Entry
ampl
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3
4
5
6
7
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0
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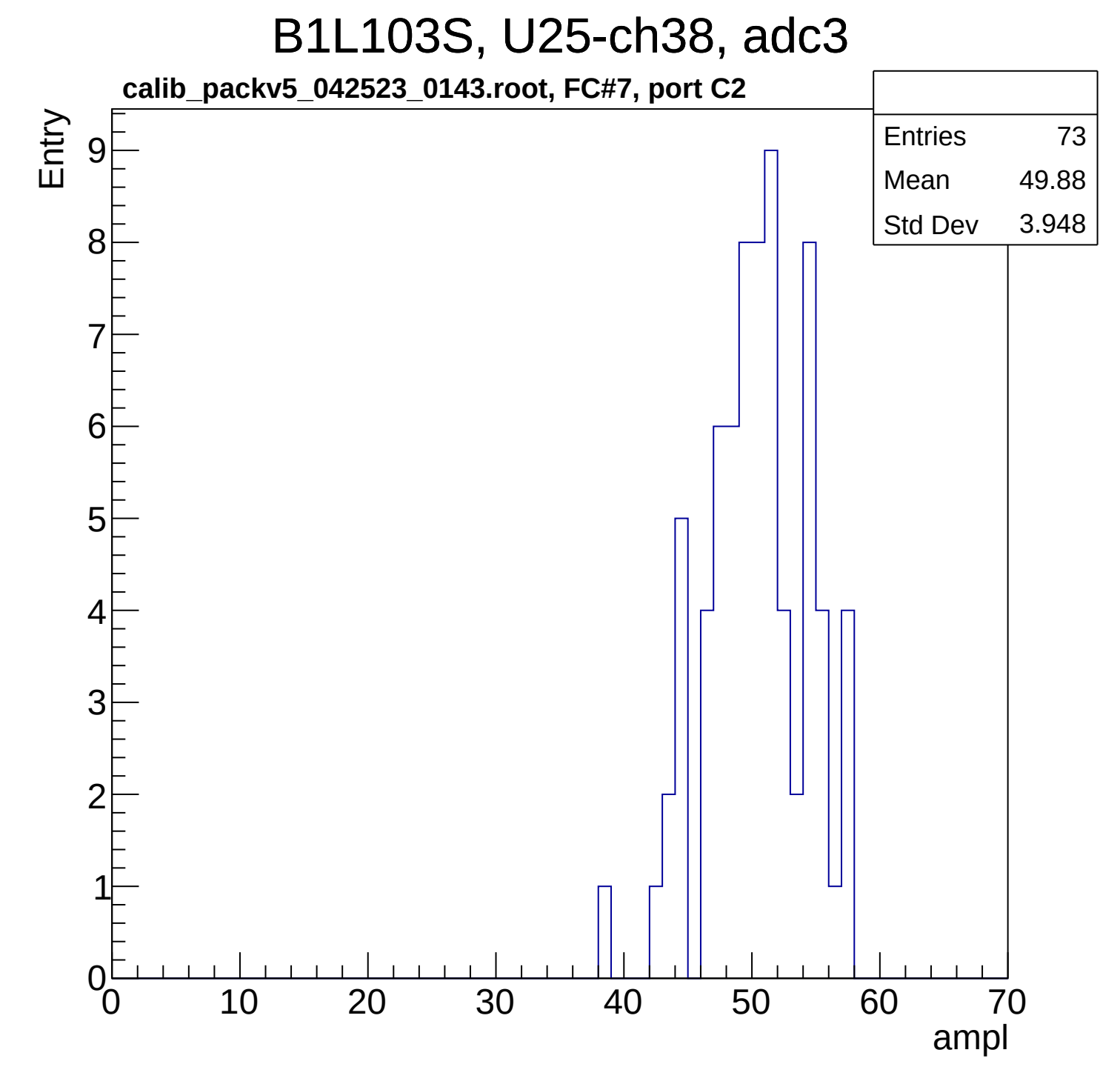
B1L103S, U25-ch38, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.88
Std Dev 3.948
Entry
ampl
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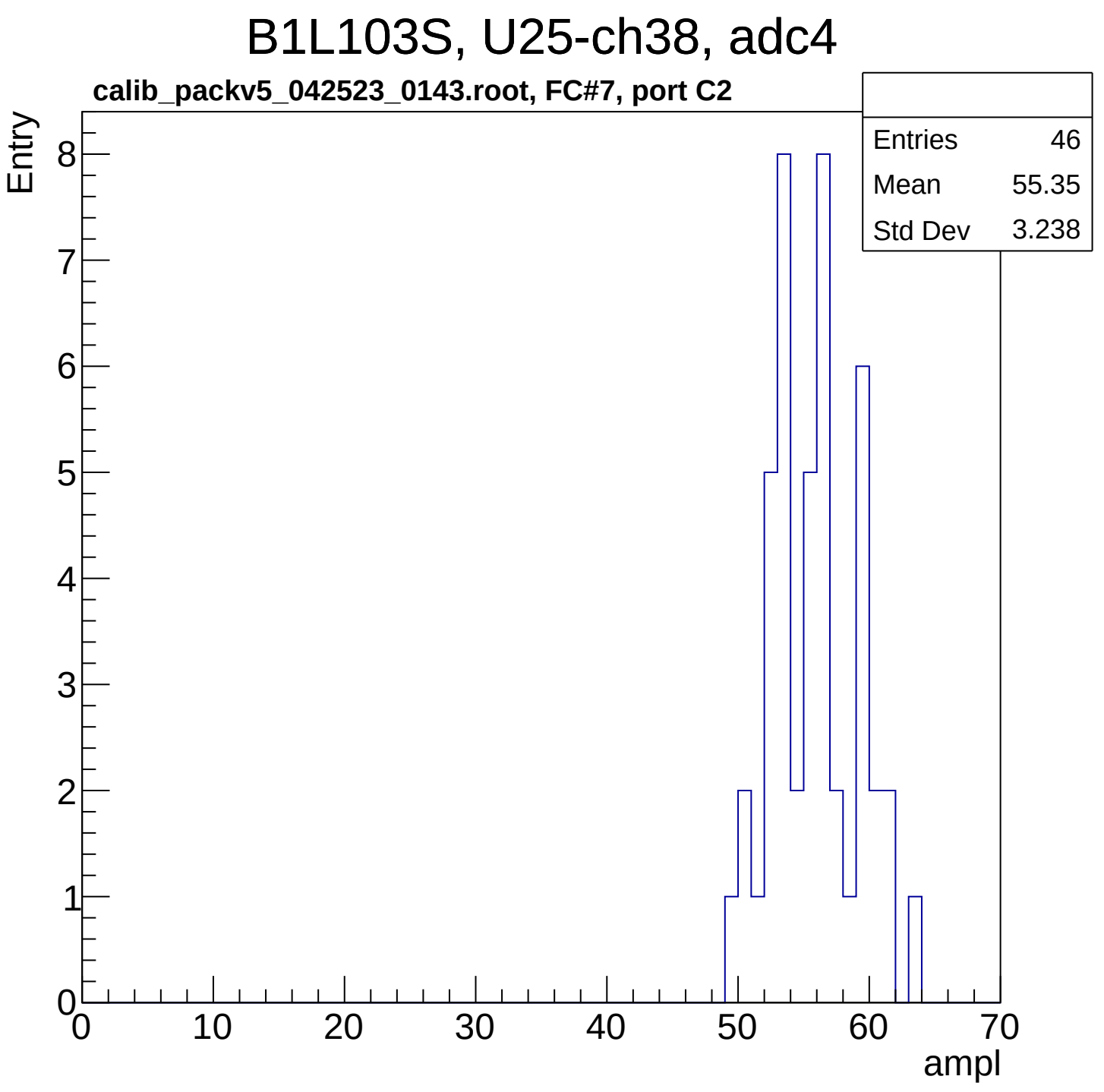
B1L103S, U25-ch38, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 55.35
Std Dev 3.238
Entry
ampl
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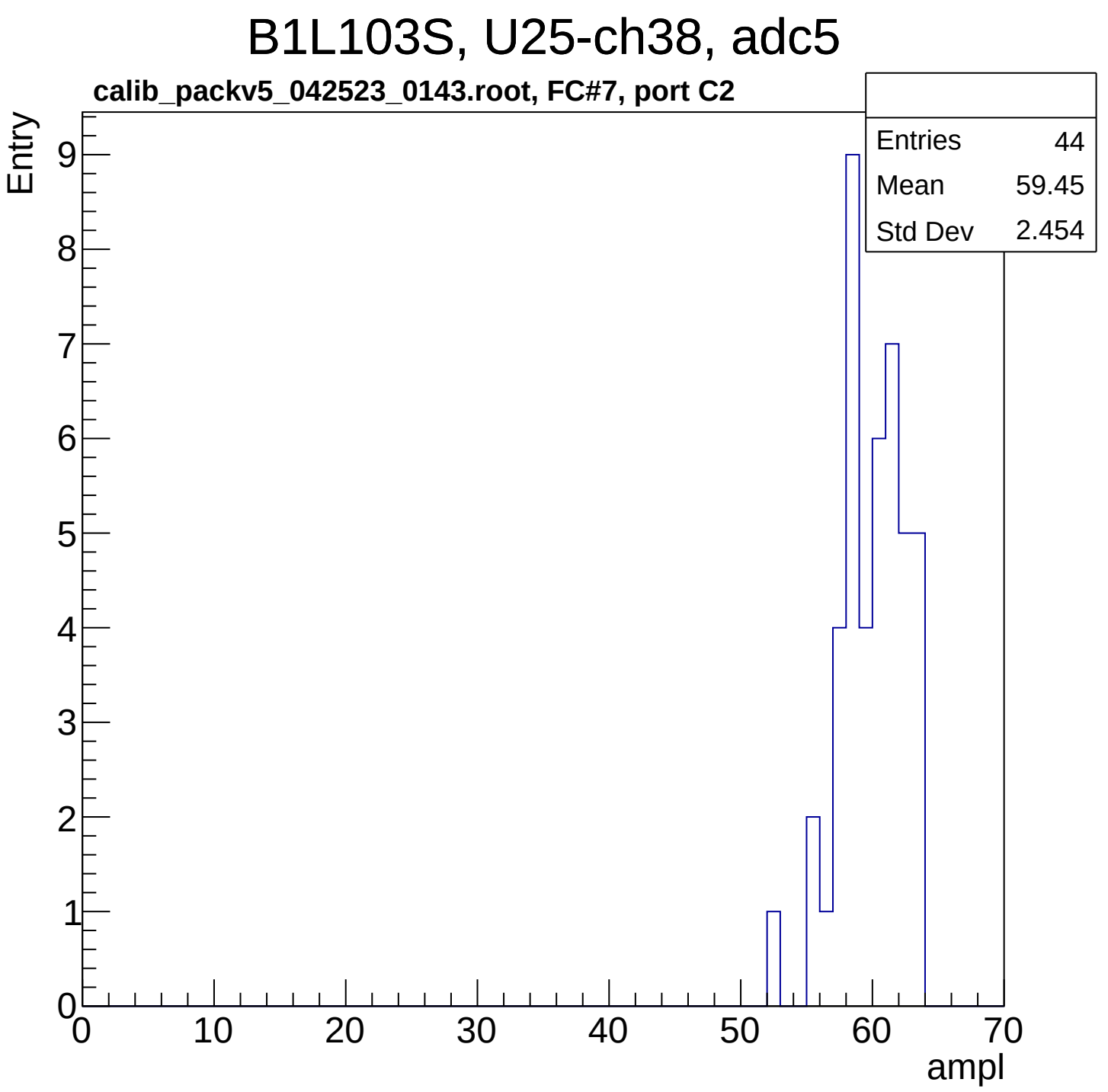
B1L103S, U25-ch38, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 59.45
Std Dev 2.454
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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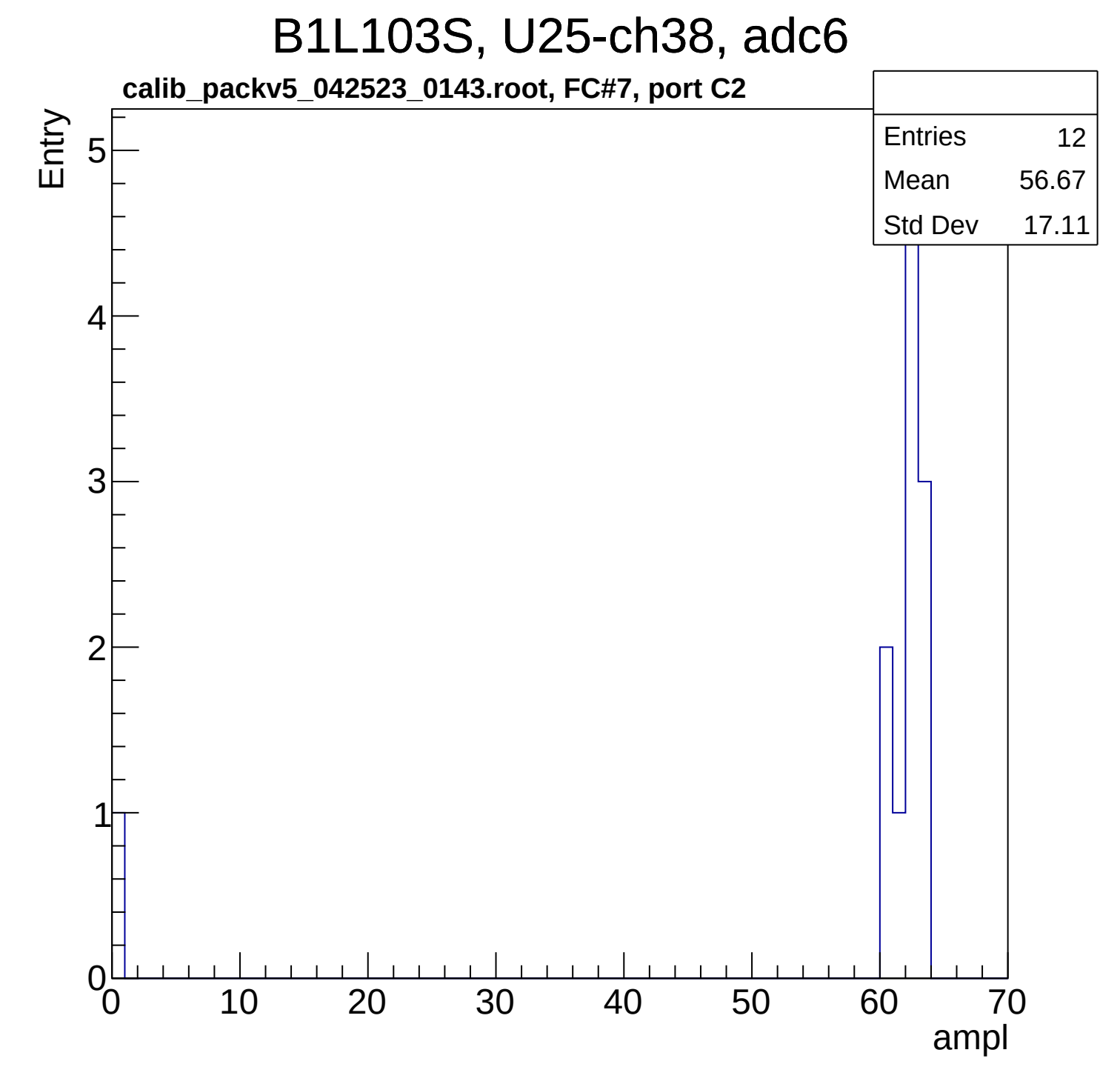
B1L103S, U25-ch38, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.67
Std Dev 17.11
Entry
0
1
2
3
4
5
0
10
20
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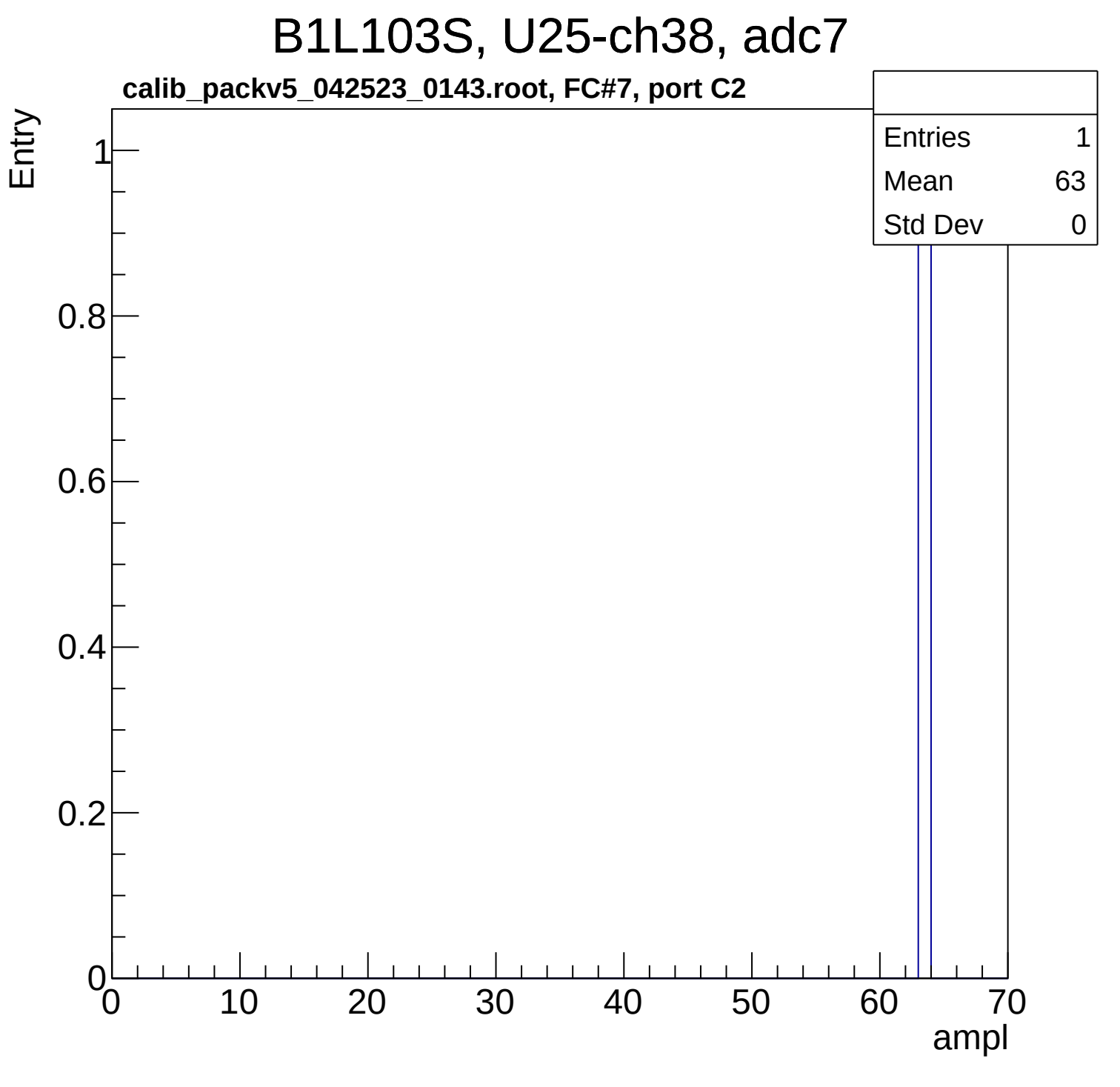
50
60
70
ampl


B1L103S, U25-ch38, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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0
0
10
20
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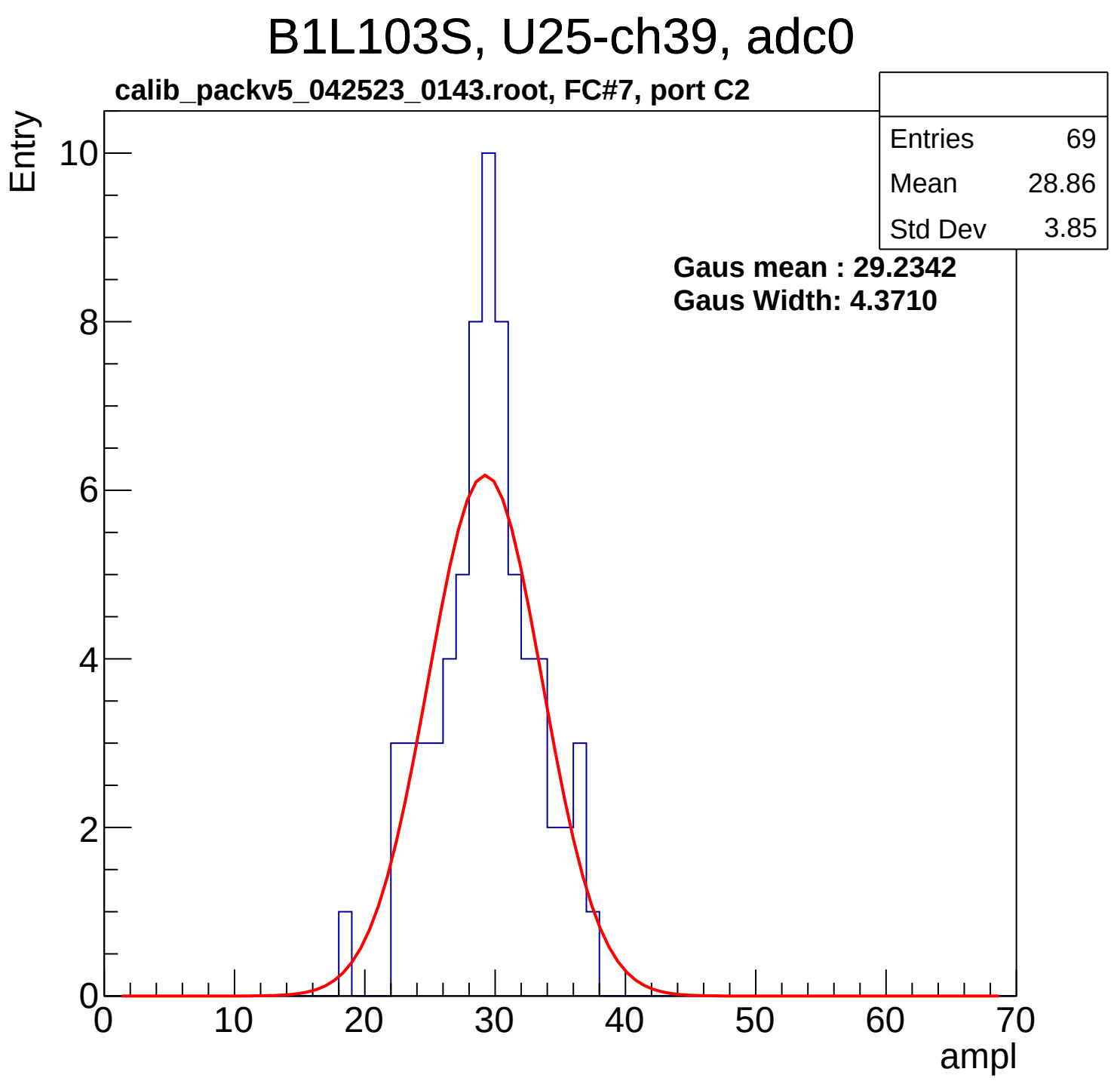
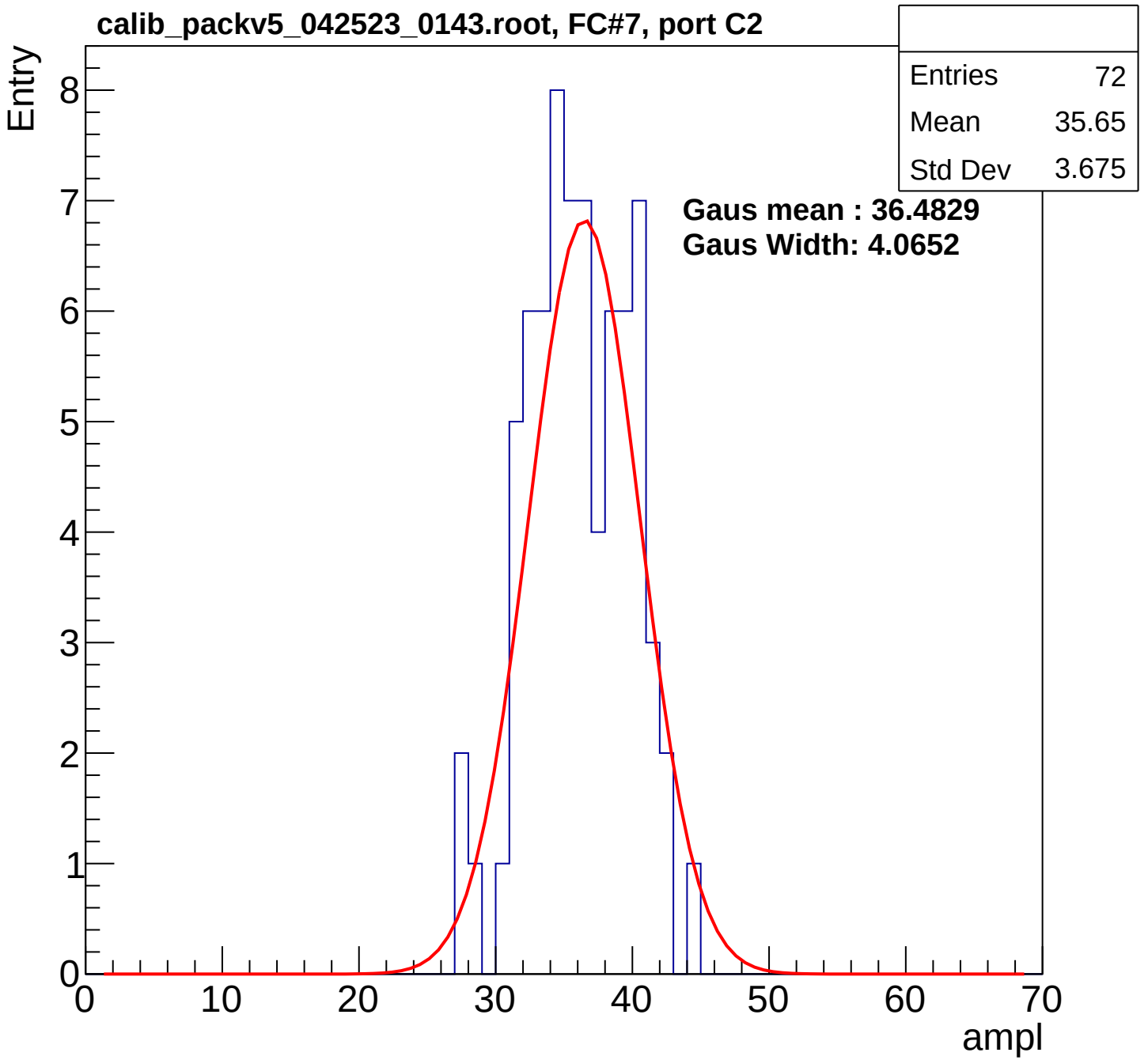
40
50
60
70
ampl


B1L103S, U25-ch39, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 28.86
Std Dev 3.85
Gaus mean : 29.2342
Gaus Width: 4.3710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch39, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.65
Std Dev 3.675
Gaus mean : 36.4829
Gaus Width: 4.0652
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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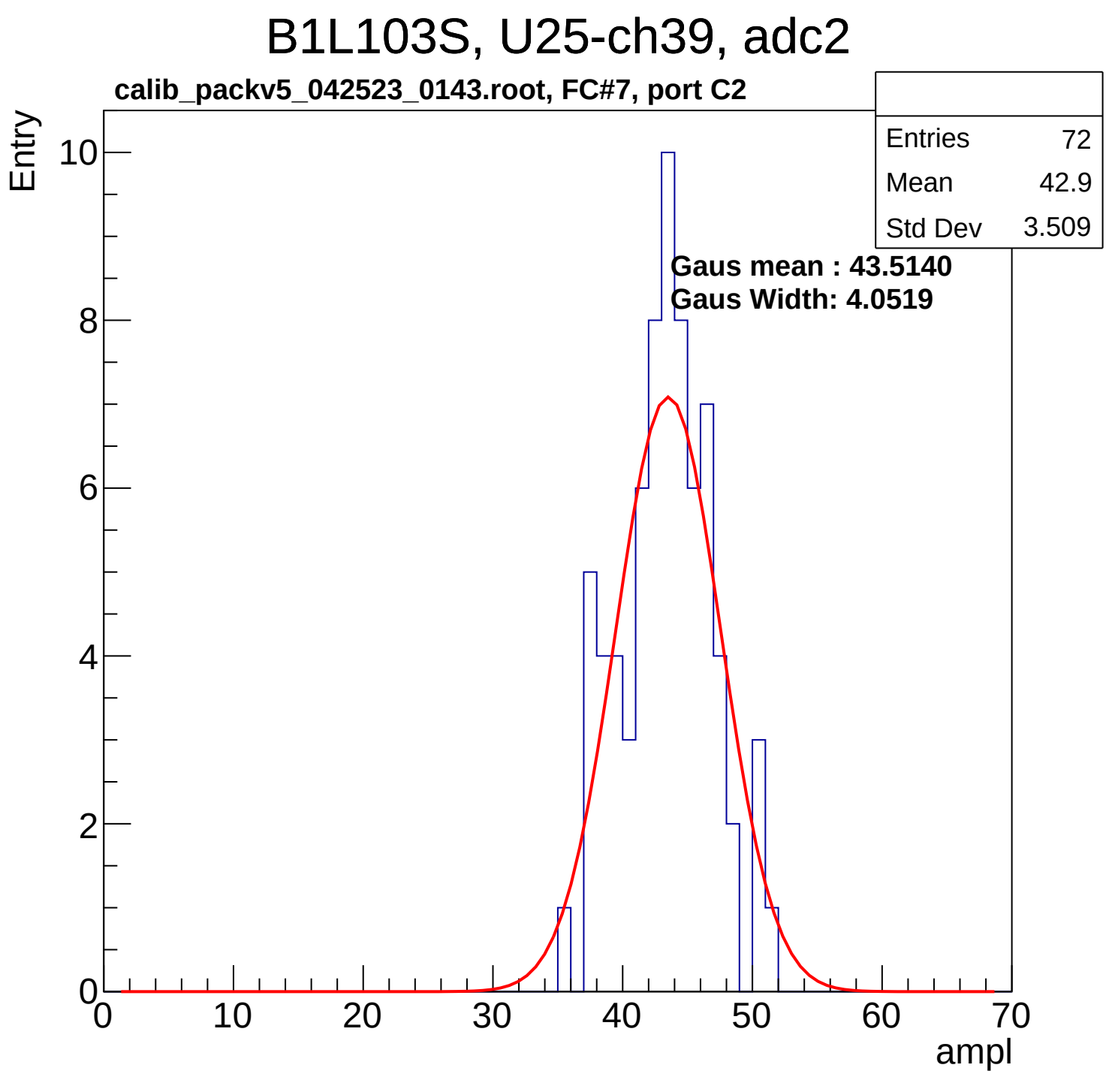
B1L103S, U25-ch39, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 42.9
Std Dev 3.509
Gaus mean : 43.5140
Gaus Width: 4.0519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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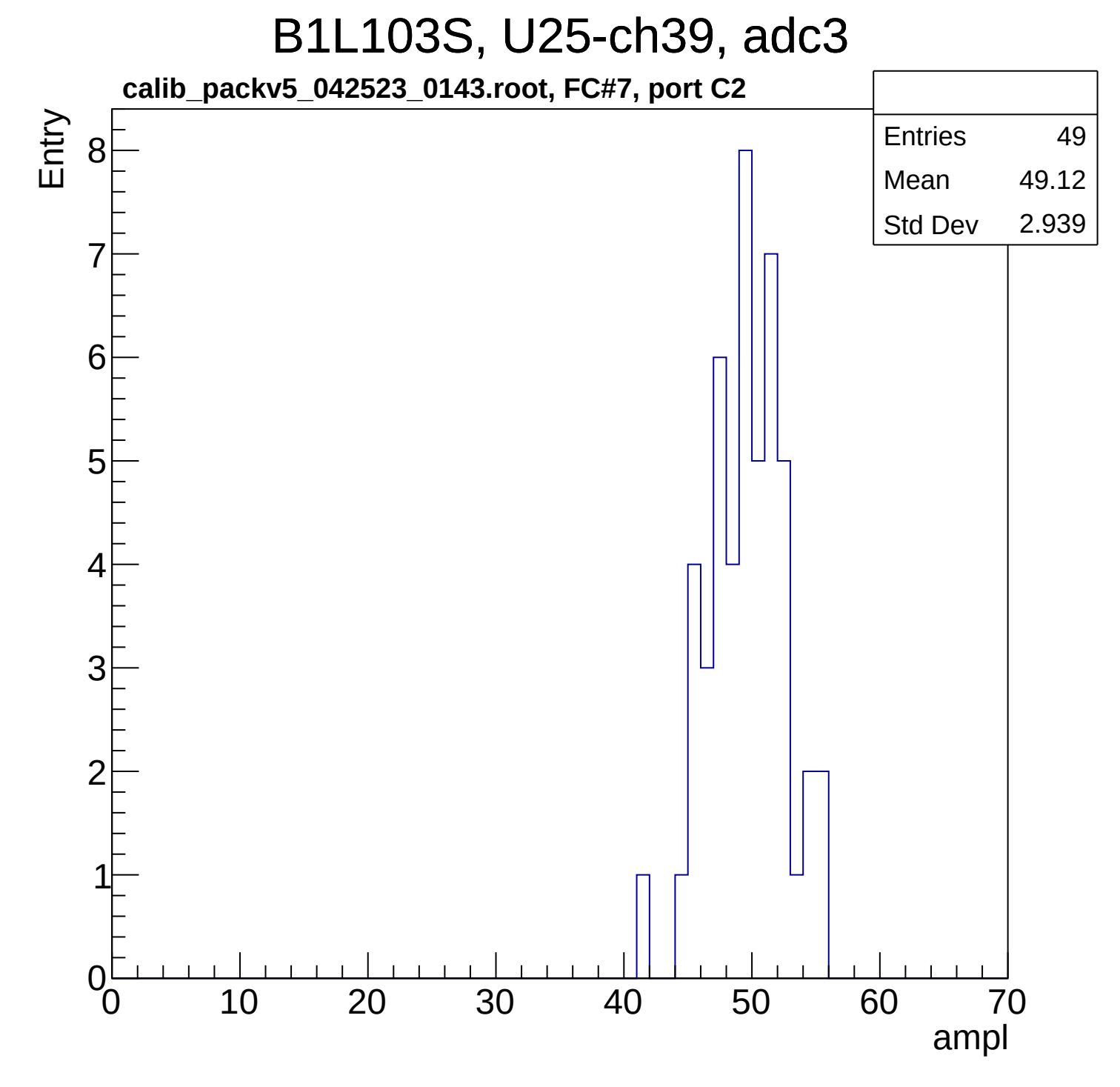
B1L103S, U25-ch39, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 49.12
Std Dev 2.939
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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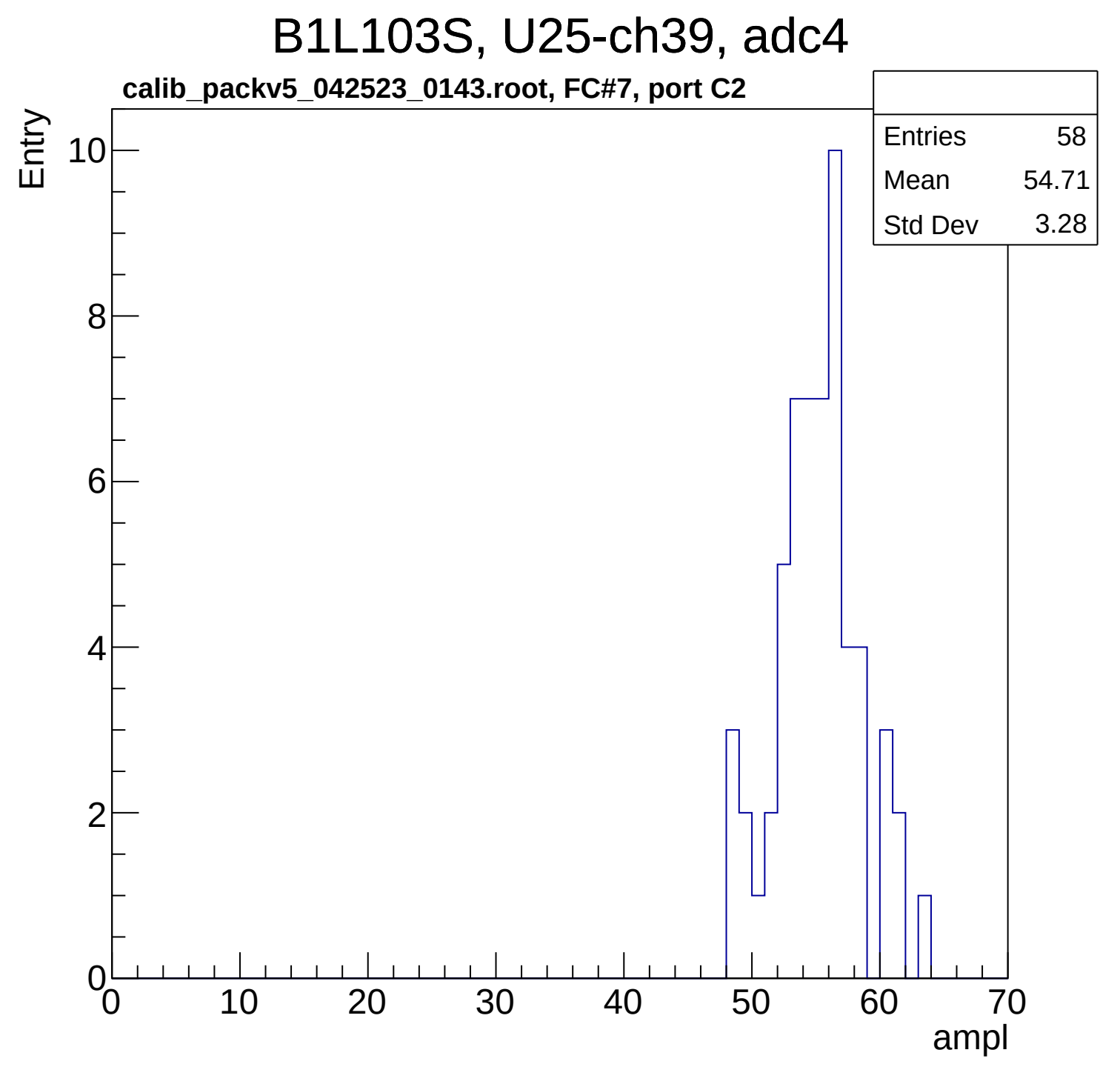
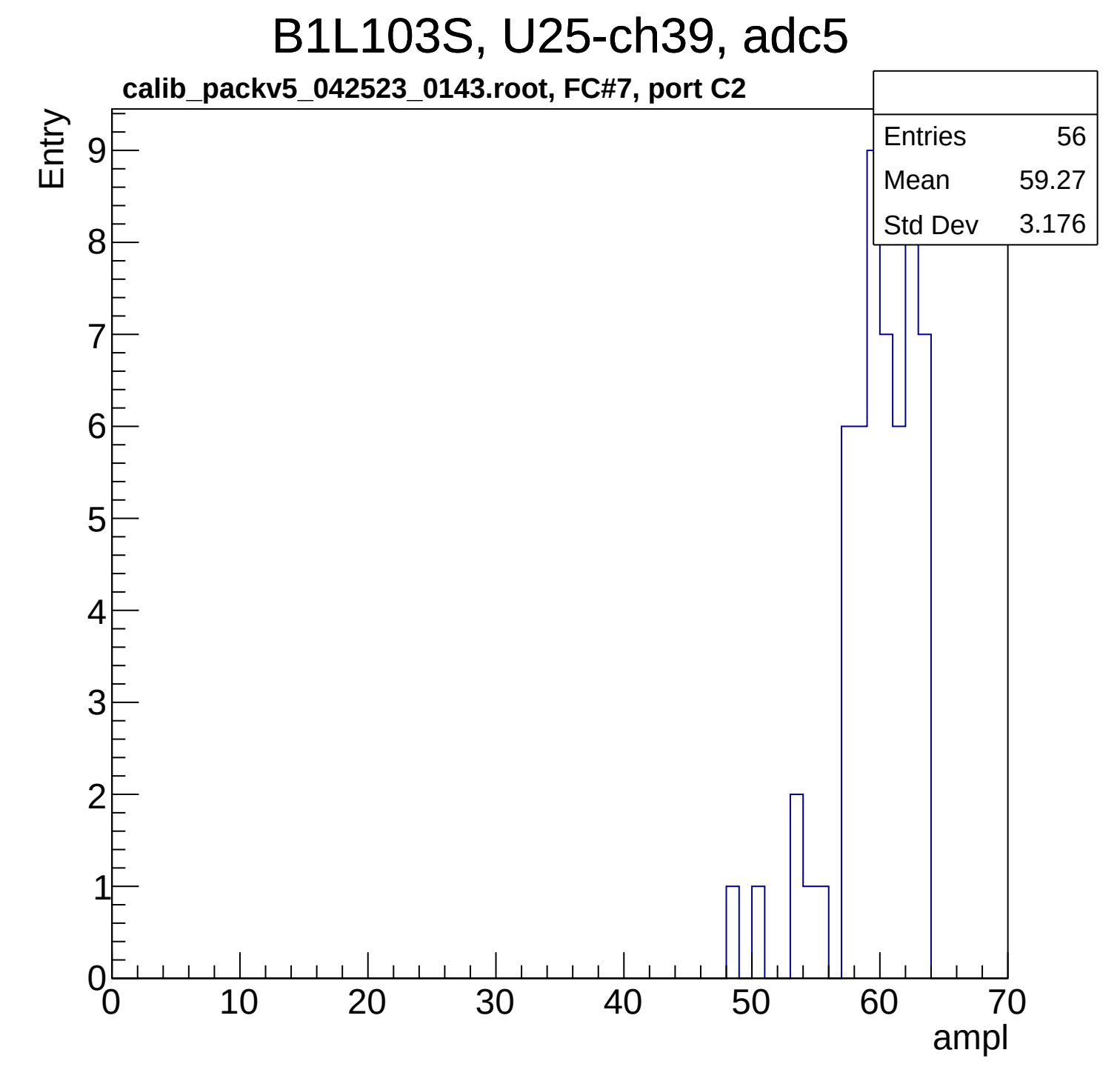

B1L103S, U25-ch39, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 54.71
Std Dev 3.28
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch39, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 59.27
Std Dev 3.176
Entry
ampl
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3
4
5
6
7
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0
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60
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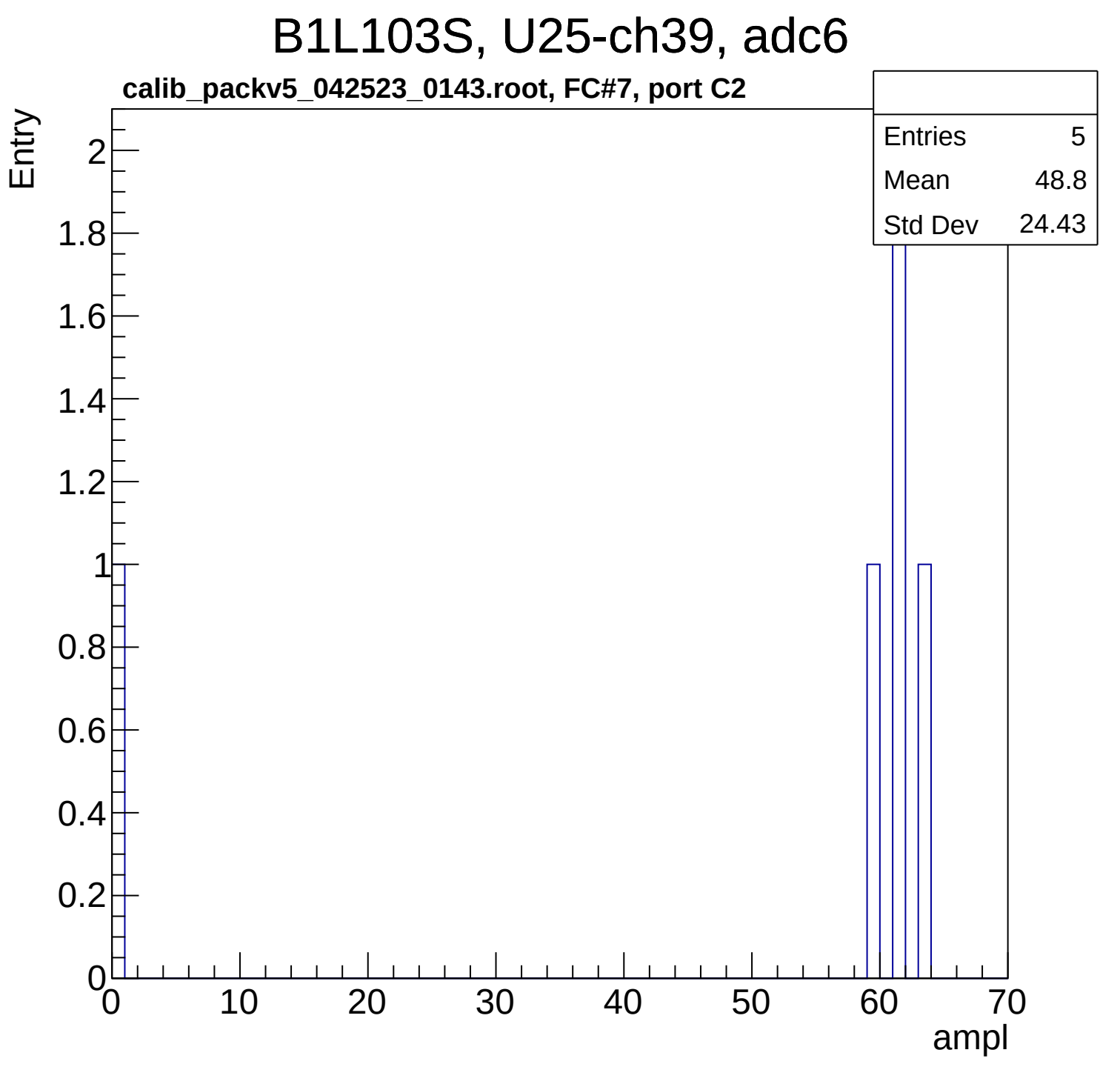
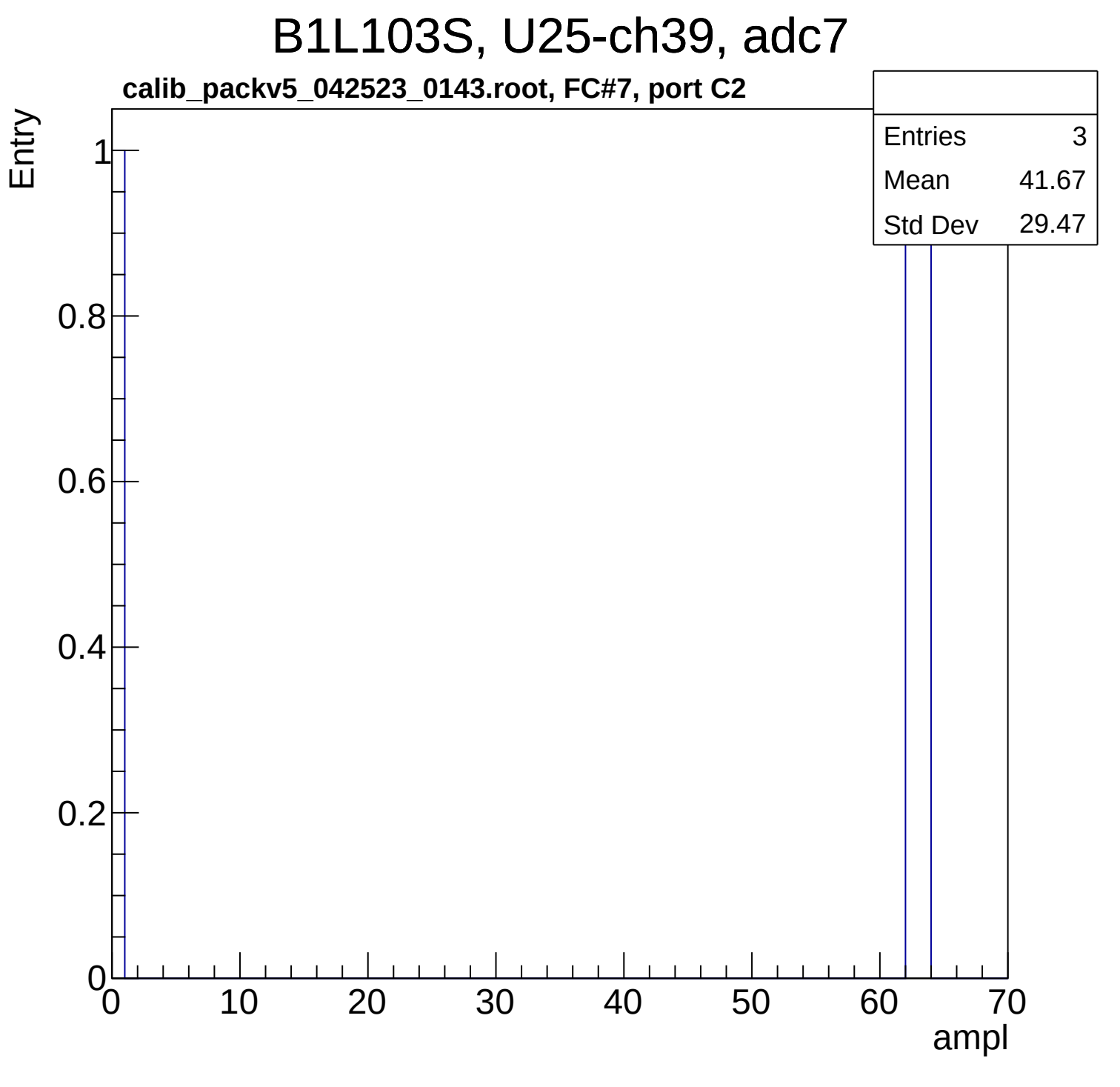

B1L103S, U25-ch39, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 48.8
Std Dev 24.43
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch39, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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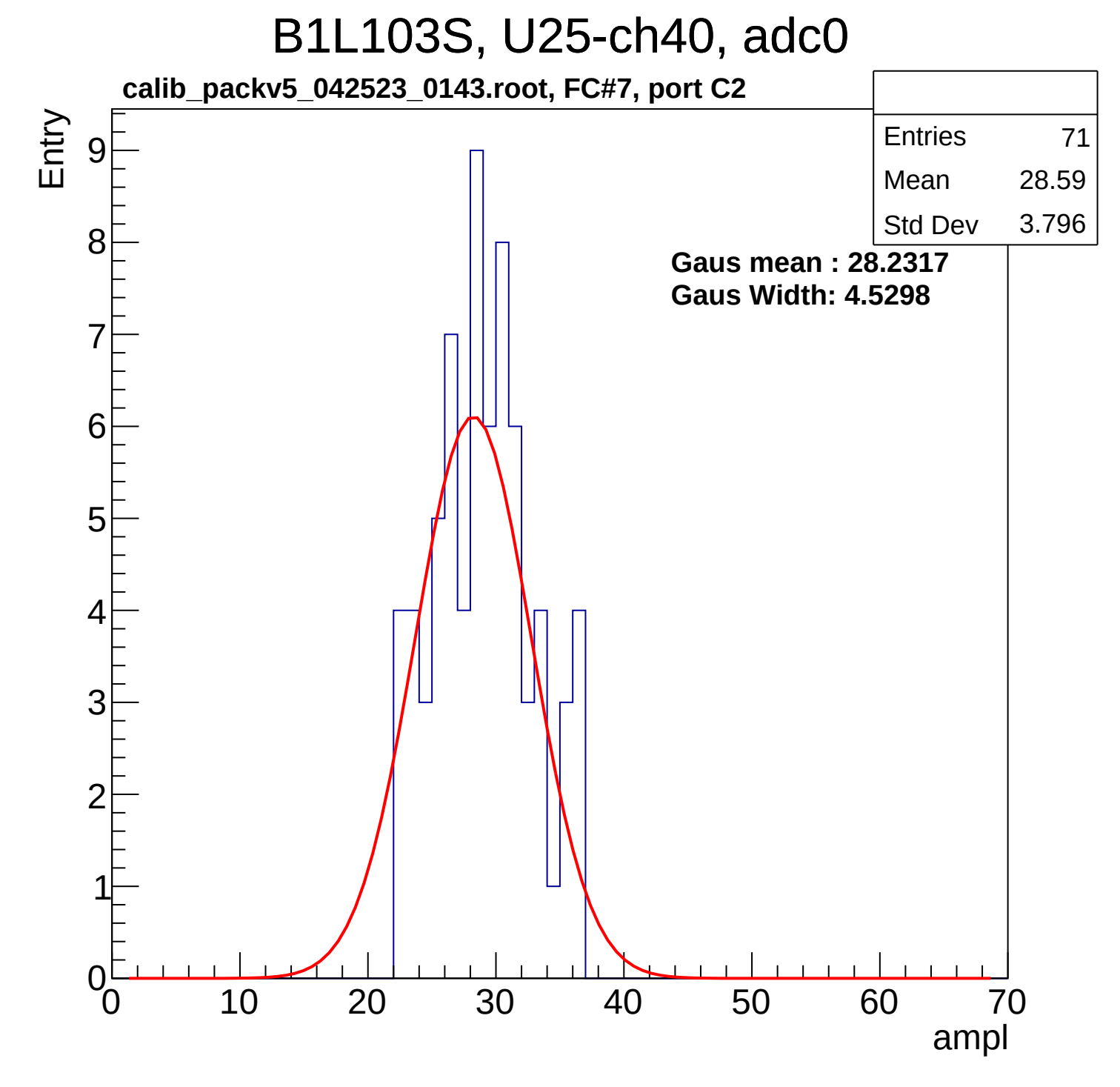
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch40, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 28.59
Std Dev 3.796
Gaus mean : 28.2317
Gaus Width: 4.5298
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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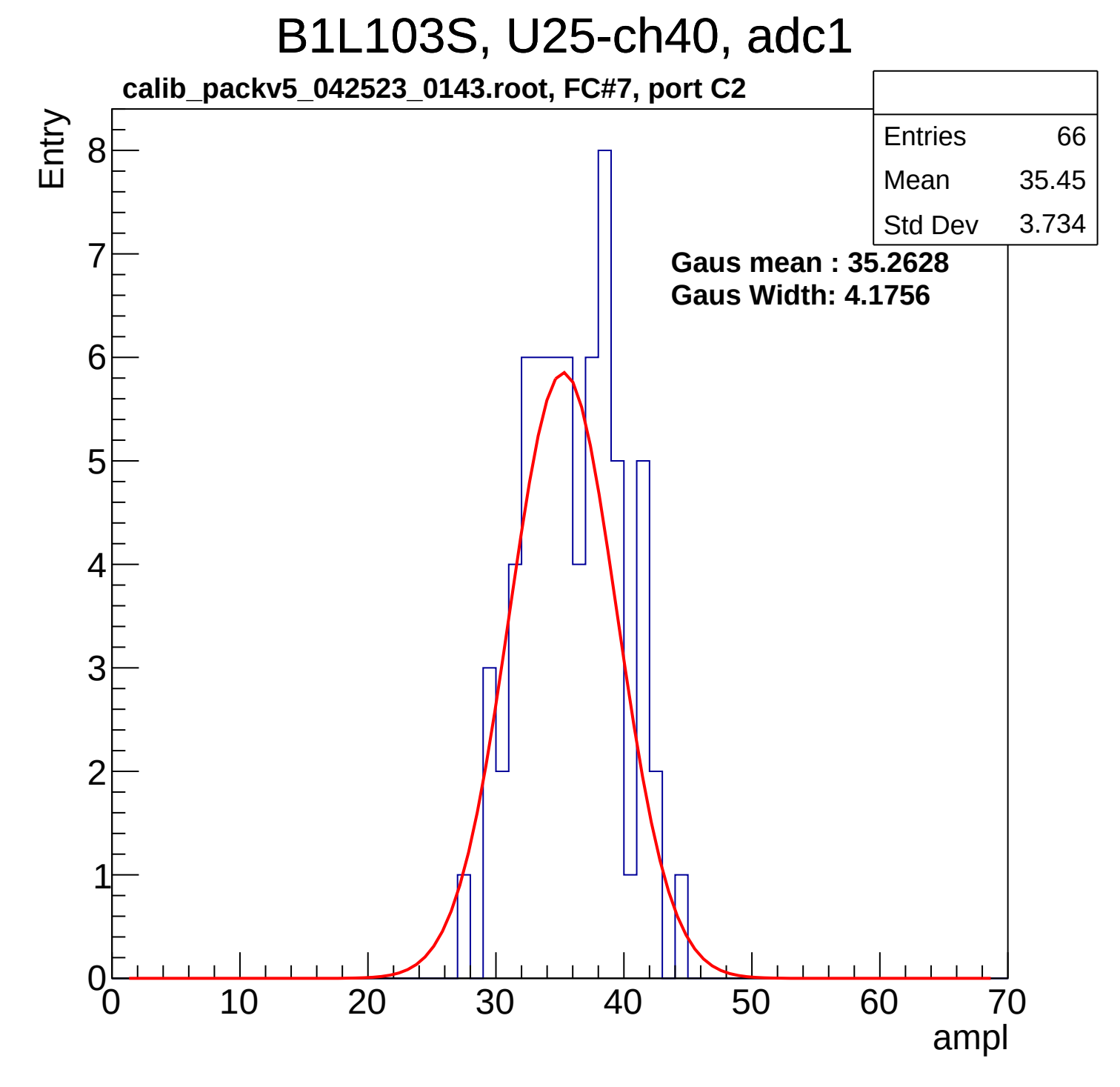
B1L103S, U25-ch40, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 35.45
Std Dev 3.734
Gaus mean : 35.2628
Gaus Width: 4.1756
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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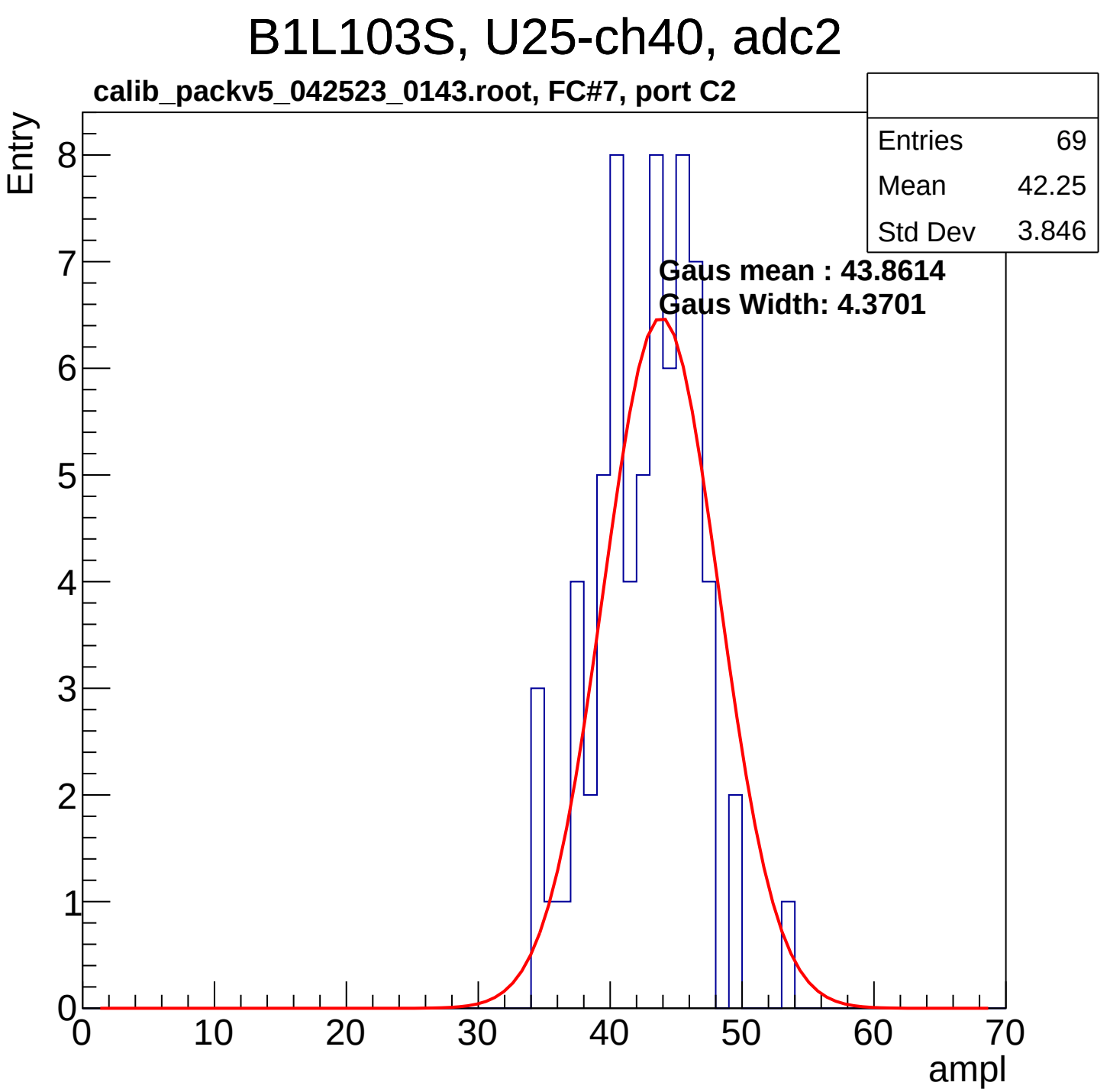
B1L103S, U25-ch40, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 42.25
Std Dev 3.846
Gaus mean : 43.8614
Gaus Width: 4.3701
Entry
ampl
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1
2
3
4
5
6
7
8
0
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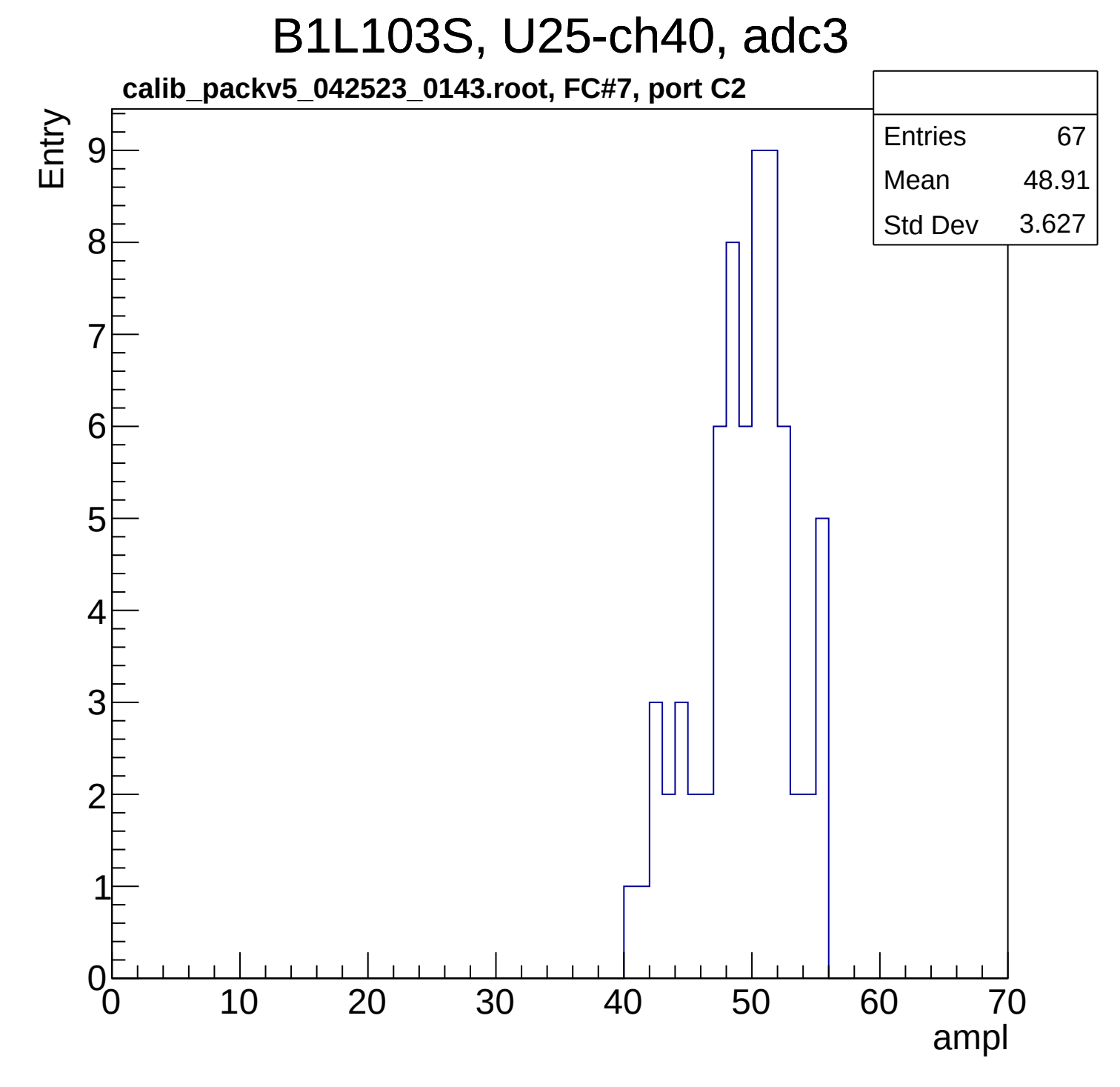
B1L103S, U25-ch40, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.91
Std Dev 3.627
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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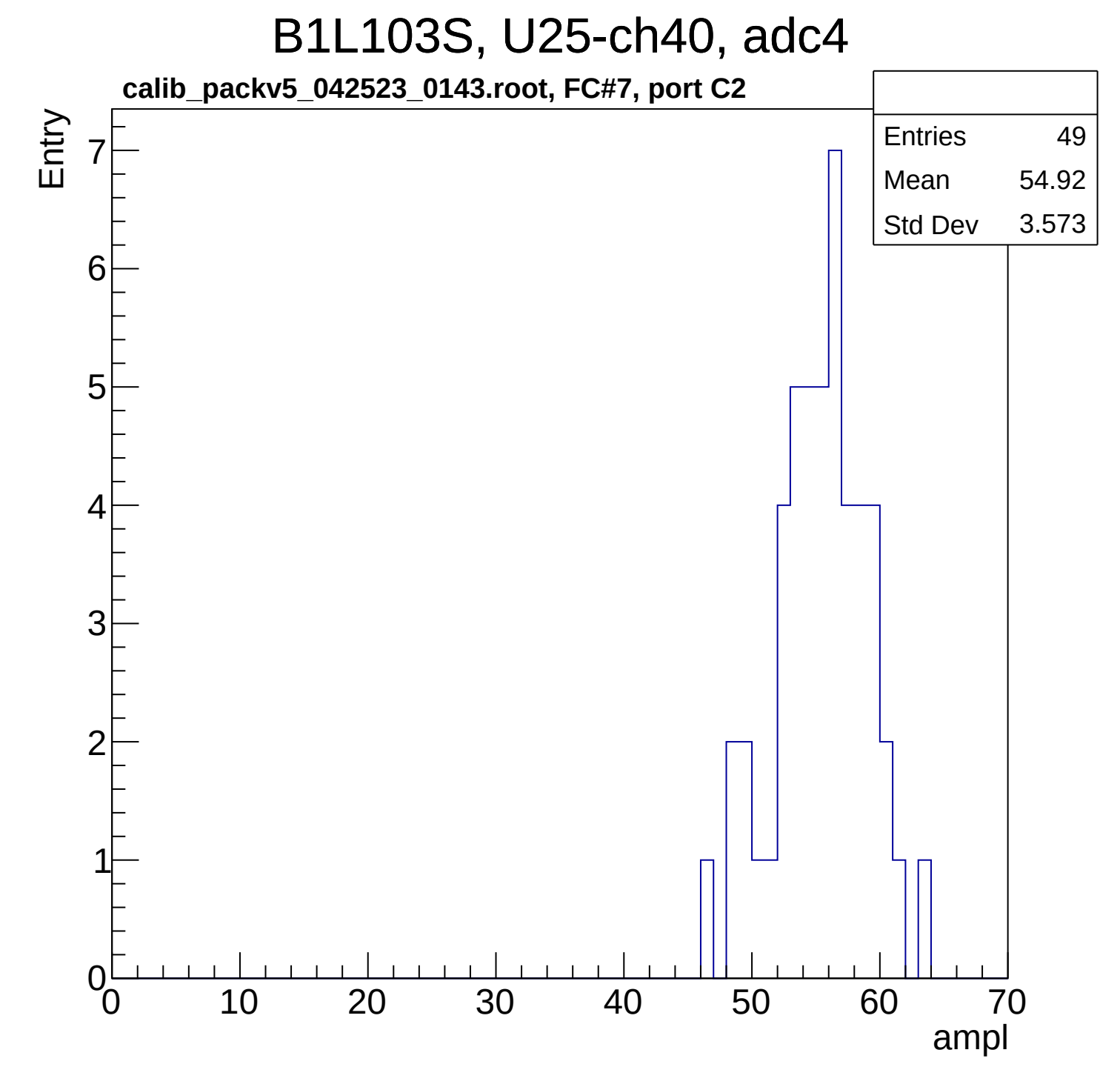
B1L103S, U25-ch40, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 54.92
Std Dev 3.573
Entry
ampl
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1
2
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4
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6
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0
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20
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40
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60
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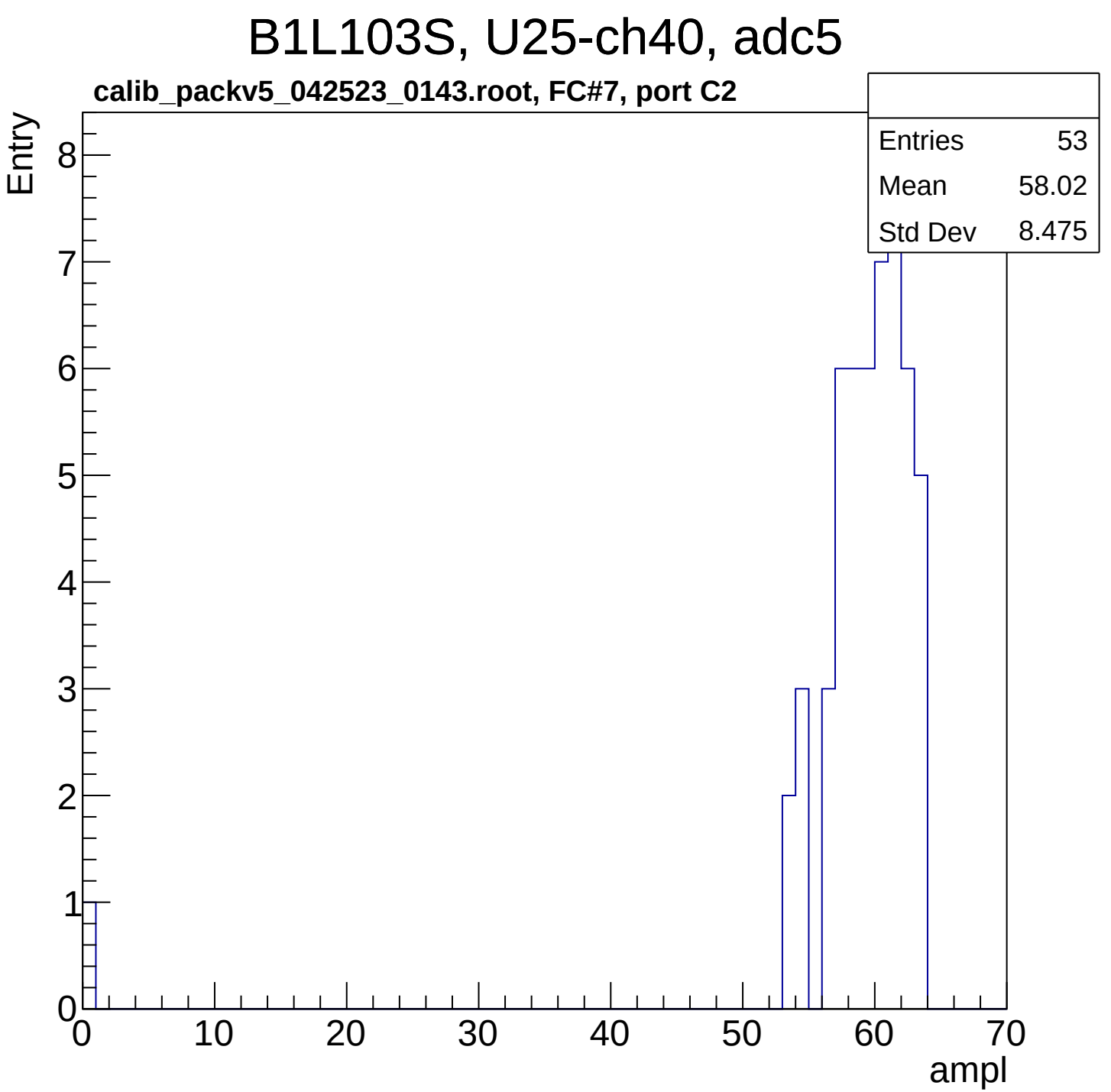
B1L103S, U25-ch40, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.02
Std Dev 8.475
Entry
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2
3
4
5
6
7
8
0
10
20
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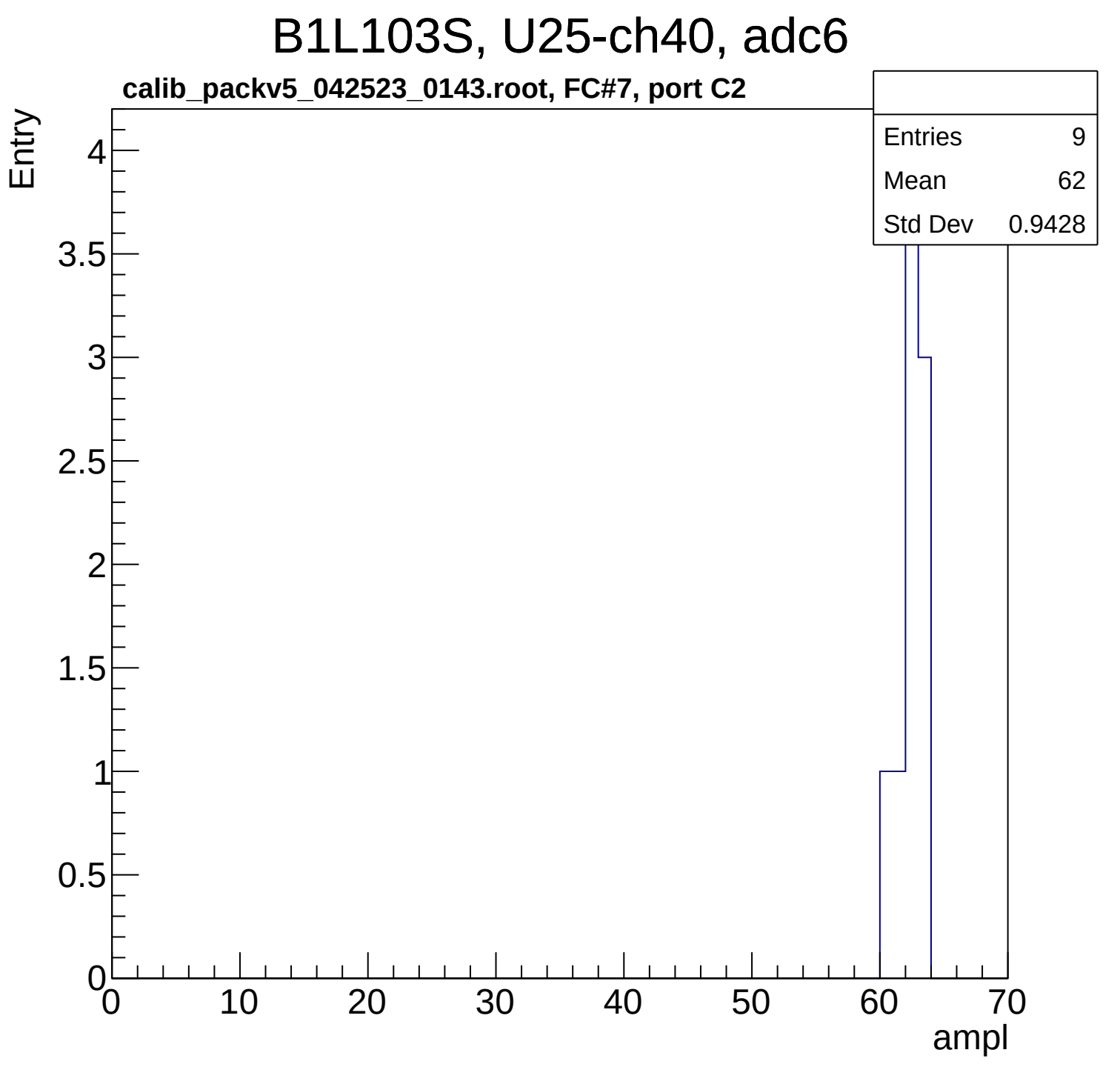
50
60
70
ampl


B1L103S, U25-ch40, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 62
Std Dev 0.9428
Entry
ampl
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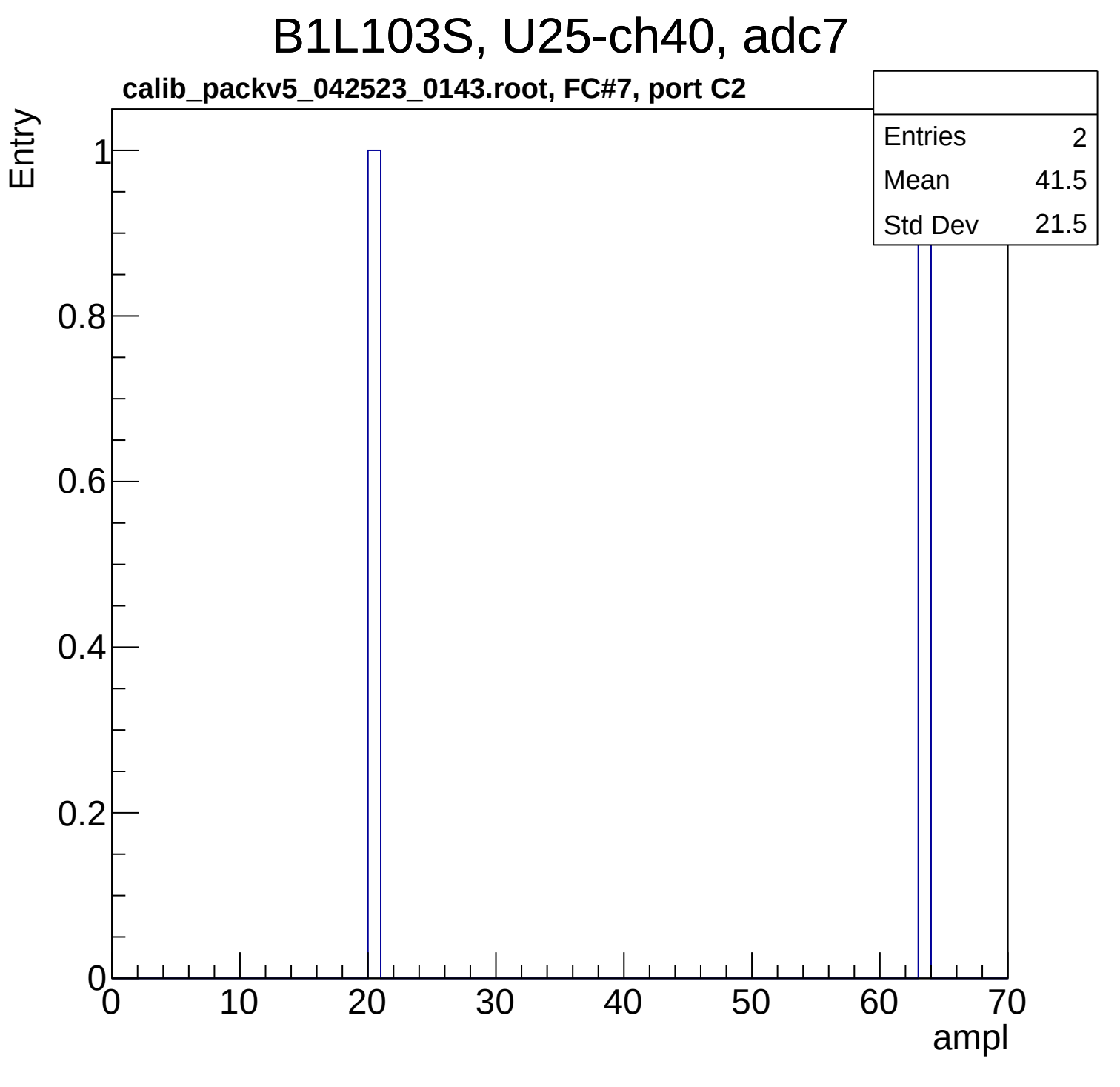
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U25-ch40, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 41.5
Std Dev 21.5
Entry
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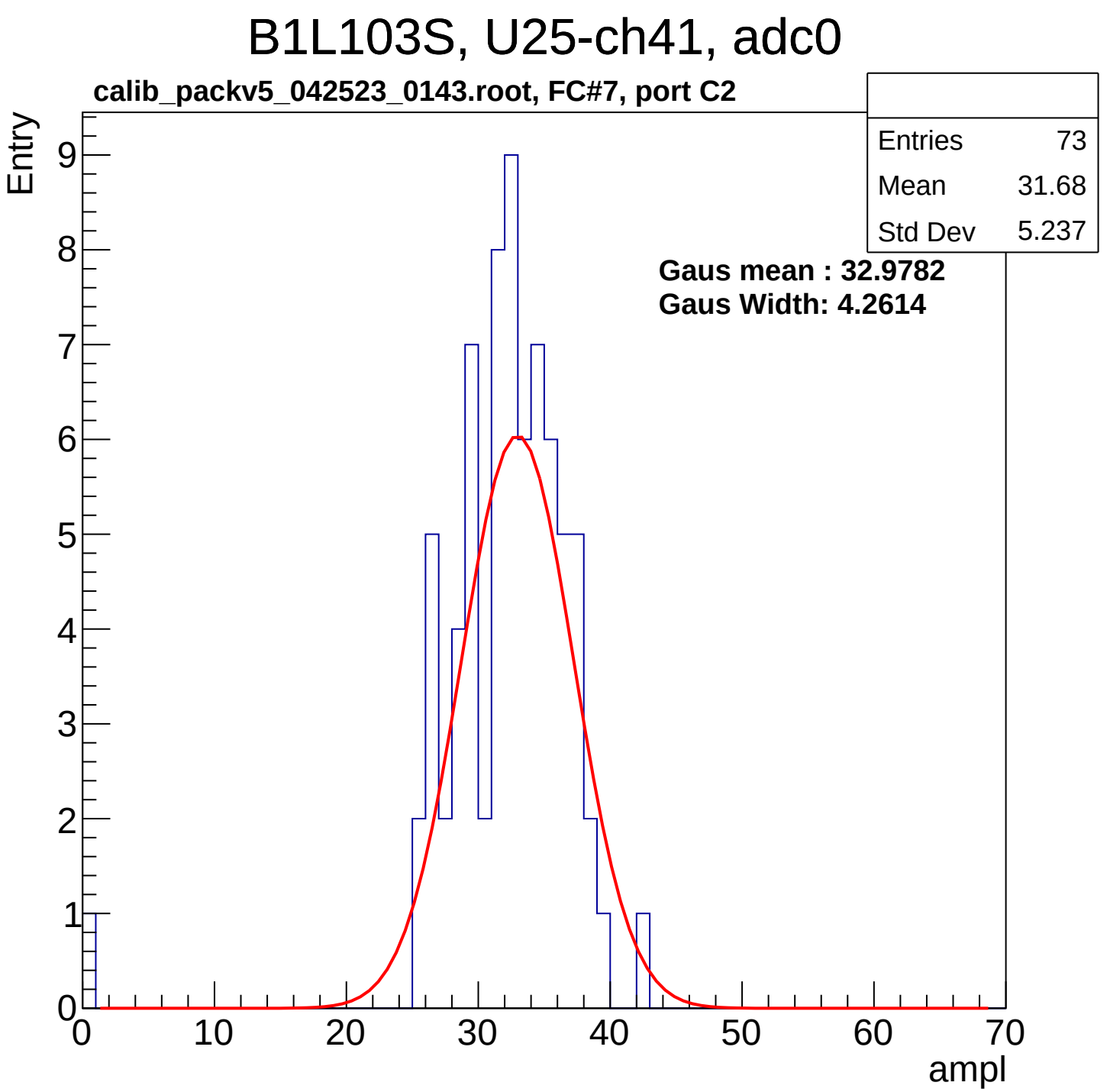
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch41, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 31.68
Std Dev 5.237
Gaus mean : 32.9782
Gaus Width: 4.2614
Entry
ampl
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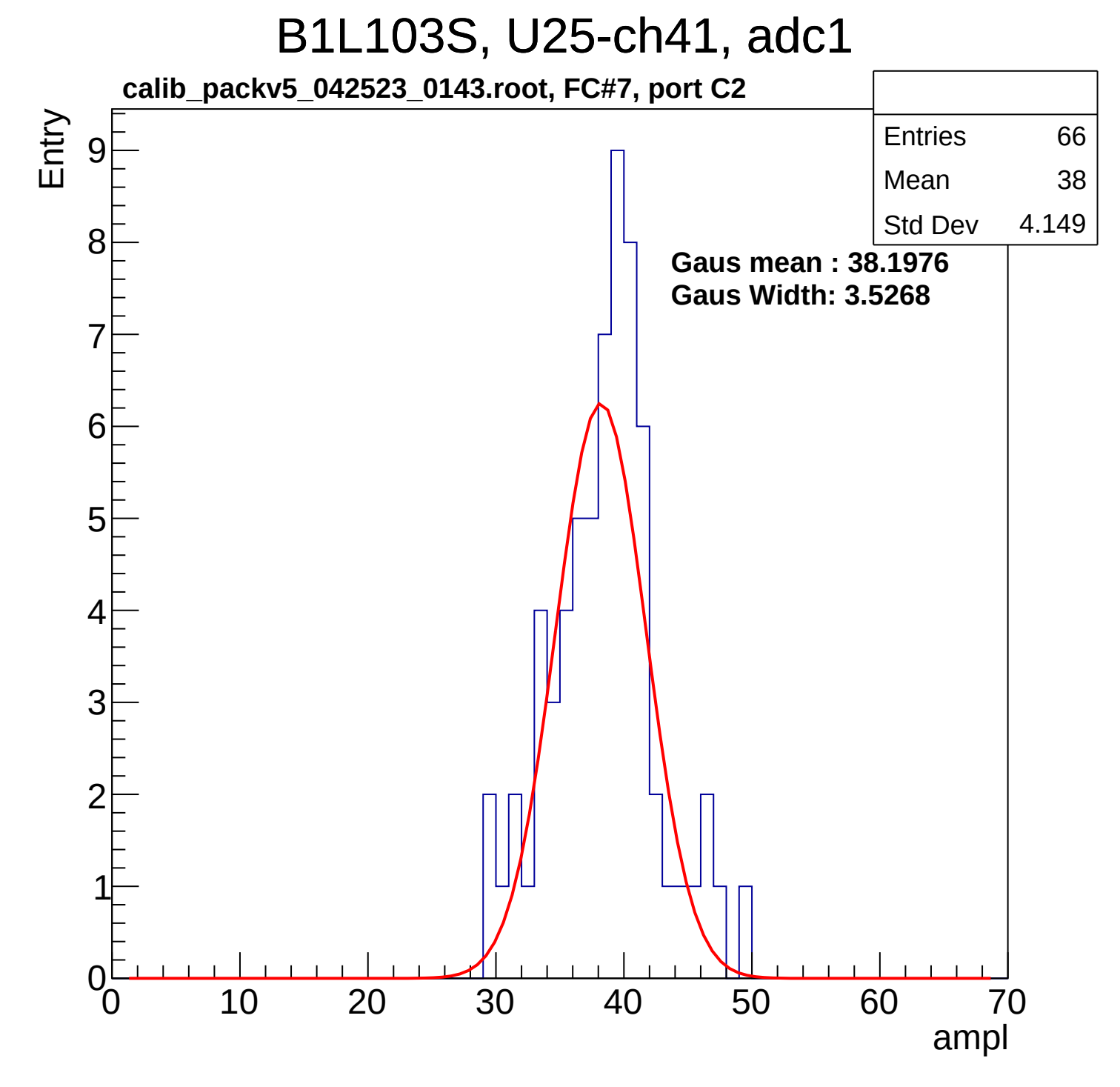
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch41, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 38
Std Dev 4.149
Gaus mean : 38.1976
Gaus Width: 3.5268
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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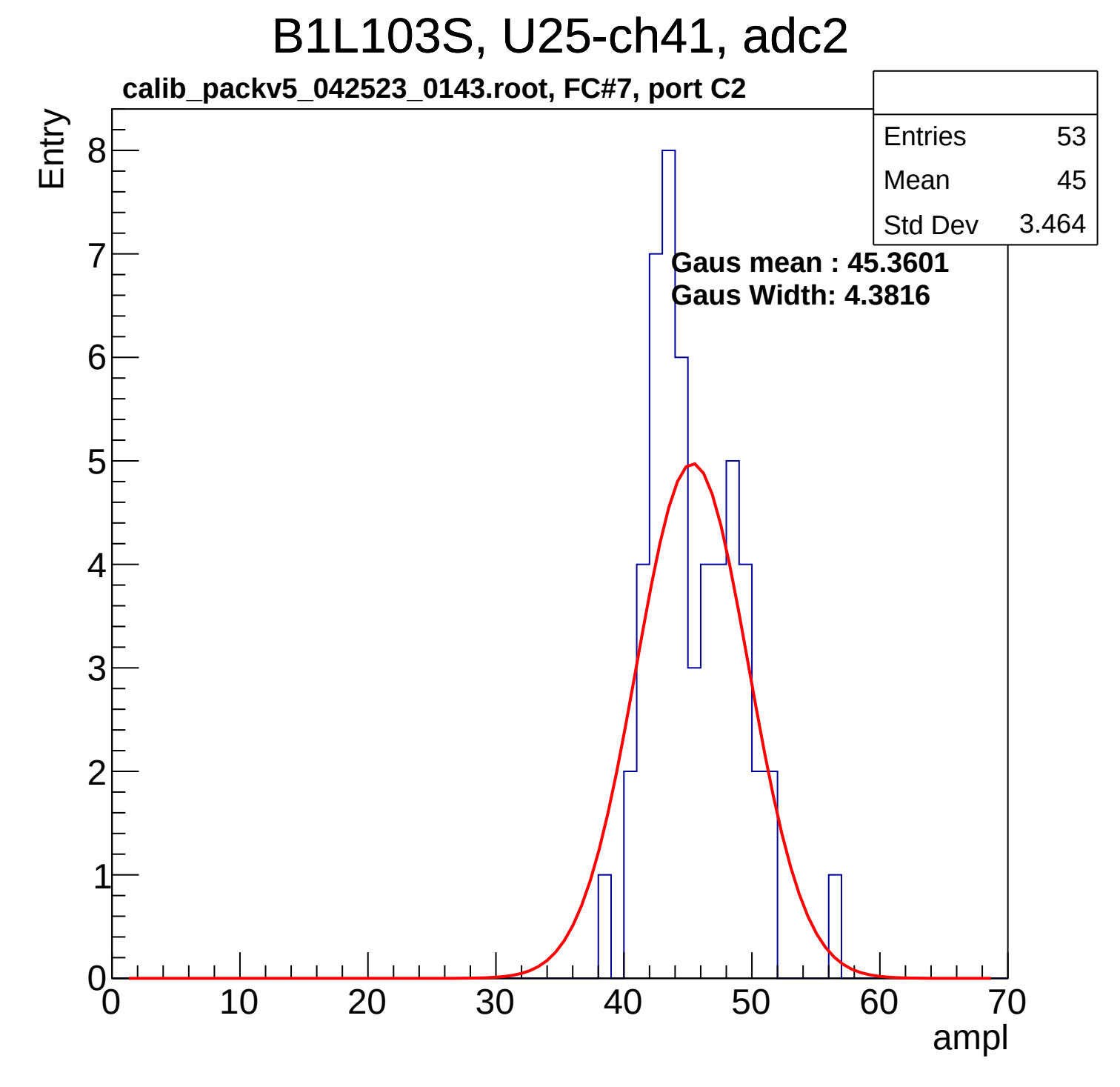
B1L103S, U25-ch41, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 45
Std Dev 3.464
Gaus mean : 45.3601
Gaus Width: 4.3816
Entry
ampl
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2
3
4
5
6
7
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0
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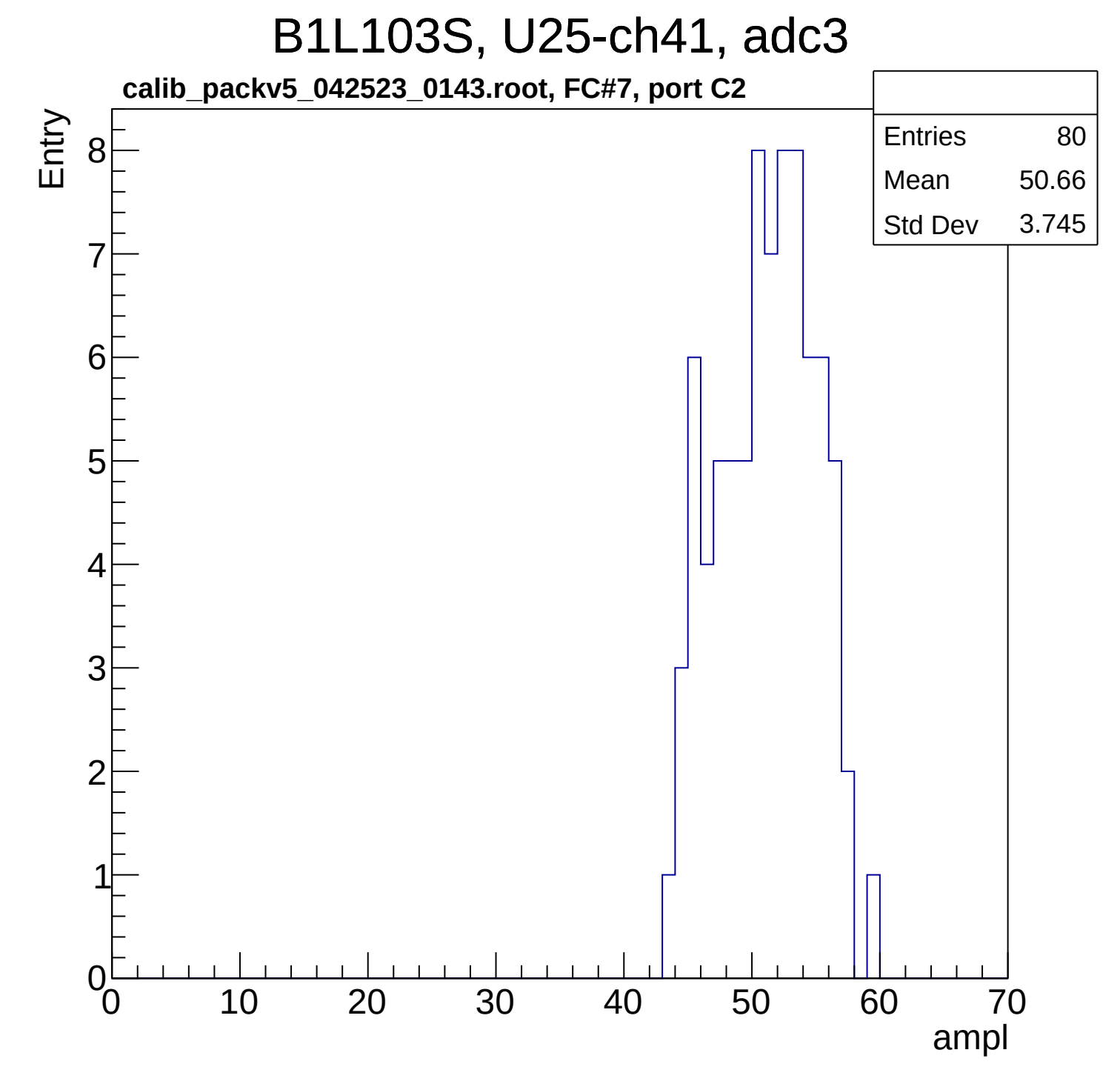
B1L103S, U25-ch41, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 50.66
Std Dev 3.745
Entry
ampl
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5
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7
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0
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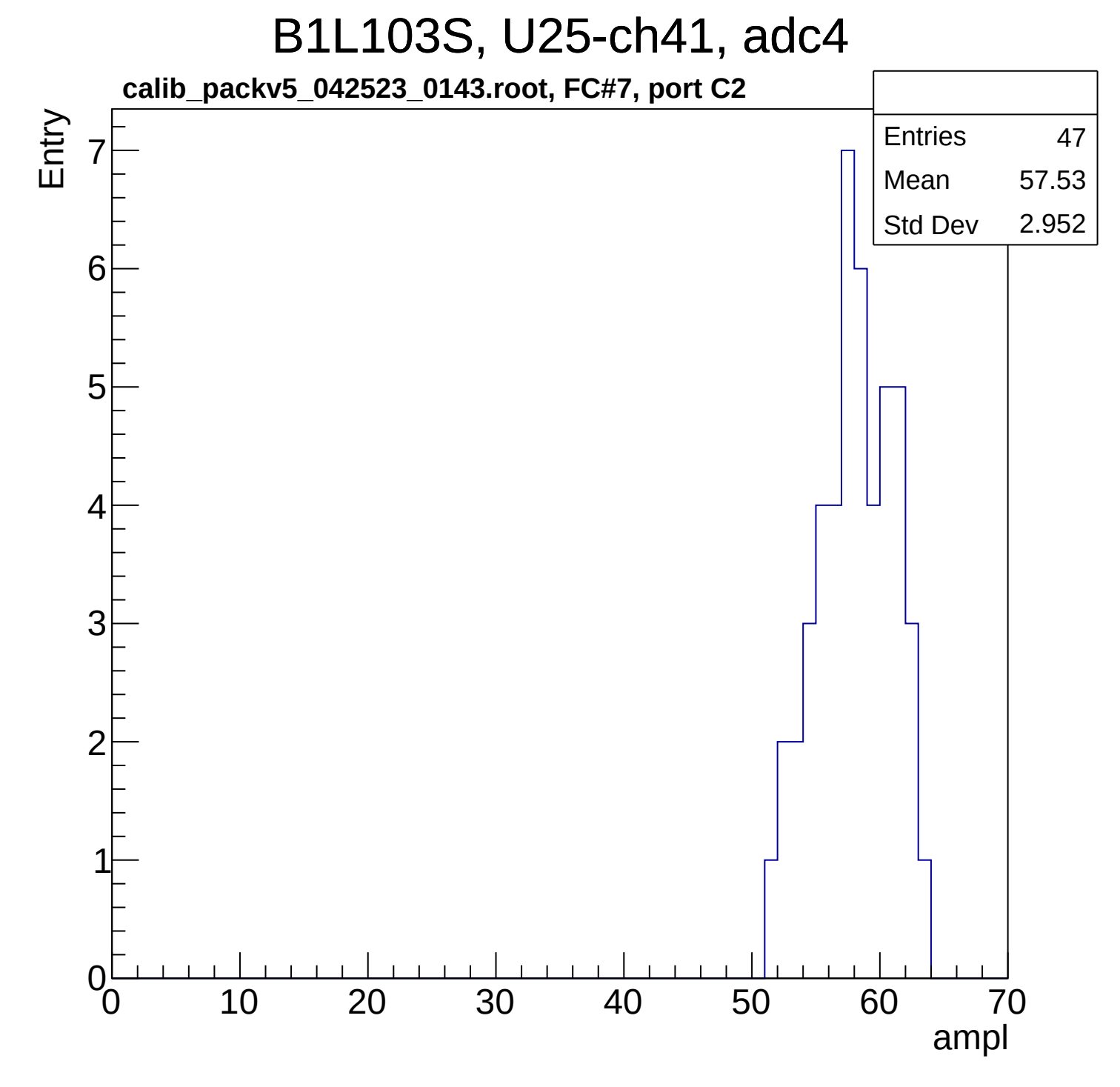
B1L103S, U25-ch41, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 57.53
Std Dev 2.952
Entry
ampl
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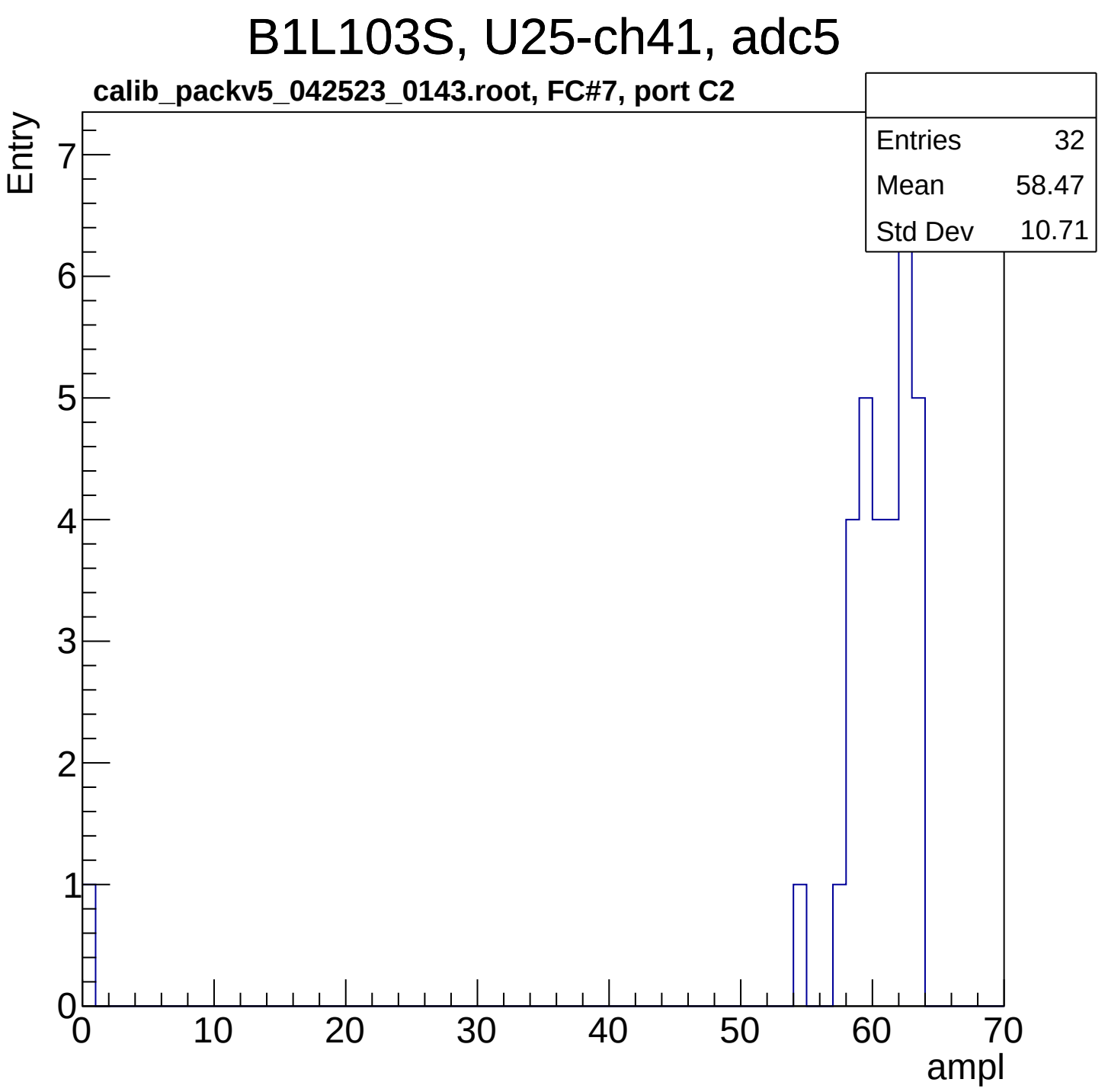
B1L103S, U25-ch41, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.47
Std Dev 10.71
Entry
ampl
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0
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60
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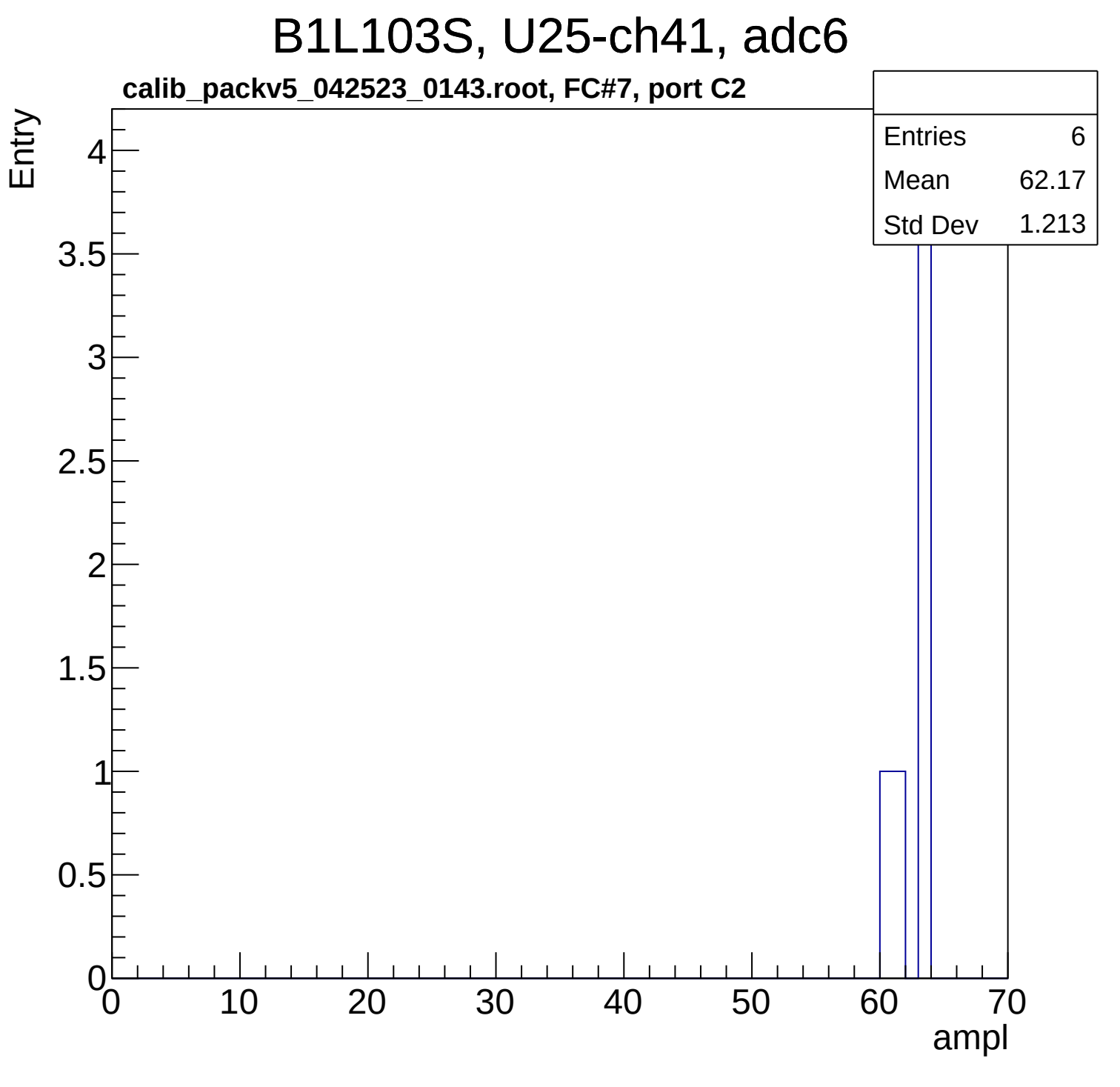
B1L103S, U25-ch41, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.17
Std Dev 1.213
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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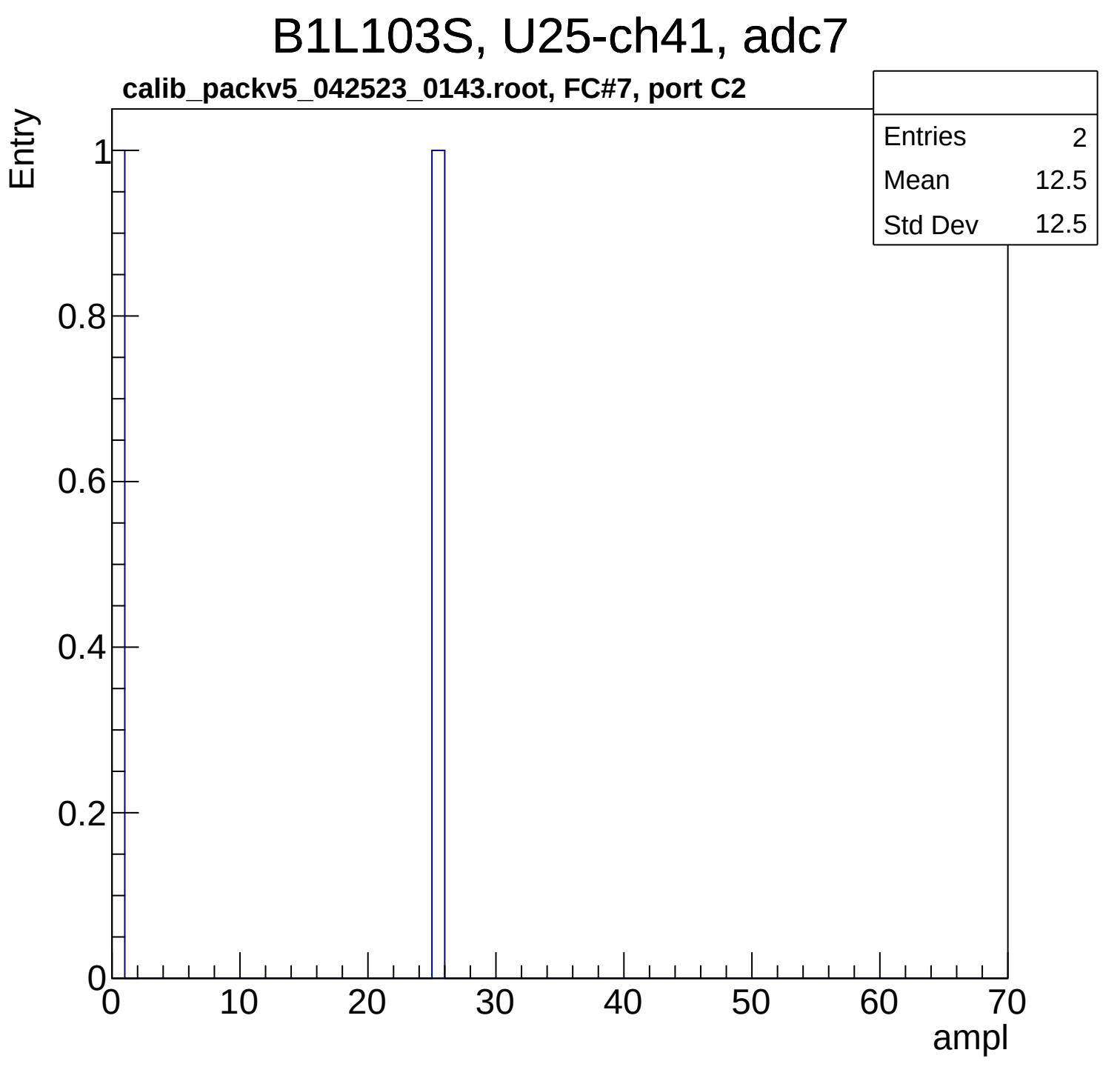
60
70


B1L103S, U25-ch41, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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0.4
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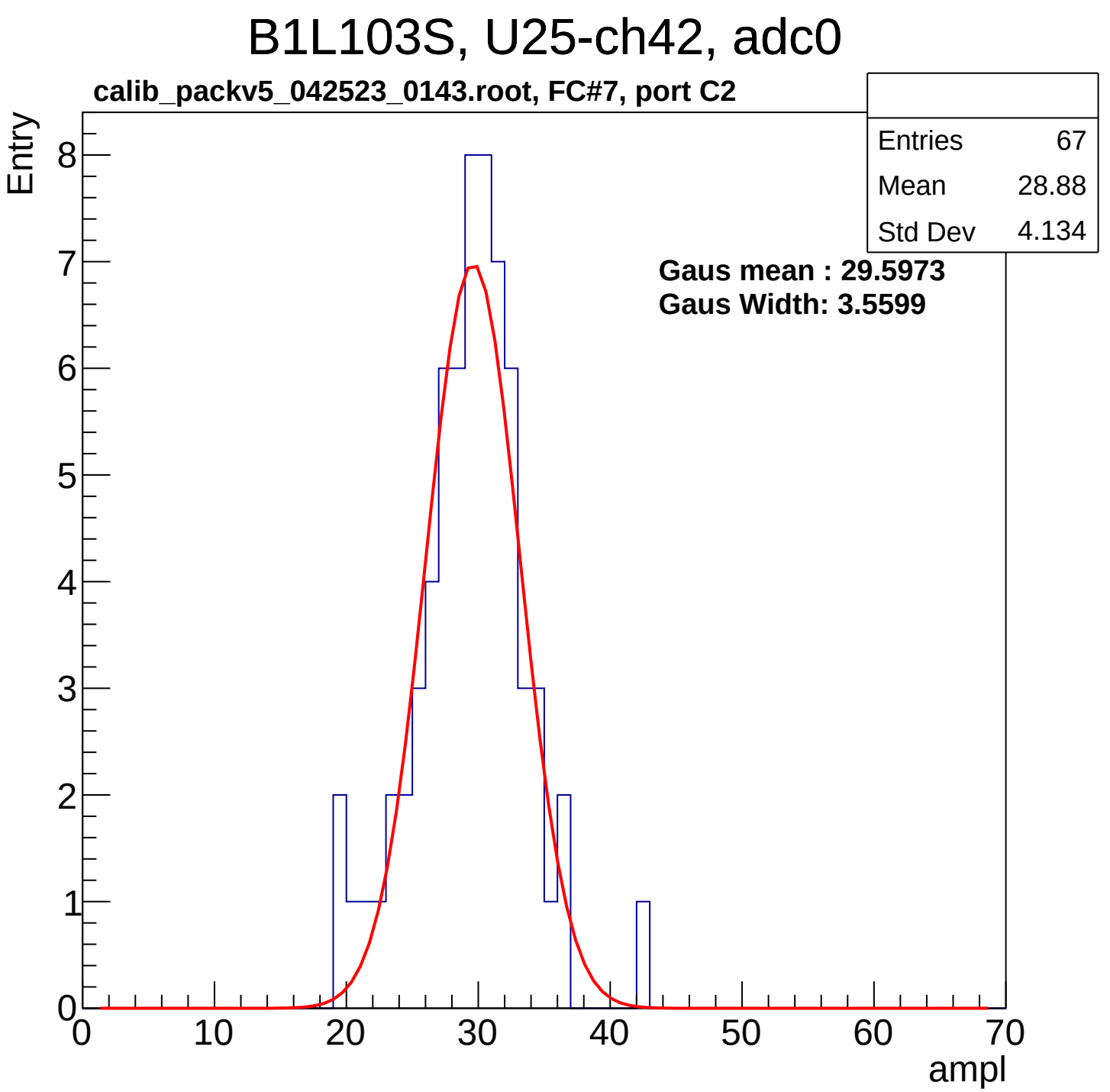
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch42, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 28.88
Std Dev 4.134
Gaus mean : 29.5973
Gaus Width: 3.5599
Entry
ampl
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2
3
4
5
6
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0
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60
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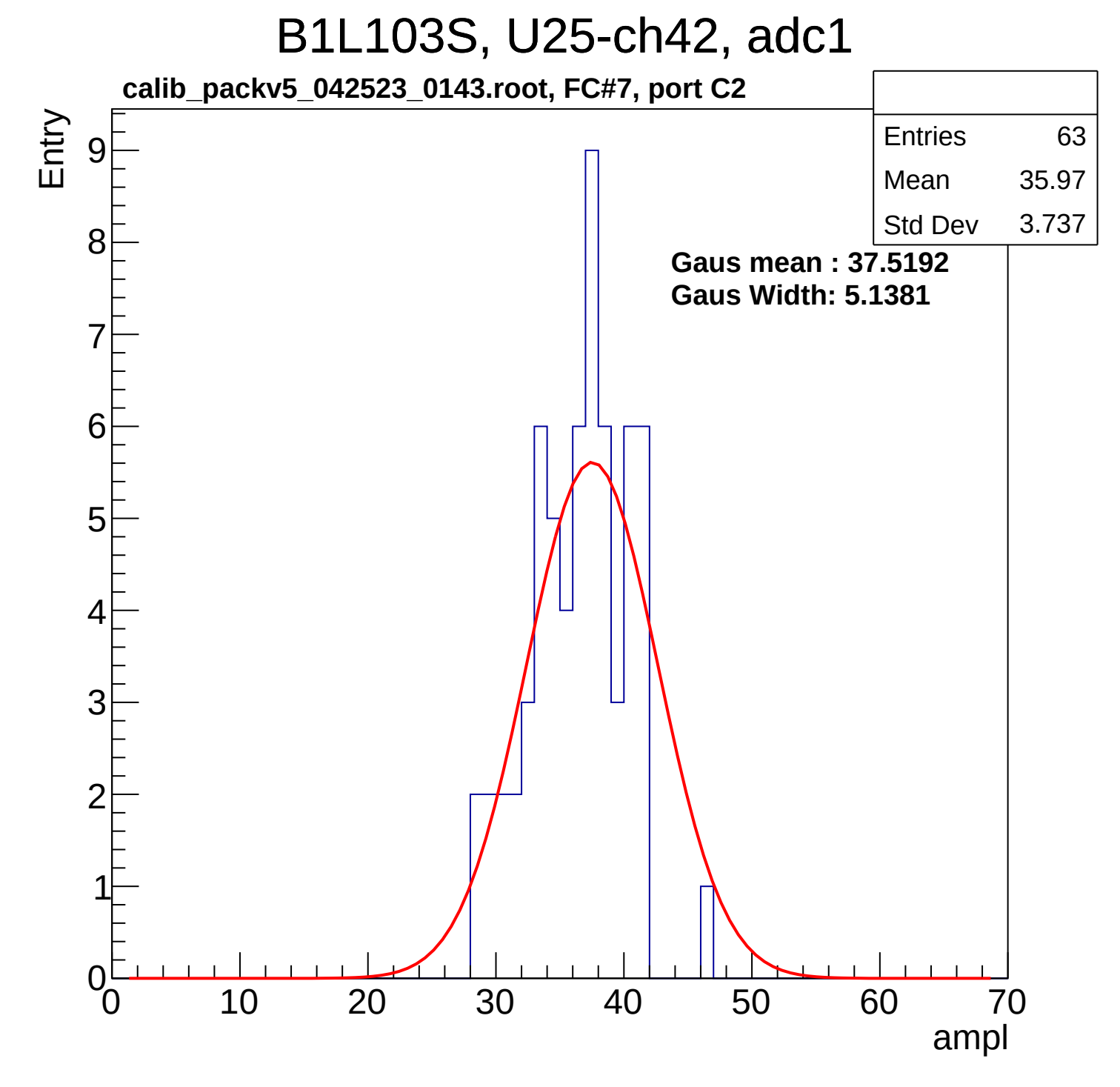
B1L103S, U25-ch42, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 35.97
Std Dev 3.737
Gaus mean : 37.5192
Gaus Width: 5.1381
Entry
ampl
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2
3
4
5
6
7
8
9
0
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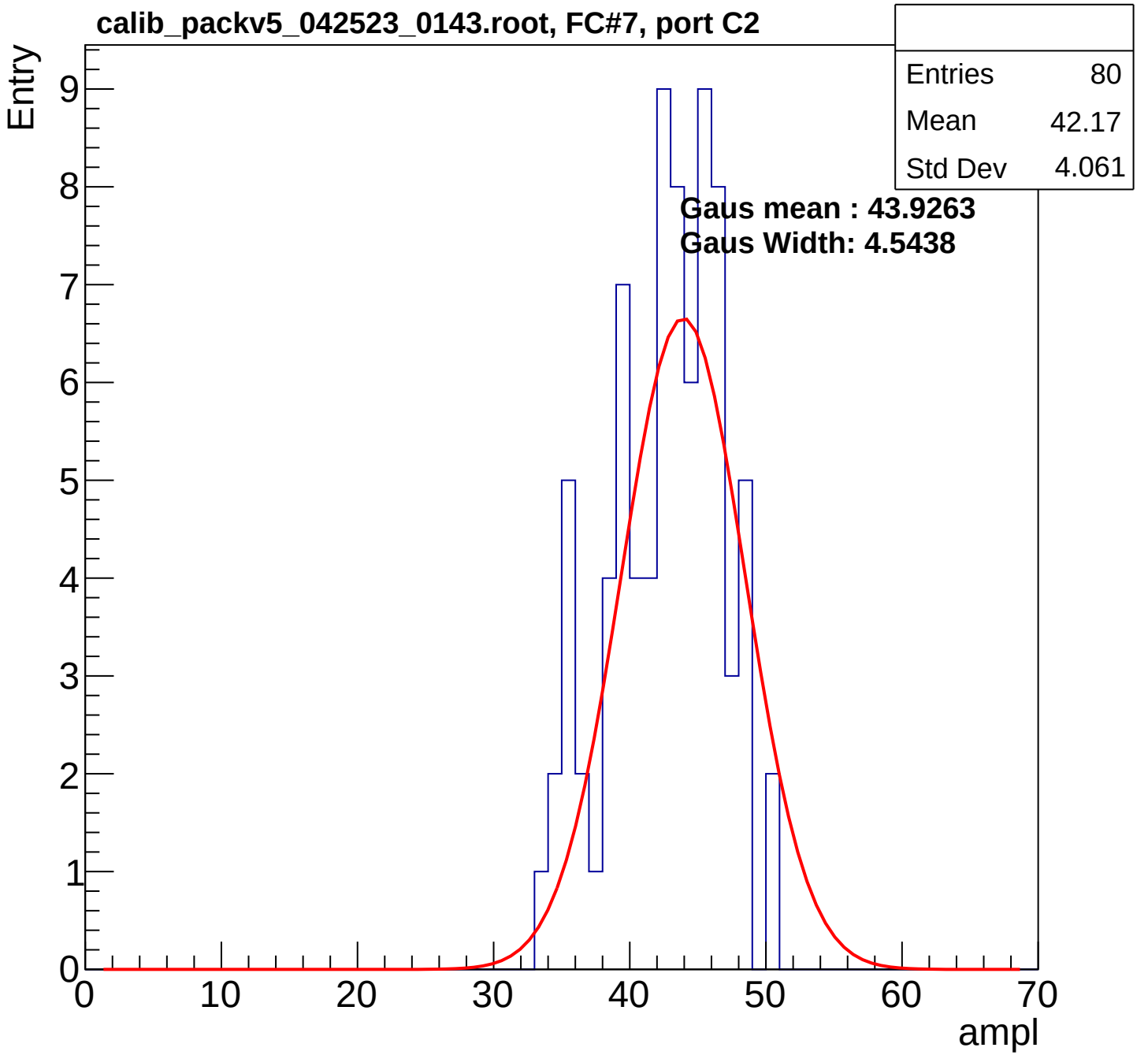
B1L103S, U25-ch42, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 42.17
Std Dev 4.061
Gaus mean : 43.9263
Gaus Width: 4.5438
Entry
ampl
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2
3
4
5
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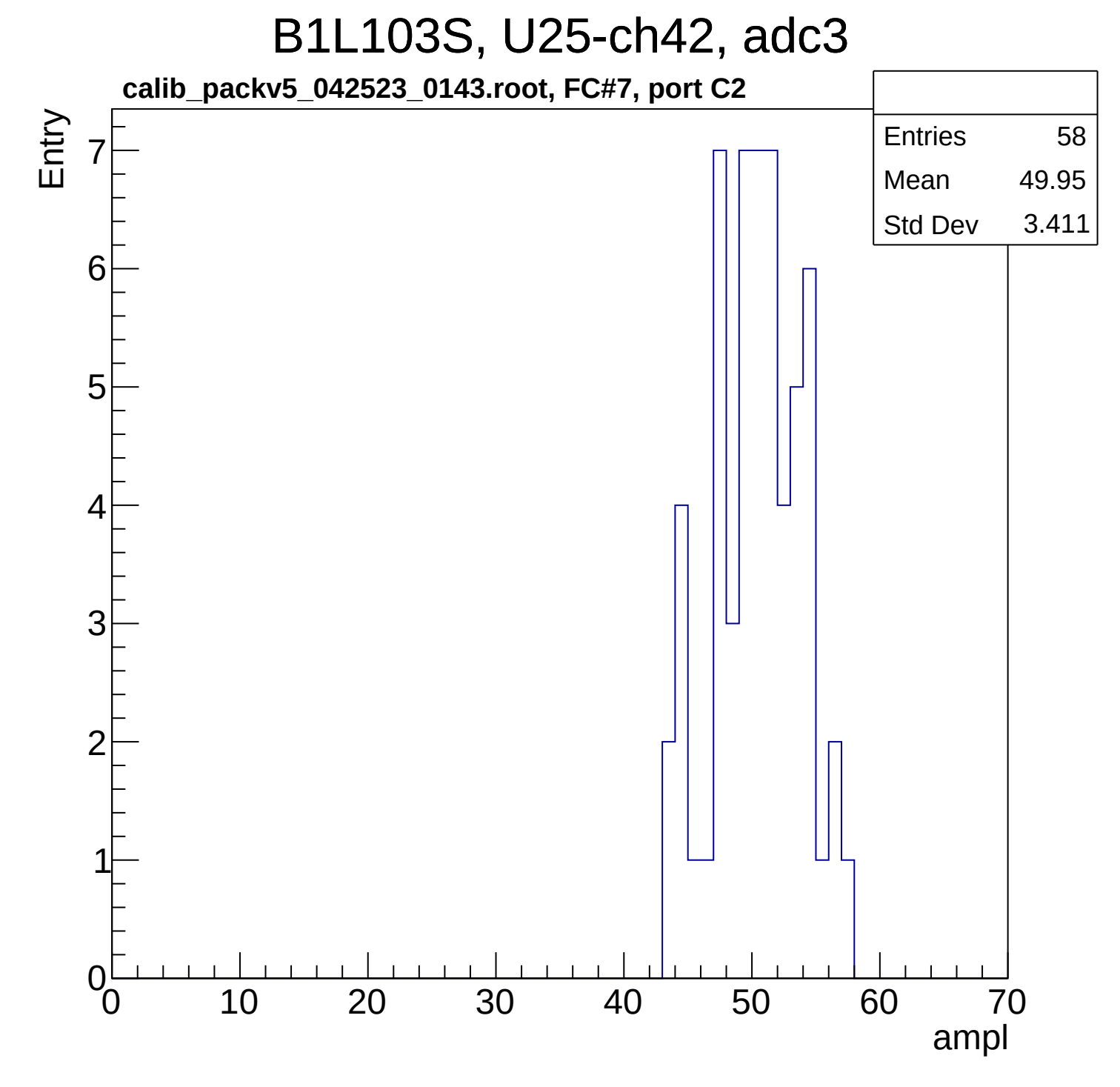
B1L103S, U25-ch42, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.95
Std Dev 3.411
Entry
ampl
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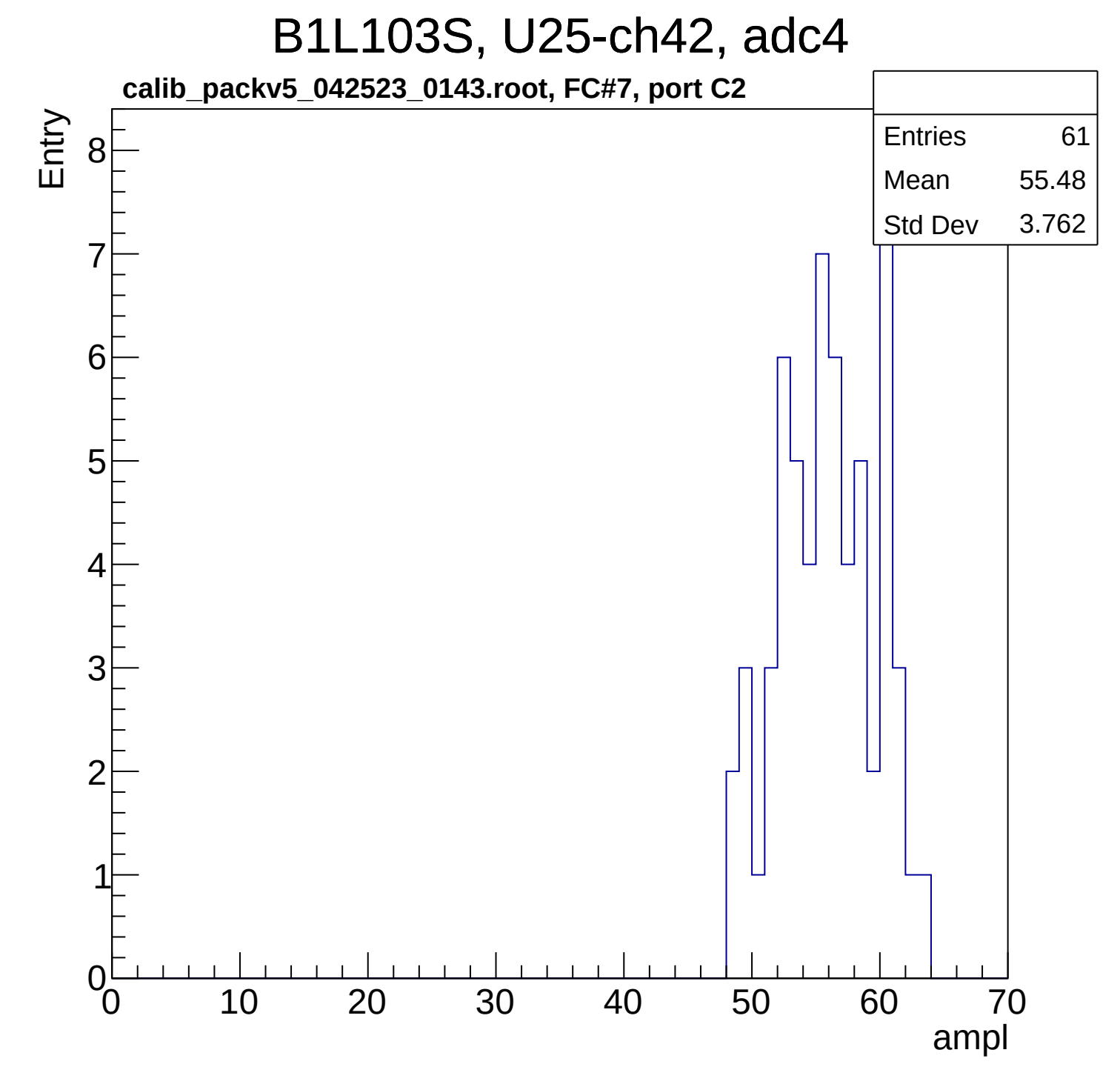
B1L103S, U25-ch42, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 55.48
Std Dev 3.762
Entry
ampl
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1
2
3
4
5
6
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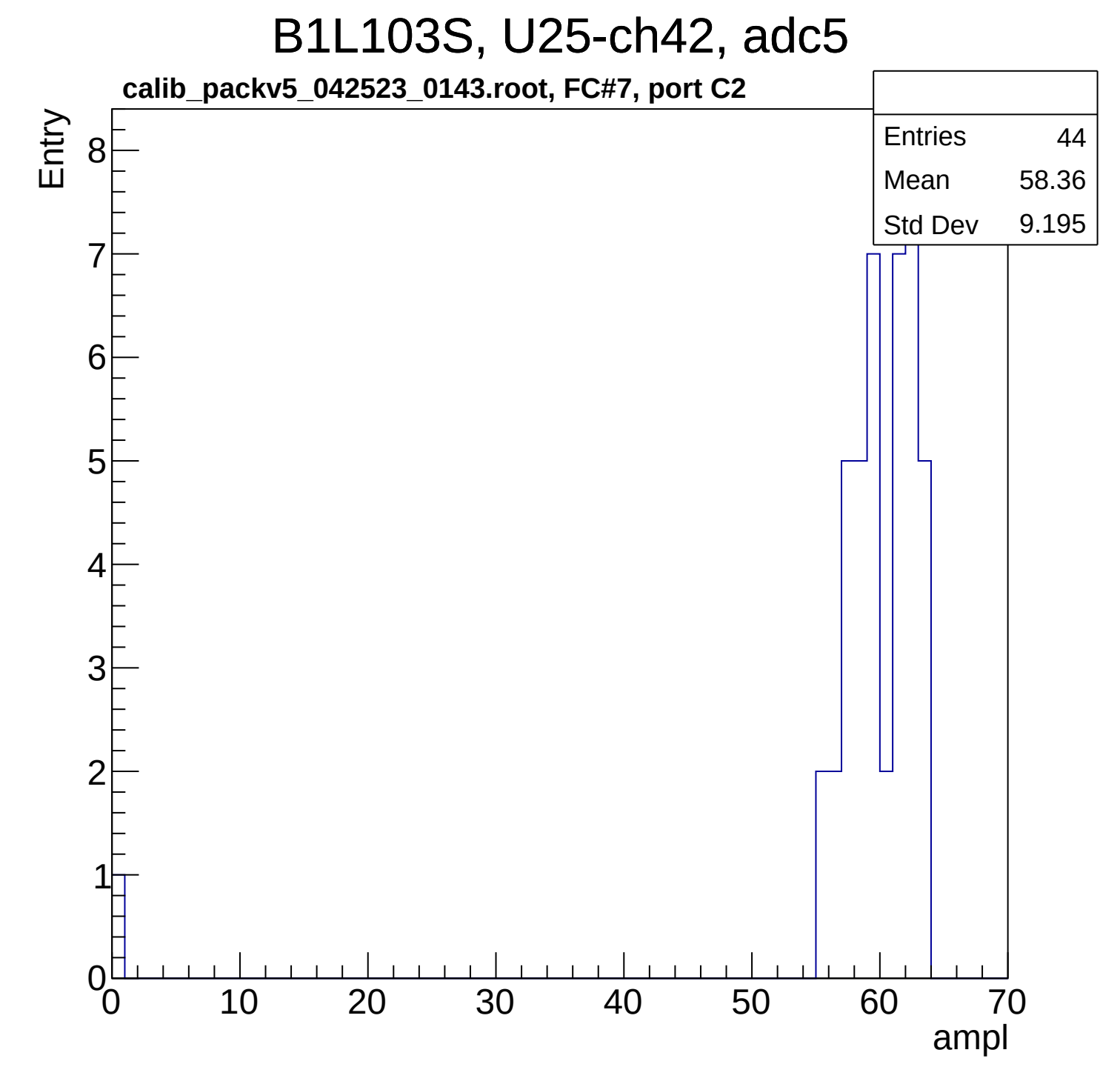
B1L103S, U25-ch42, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.36
Std Dev 9.195
Entry
ampl
0
1
2
3
4
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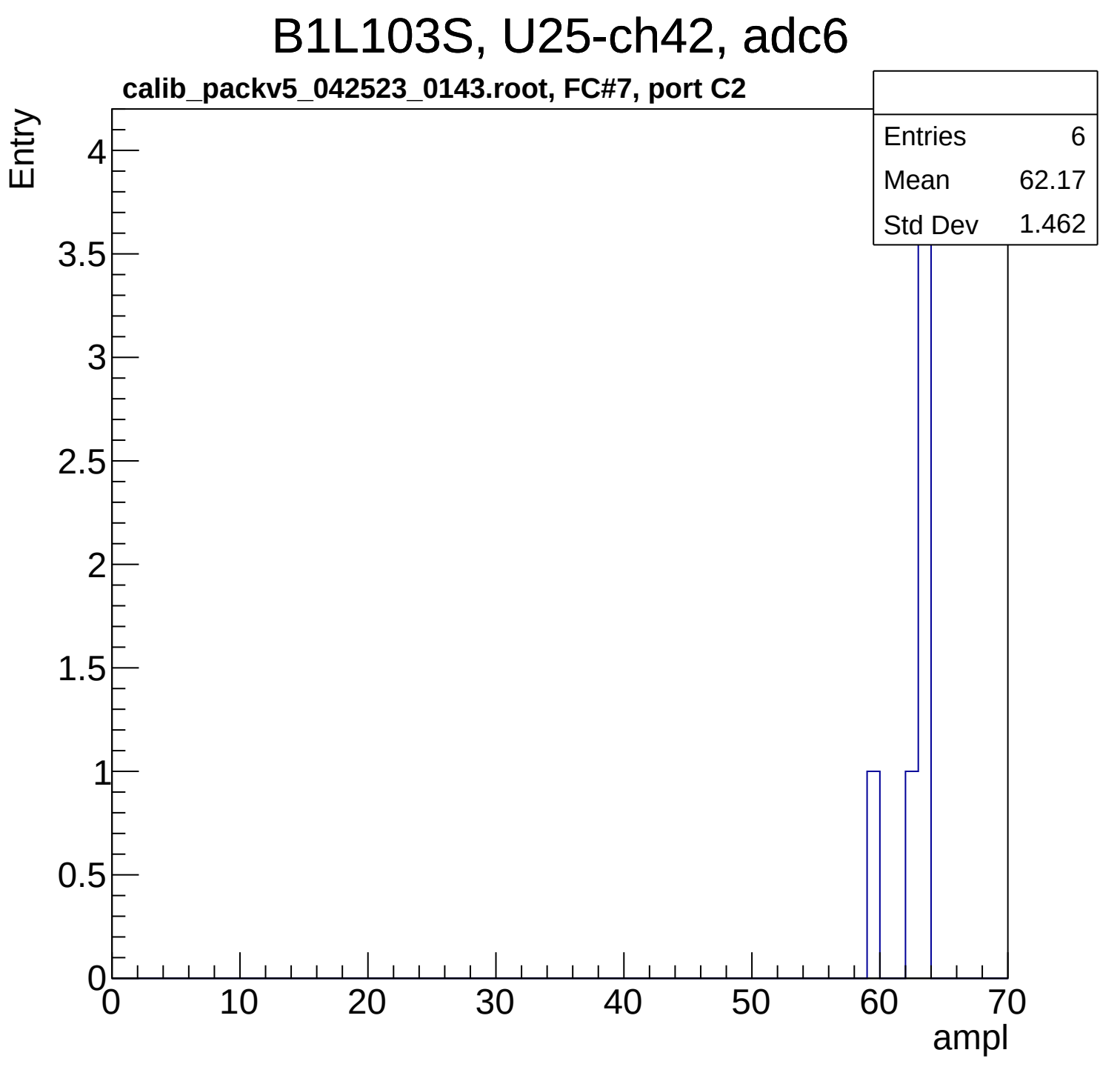
B1L103S, U25-ch42, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.17
Std Dev 1.462
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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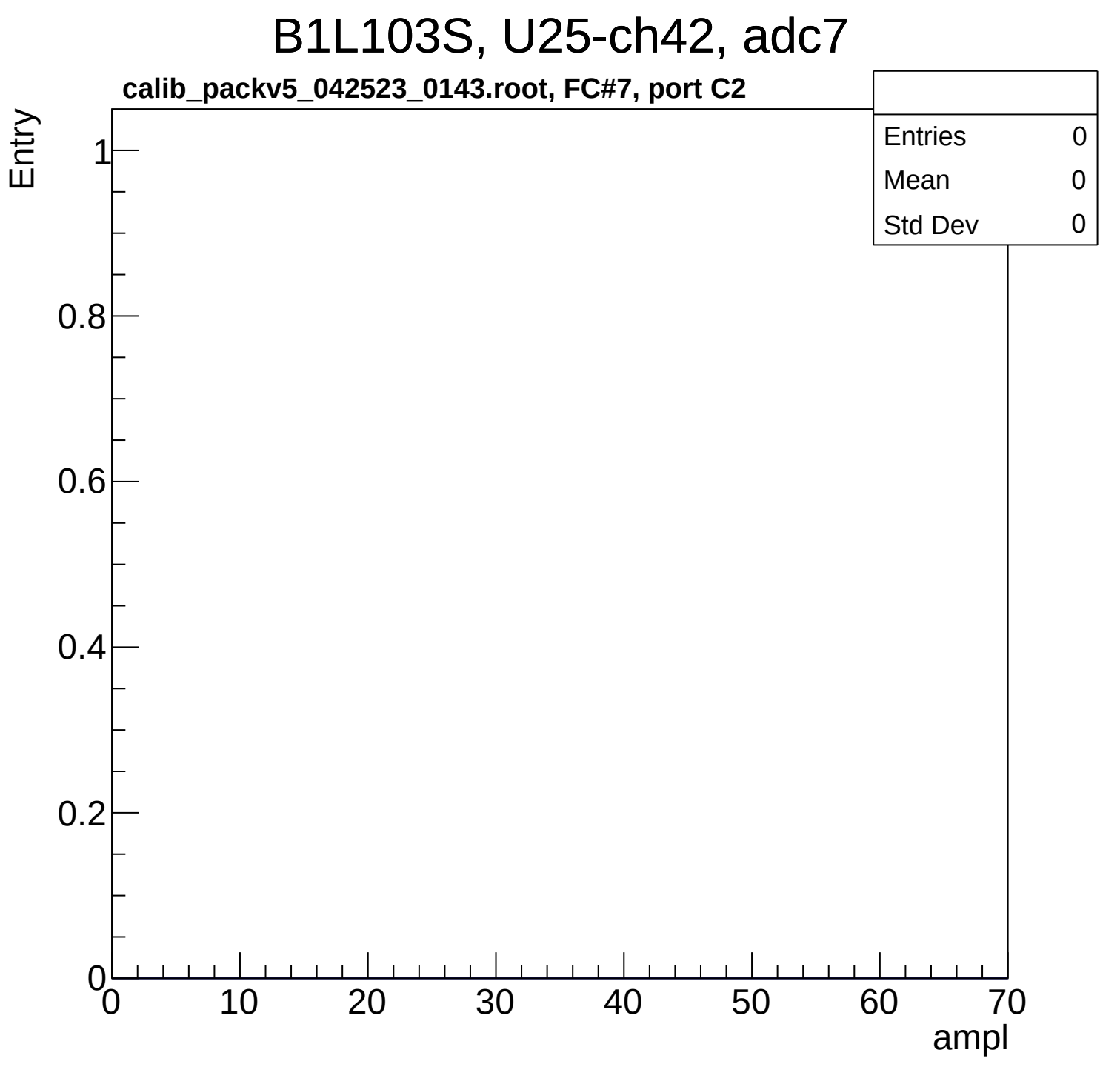
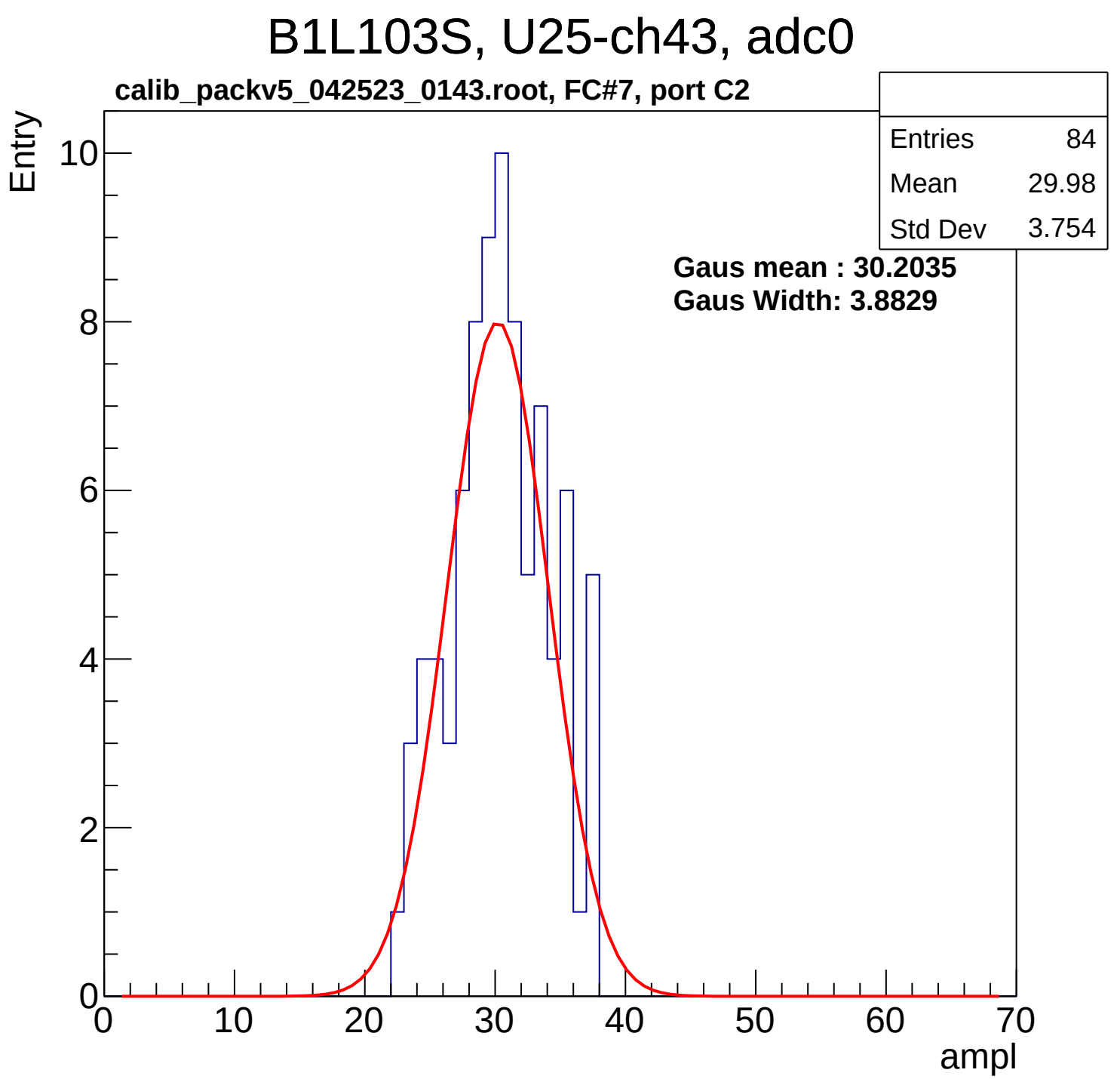

B1L103S, U25-ch42, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch43, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 29.98
Std Dev 3.754
Gaus mean : 30.2035
Gaus Width: 3.8829
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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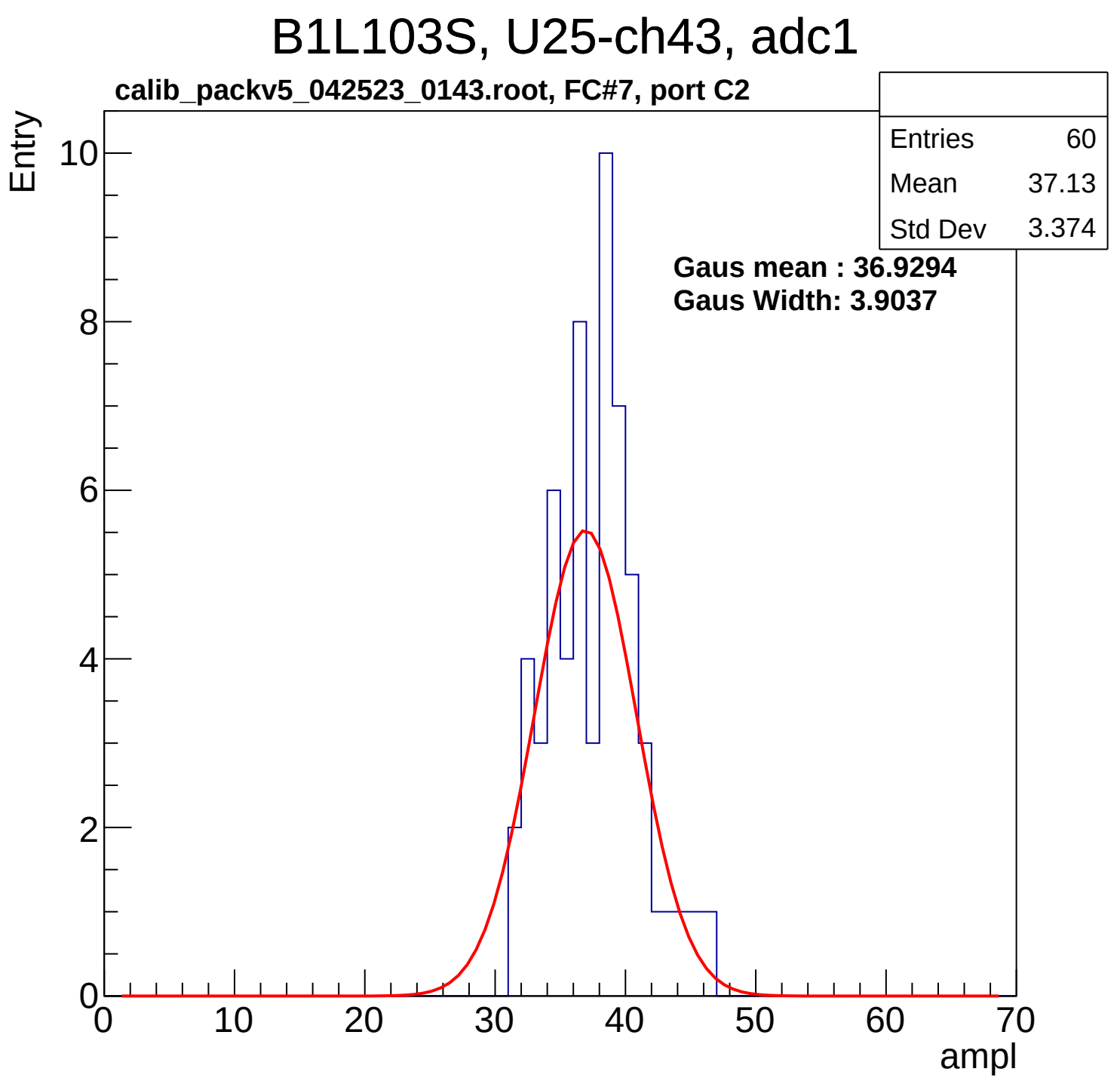
B1L103S, U25-ch43, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 37.13
Std Dev 3.374
Gaus mean : 36.9294
Gaus Width: 3.9037
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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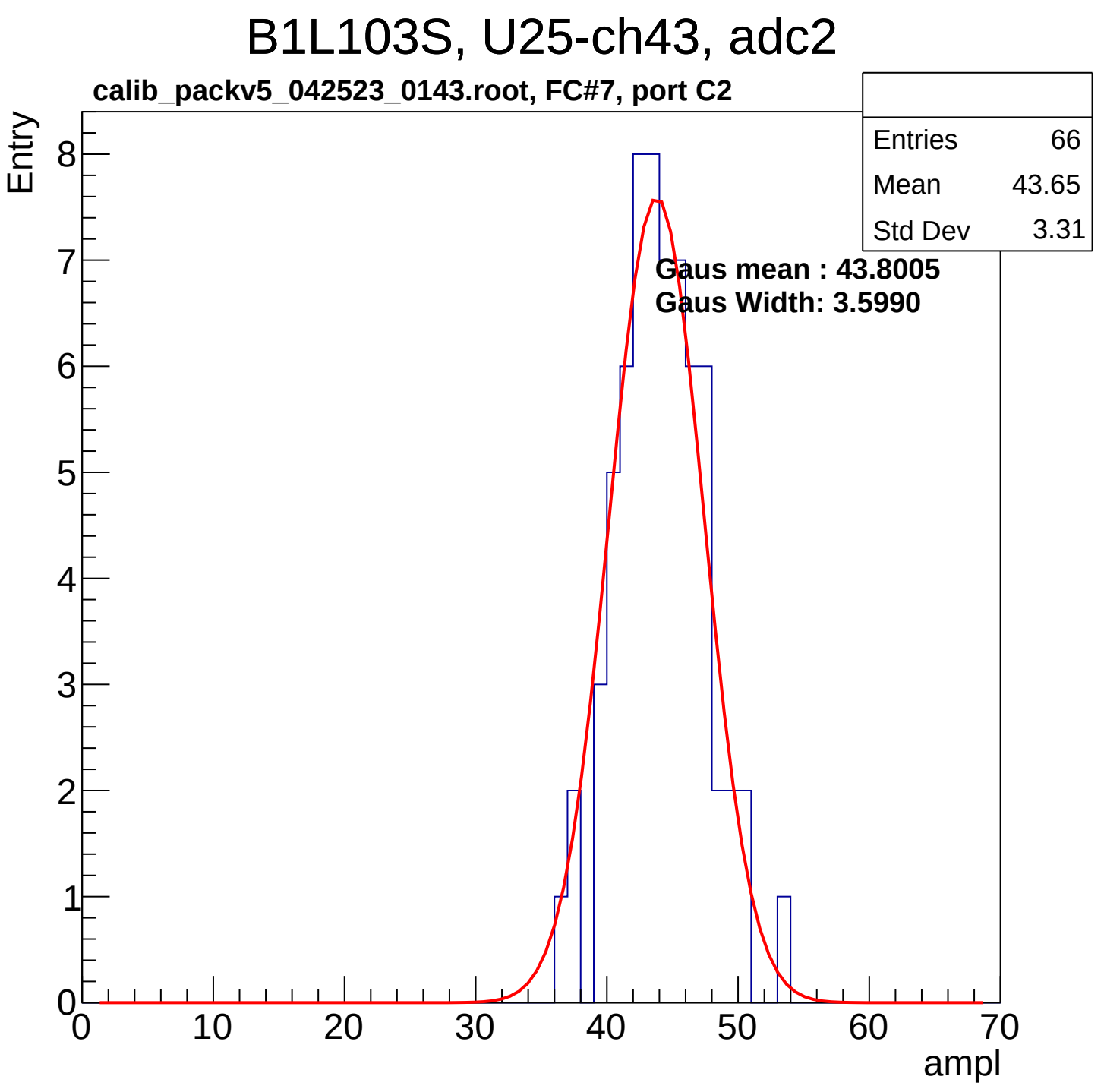
B1L103S, U25-ch43, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 43.65
Std Dev 3.31
Gaus mean : 43.8005
Gaus Width: 3.5990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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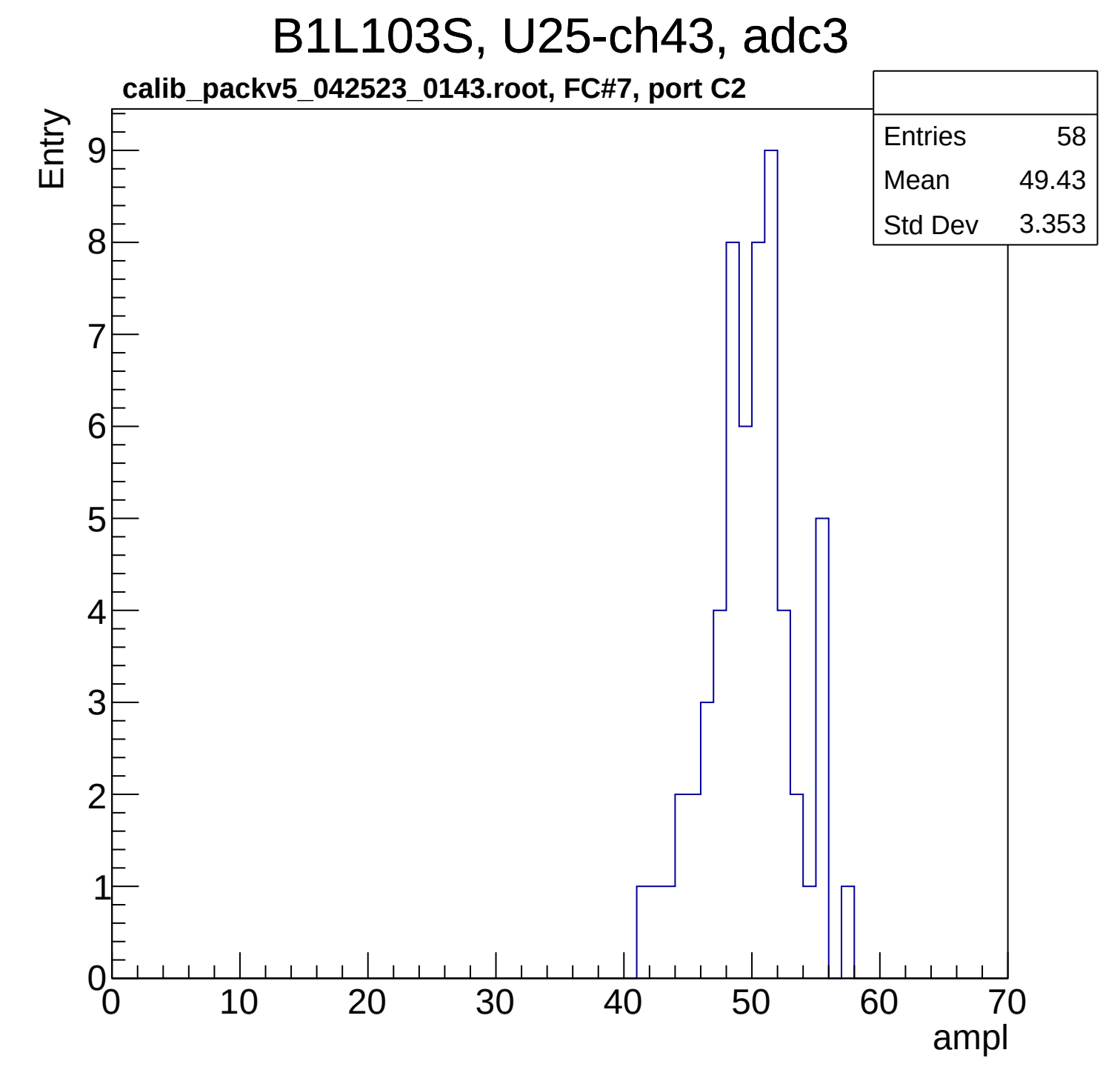
B1L103S, U25-ch43, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 49.43
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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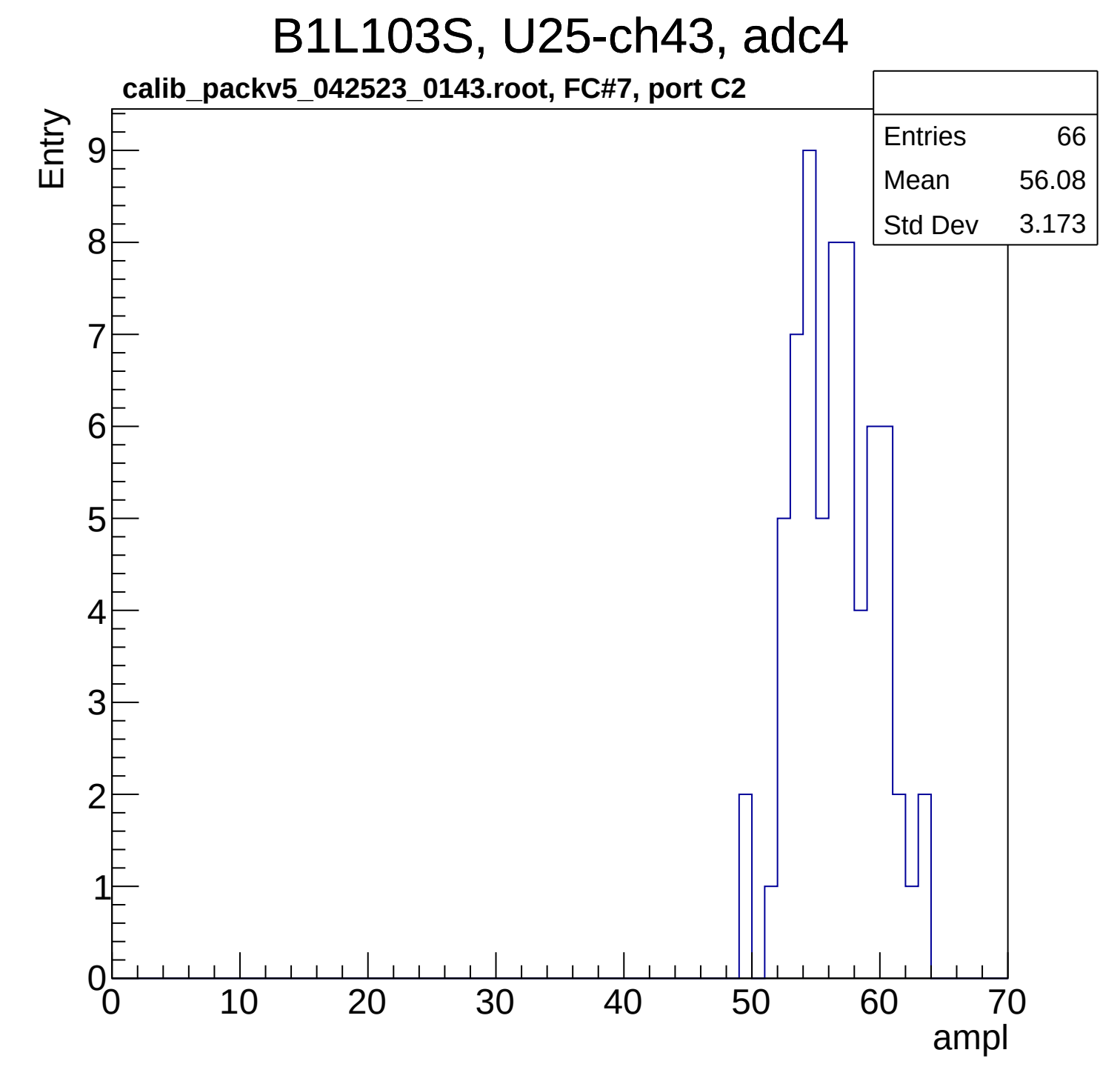
B1L103S, U25-ch43, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 56.08
Std Dev 3.173
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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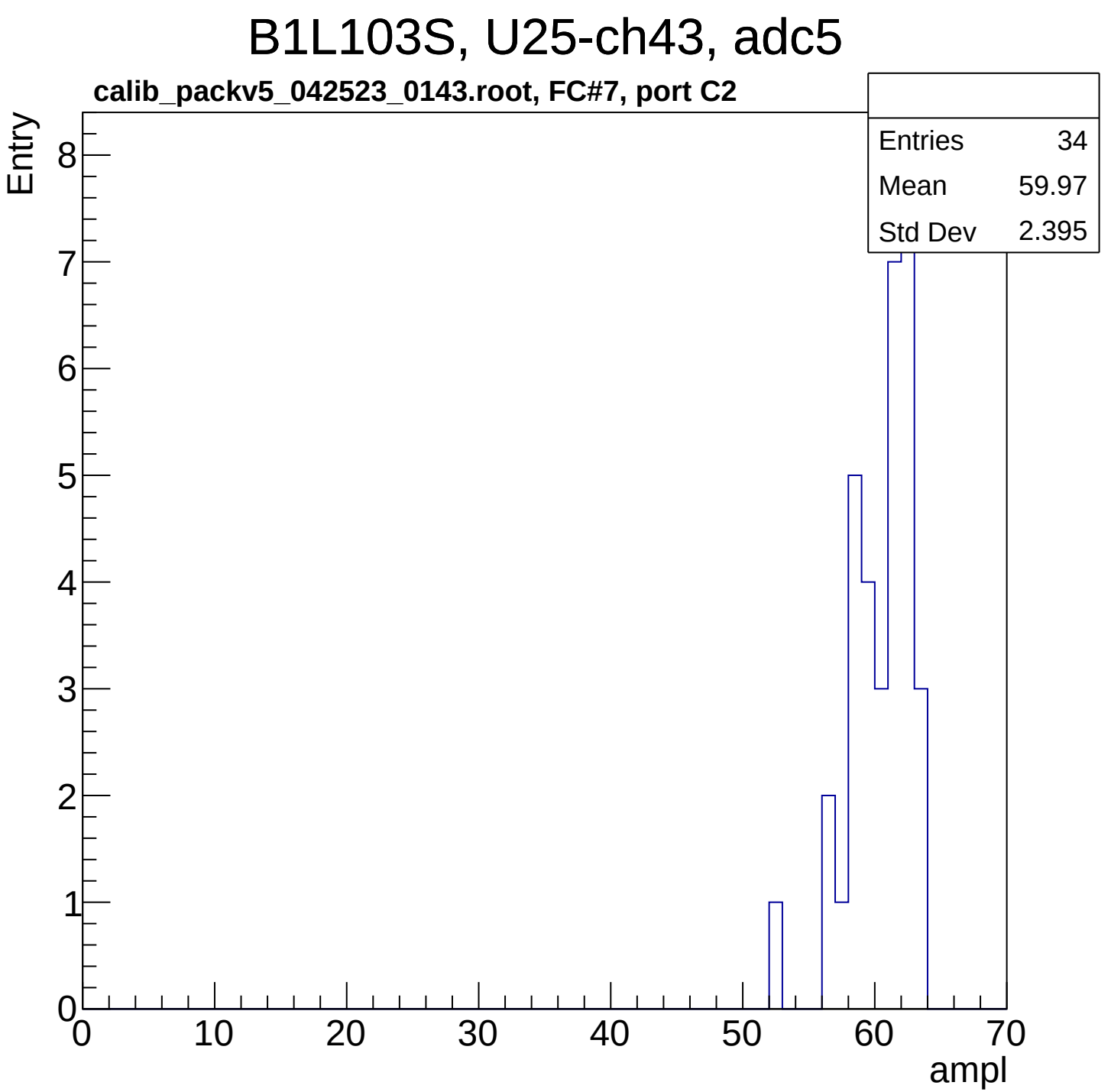
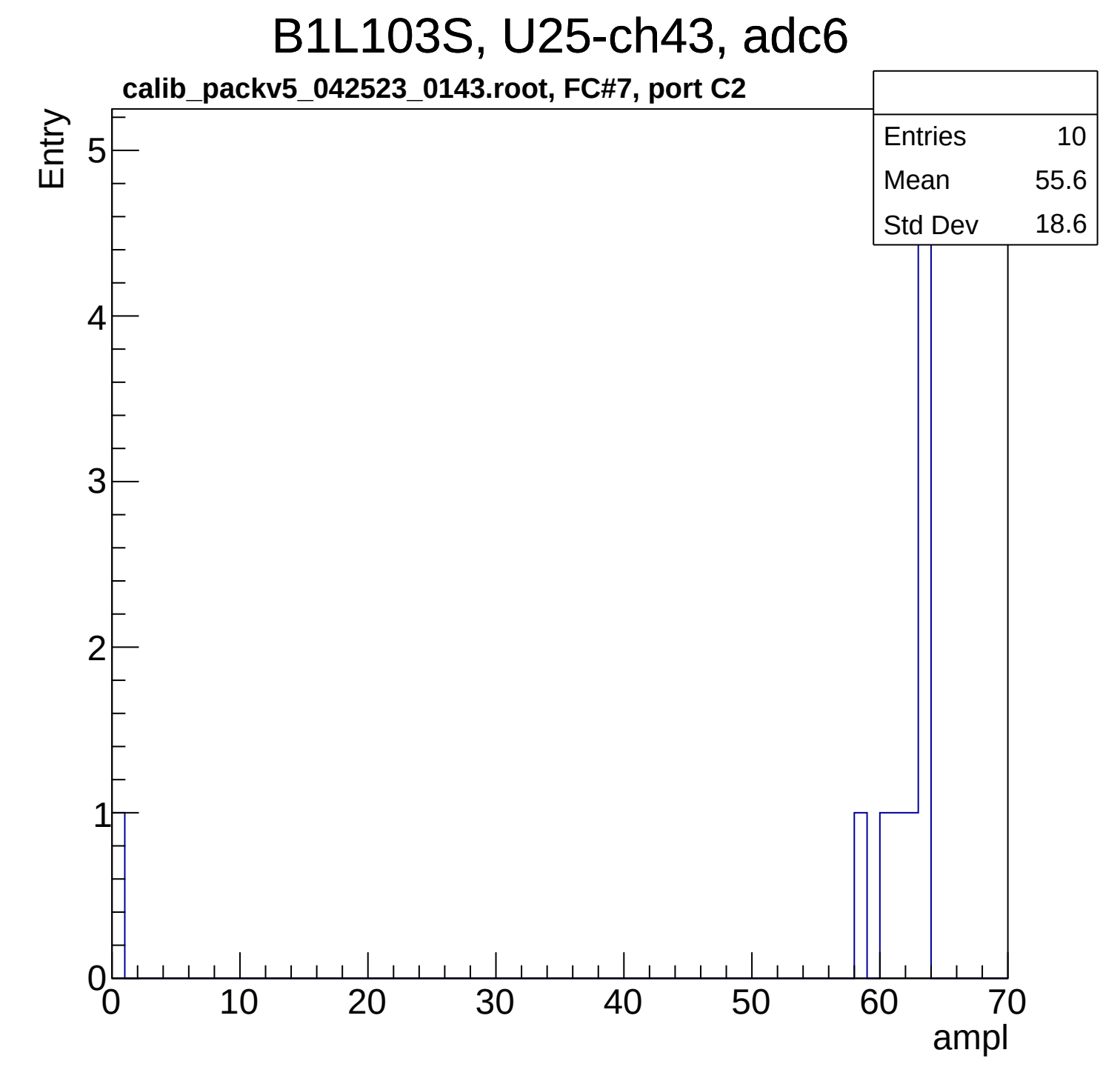
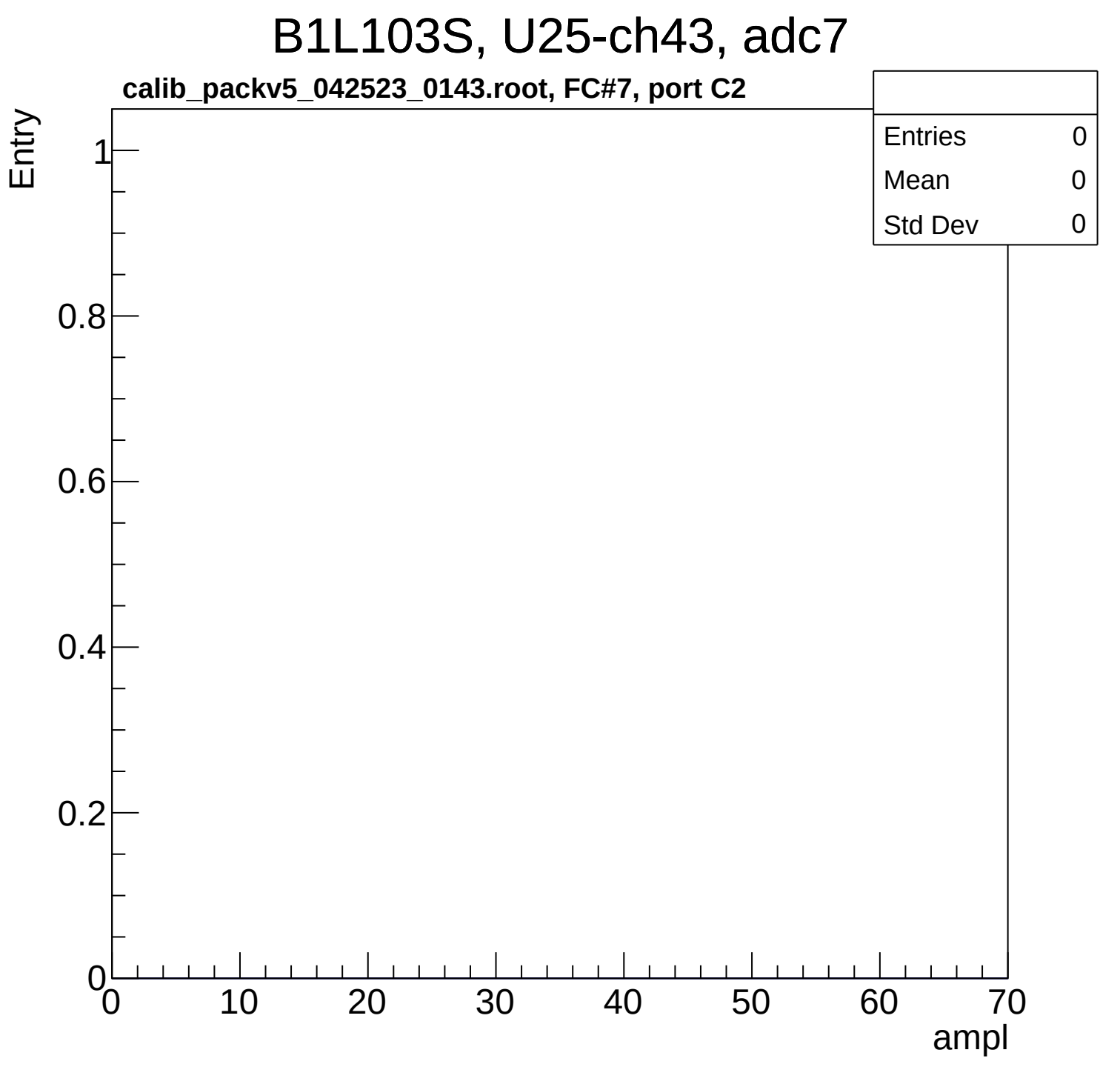
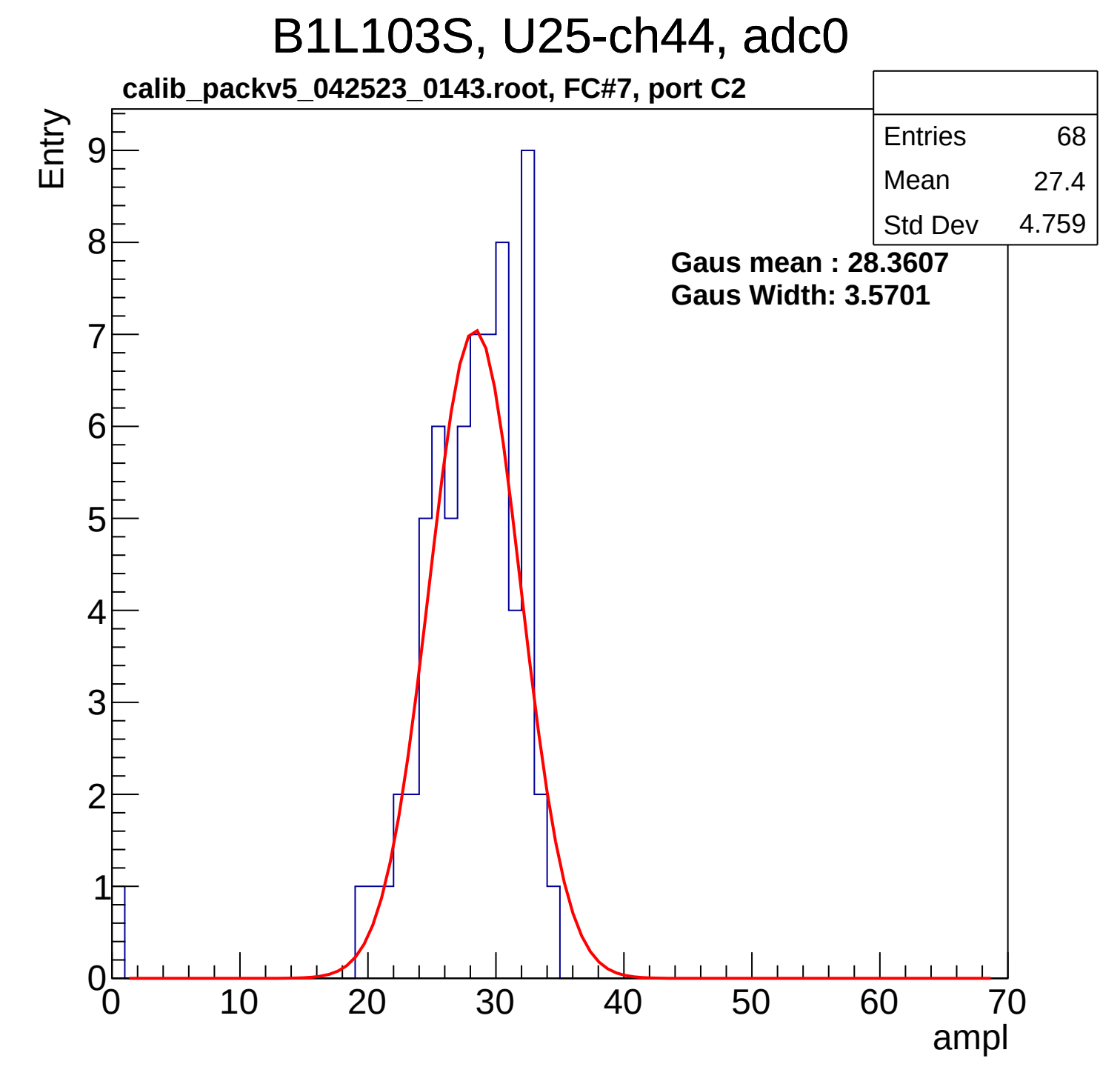

B1L103S, U25-ch43, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 59.97
Std Dev 2.395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch43, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 55.6
Std Dev 18.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch43, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch44, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 27.4
Std Dev 4.759
Gaus mean : 28.3607
Gaus Width: 3.5701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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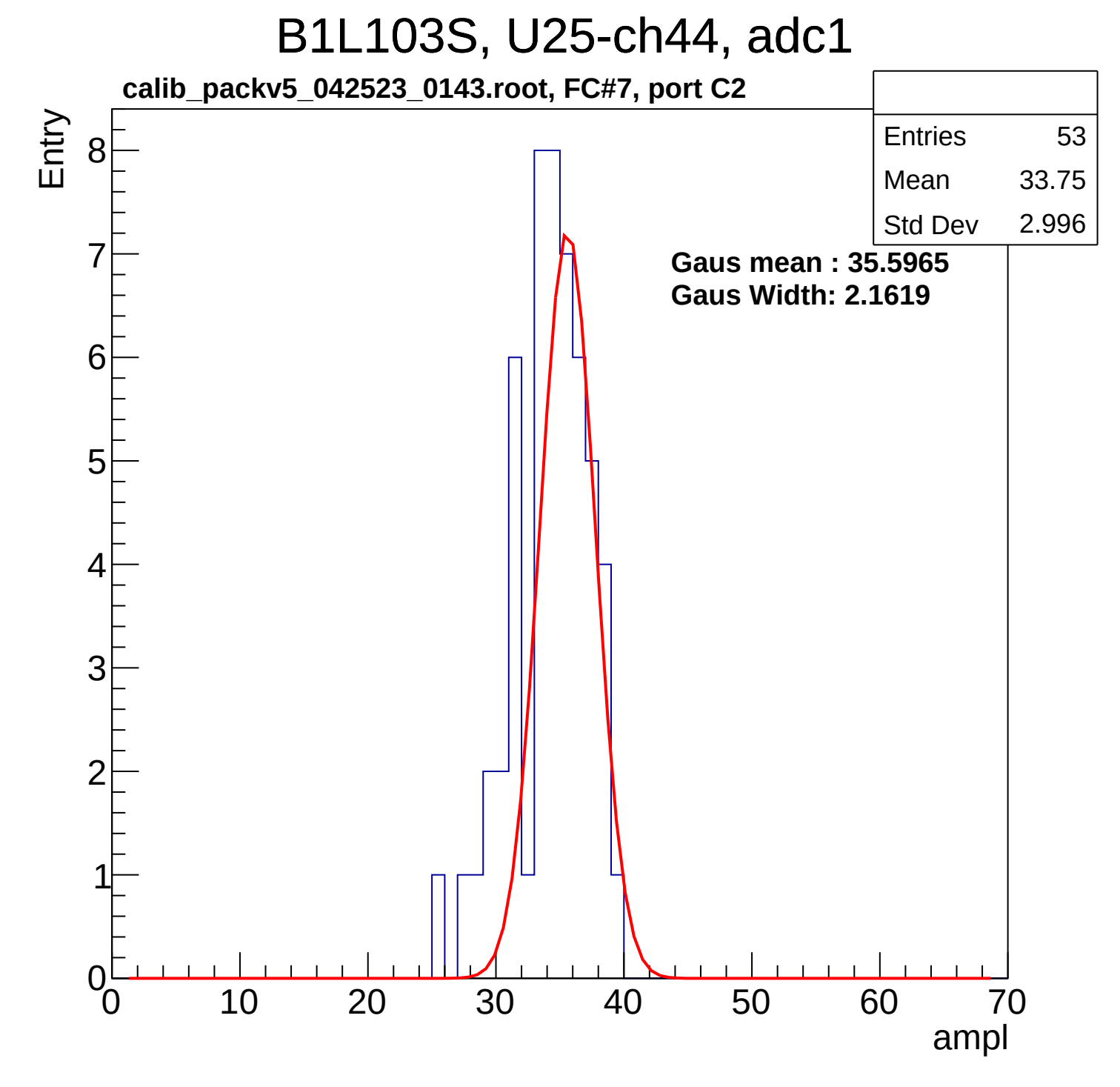
B1L103S, U25-ch44, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 33.75
Std Dev 2.996
Gaus mean : 35.5965
Gaus Width: 2.1619
Entry
ampl
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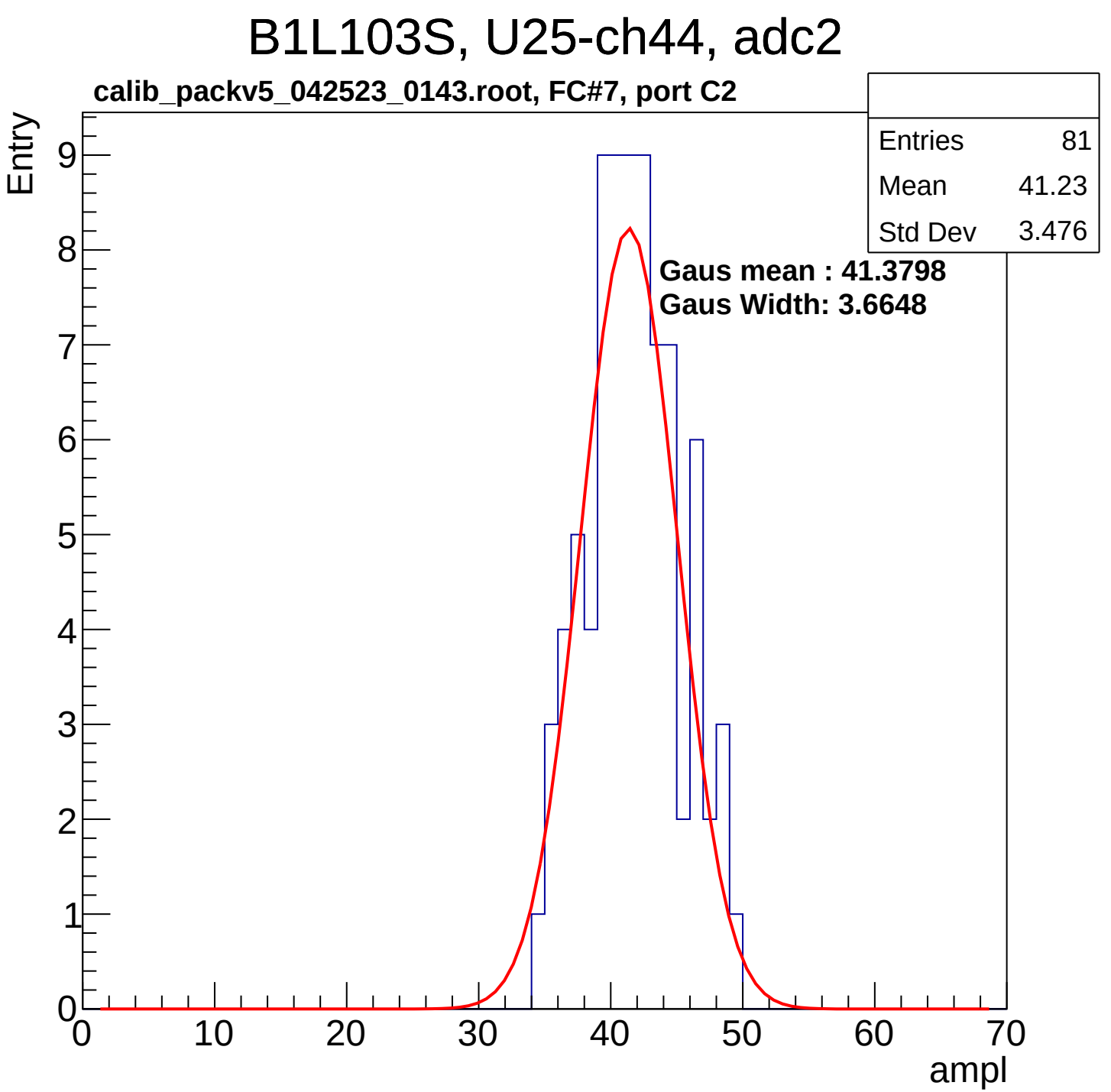
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch44, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 41.23
Std Dev 3.476
Gaus mean : 41.3798
Gaus Width: 3.6648
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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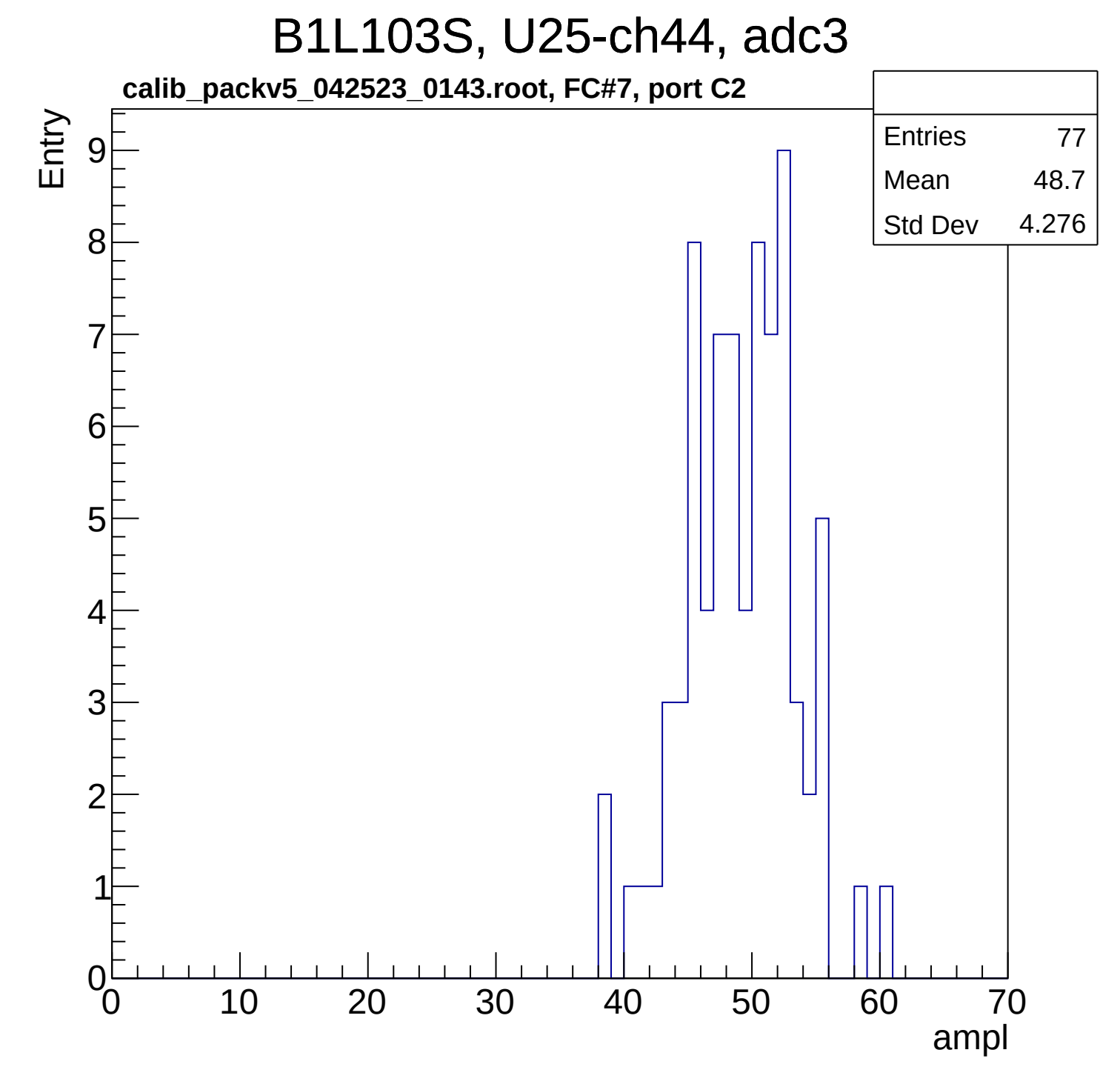
B1L103S, U25-ch44, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 48.7
Std Dev 4.276
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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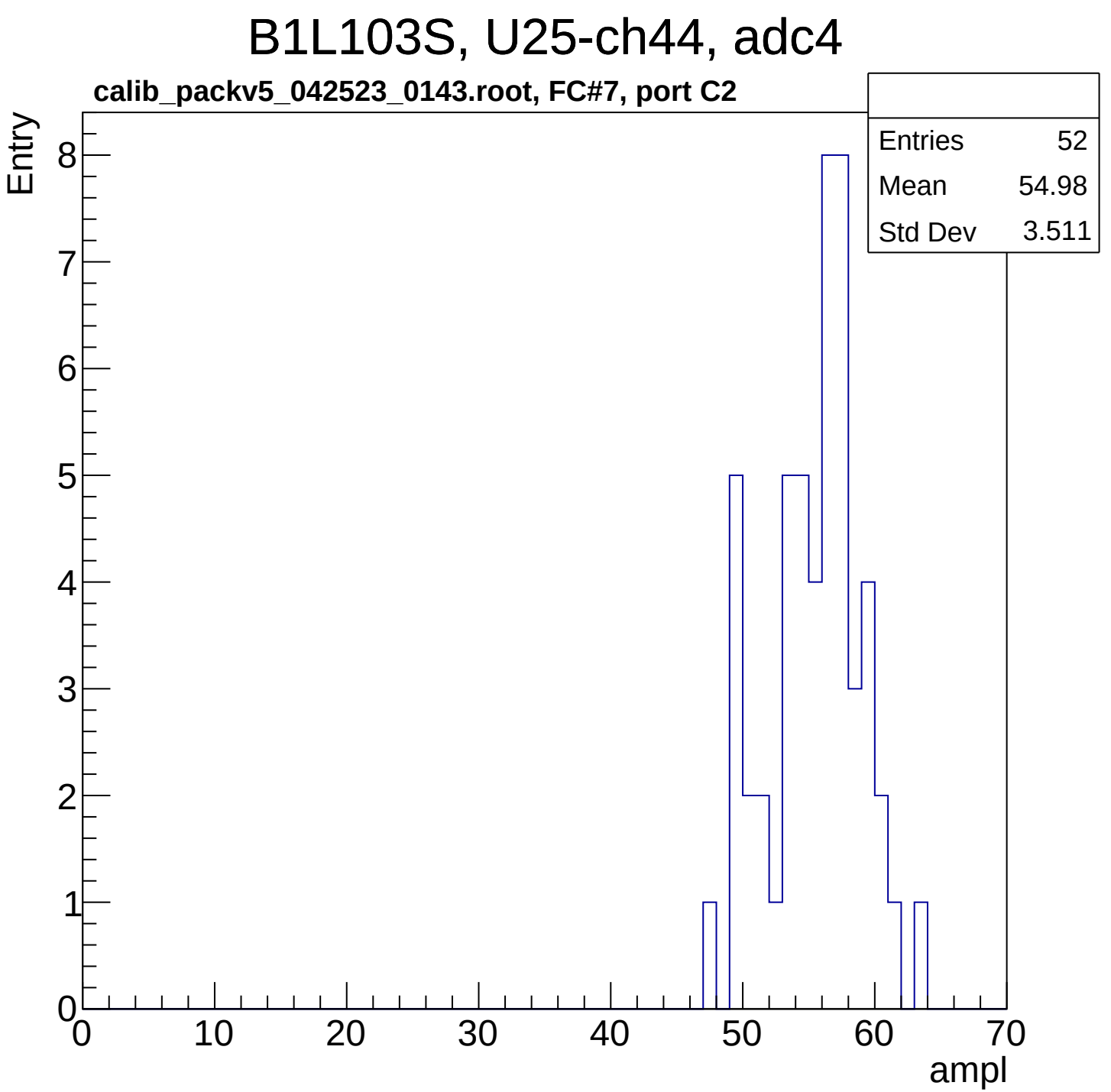
B1L103S, U25-ch44, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 54.98
Std Dev 3.511
Entry
ampl
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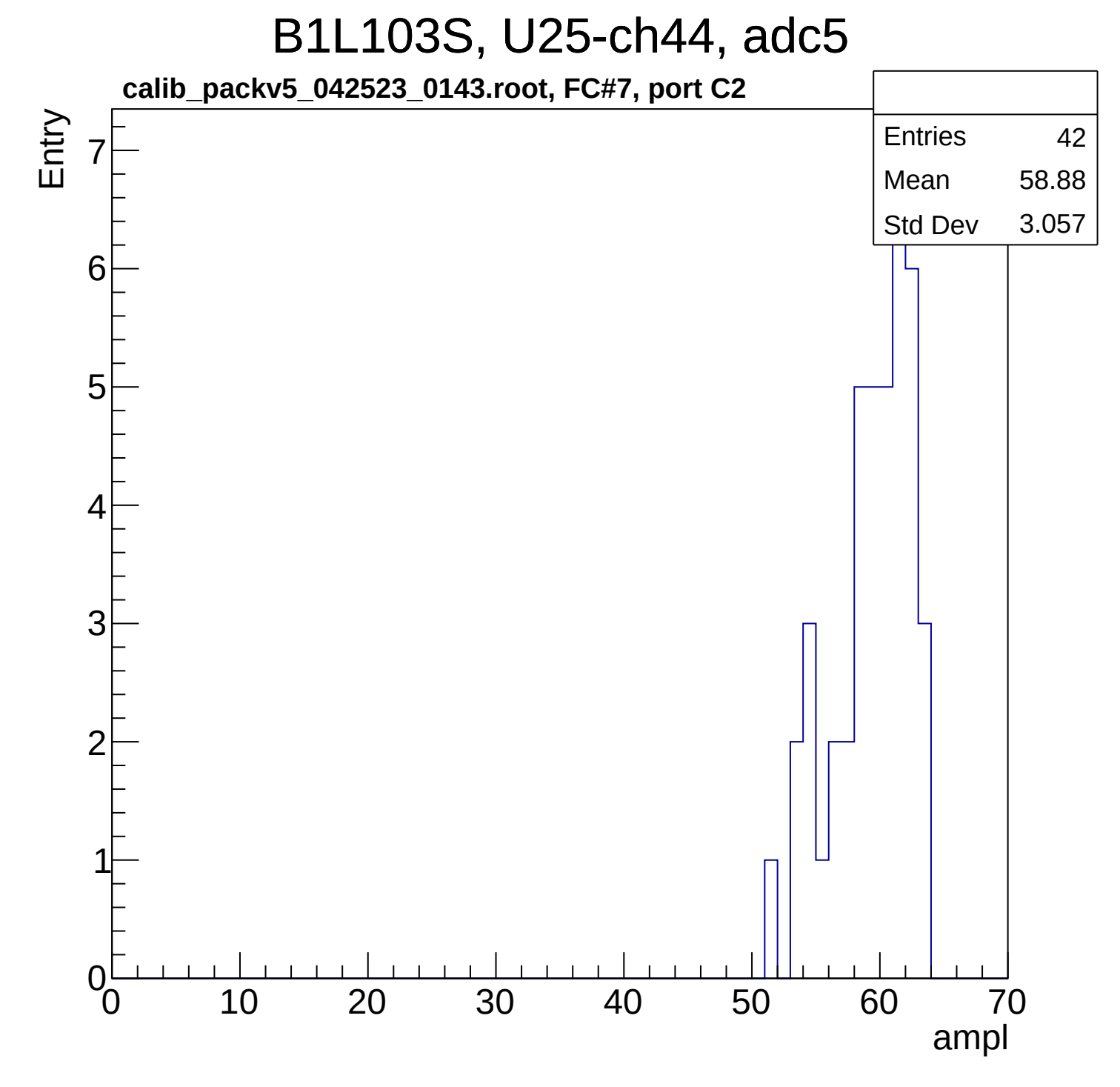
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch44, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.88
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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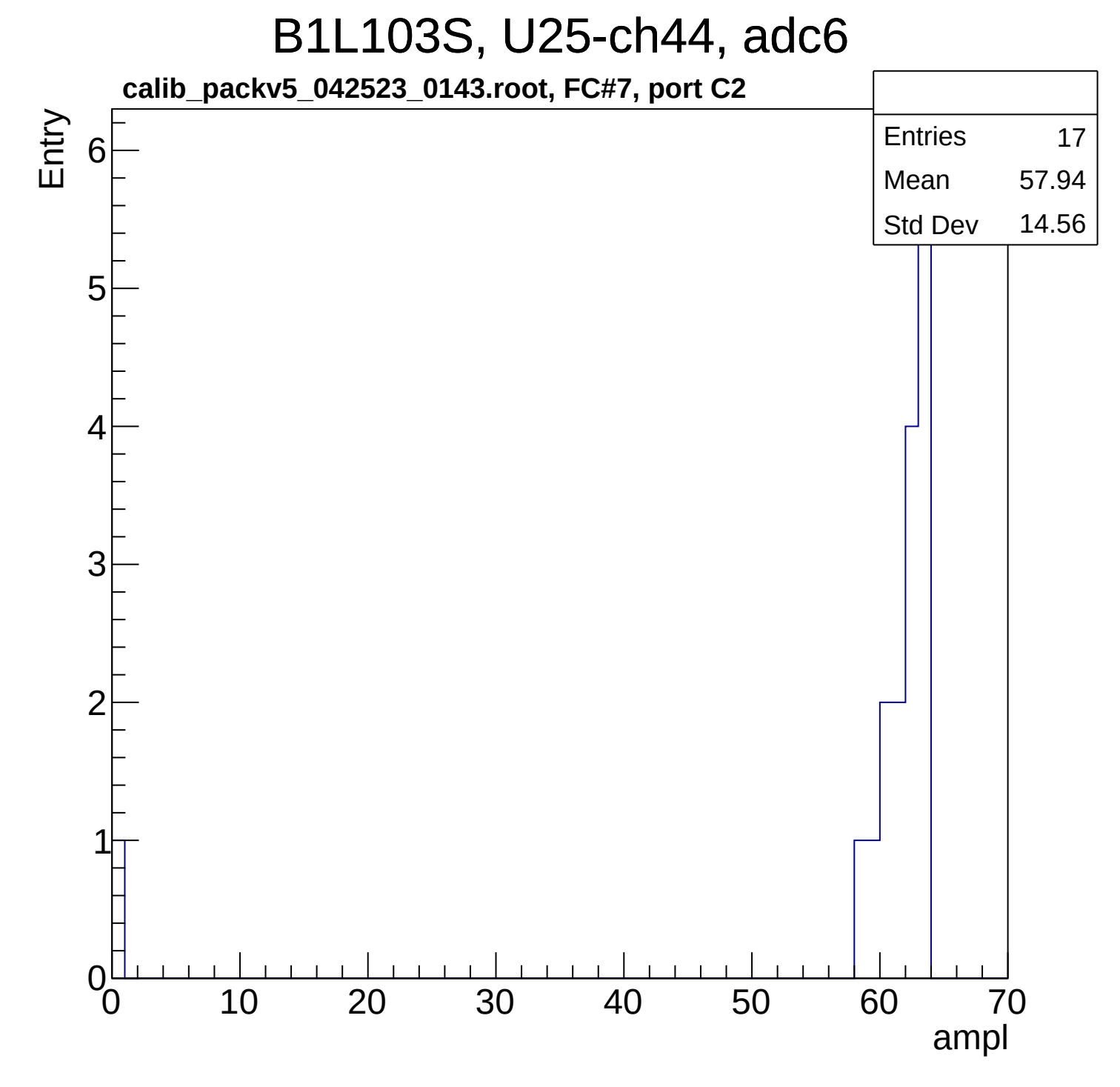
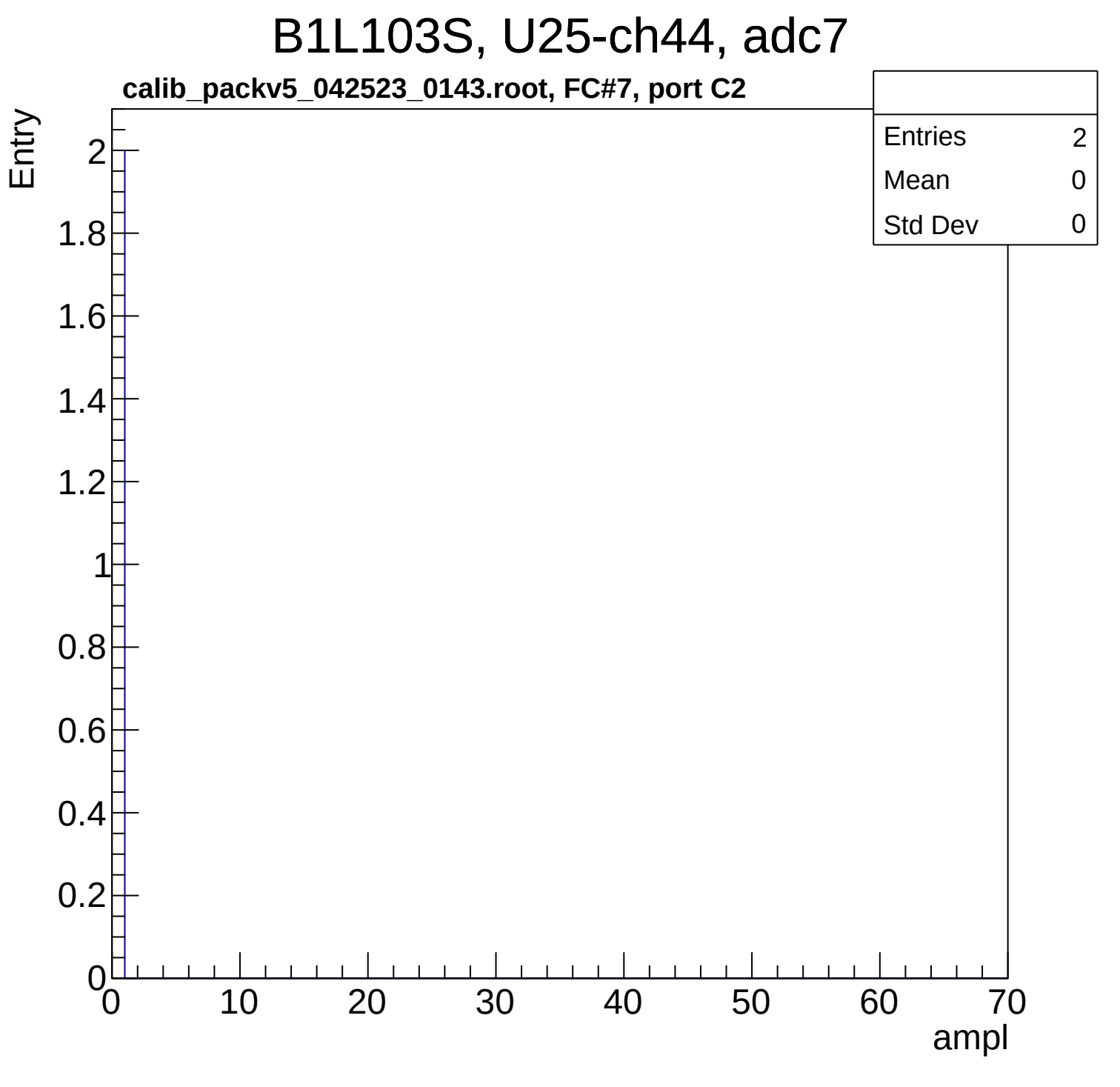

B1L103S, U25-ch44, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 57.94
Std Dev 14.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch44, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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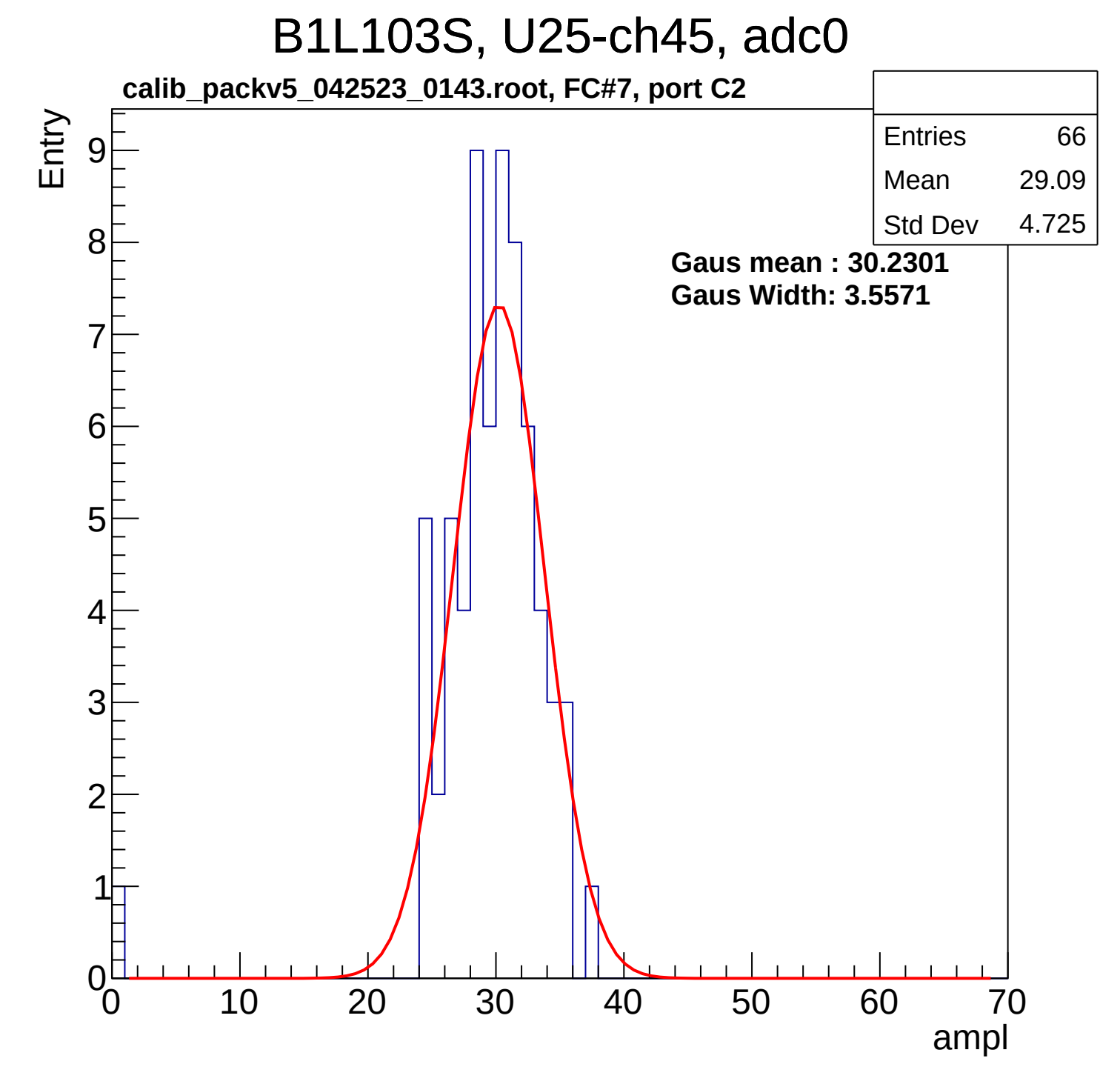
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch45, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.09
Std Dev 4.725
Gaus mean : 30.2301
Gaus Width: 3.5571
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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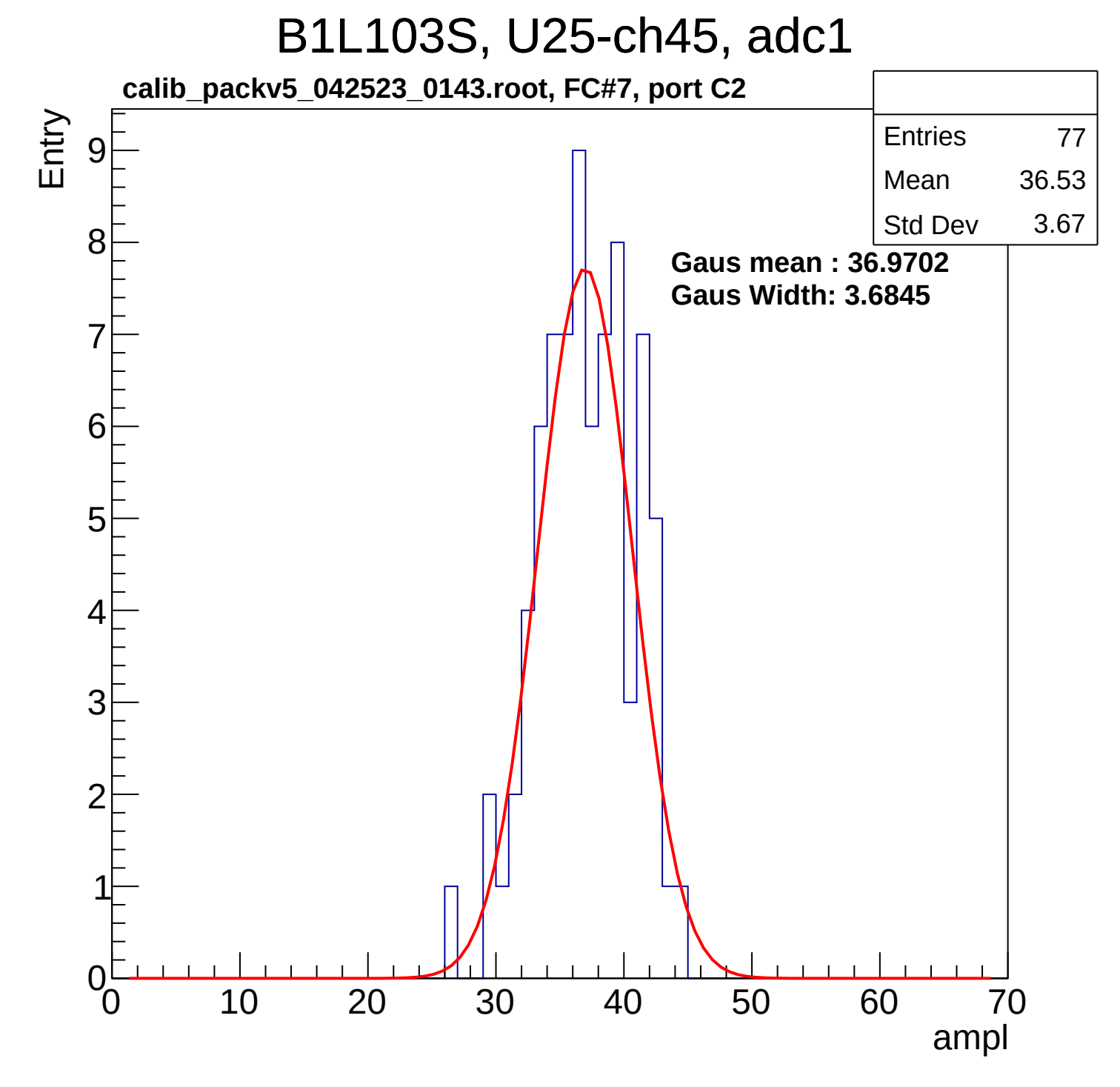
B1L103S, U25-ch45, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 36.53
Std Dev 3.67
Gaus mean : 36.9702
Gaus Width: 3.6845
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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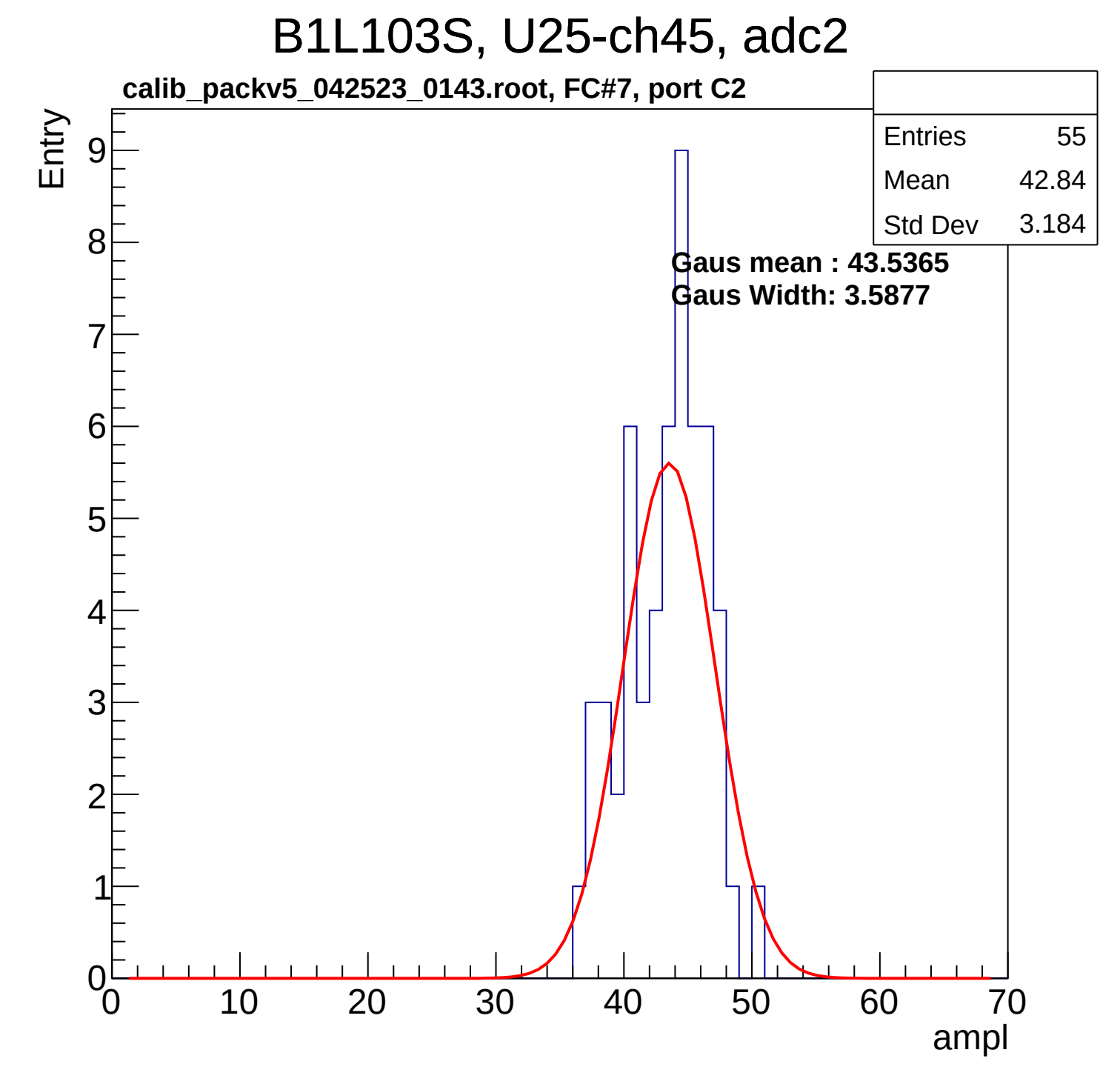
B1L103S, U25-ch45, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 42.84
Std Dev 3.184
Gaus mean : 43.5365
Gaus Width: 3.5877
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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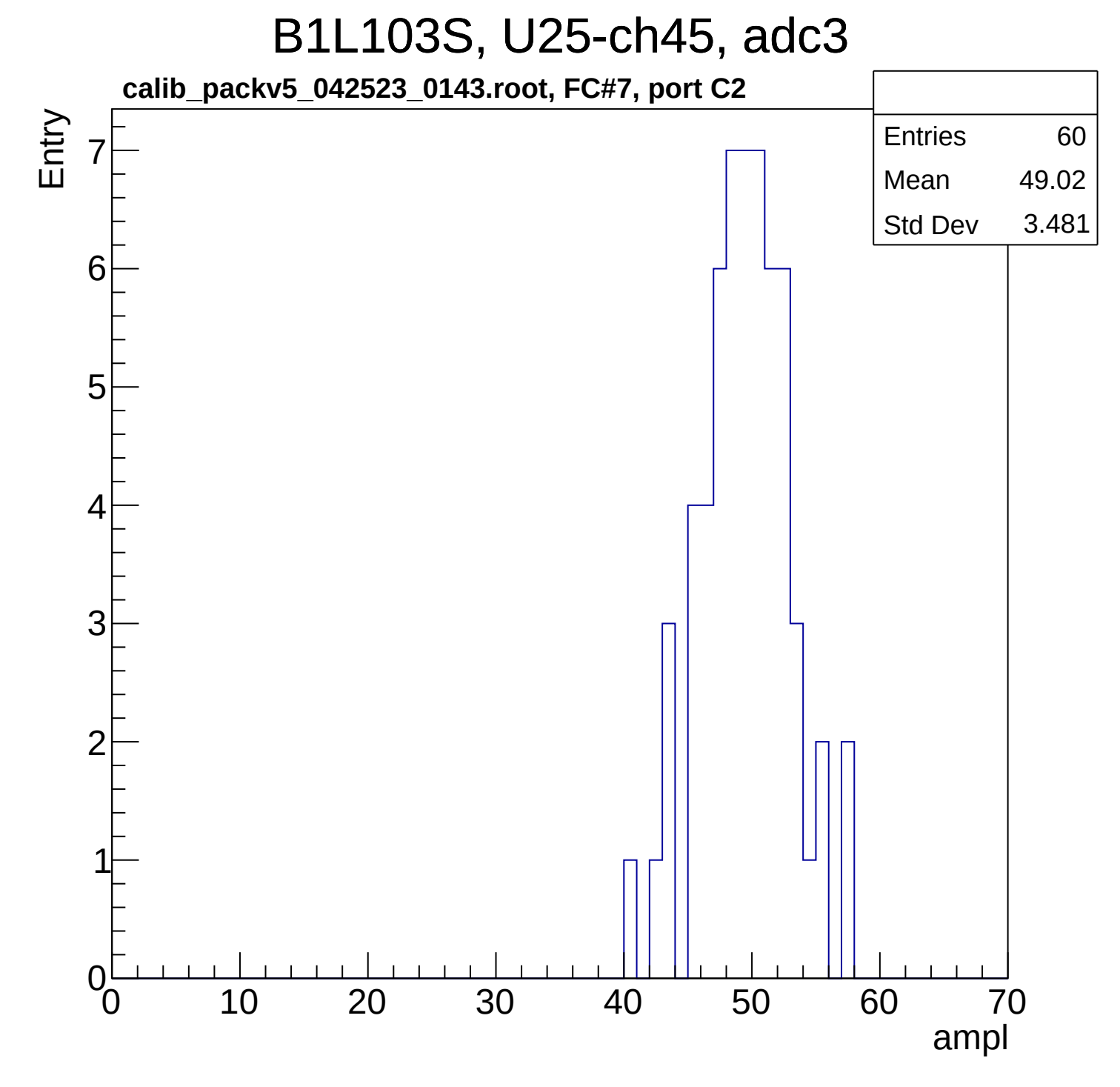
B1L103S, U25-ch45, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.02
Std Dev 3.481
Entry
ampl
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0
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60
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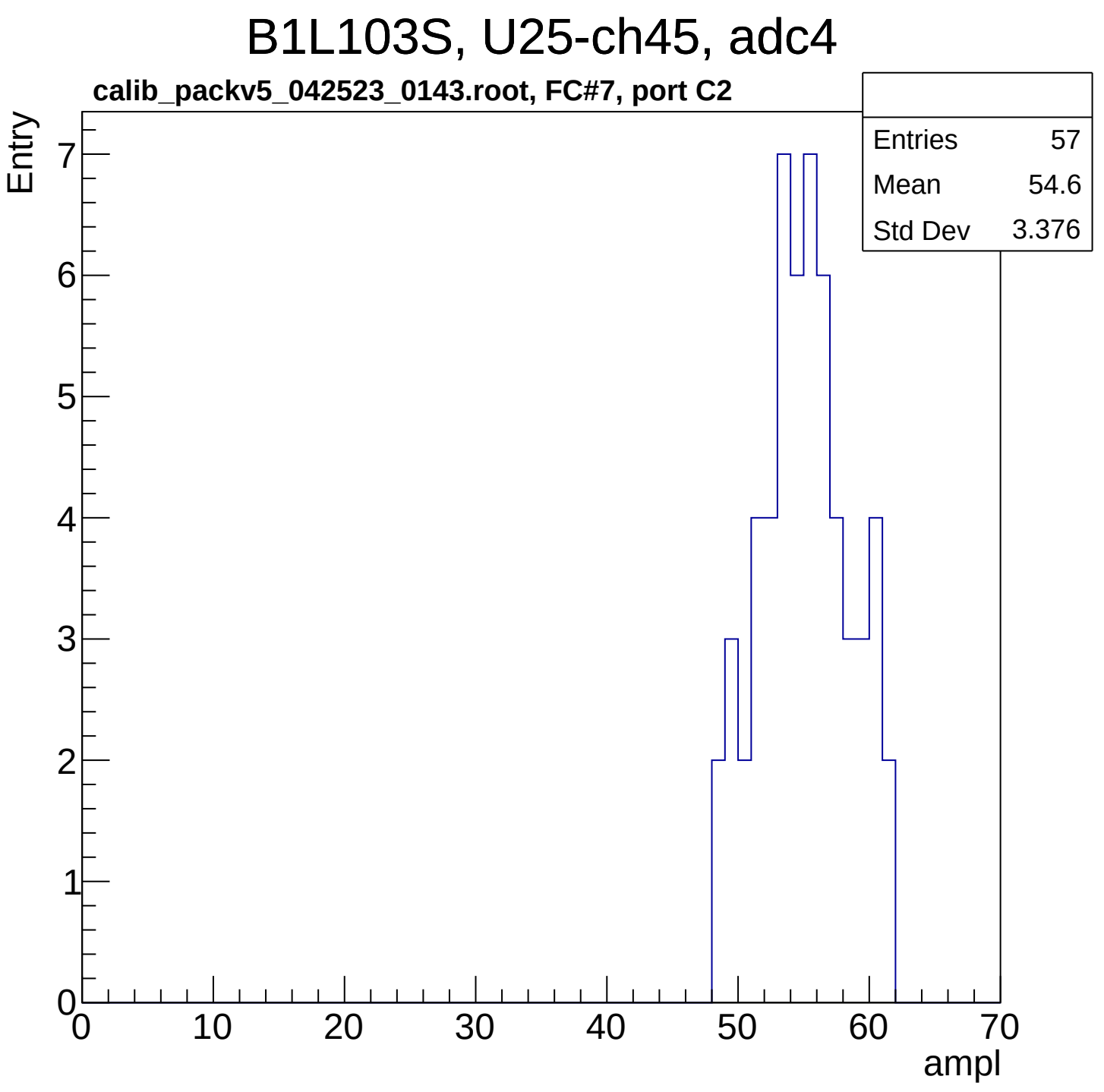
B1L103S, U25-ch45, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 54.6
Std Dev 3.376
Entry
ampl
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4
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0
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60
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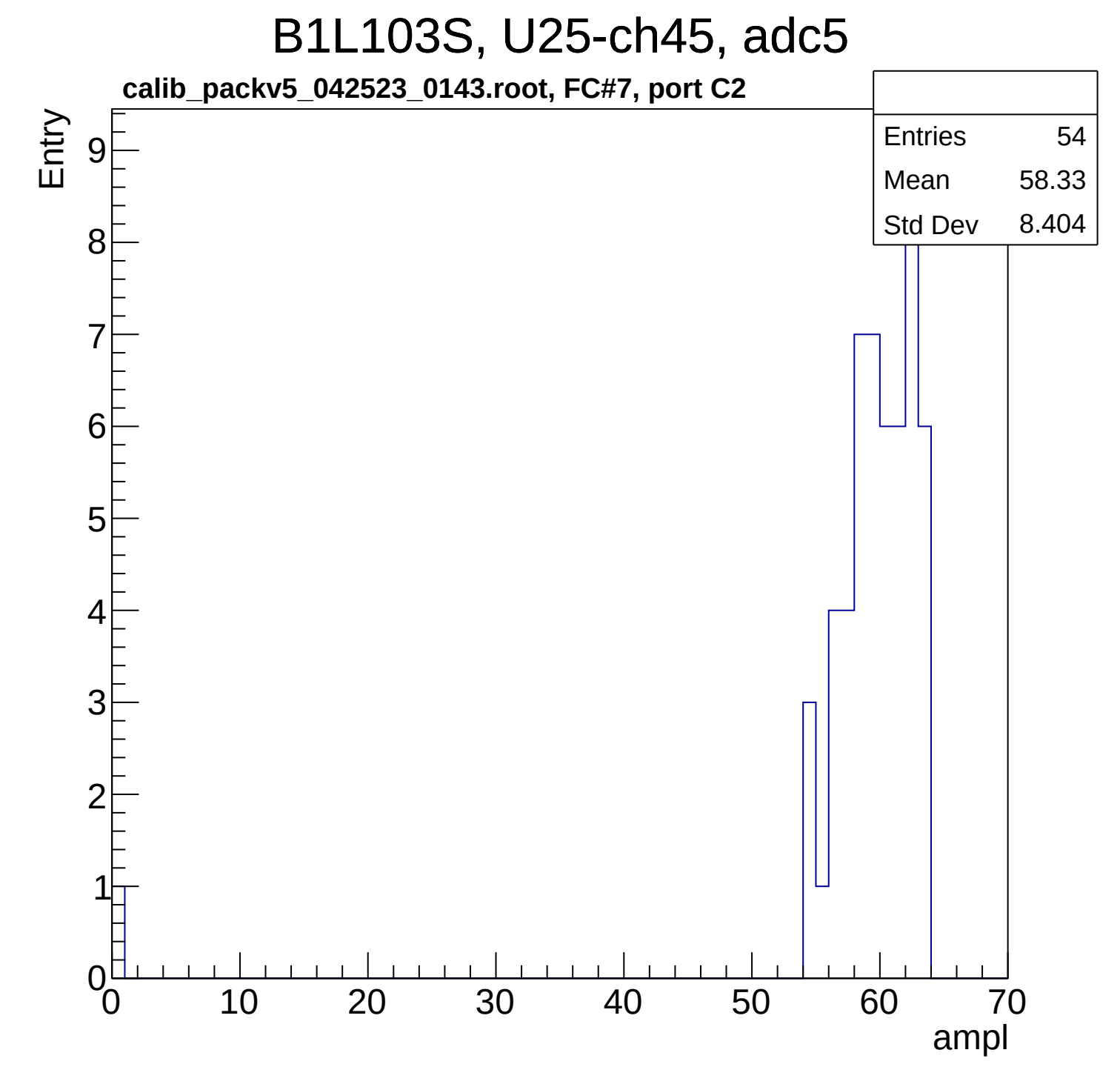
B1L103S, U25-ch45, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.33
Std Dev 8.404
Entry
ampl
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3
4
5
6
7
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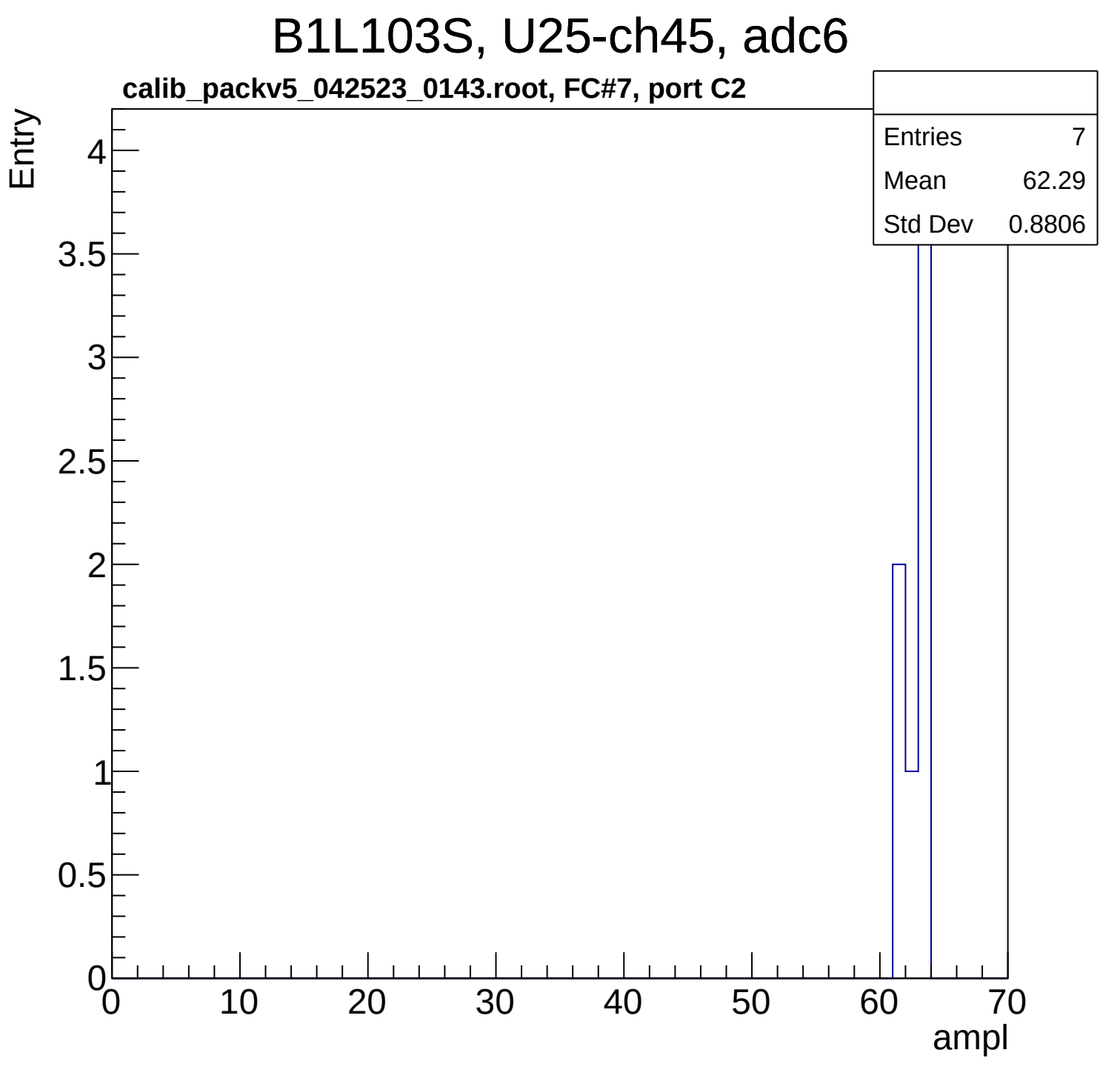
B1L103S, U25-ch45, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 62.29
Std Dev 0.8806
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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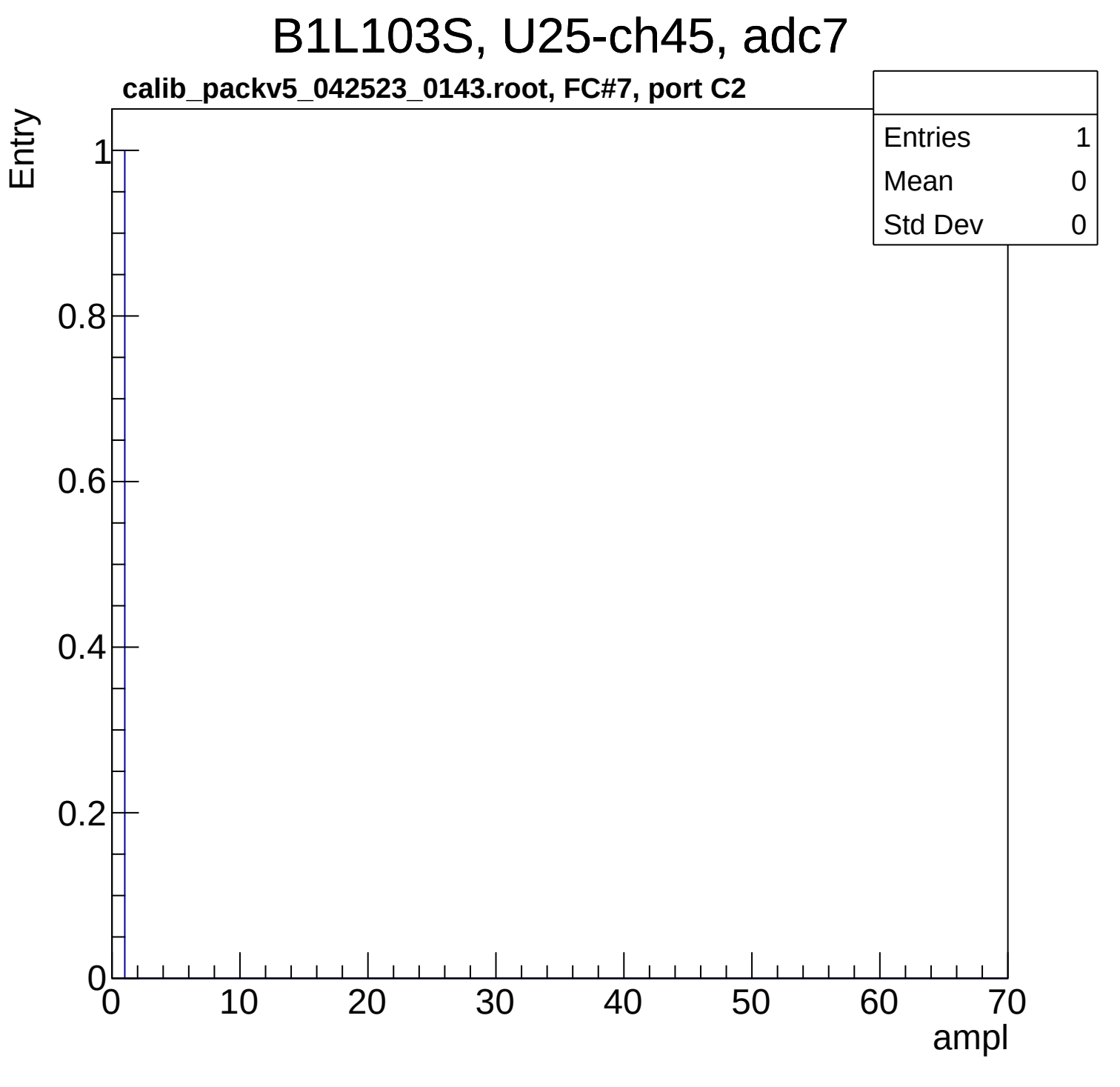
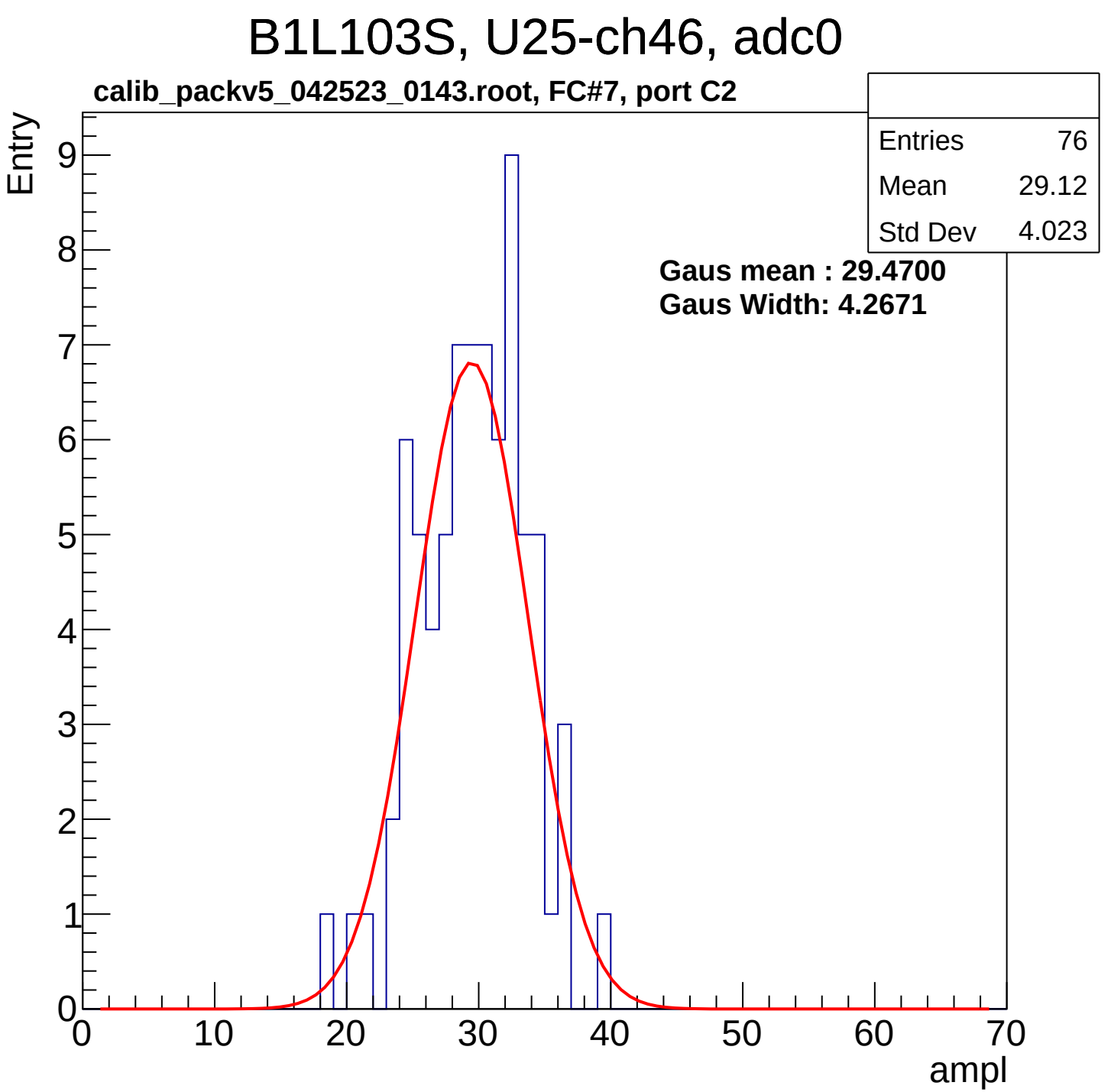
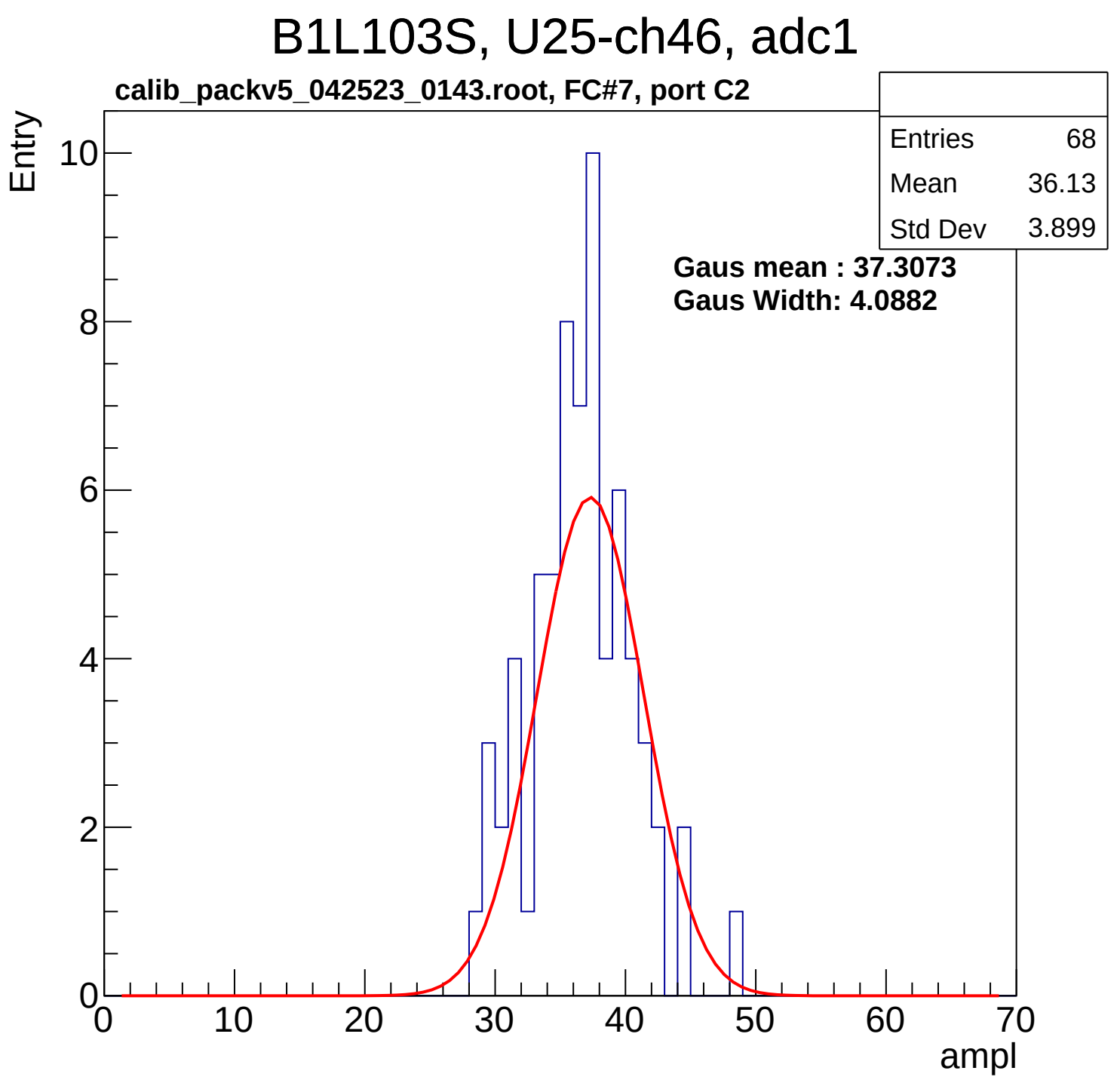
50
60
70


B1L103S, U25-ch45, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch46, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.12
Std Dev 4.023
Gaus mean : 29.4700
Gaus Width: 4.2671
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch46, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 36.13
Std Dev 3.899
Gaus mean : 37.3073
Gaus Width: 4.0882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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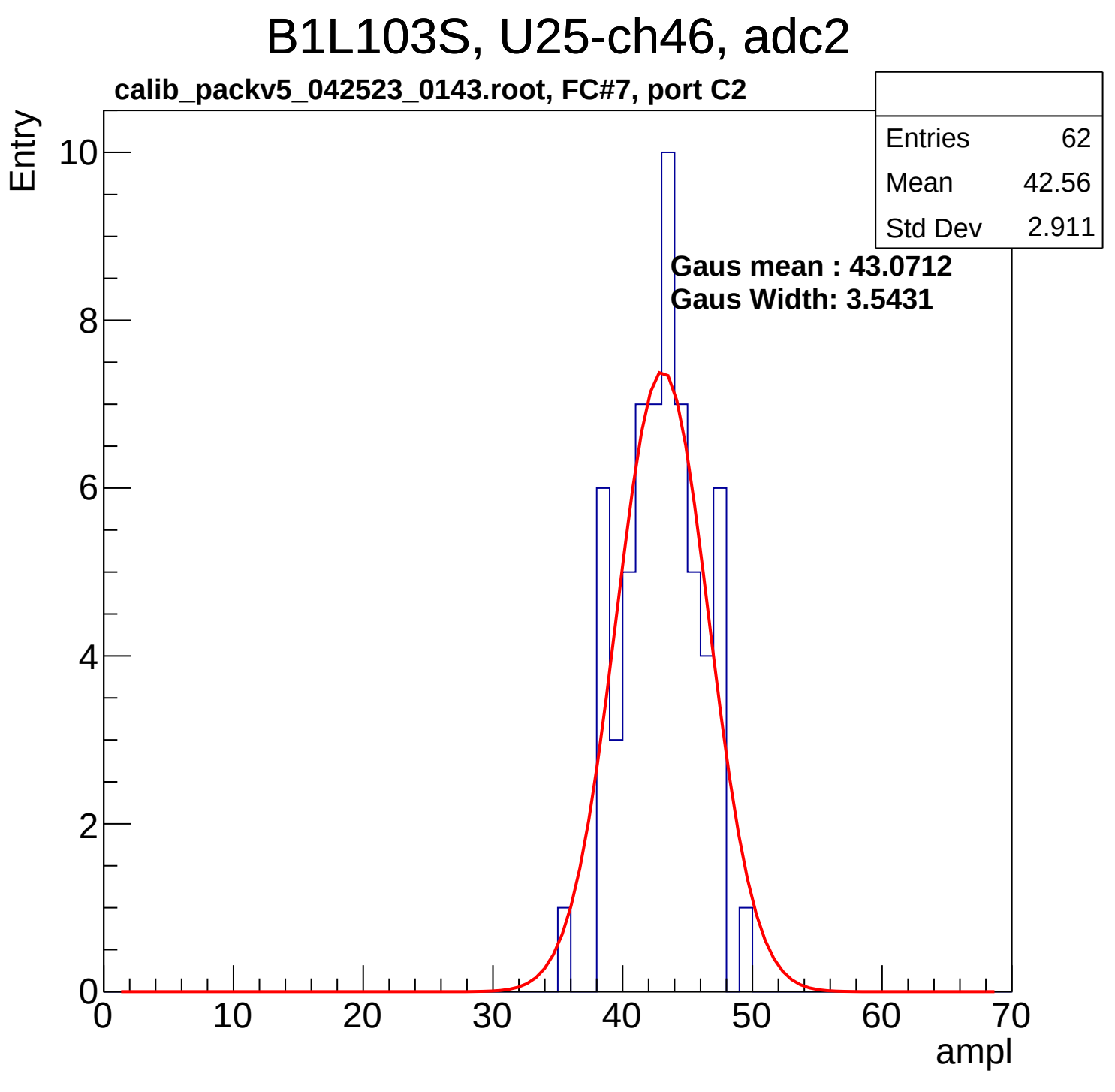
B1L103S, U25-ch46, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.56
Std Dev 2.911
Gaus mean : 43.0712
Gaus Width: 3.5431
Entry
ampl
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4
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8
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10
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40
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60
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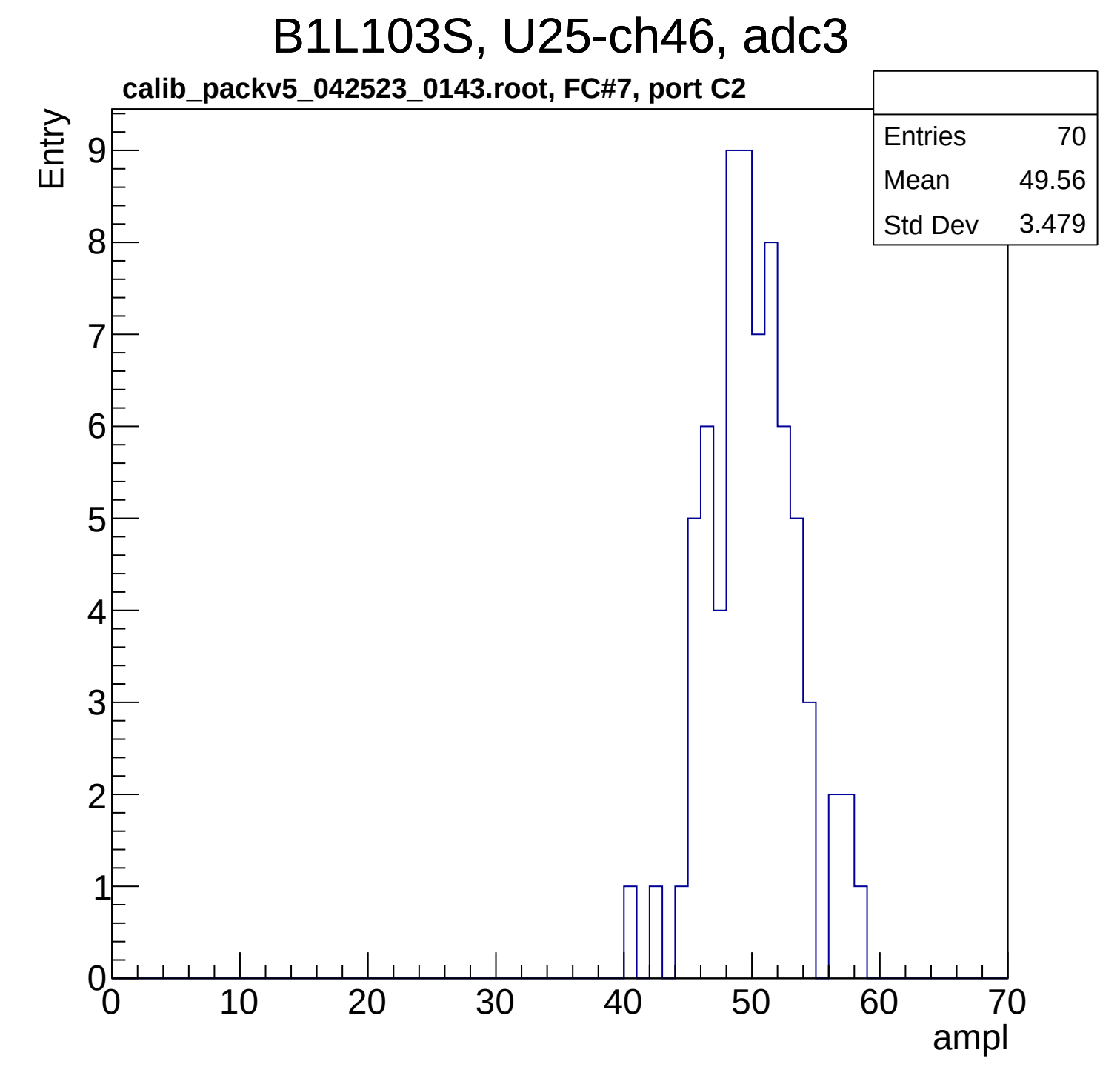
B1L103S, U25-ch46, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 49.56
Std Dev 3.479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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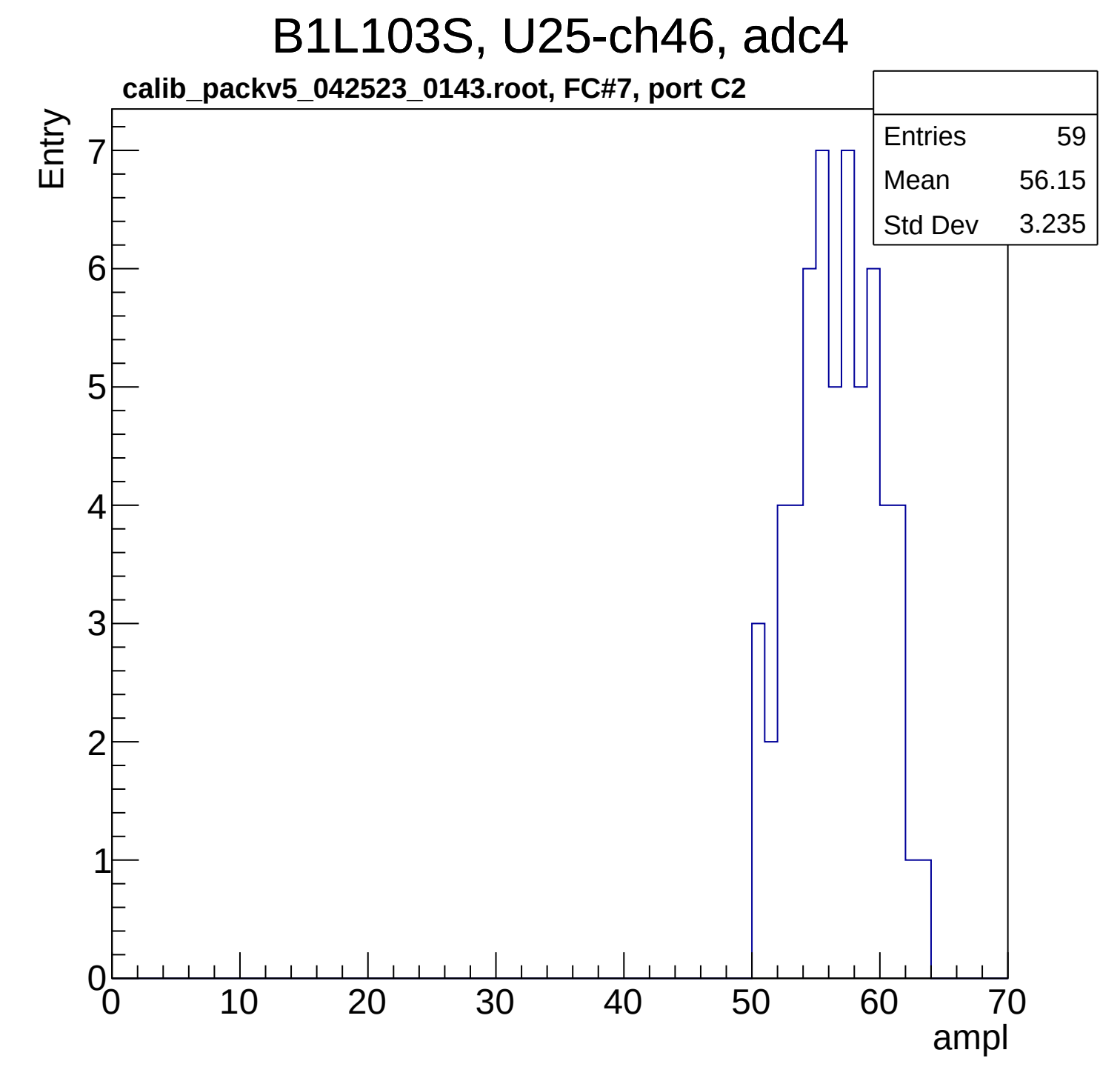
B1L103S, U25-ch46, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 56.15
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
0
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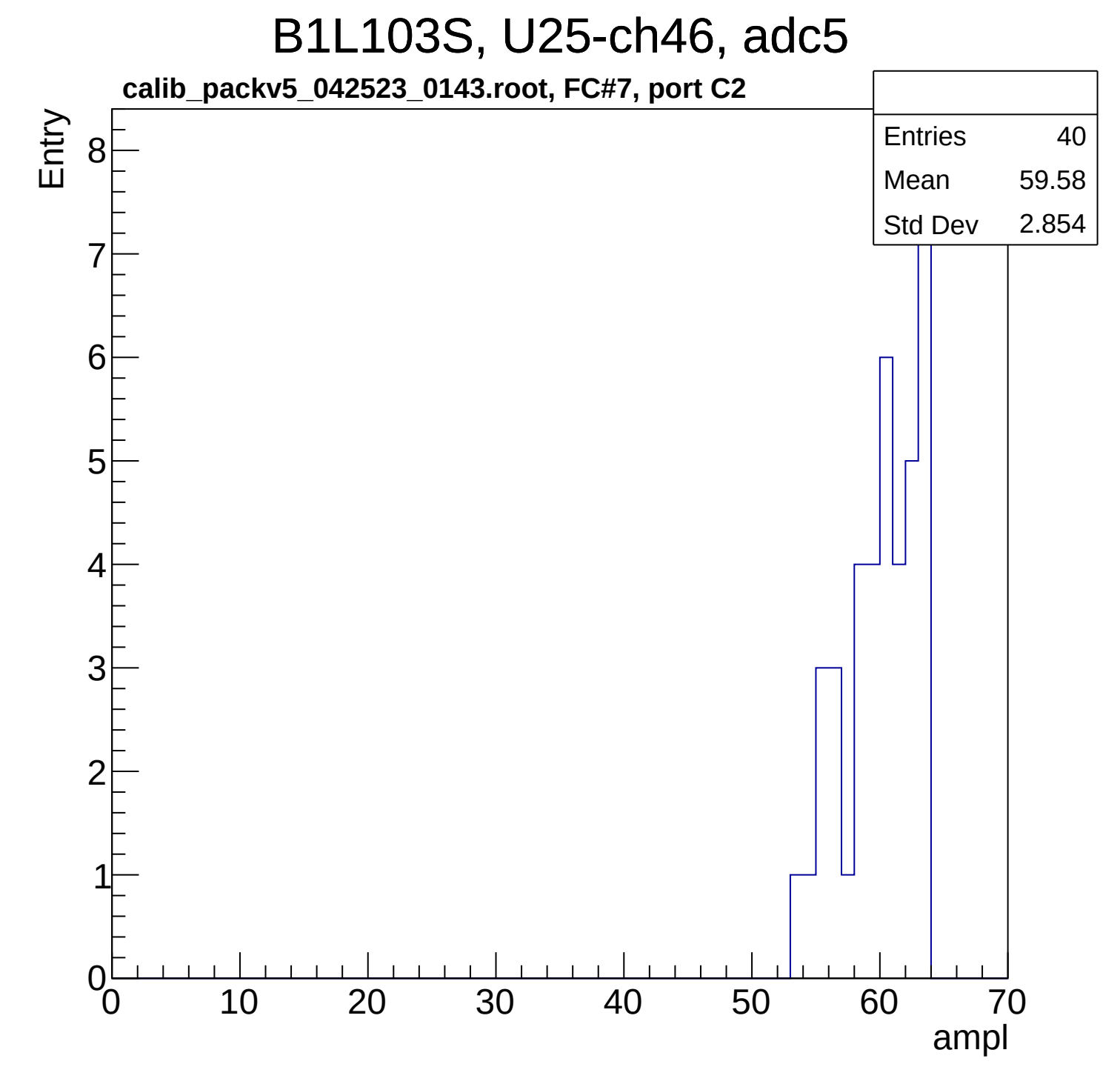
B1L103S, U25-ch46, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 59.58
Std Dev 2.854
Entry
ampl
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1
2
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5
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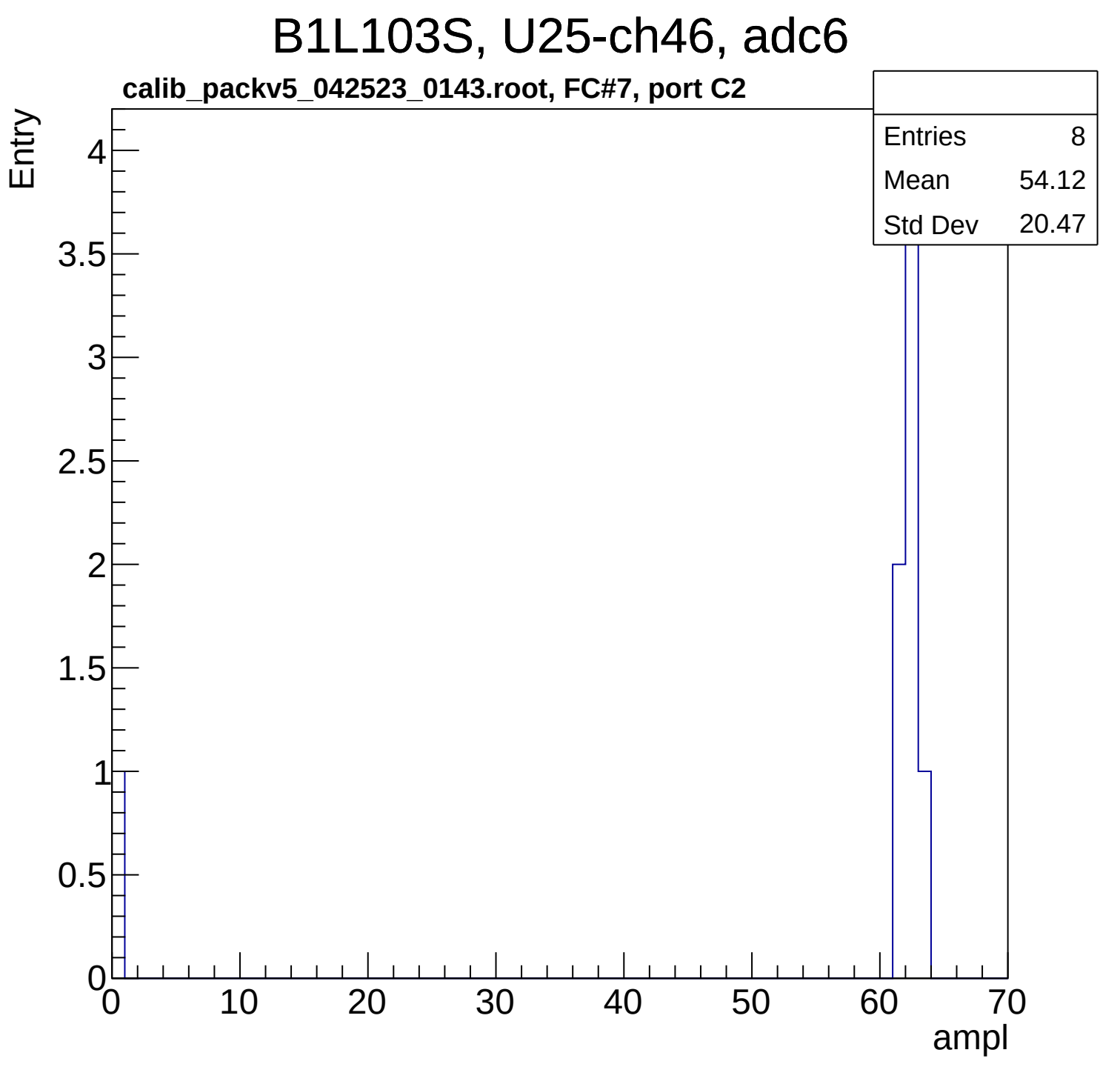
B1L103S, U25-ch46, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 54.12
Std Dev 20.47
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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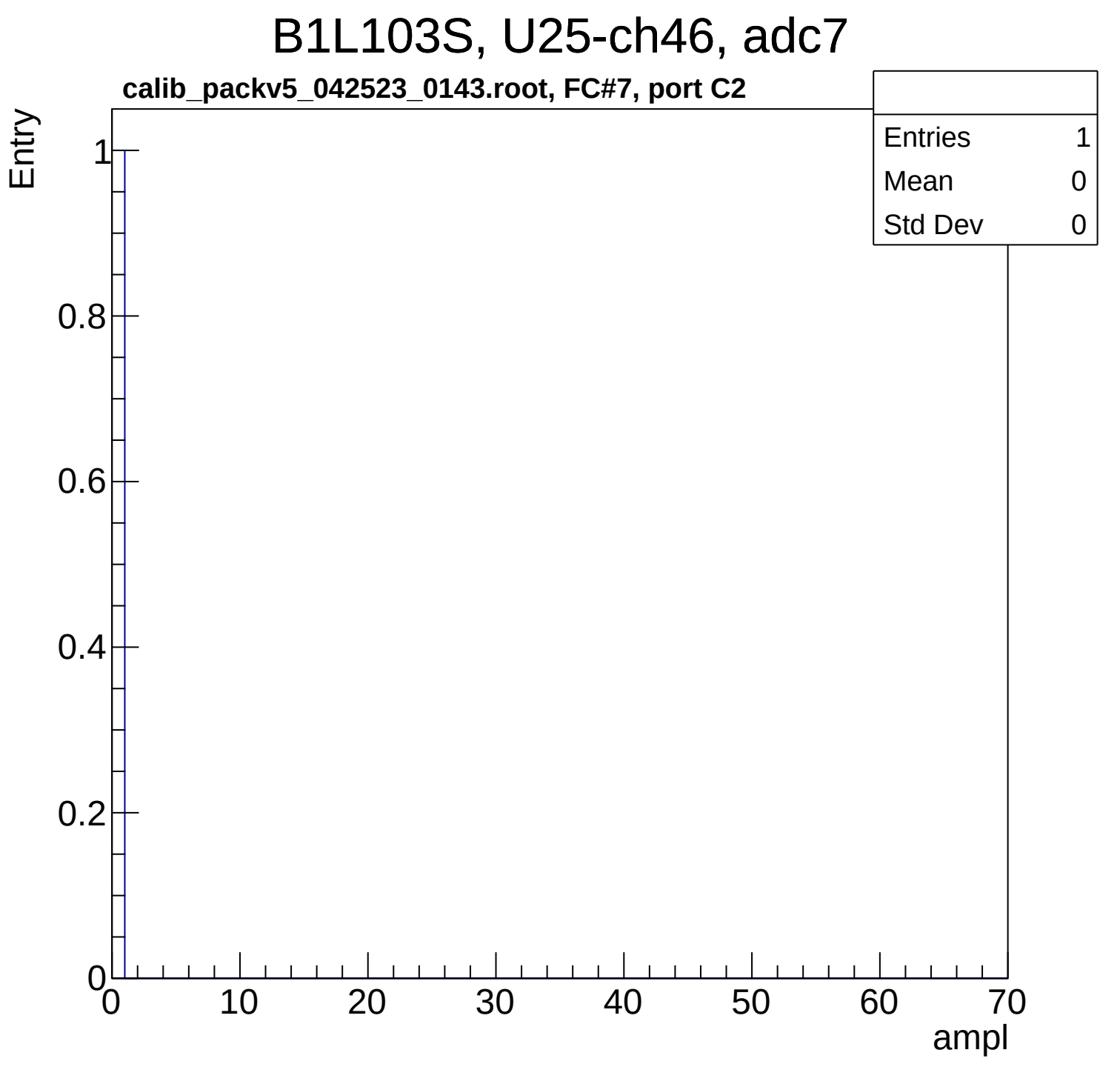
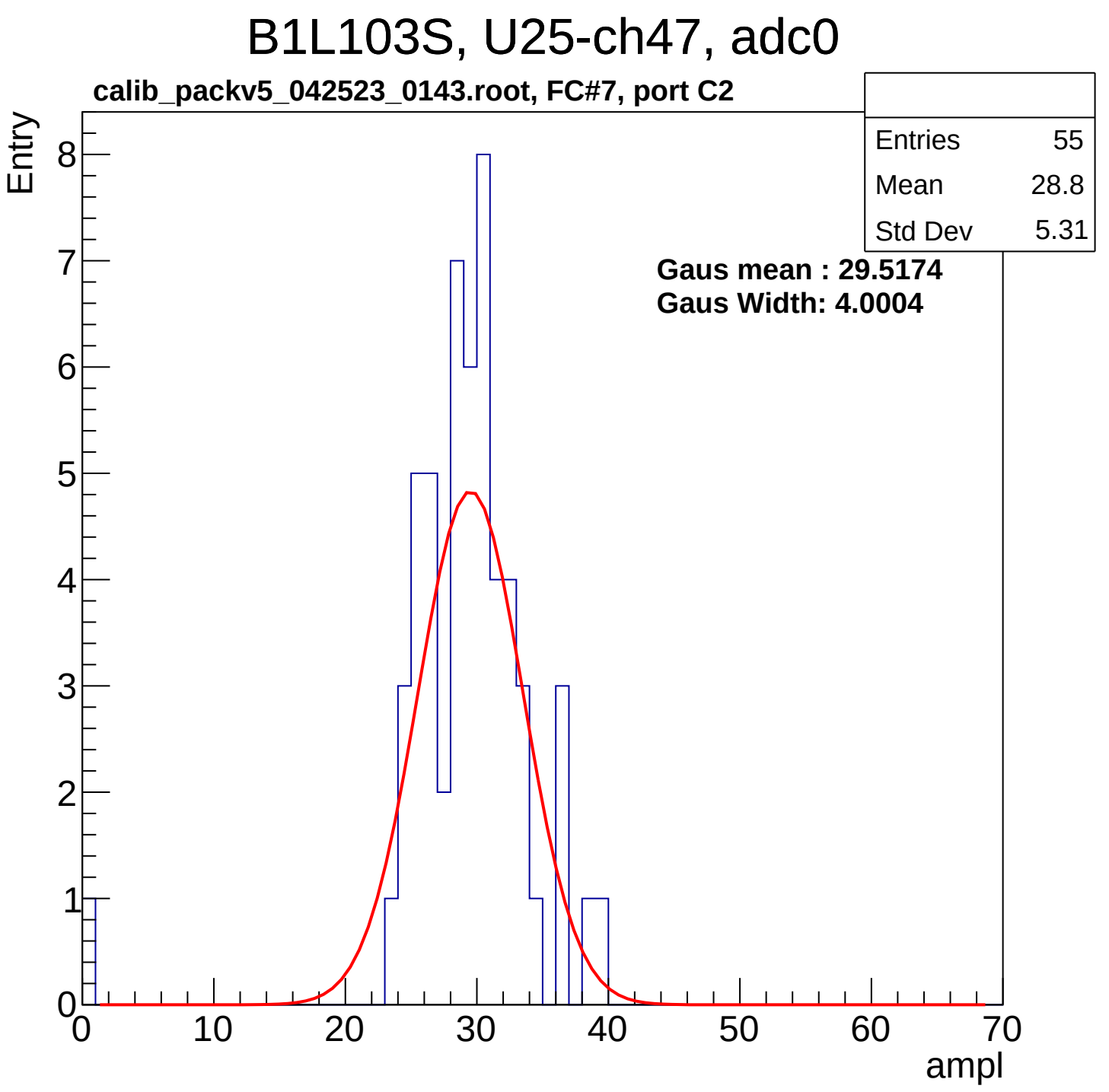

B1L103S, U25-ch46, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch47, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 28.8
Std Dev 5.31
Gaus mean : 29.5174
Gaus Width: 4.0004
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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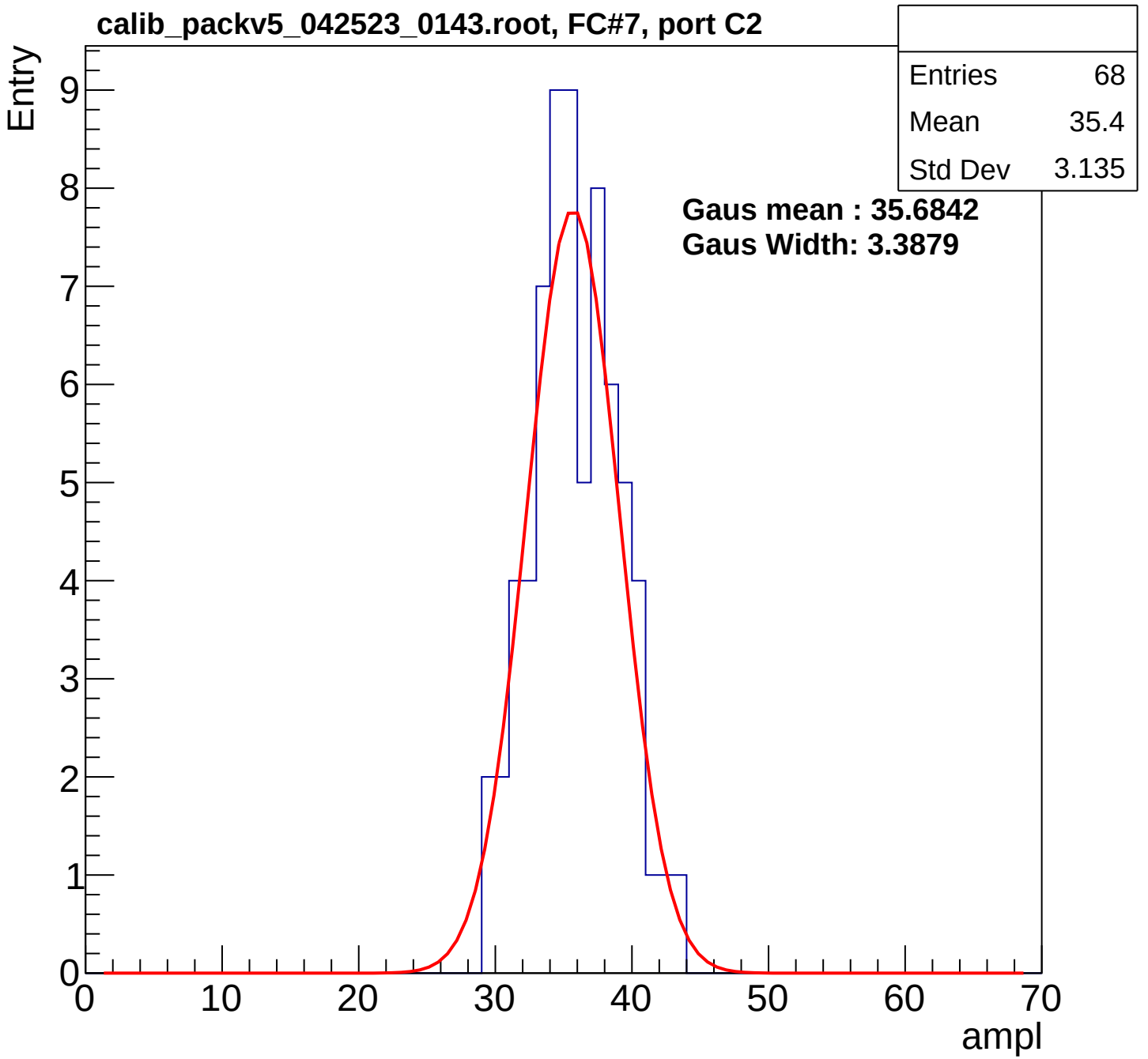
B1L103S, U25-ch47, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 35.4
Std Dev 3.135
Gaus mean : 35.6842
Gaus Width: 3.3879
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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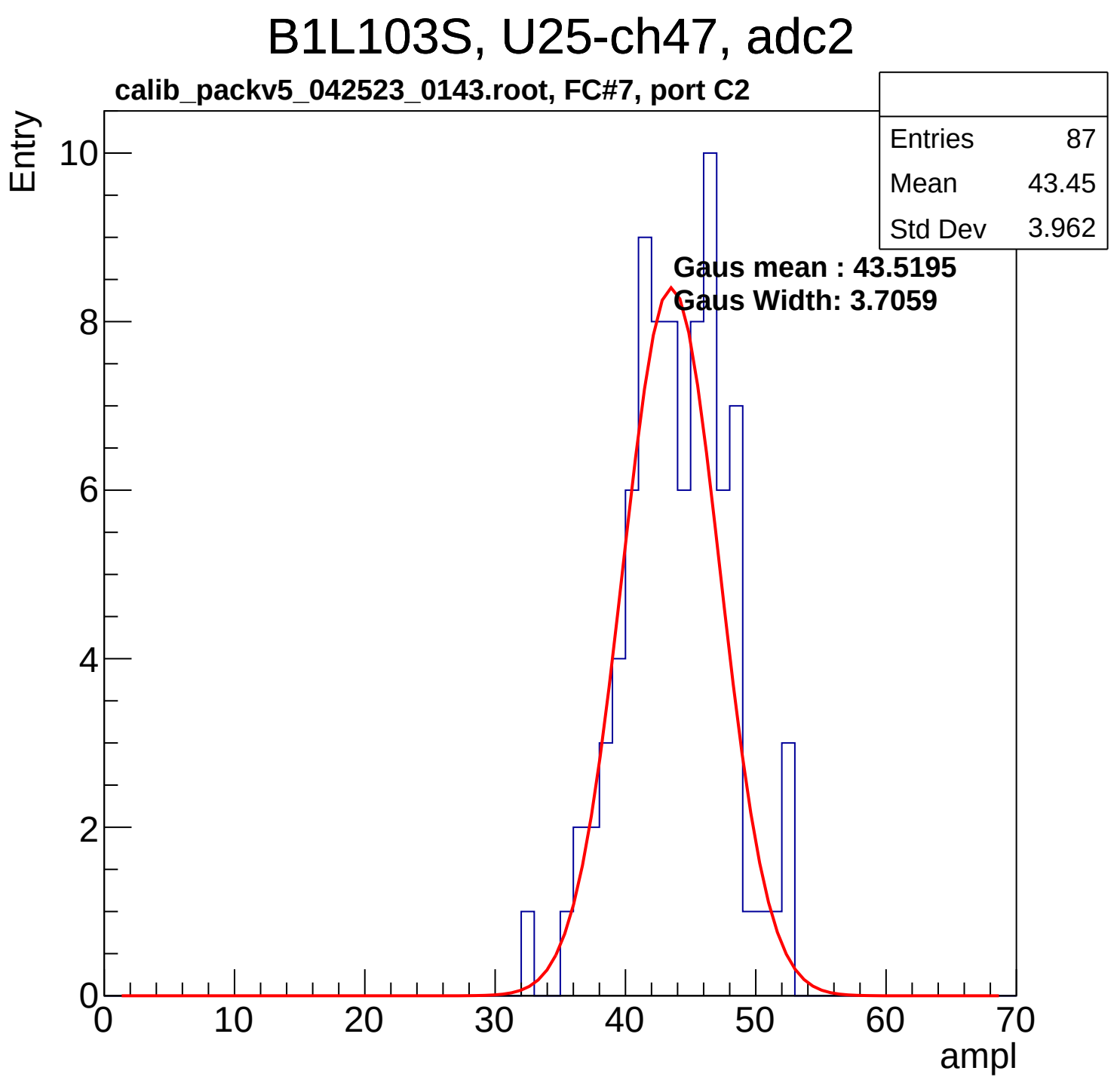
B1L103S, U25-ch47, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 43.45
Std Dev 3.962
Gaus mean : 43.5195
Gaus Width: 3.7059
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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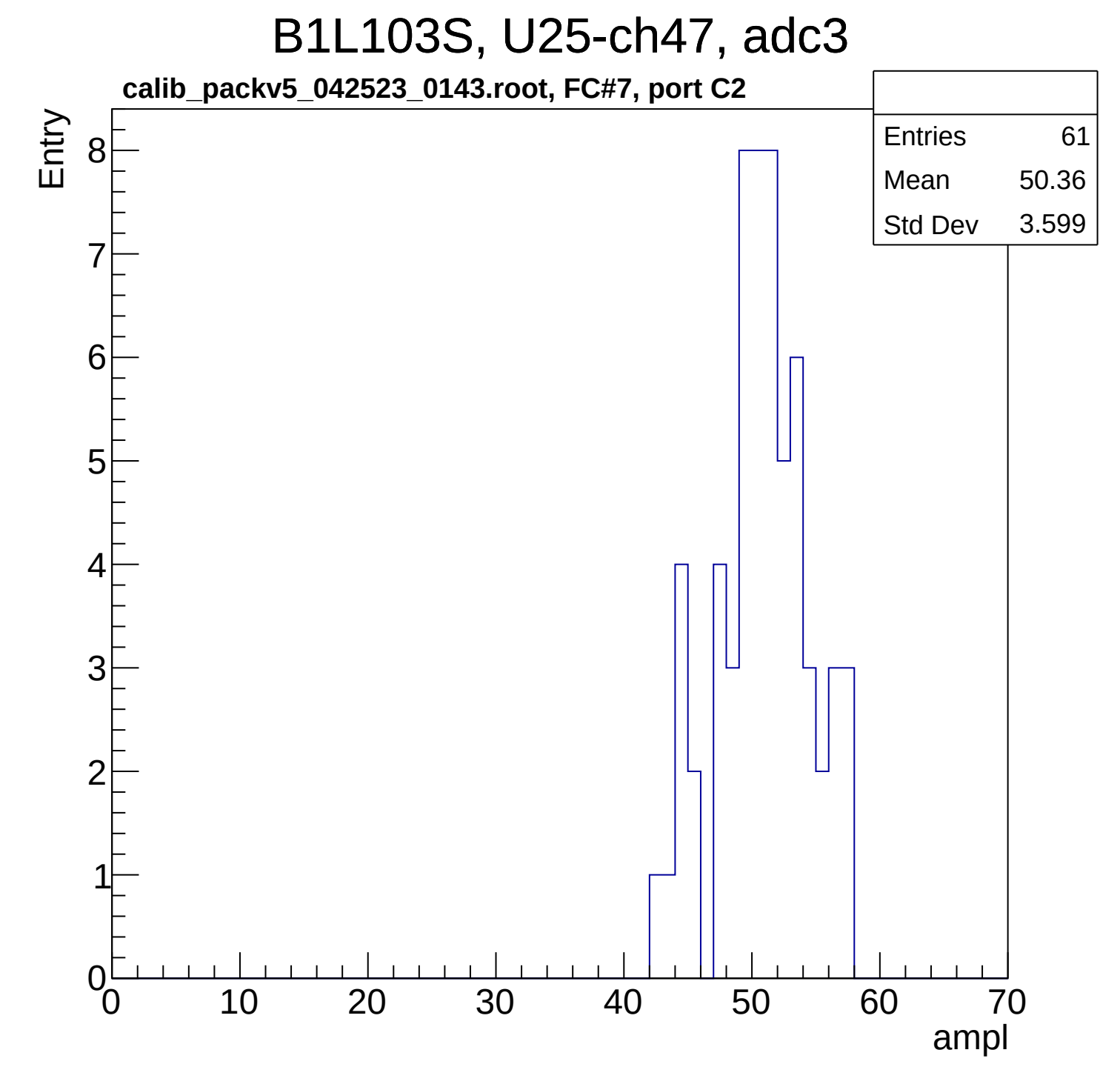
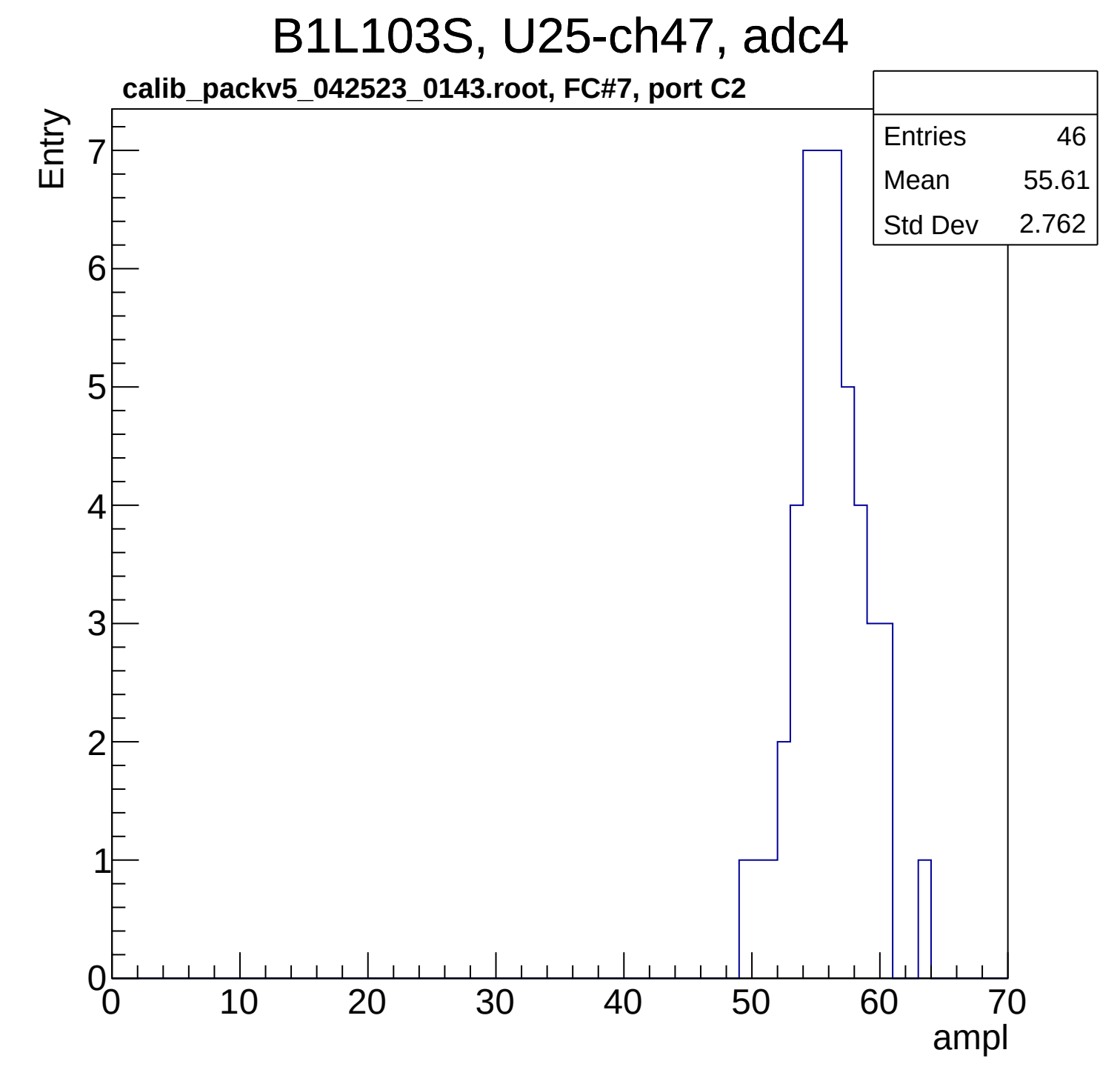
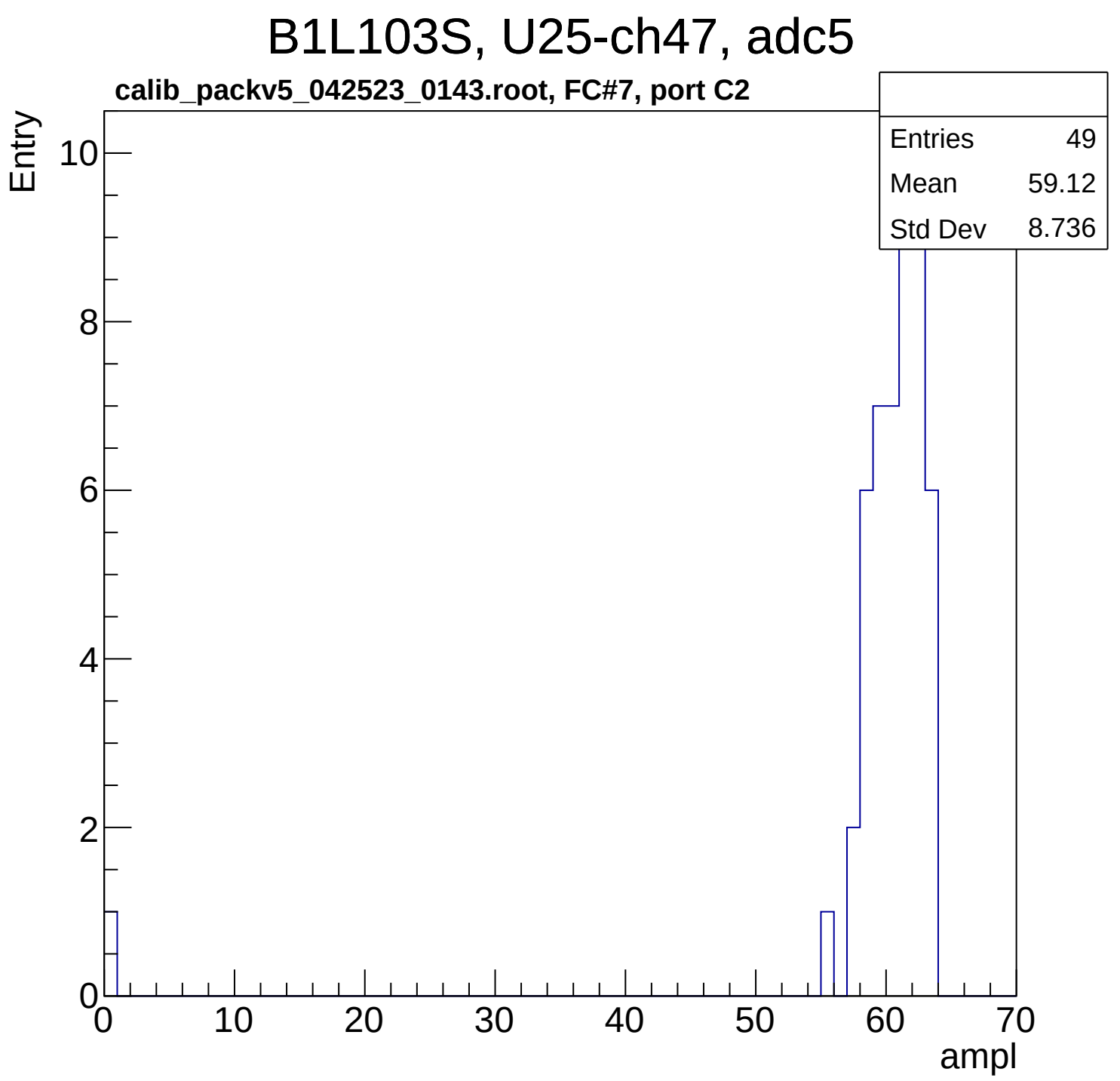
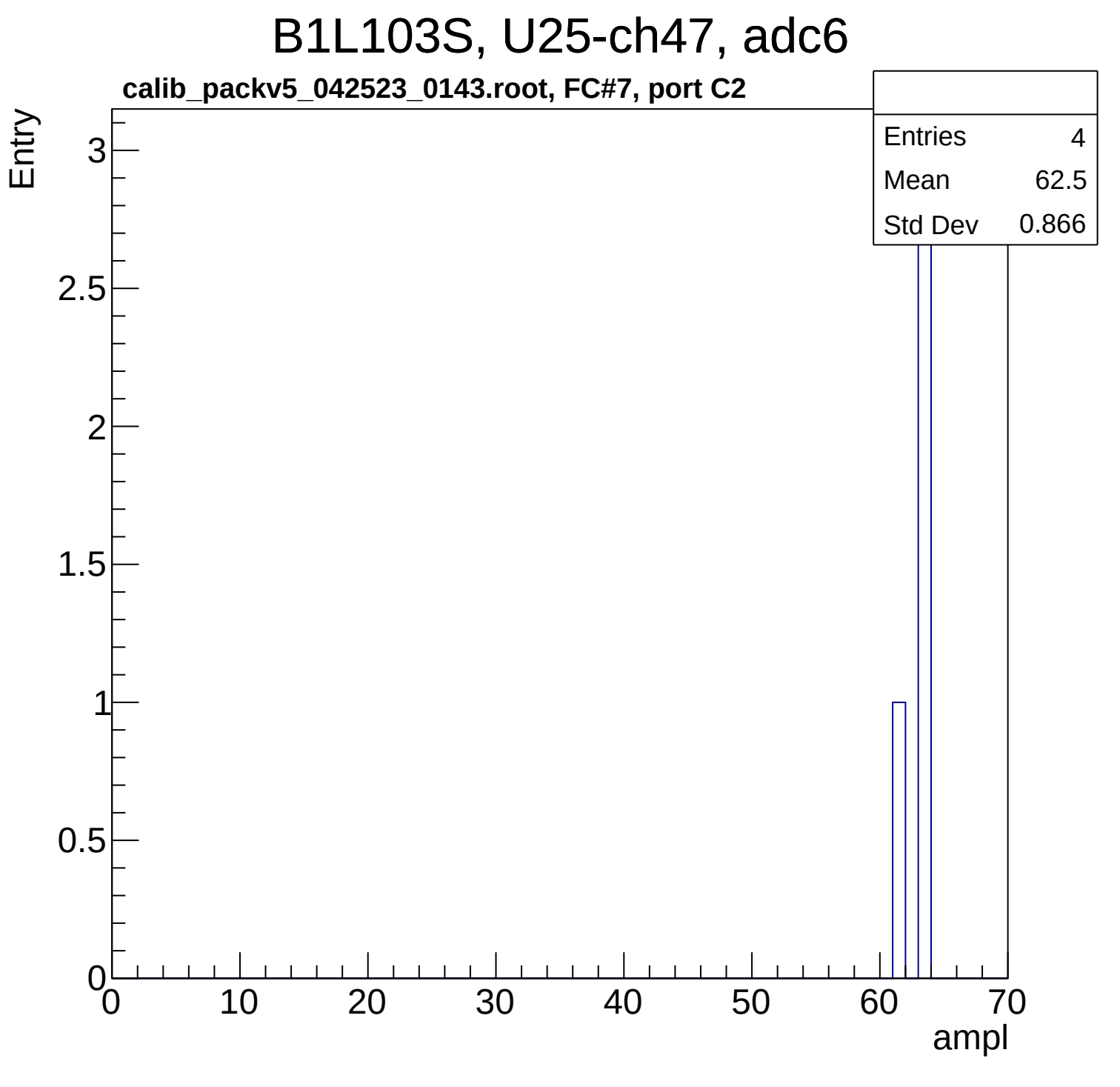
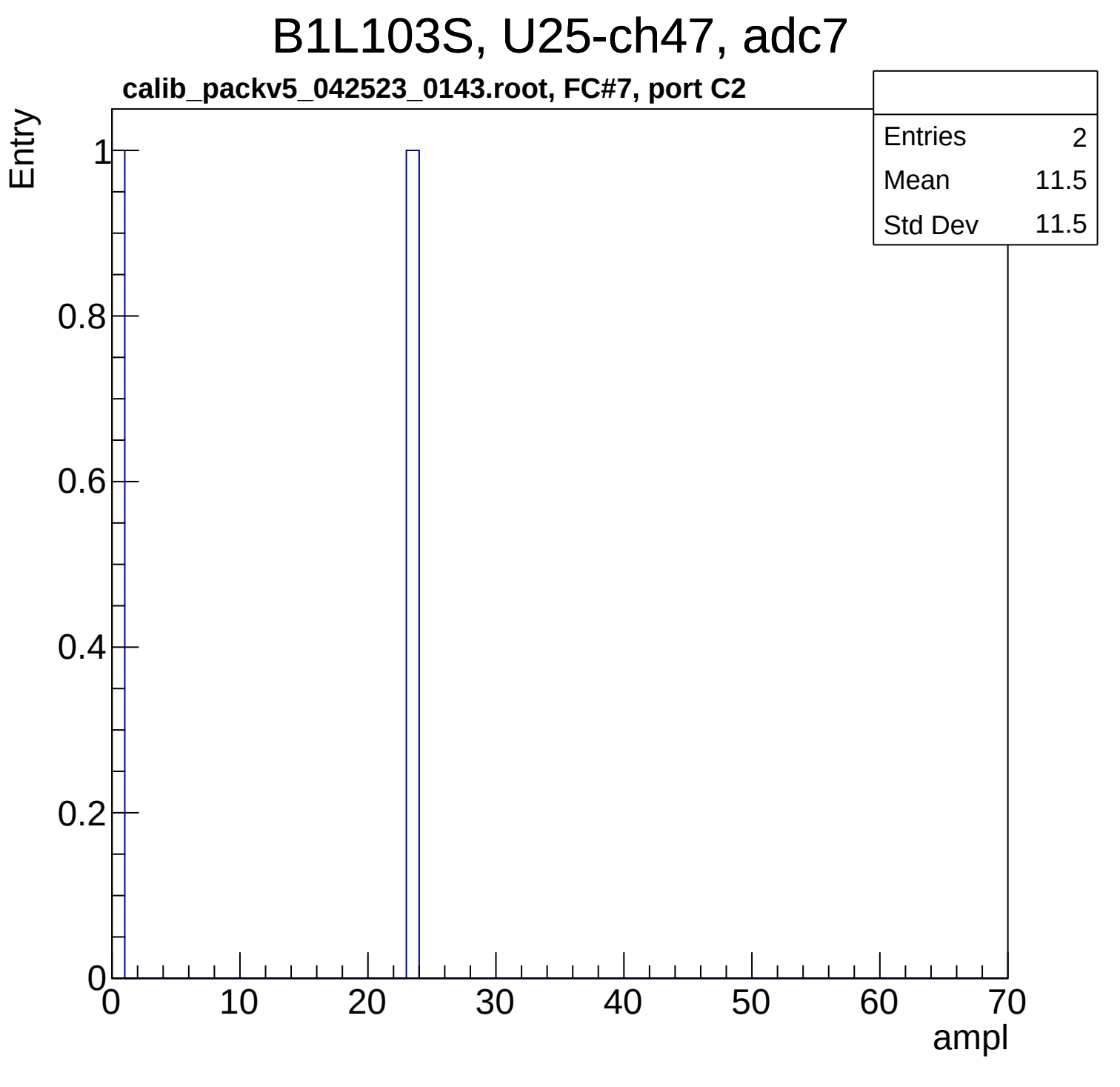
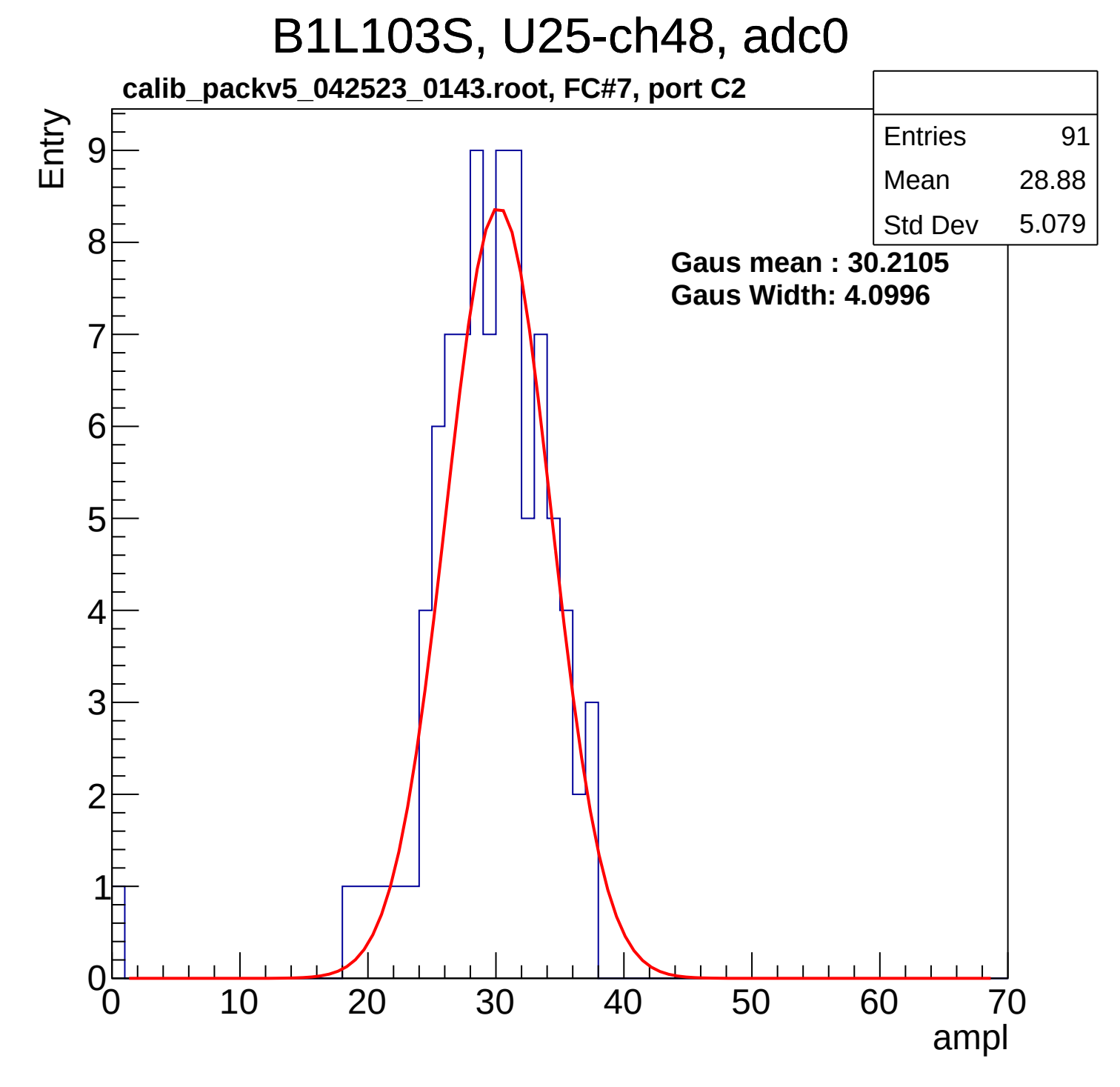

B1L103S, U25-ch47, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 50.36
Std Dev 3.599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

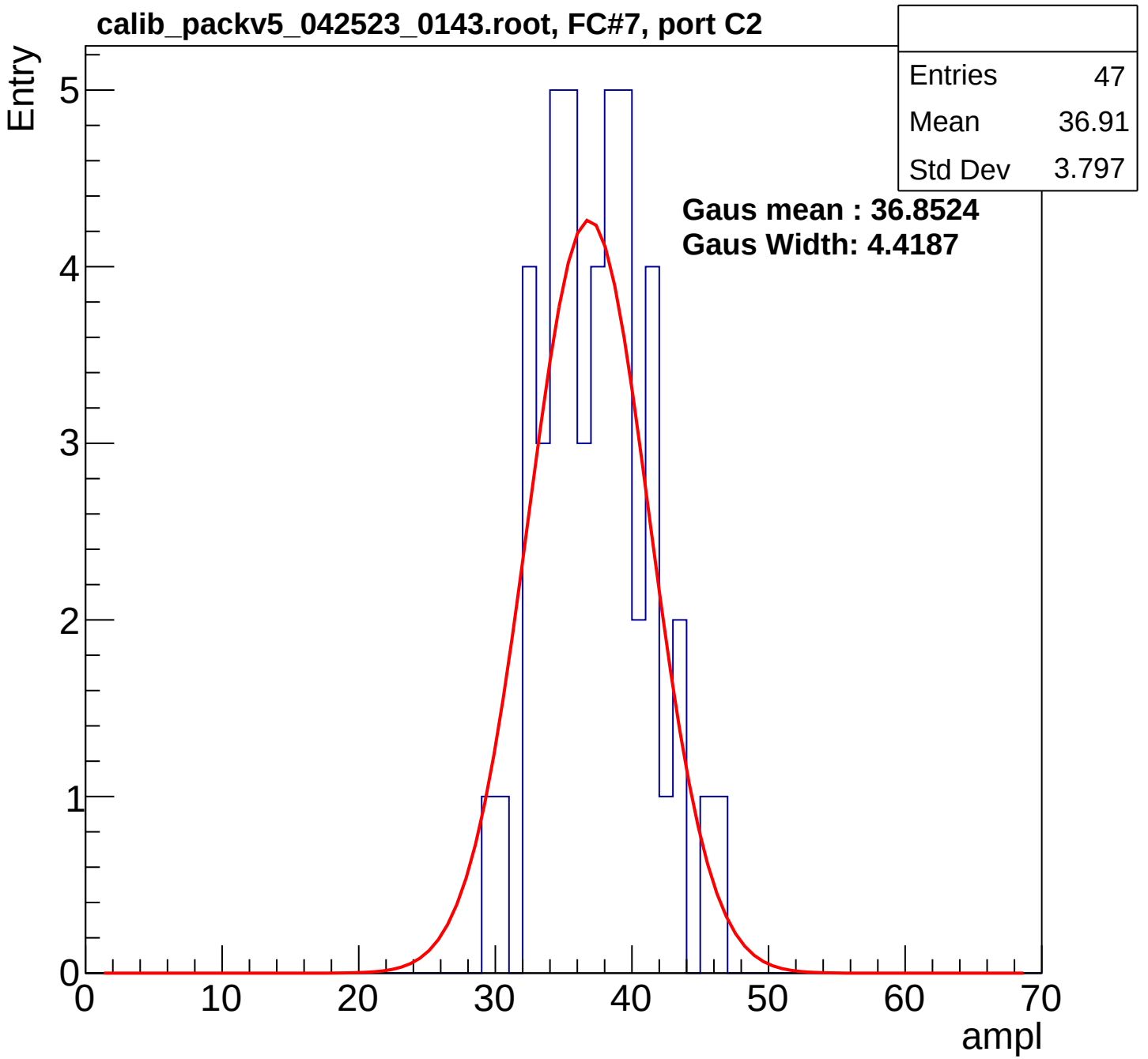

B1L103S, U25-ch47, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 55.61
Std Dev 2.762
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

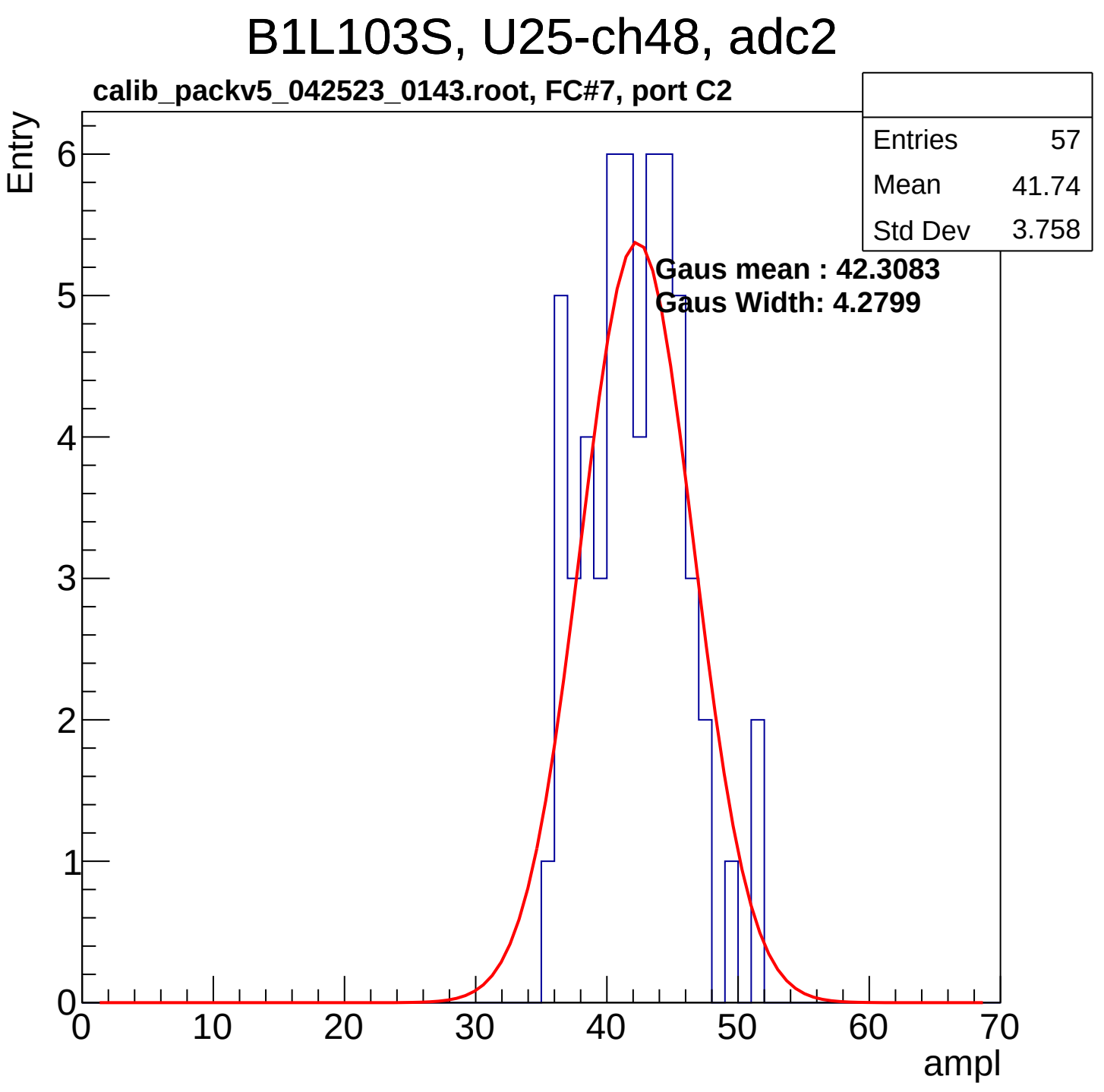

B1L103S, U25-ch47, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.12
Std Dev 8.736
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

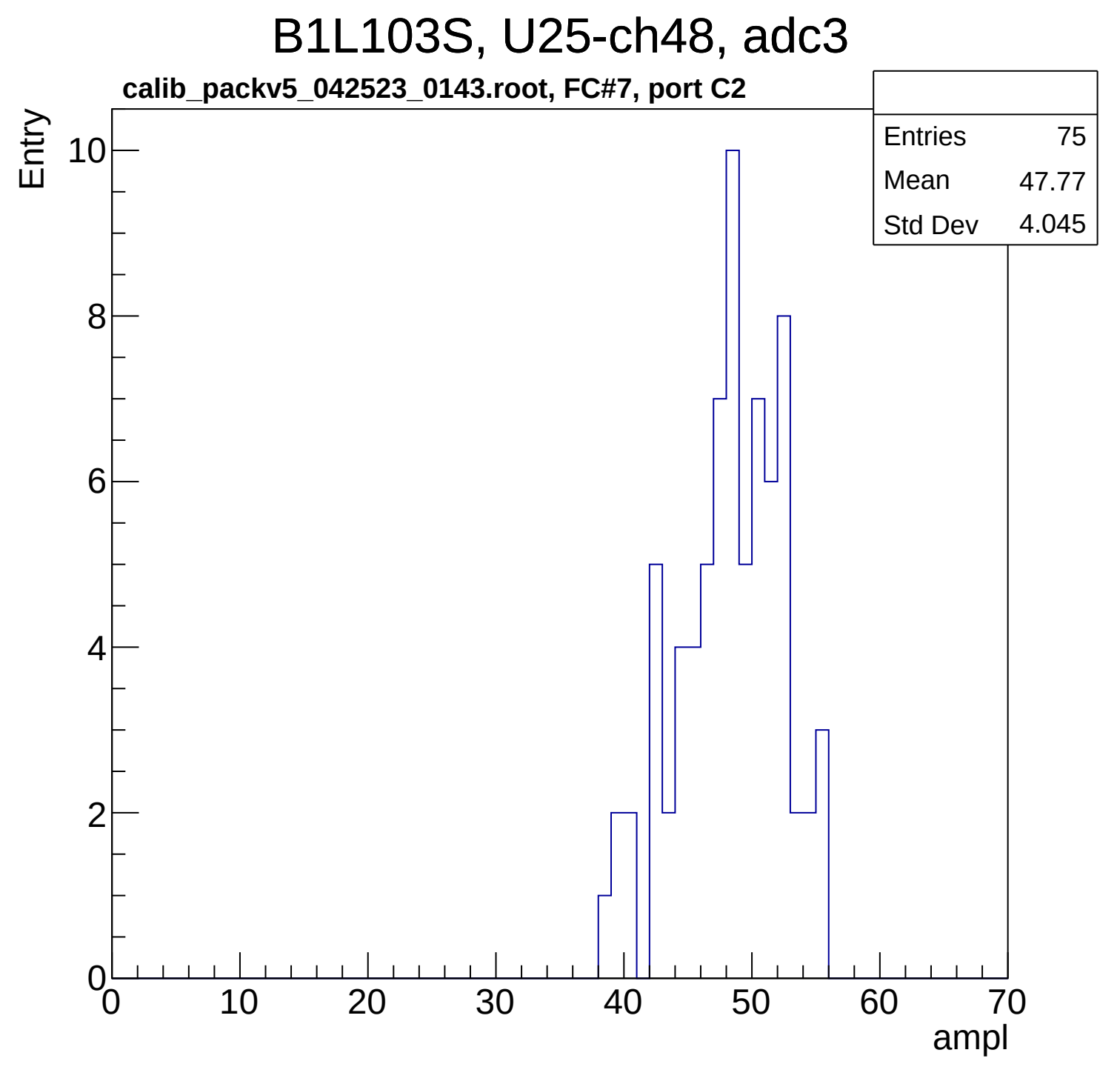

B1L103S, U25-ch47, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

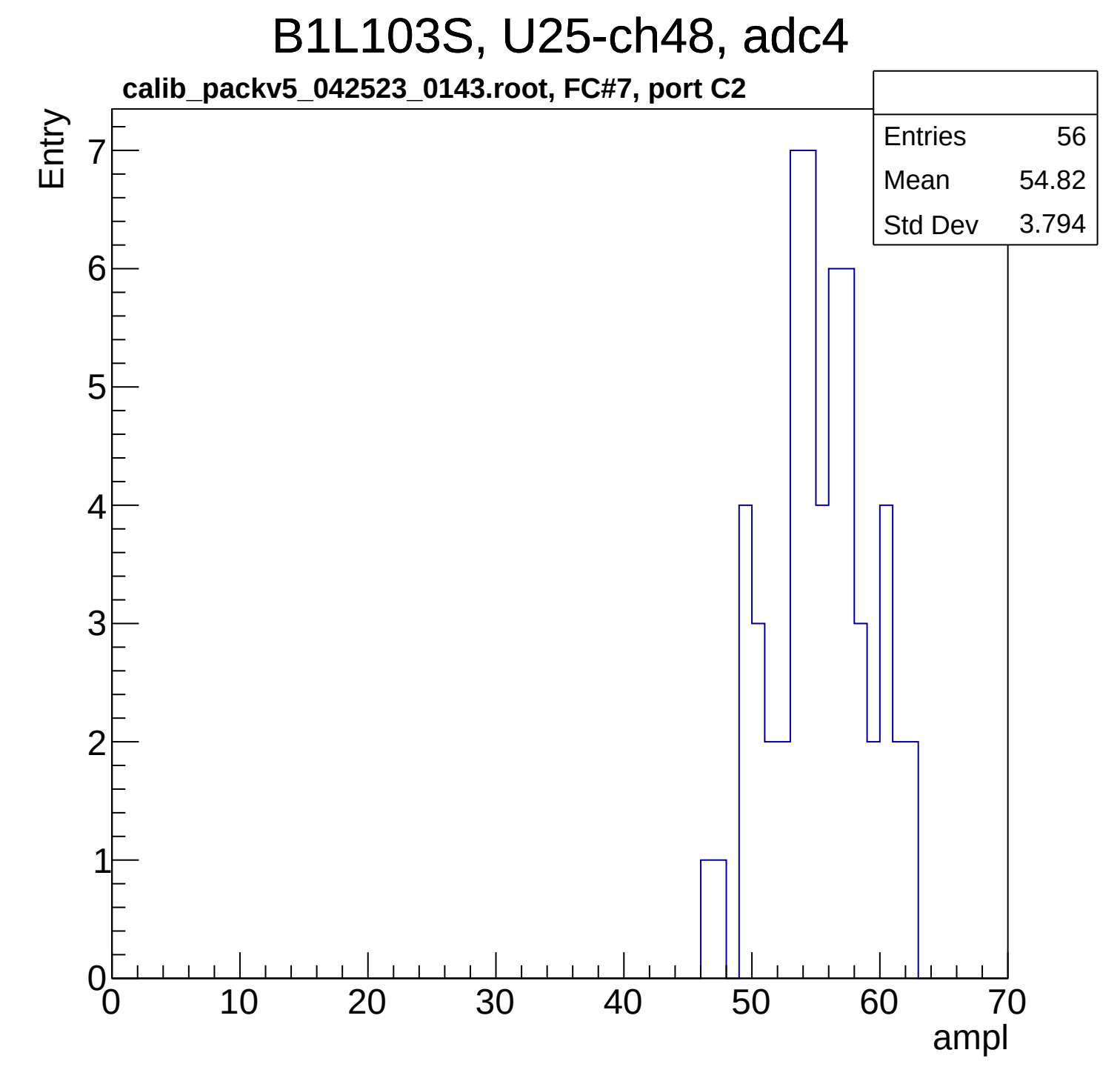

B1L103S, U25-ch47, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

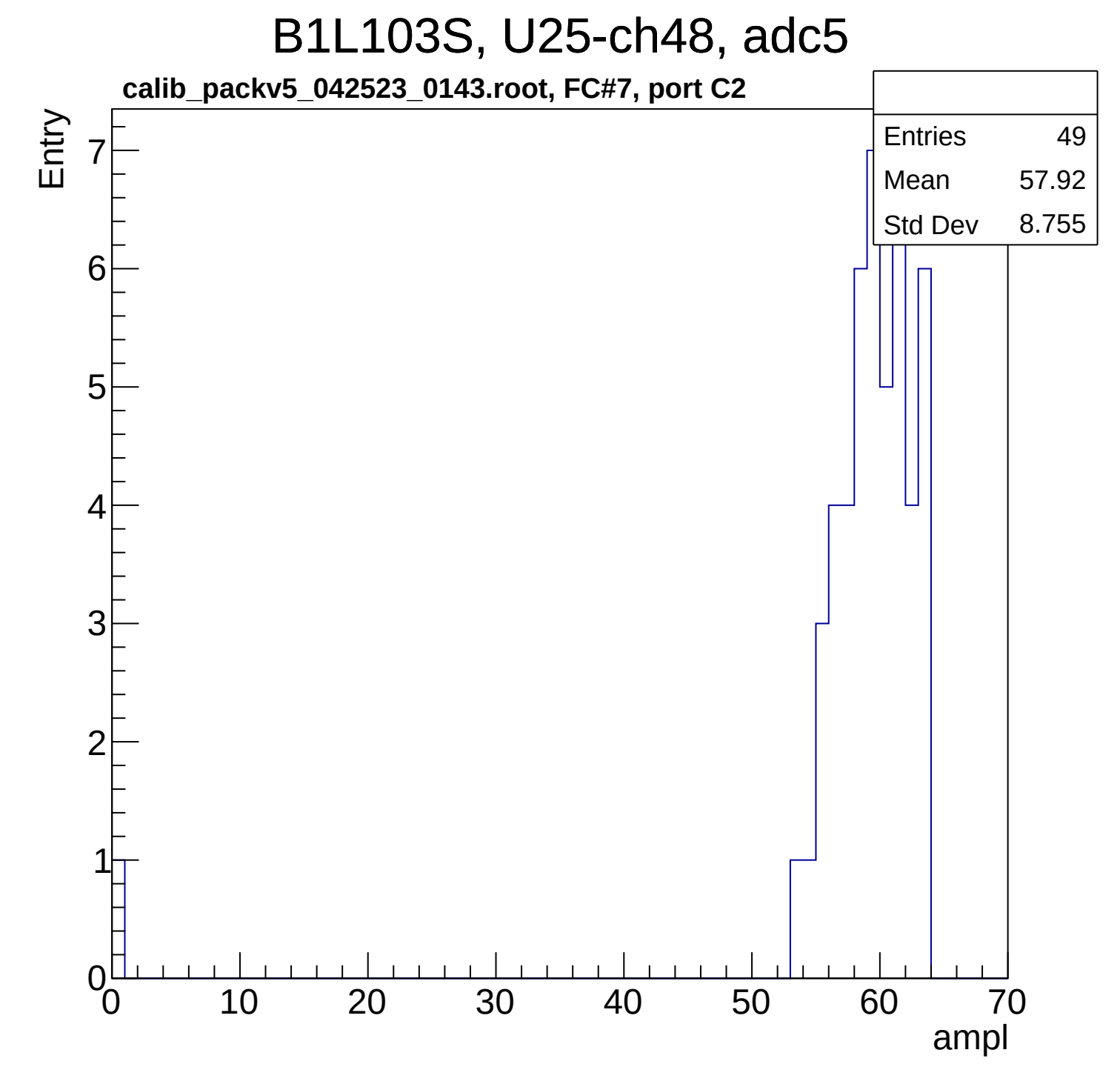

B1L103S, U25-ch48, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 28.88
Std Dev 5.079
Gaus mean : 30.2105
Gaus Width: 4.0996
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch48, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 36.91
Std Dev 3.797
Gaus mean : 36.8524
Gaus Width: 4.4187
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U25-ch48, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 41.74
Std Dev 3.758
Gaus mean : 42.3083
Gaus Width: 4.2799
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch48, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 47.77
Std Dev 4.045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch48, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 54.82
Std Dev 3.794
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch48, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 57.92
Std Dev 8.755
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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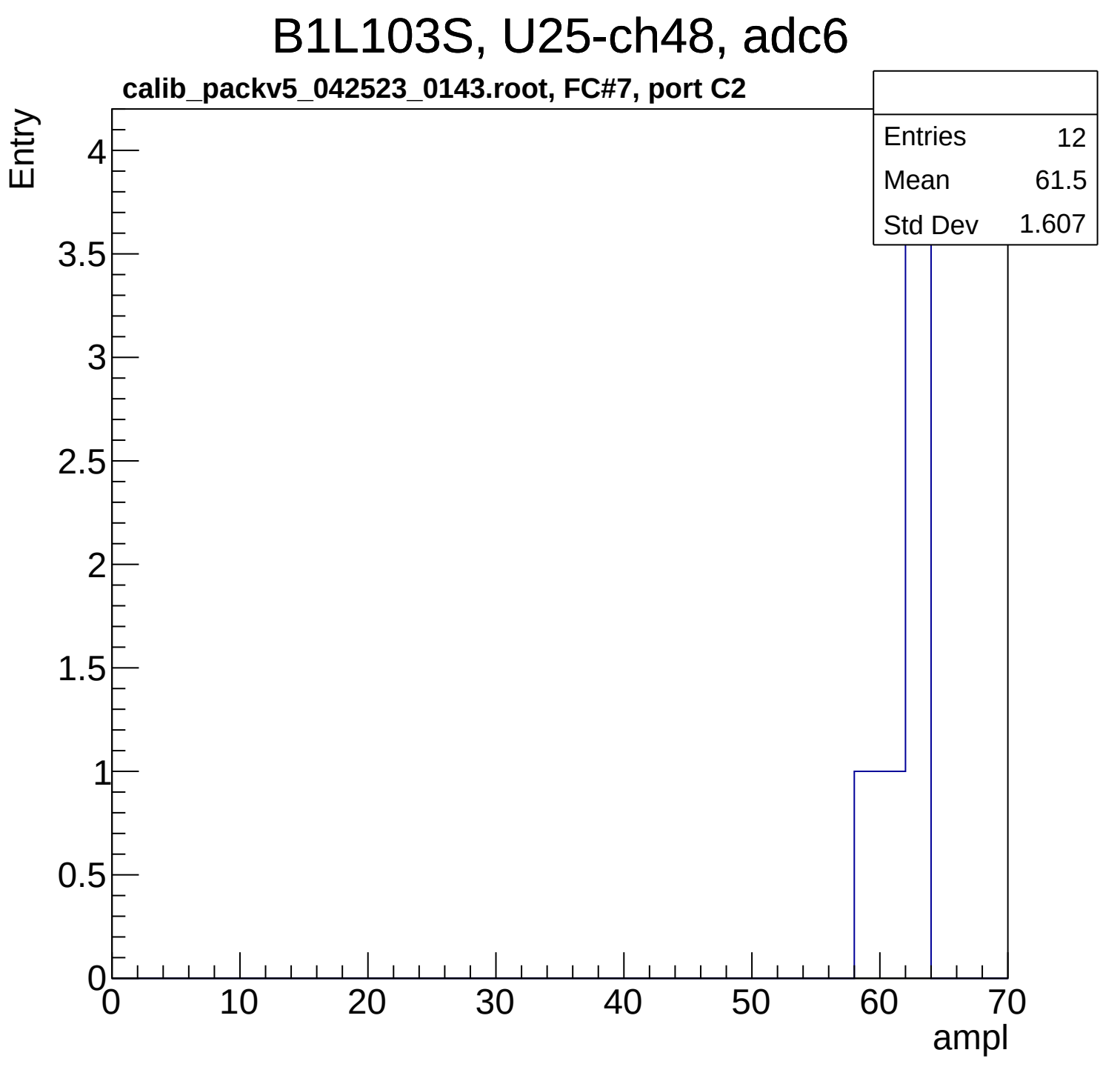
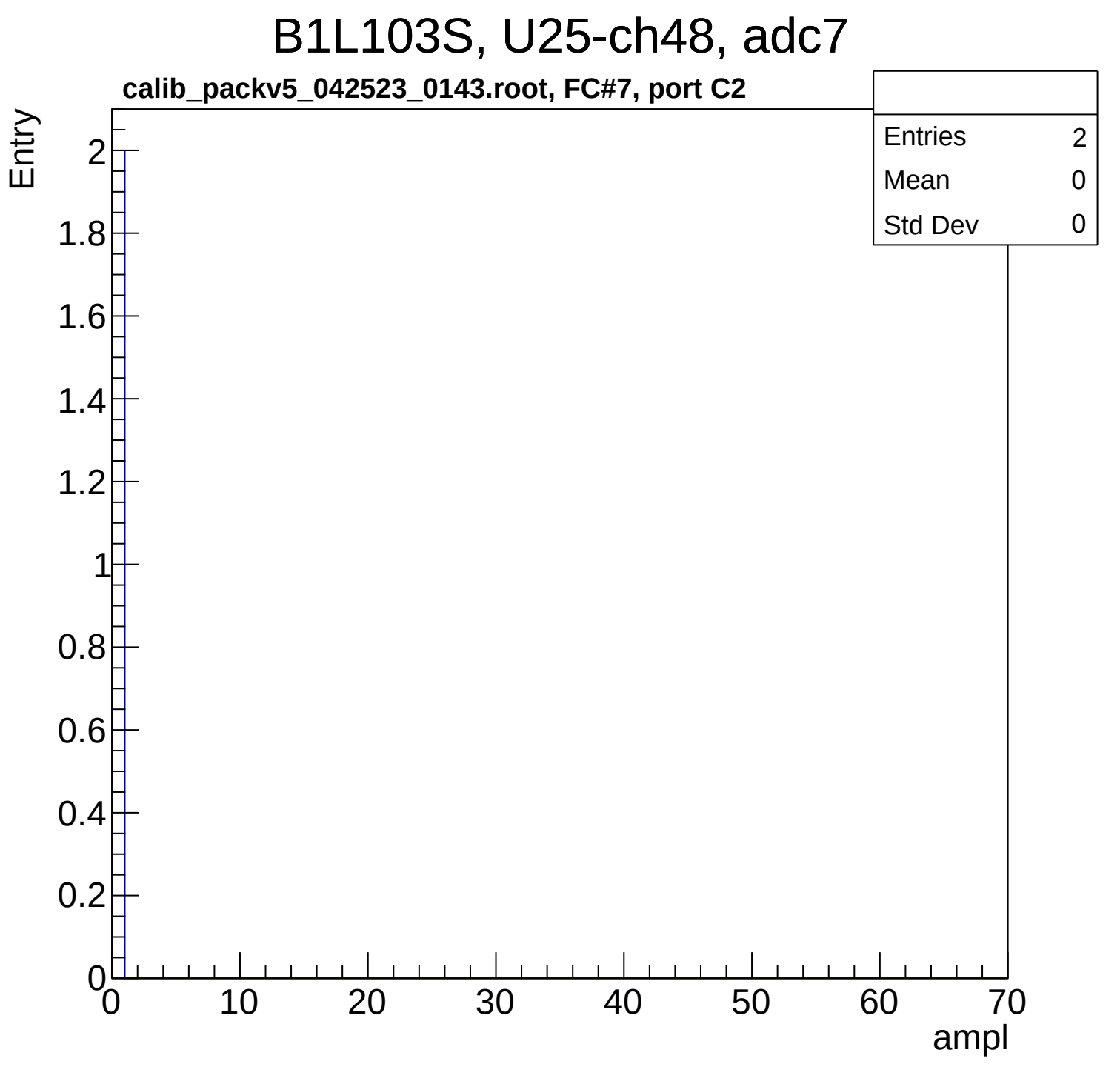
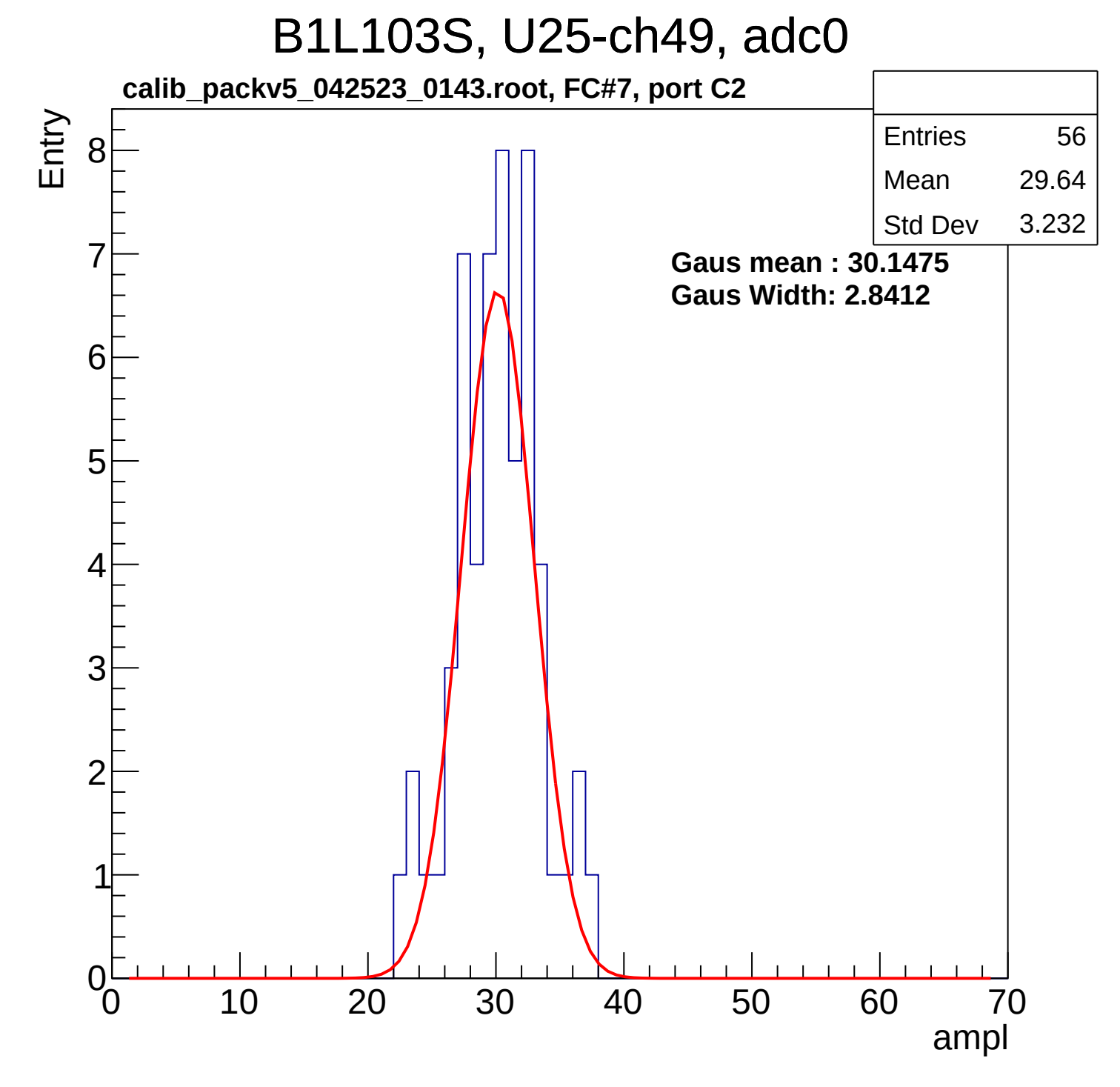
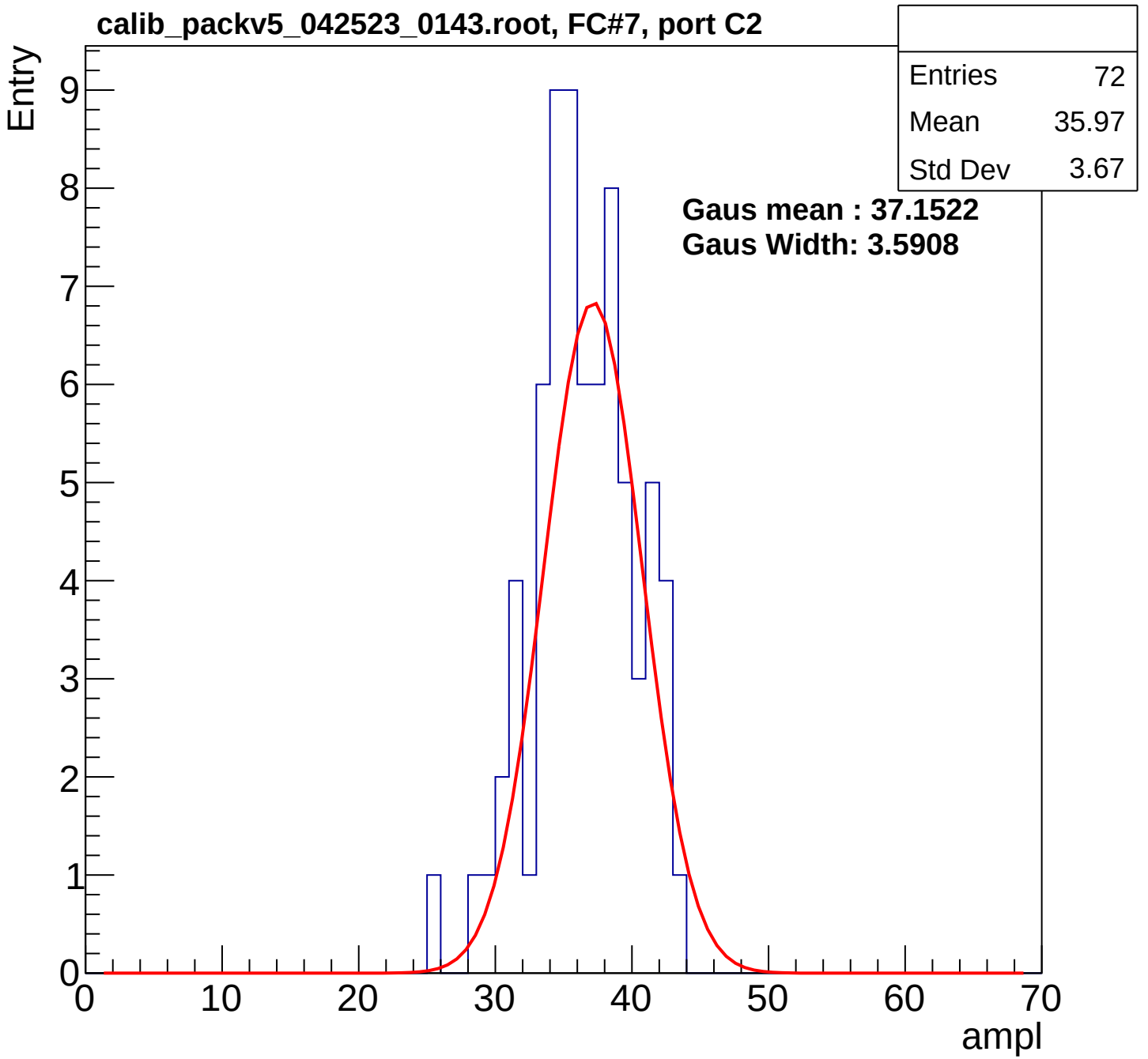
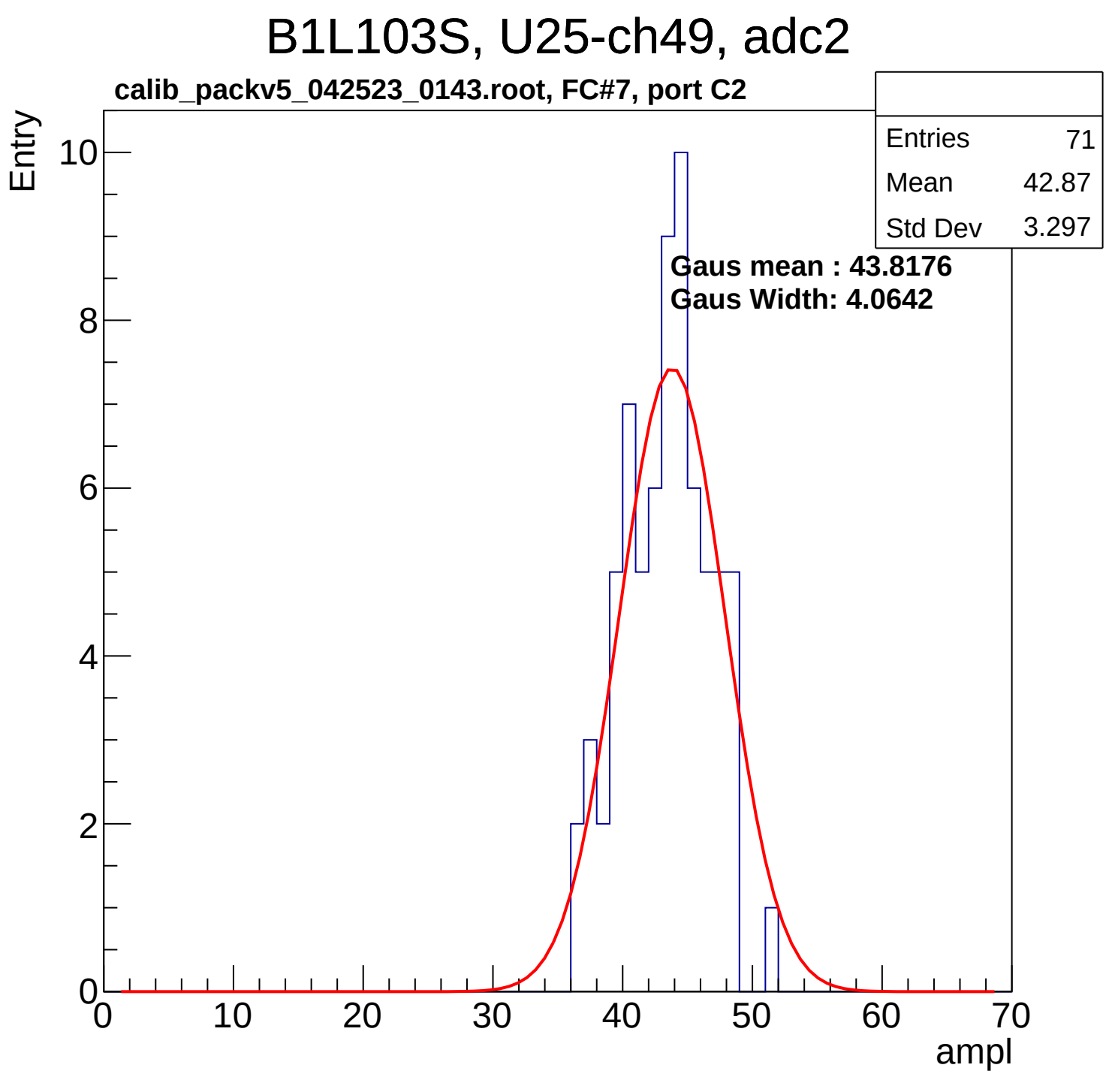
40
50
60
70

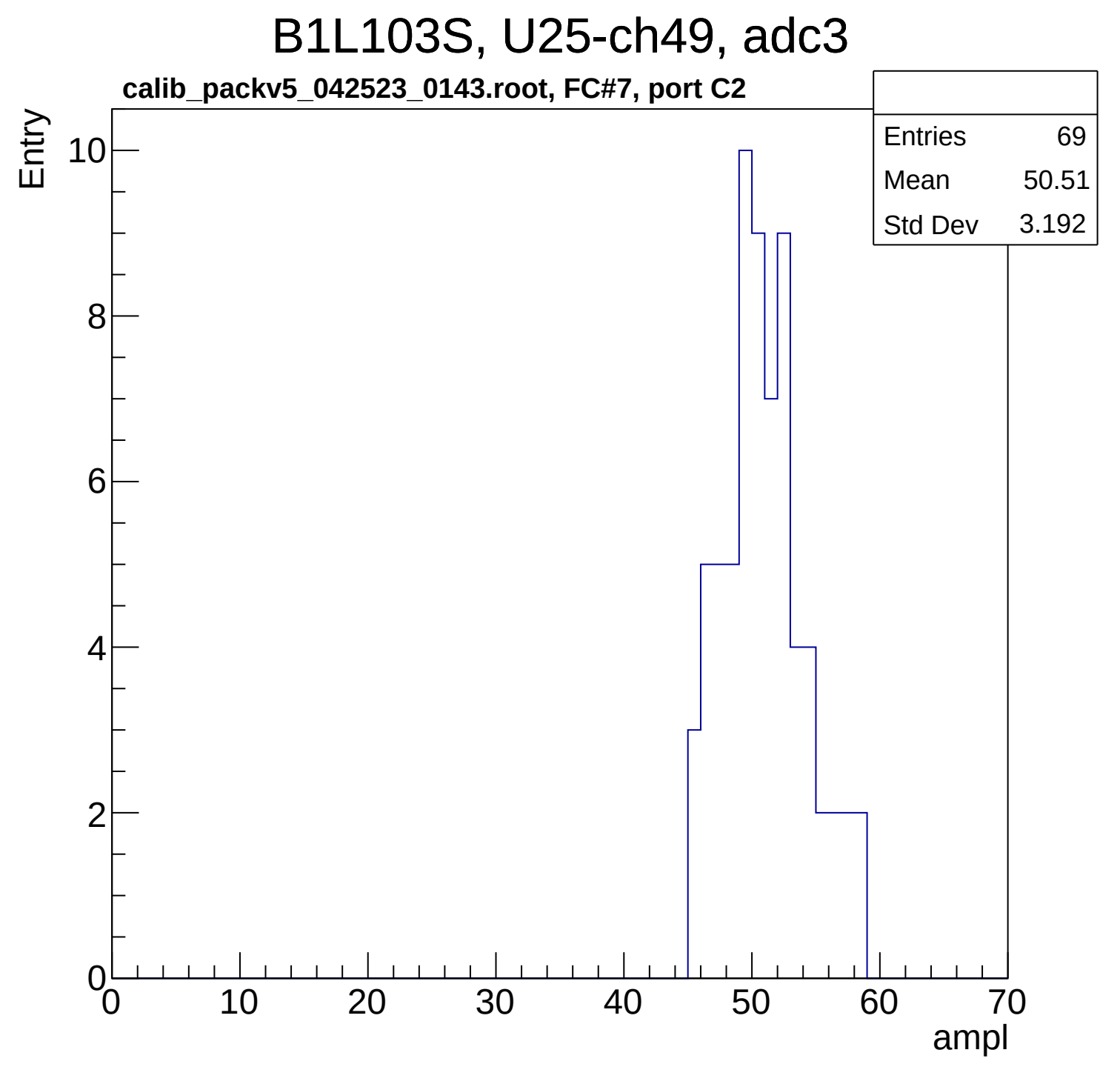

B1L103S, U25-ch48, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 61.5
Std Dev 1.607
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

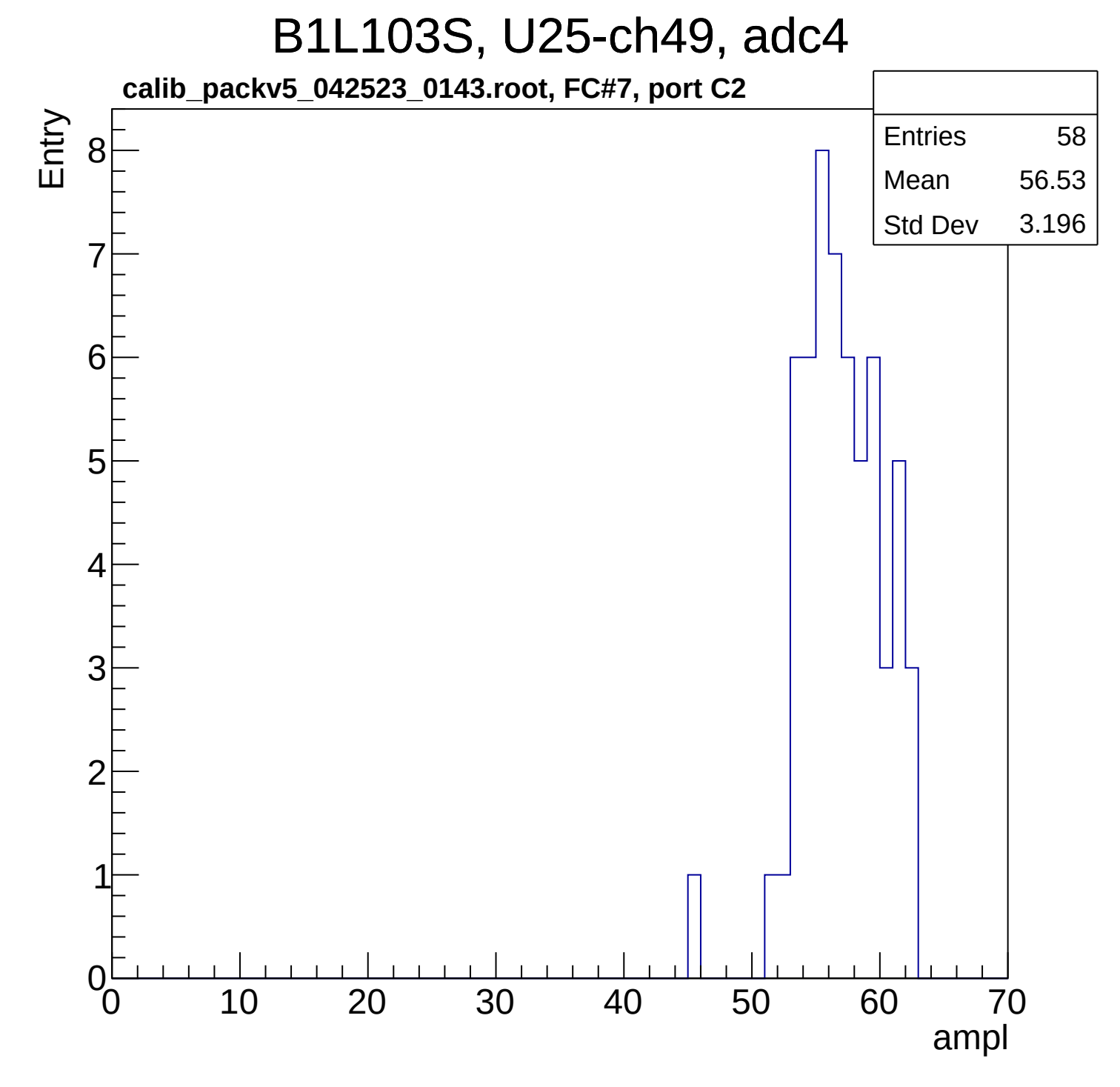

B1L103S, U25-ch48, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch49, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 29.64
Std Dev 3.232
Gaus mean : 30.1475
Gaus Width: 2.8412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.97
Std Dev 3.67
Gaus mean : 37.1522
Gaus Width: 3.5908
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 42.87
Std Dev 3.297
Gaus mean : 43.8176
Gaus Width: 4.0642
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.51
Std Dev 3.192
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch49, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 56.53
Std Dev 3.196
Entry
ampl
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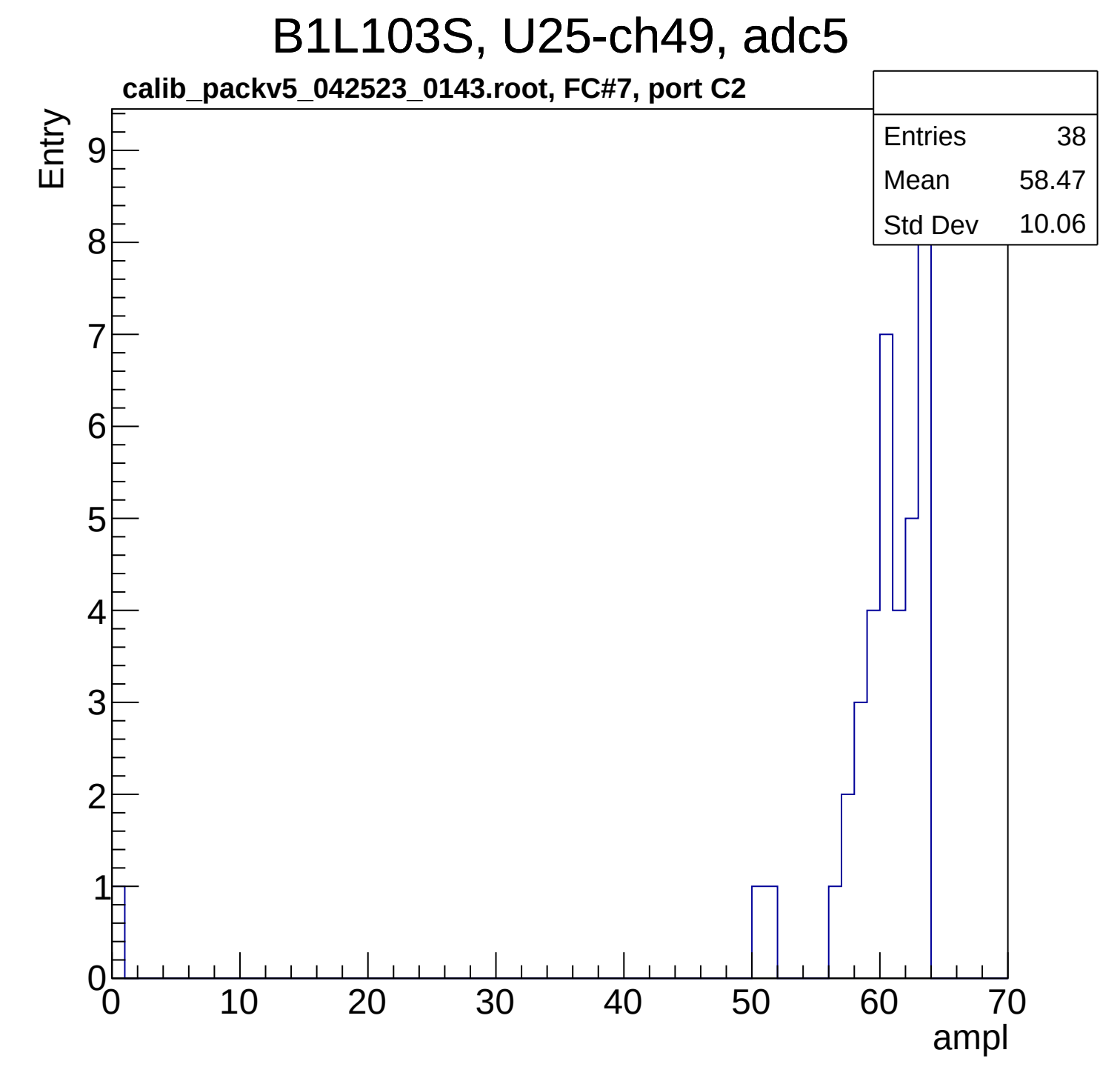
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.47
Std Dev 10.06
Entry
ampl
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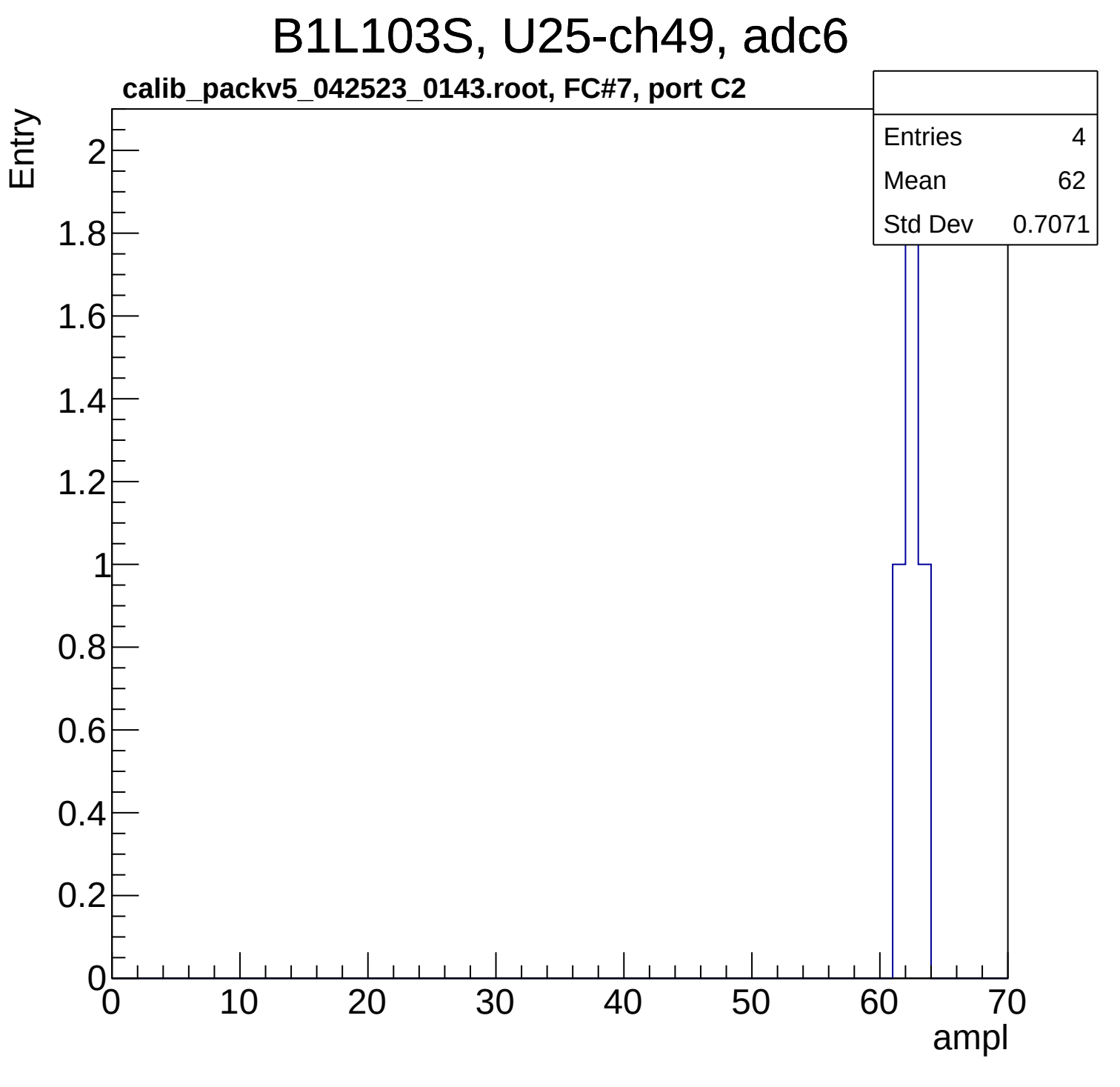
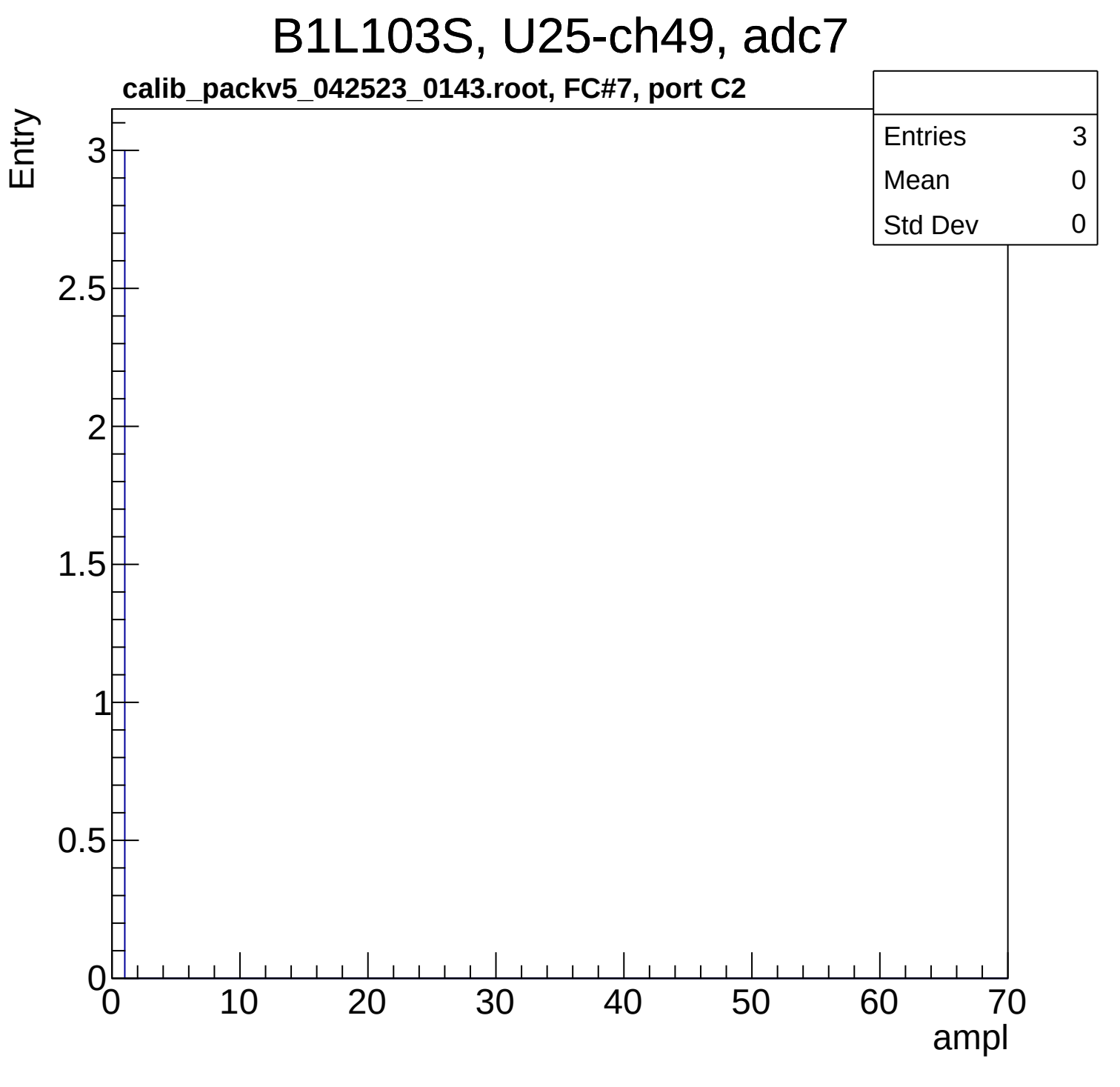
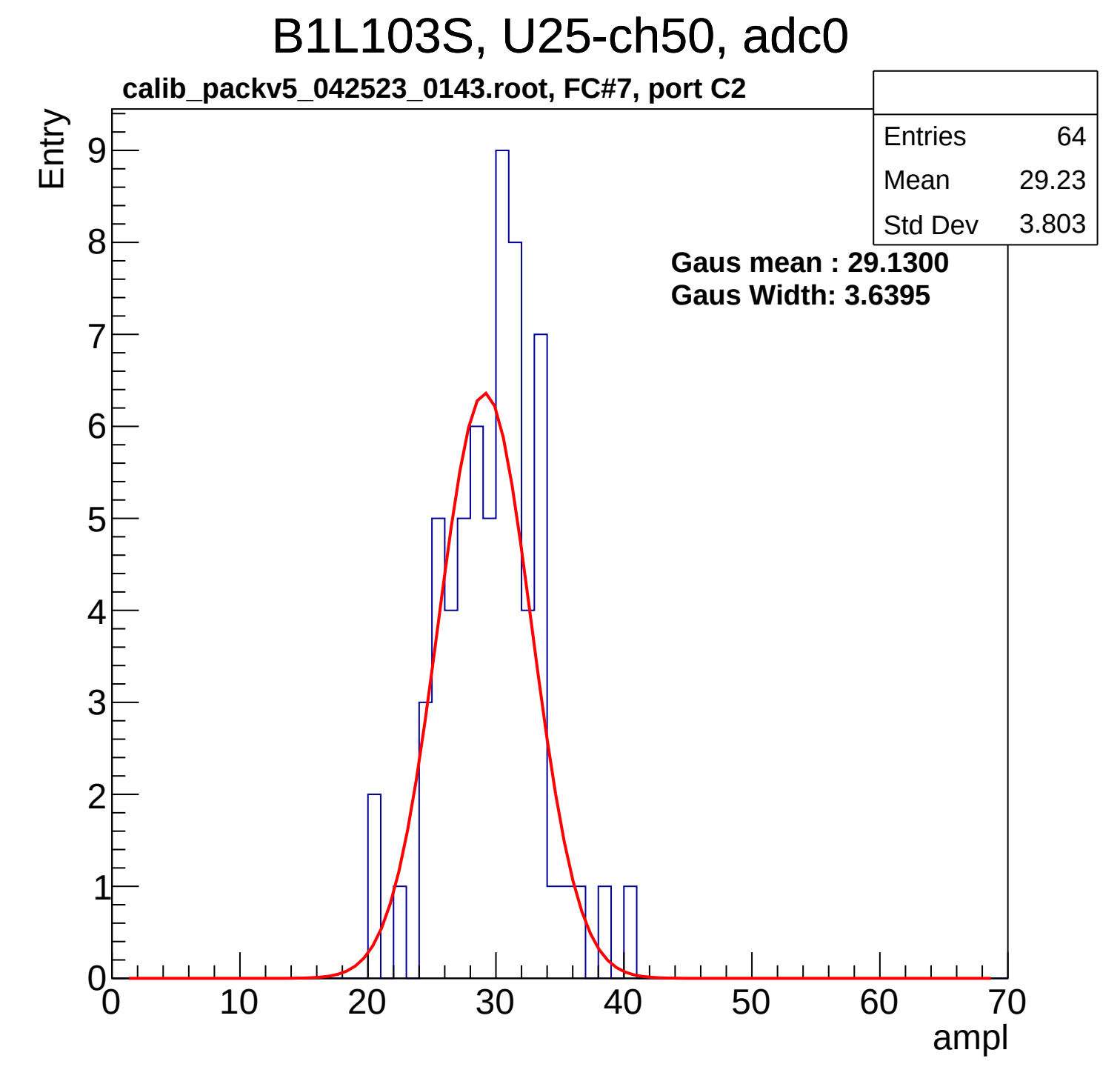
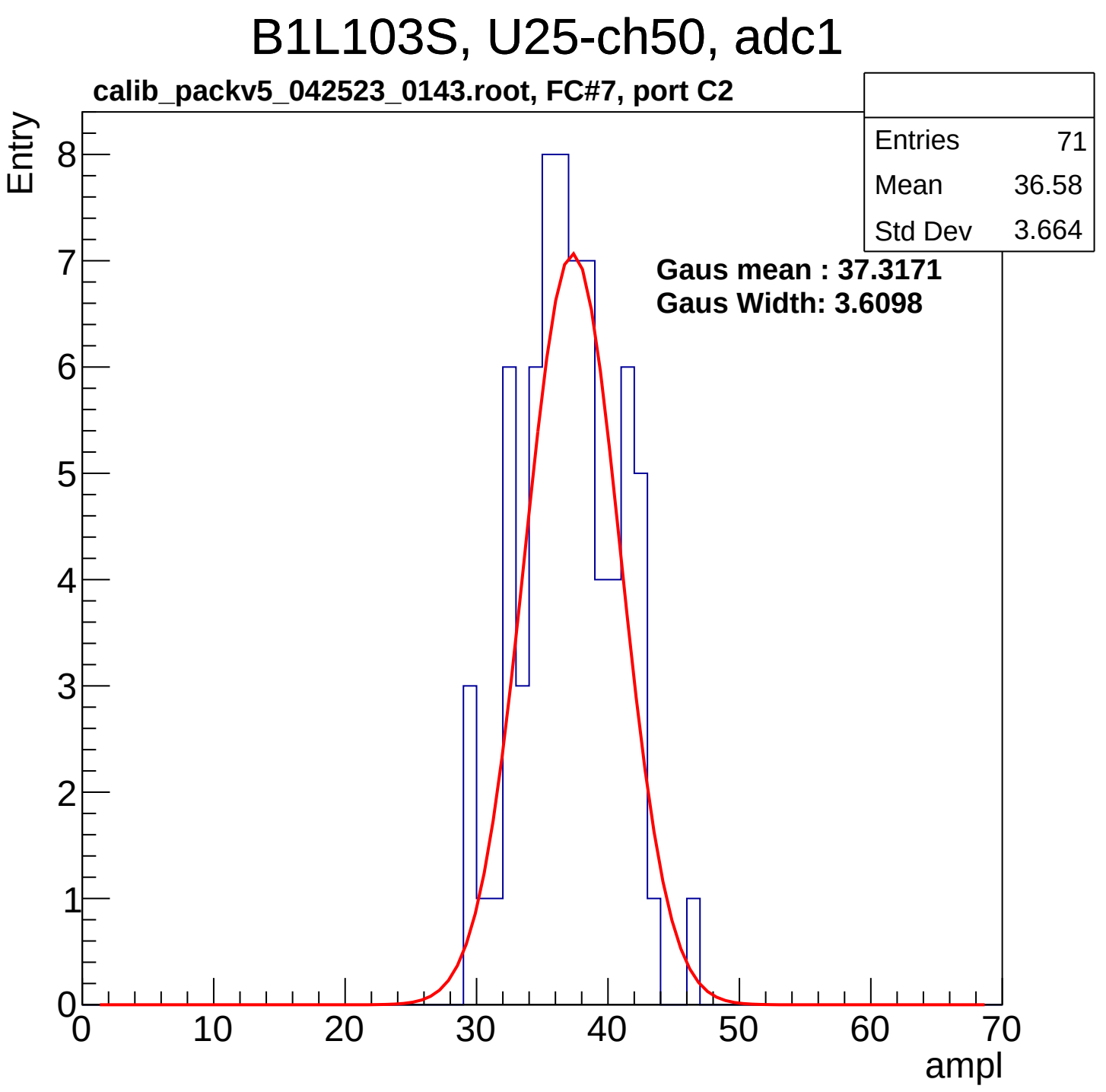
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch49, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch50, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 29.23
Std Dev 3.803
Gaus mean : 29.1300
Gaus Width: 3.6395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch50, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.58
Std Dev 3.664
Gaus mean : 37.3171
Gaus Width: 3.6098
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
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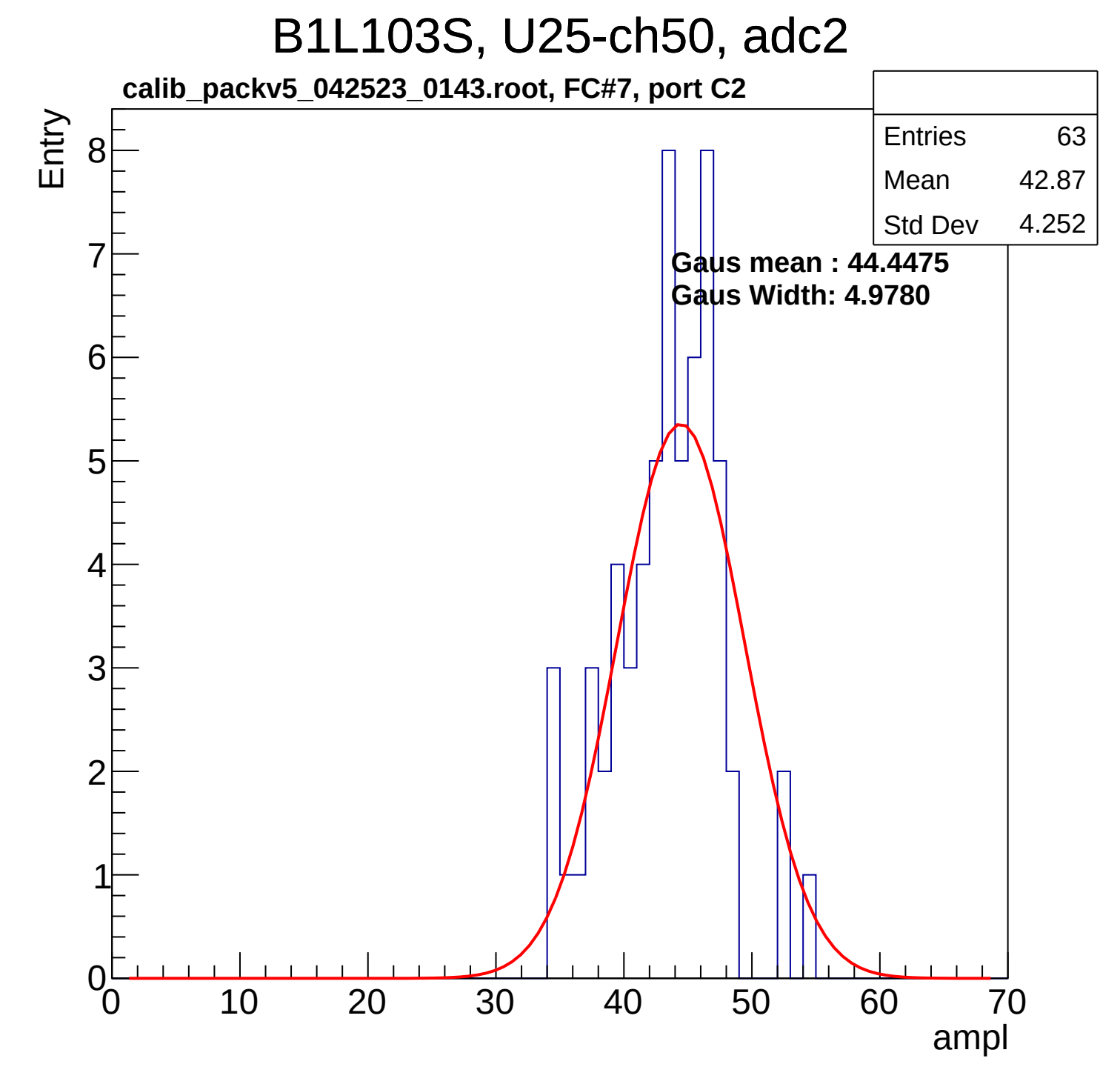
70


B1L103S, U25-ch50, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 42.87
Std Dev 4.252
Gaus mean : 44.4475
Gaus Width: 4.9780
Entry
ampl
0
1
2
3
4
5
6
7
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0
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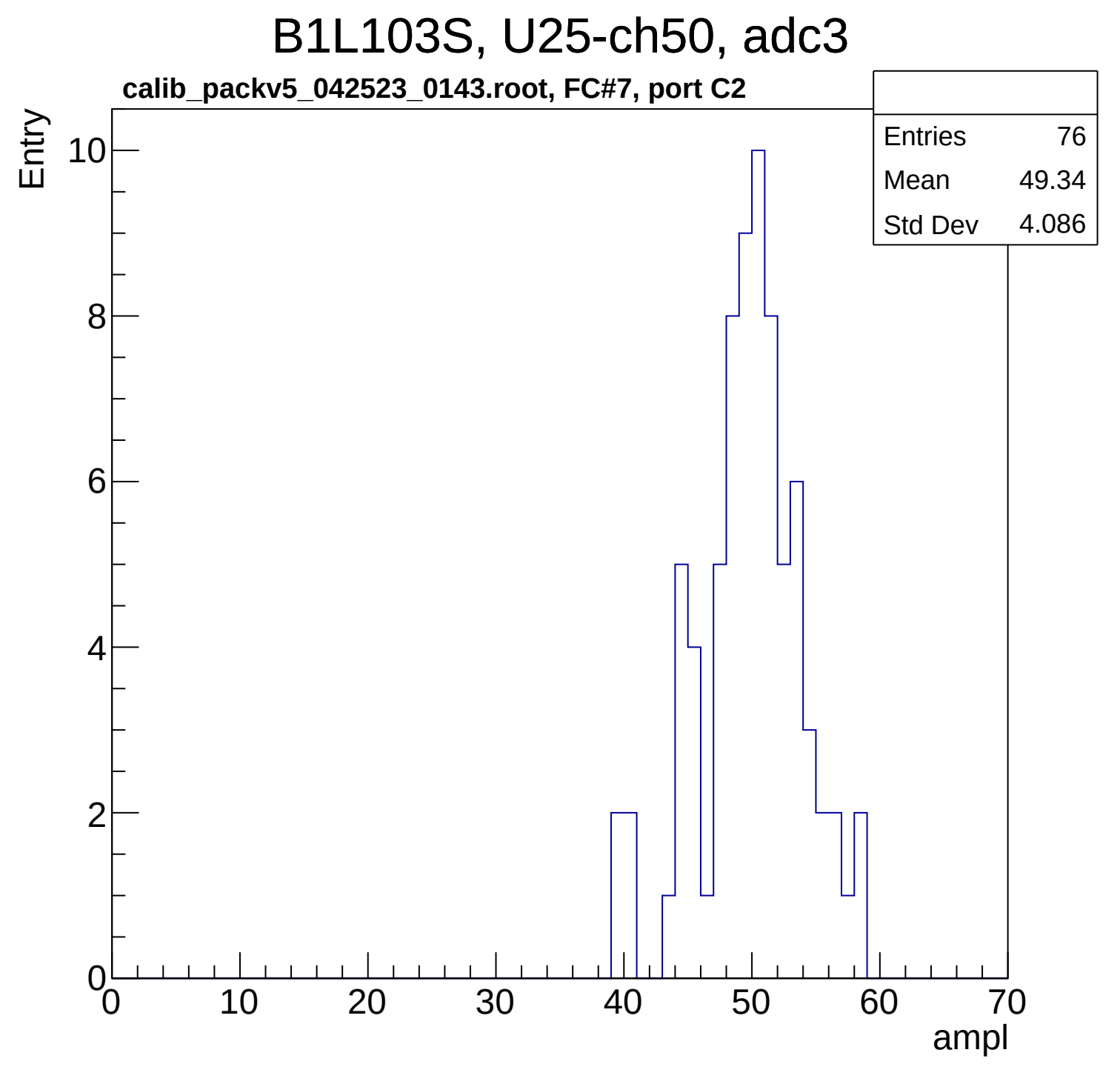
B1L103S, U25-ch50, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 49.34
Std Dev 4.086
Entry
ampl
0
2
4
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0
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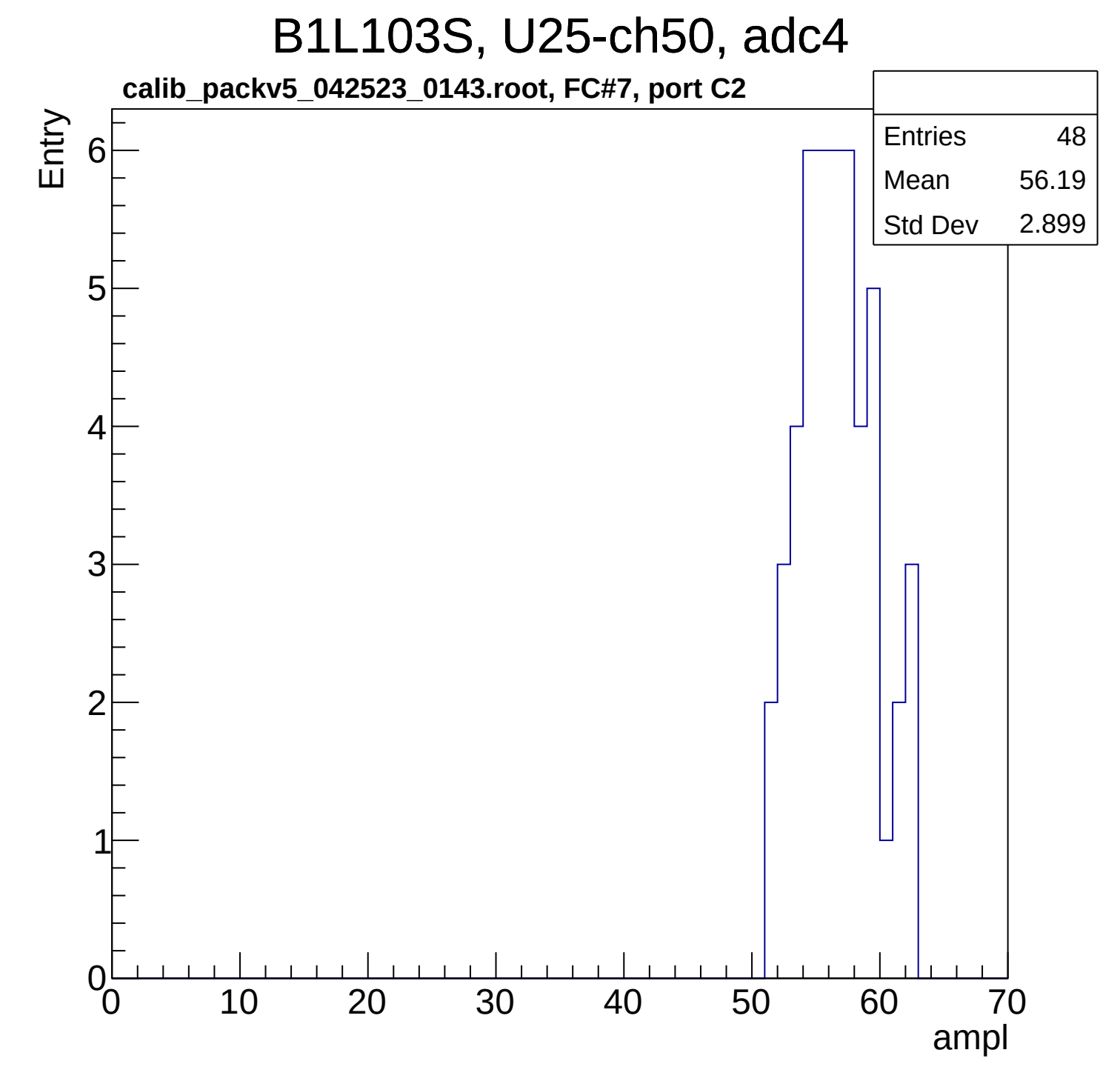
B1L103S, U25-ch50, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 56.19
Std Dev 2.899
Entry
ampl
0
1
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0
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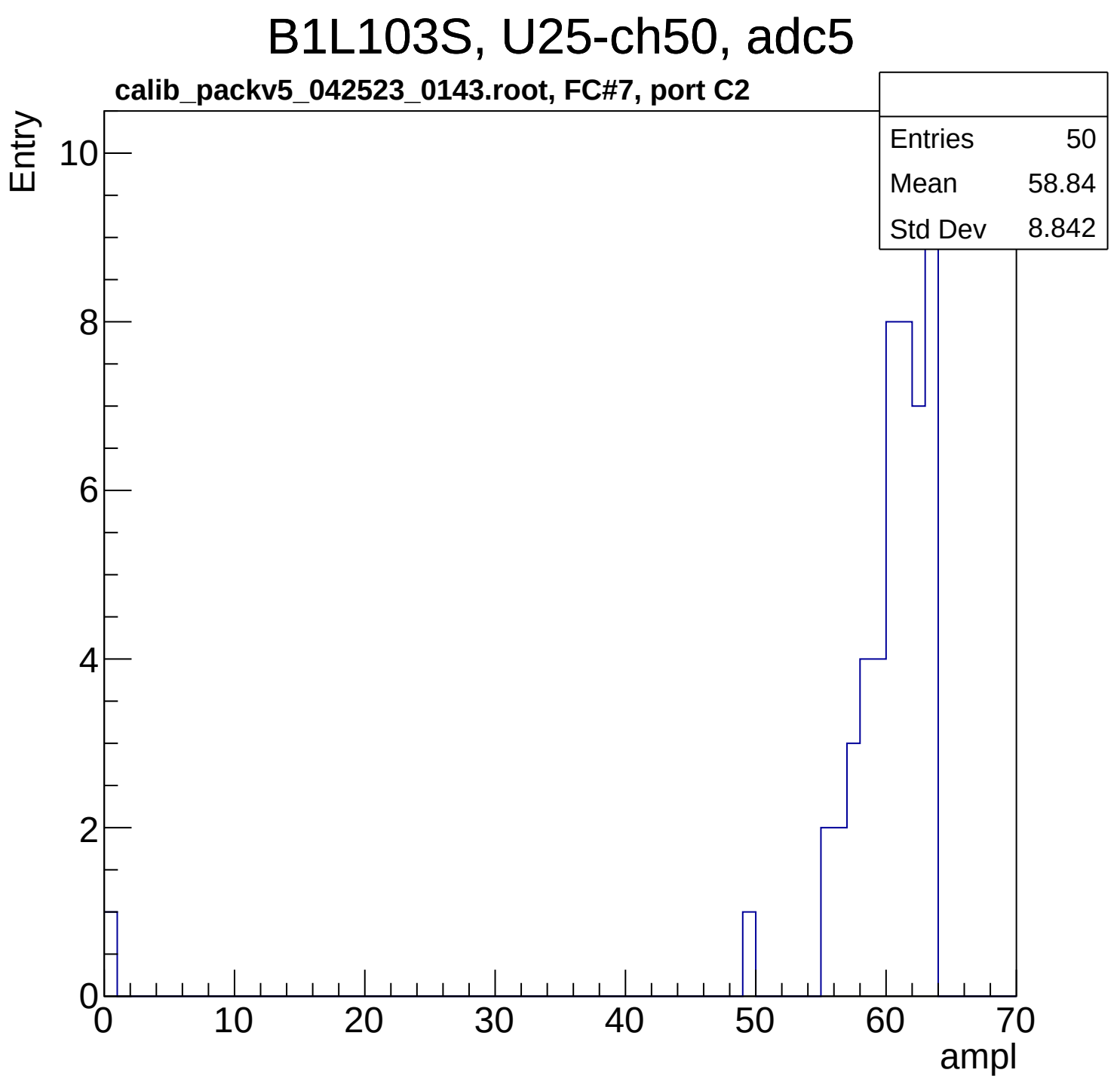
B1L103S, U25-ch50, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.84
Std Dev 8.842
Entry
ampl
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2
4
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0
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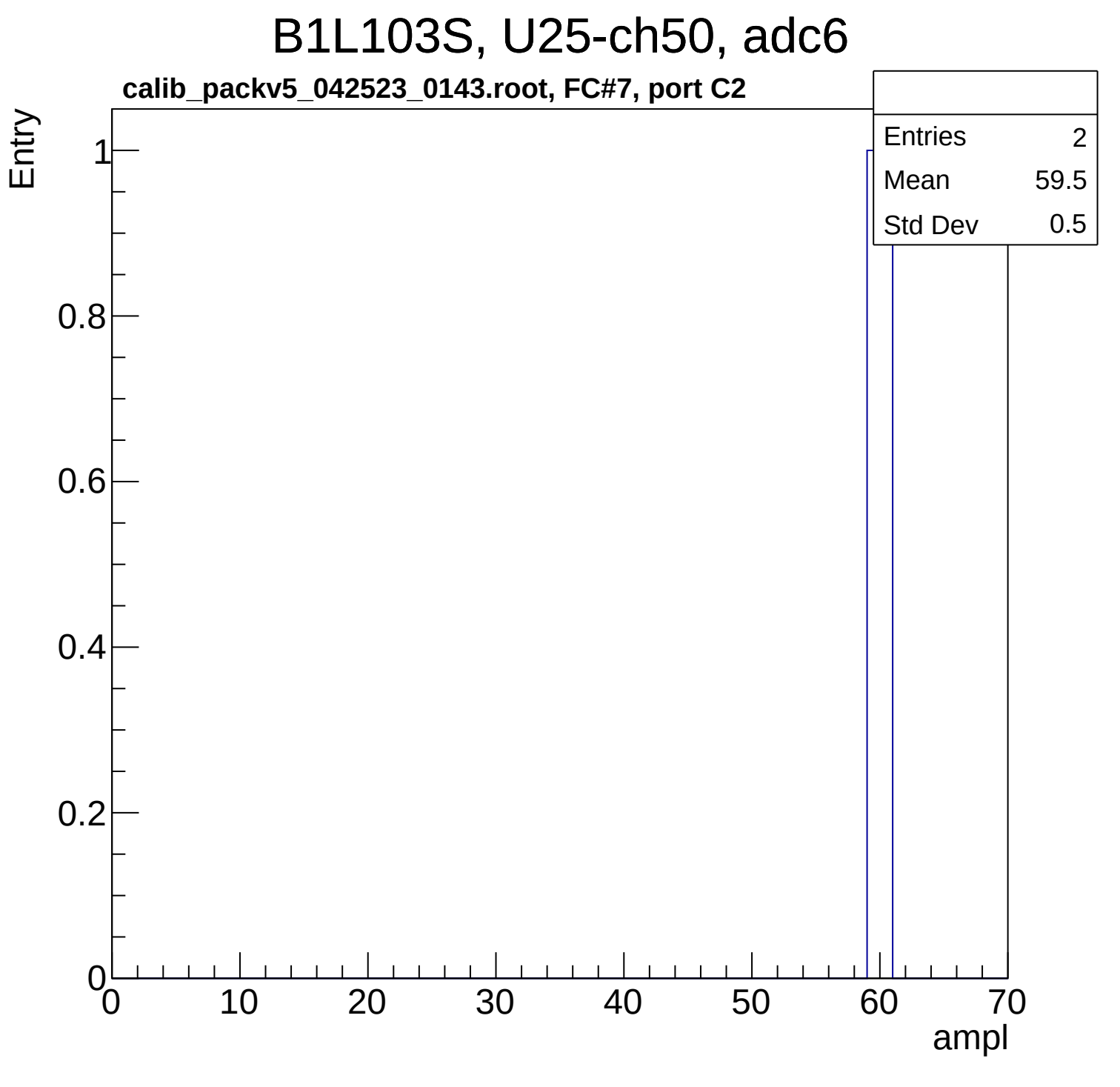
B1L103S, U25-ch50, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 59.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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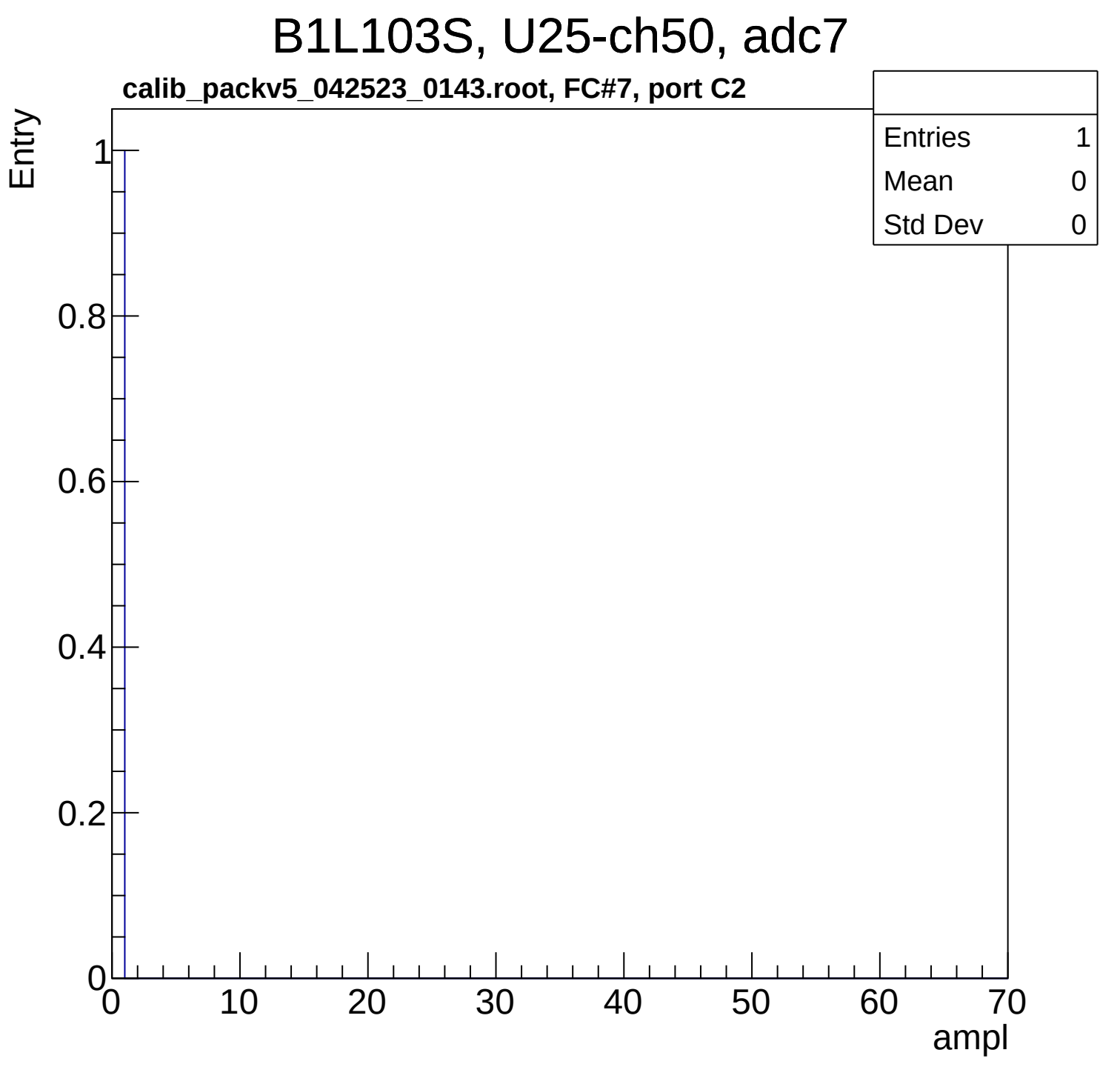
50
60
70
ampl


B1L103S, U25-ch50, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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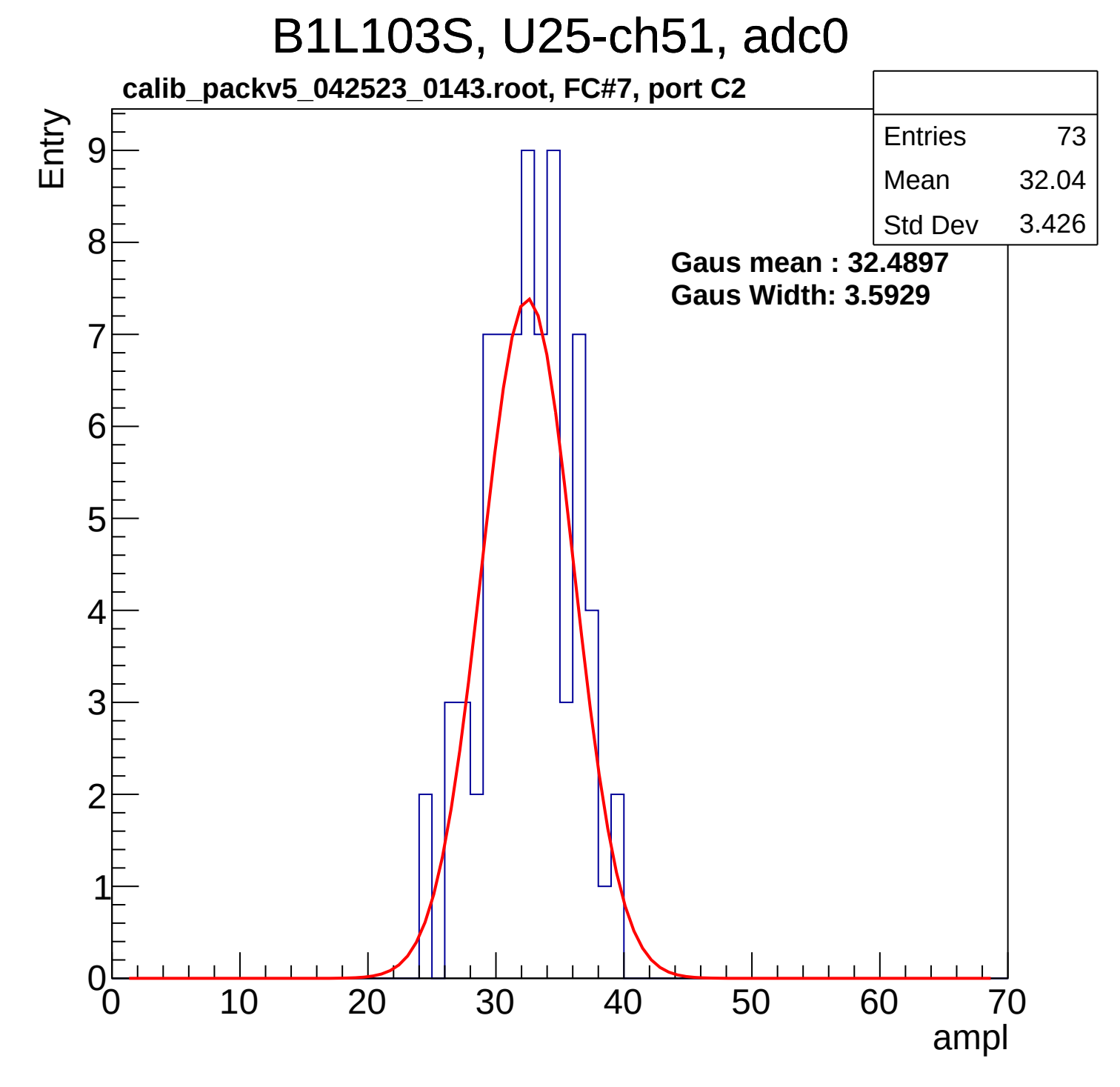
30
40
50
60
70
ampl


B1L103S, U25-ch51, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 32.04
Std Dev 3.426
Gaus mean : 32.4897
Gaus Width: 3.5929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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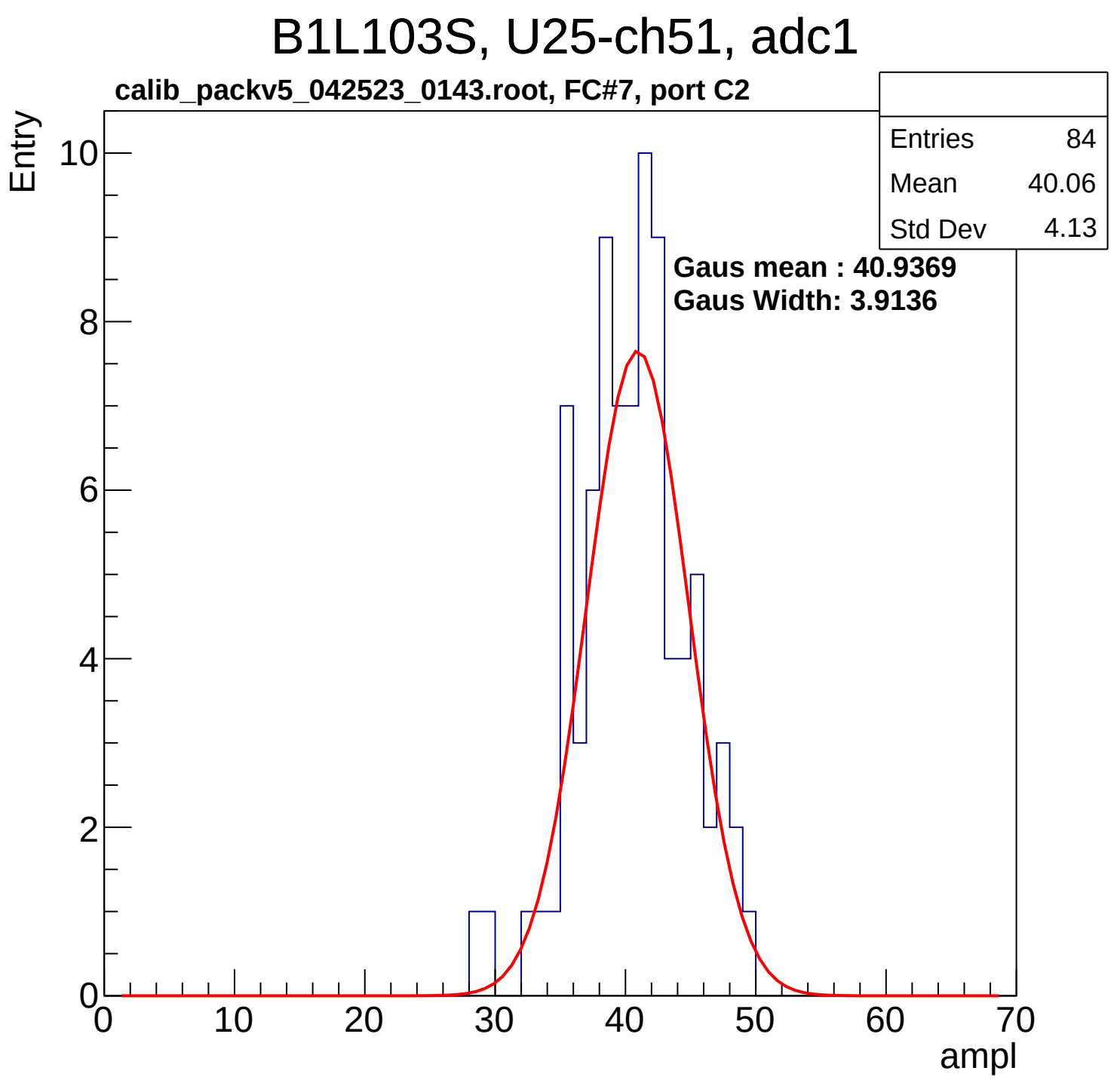
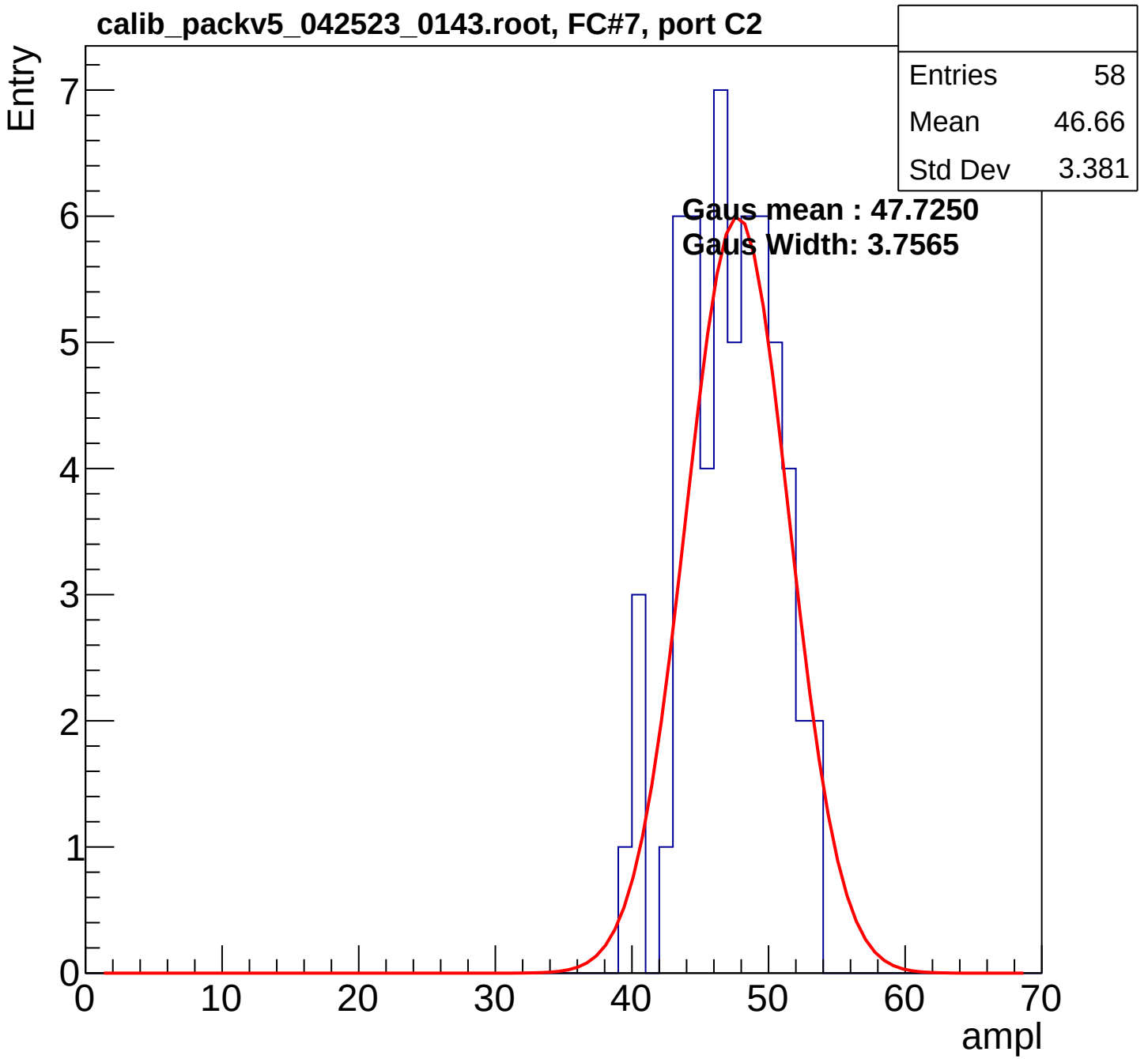

B1L103S, U25-ch51, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 40.06
Std Dev 4.13
Gaus mean : 40.9369
Gaus Width: 3.9136
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch51, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 46.66
Std Dev 3.381
Gaus mean : 47.7250
Gaus Width: 3.7565
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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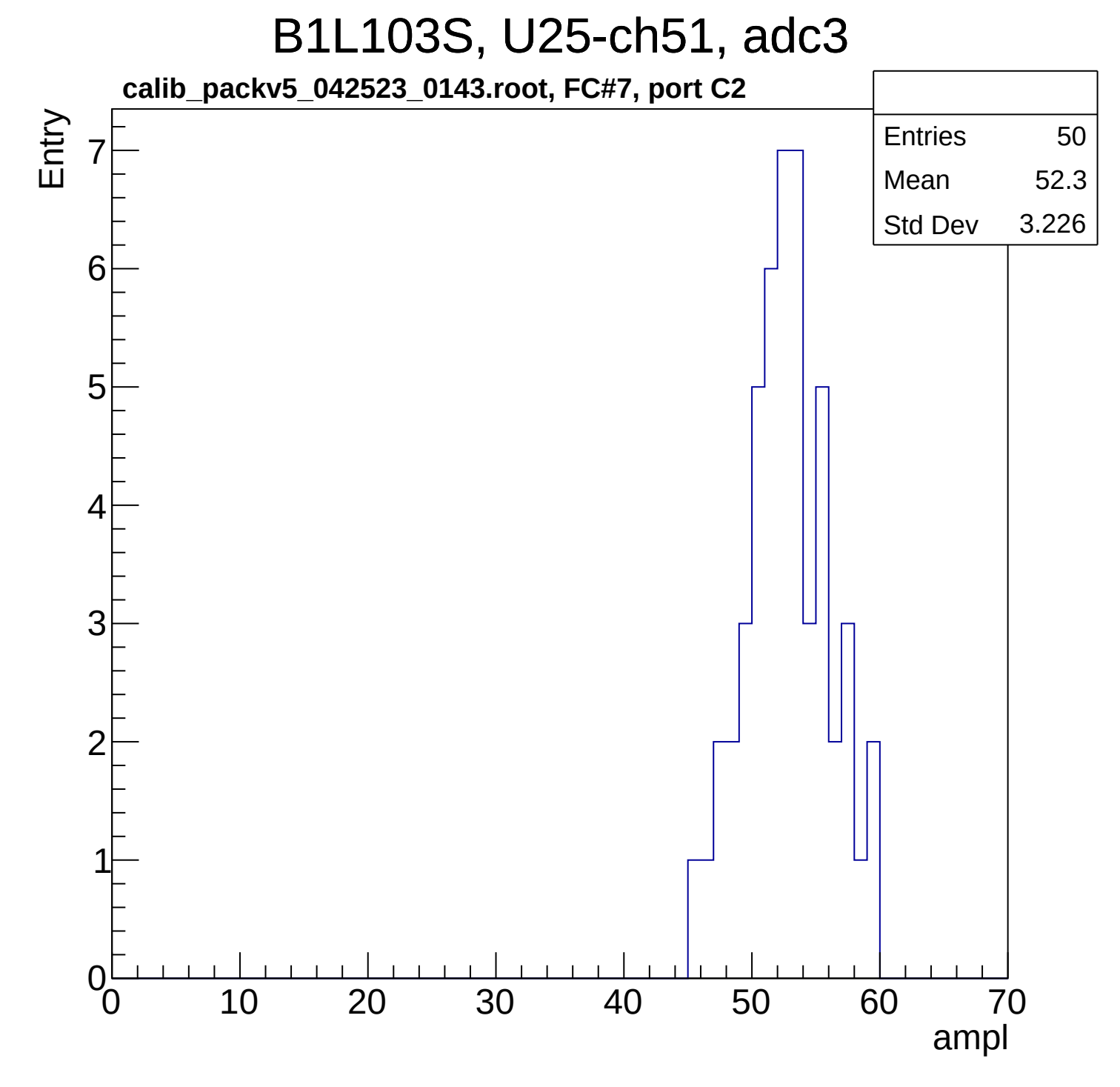
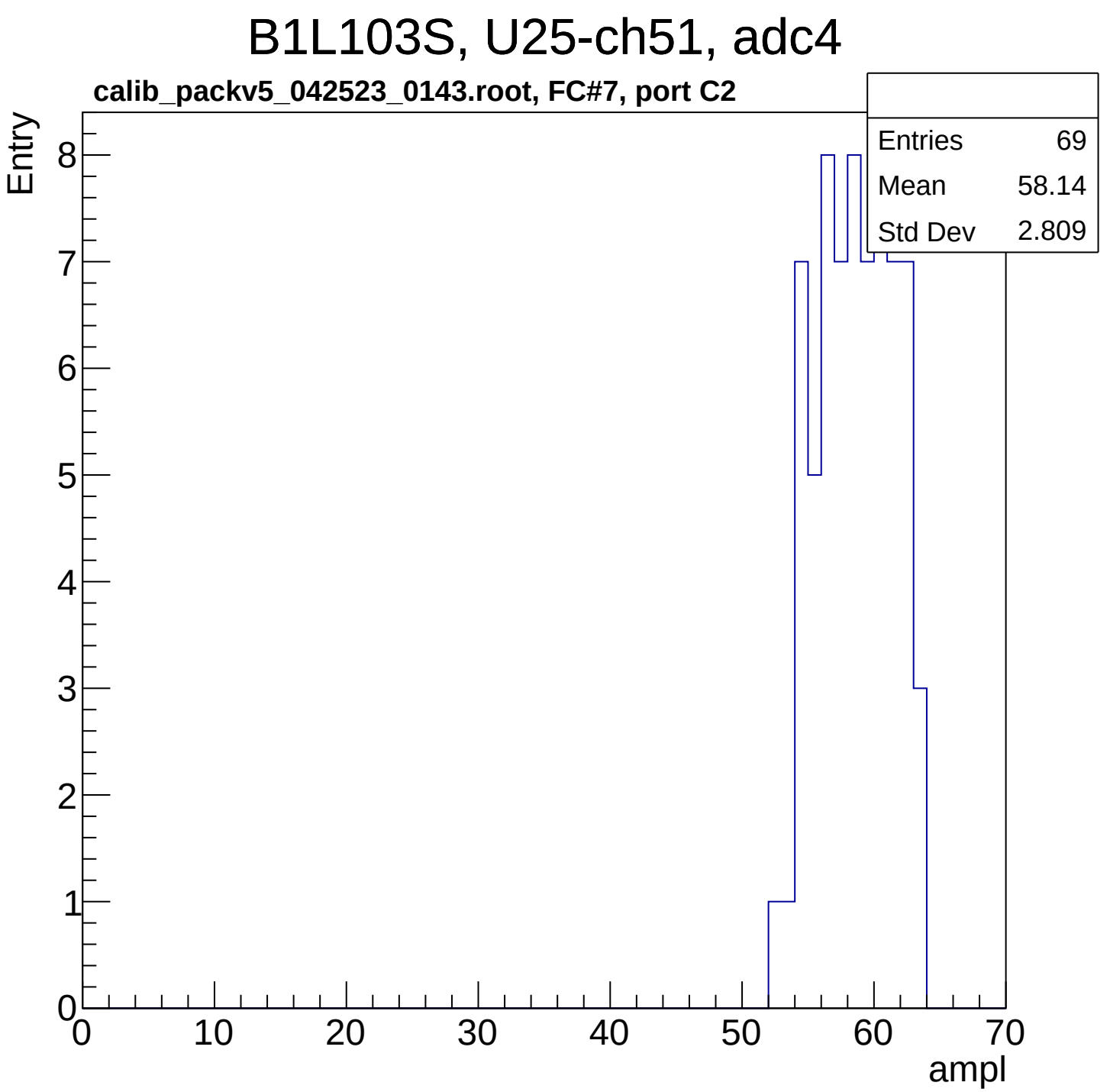

B1L103S, U25-ch51, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 52.3
Std Dev 3.226
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch51, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 58.14
Std Dev 2.809
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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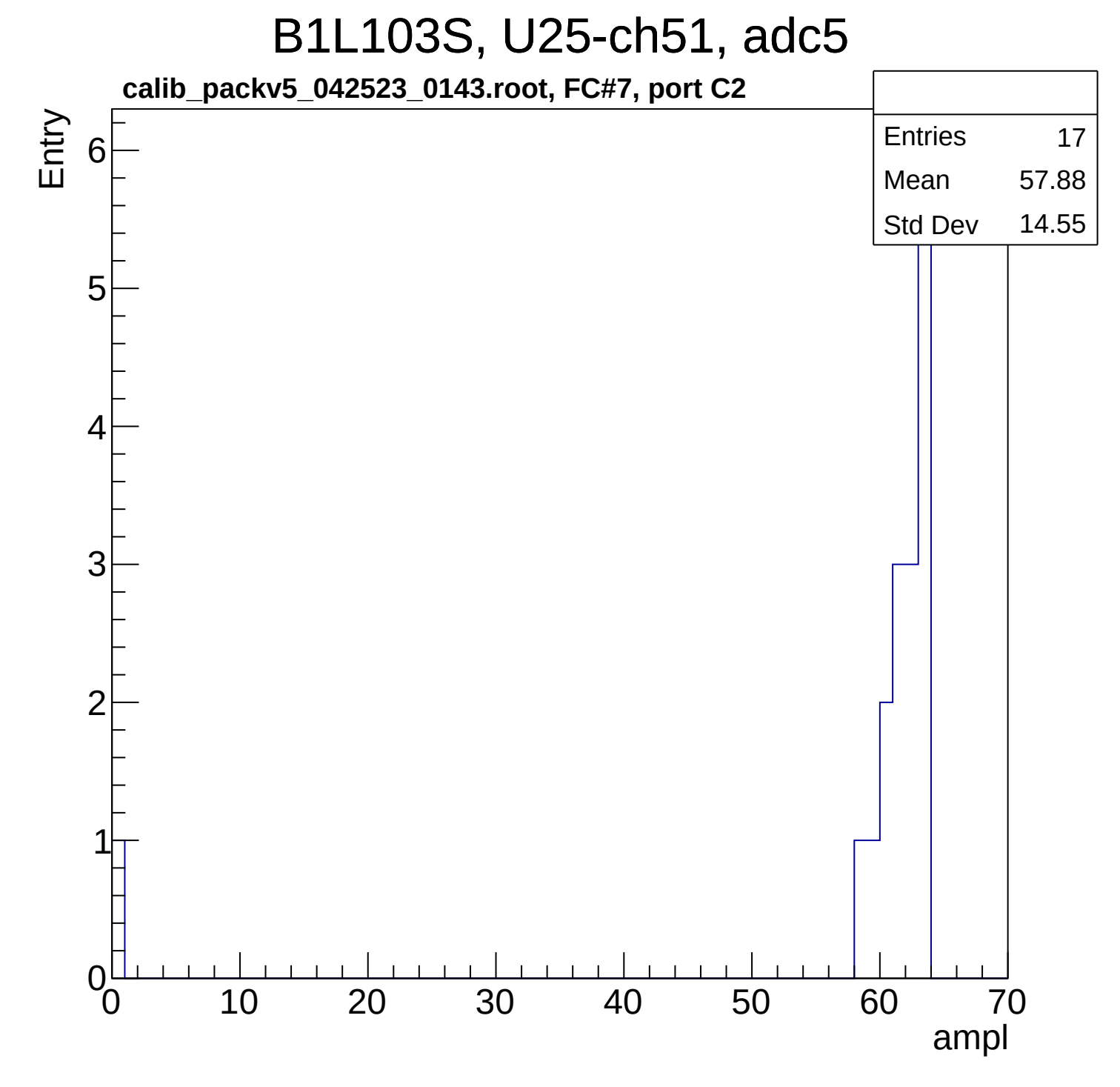
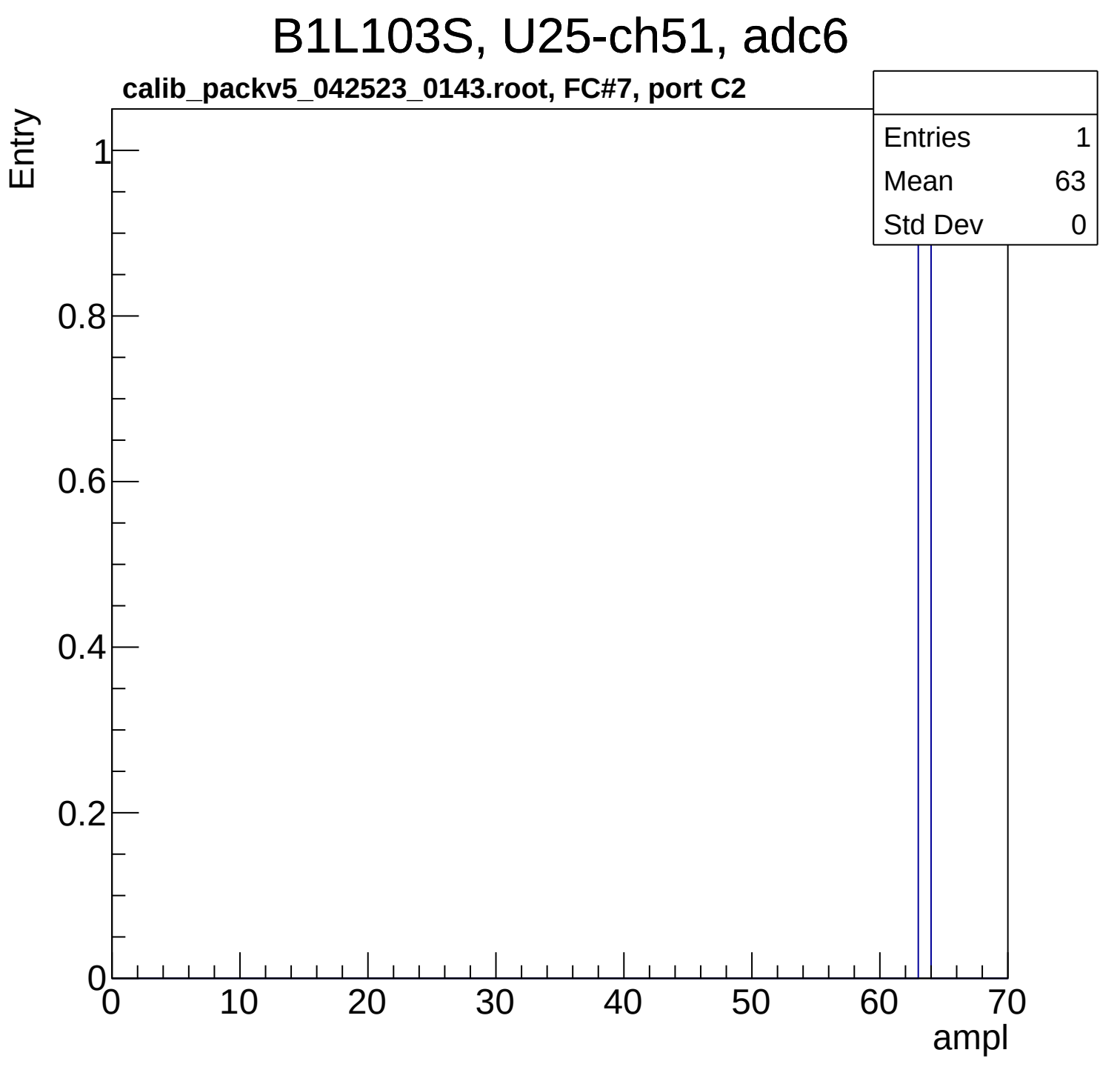
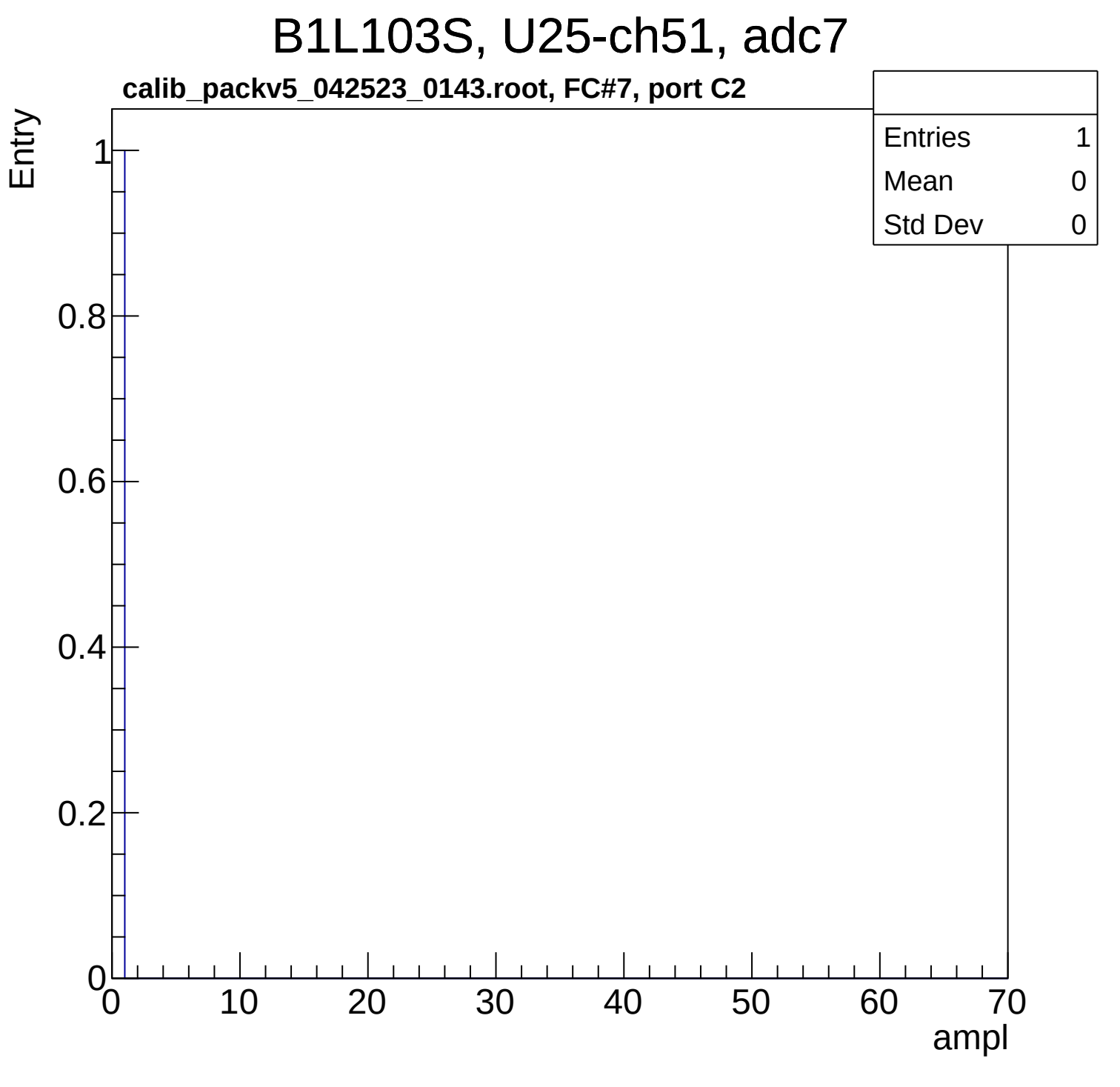
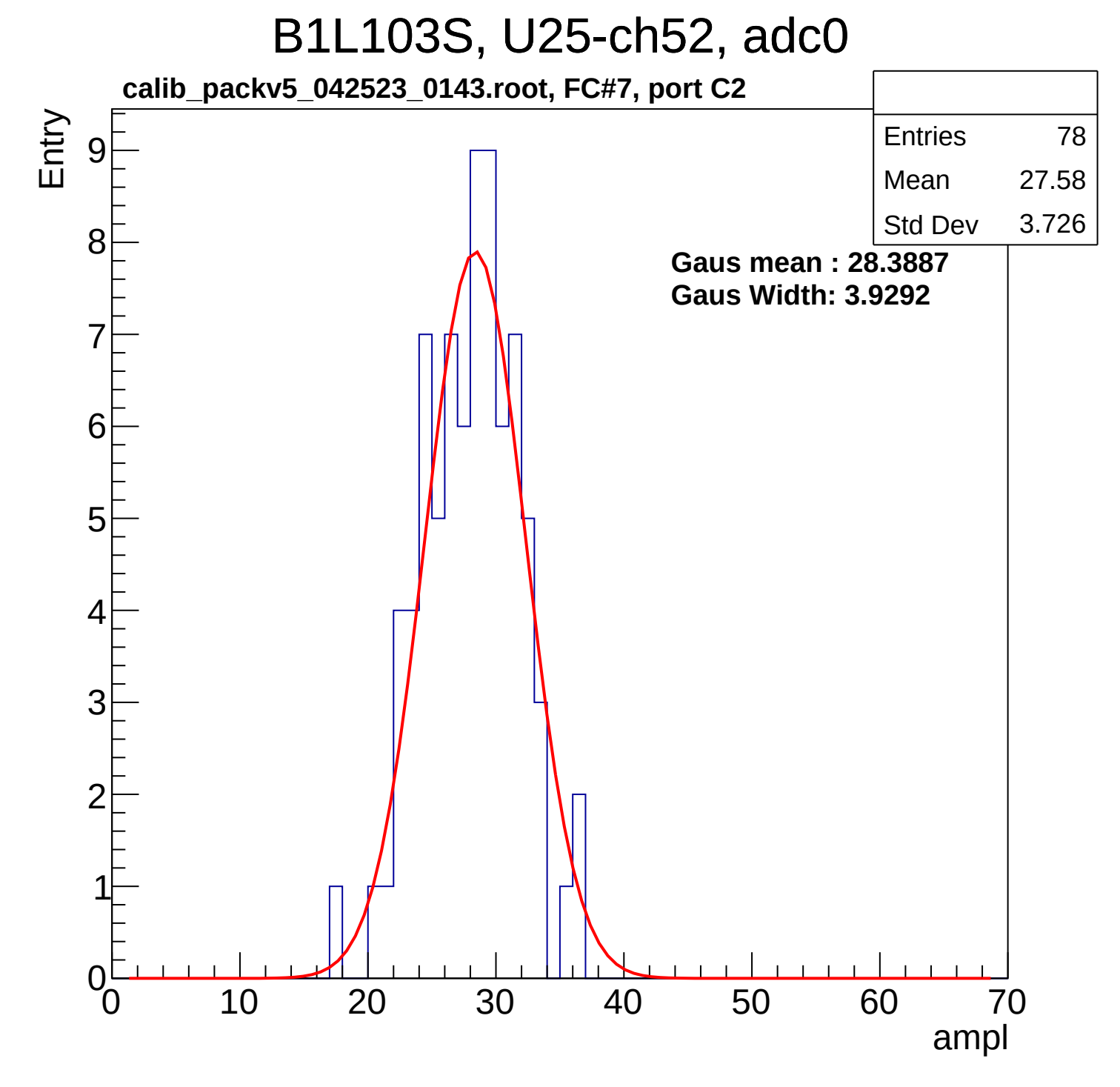
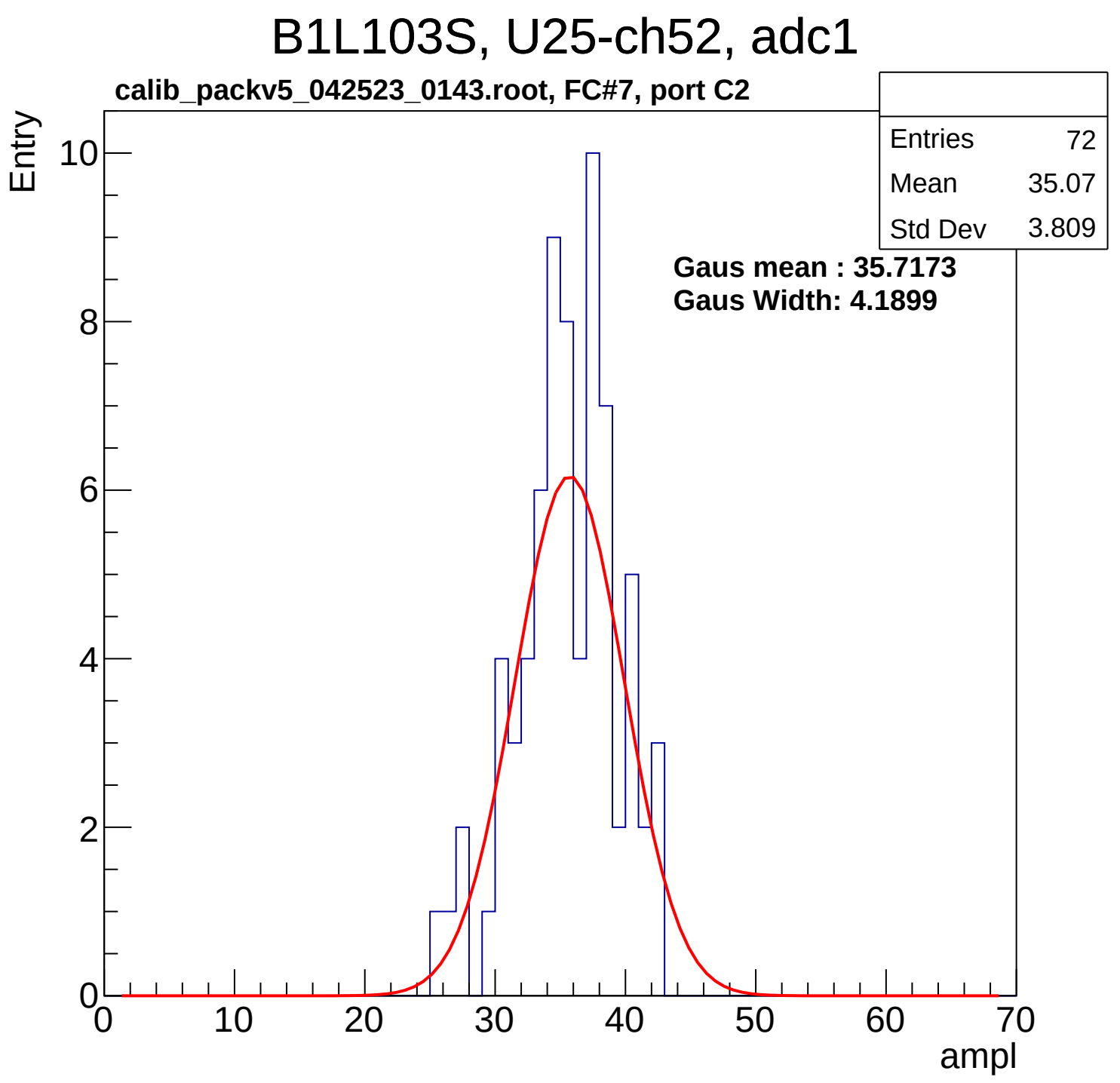
40
50
60
70

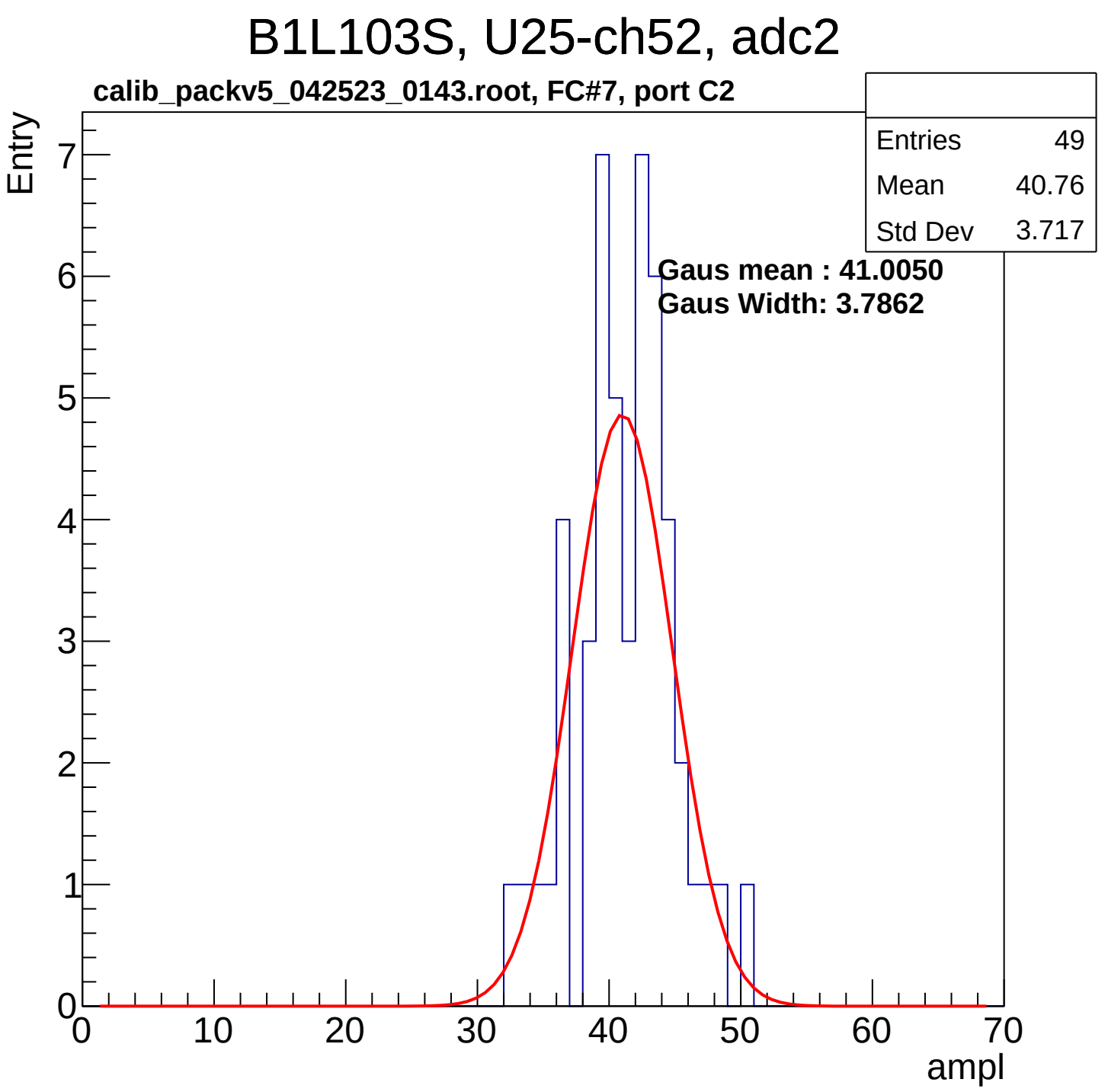

B1L103S, U25-ch51, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 57.88
Std Dev 14.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

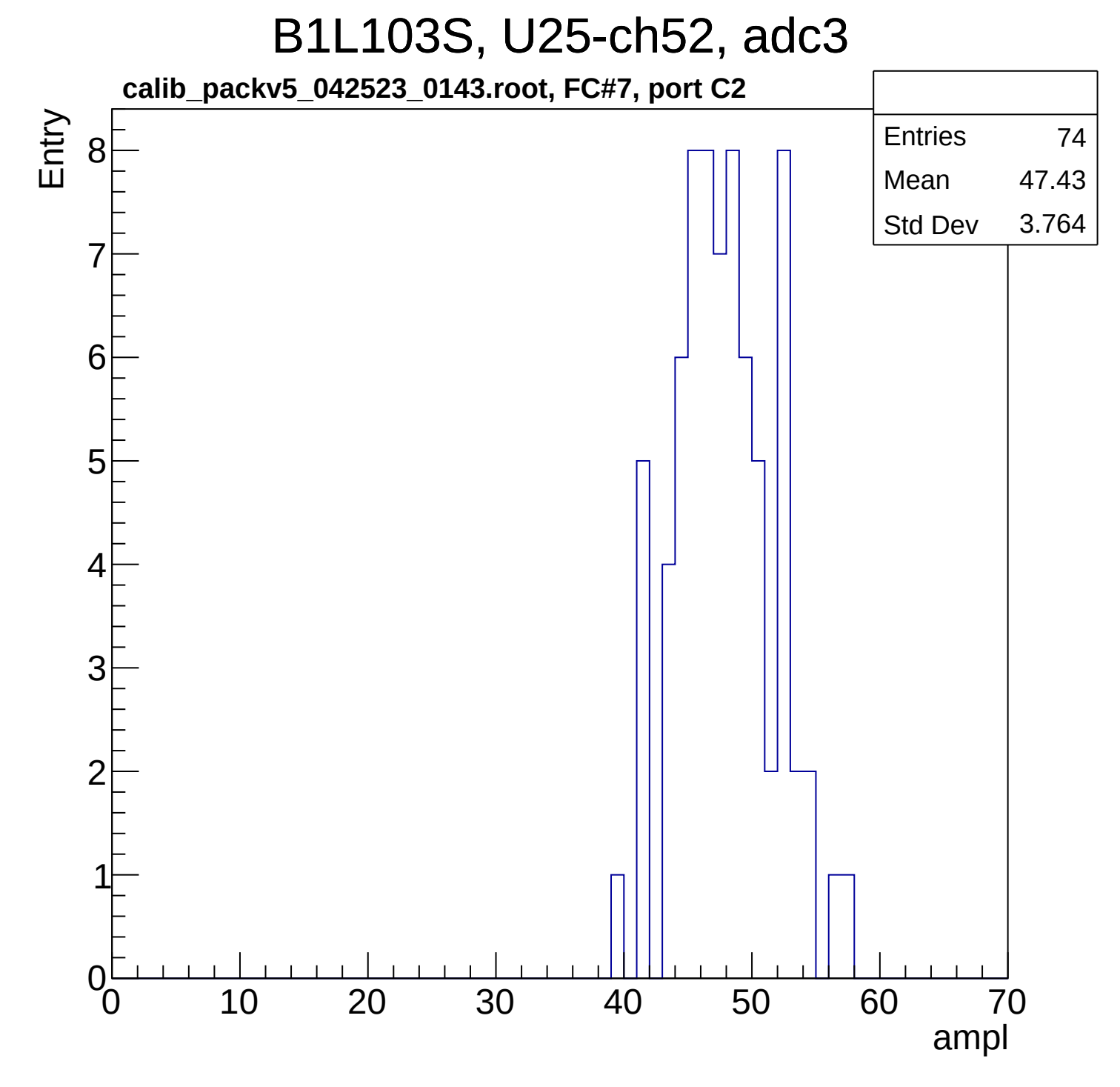

B1L103S, U25-ch51, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch51, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch52, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 27.58
Std Dev 3.726
Gaus mean : 28.3887
Gaus Width: 3.9292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch52, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 35.07
Std Dev 3.809
Gaus mean : 35.7173
Gaus Width: 4.1899
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch52, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 40.76
Std Dev 3.717
Gaus mean : 41.0050
Gaus Width: 3.7862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch52, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 47.43
Std Dev 3.764
Entry
ampl
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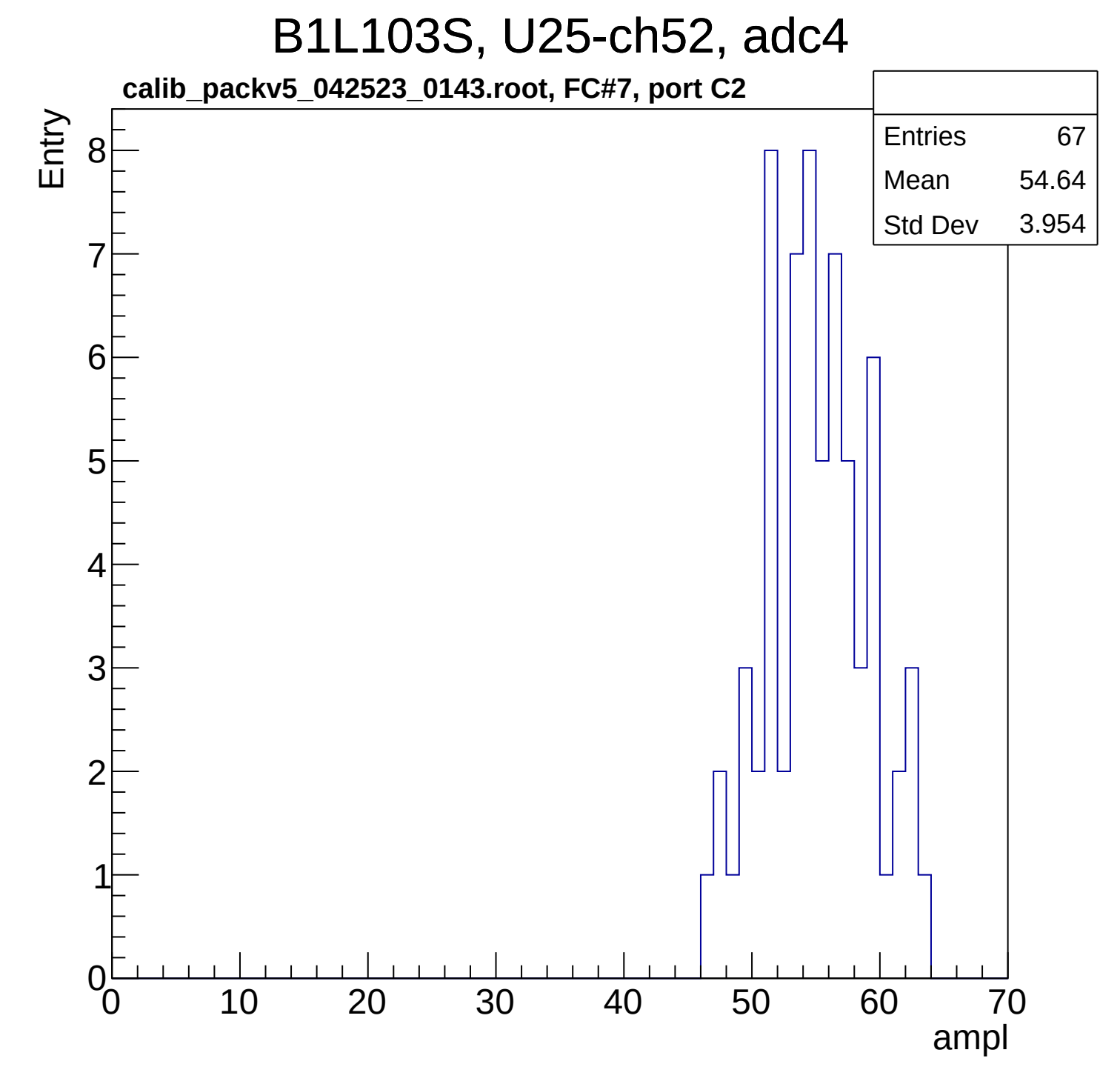
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch52, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.64
Std Dev 3.954
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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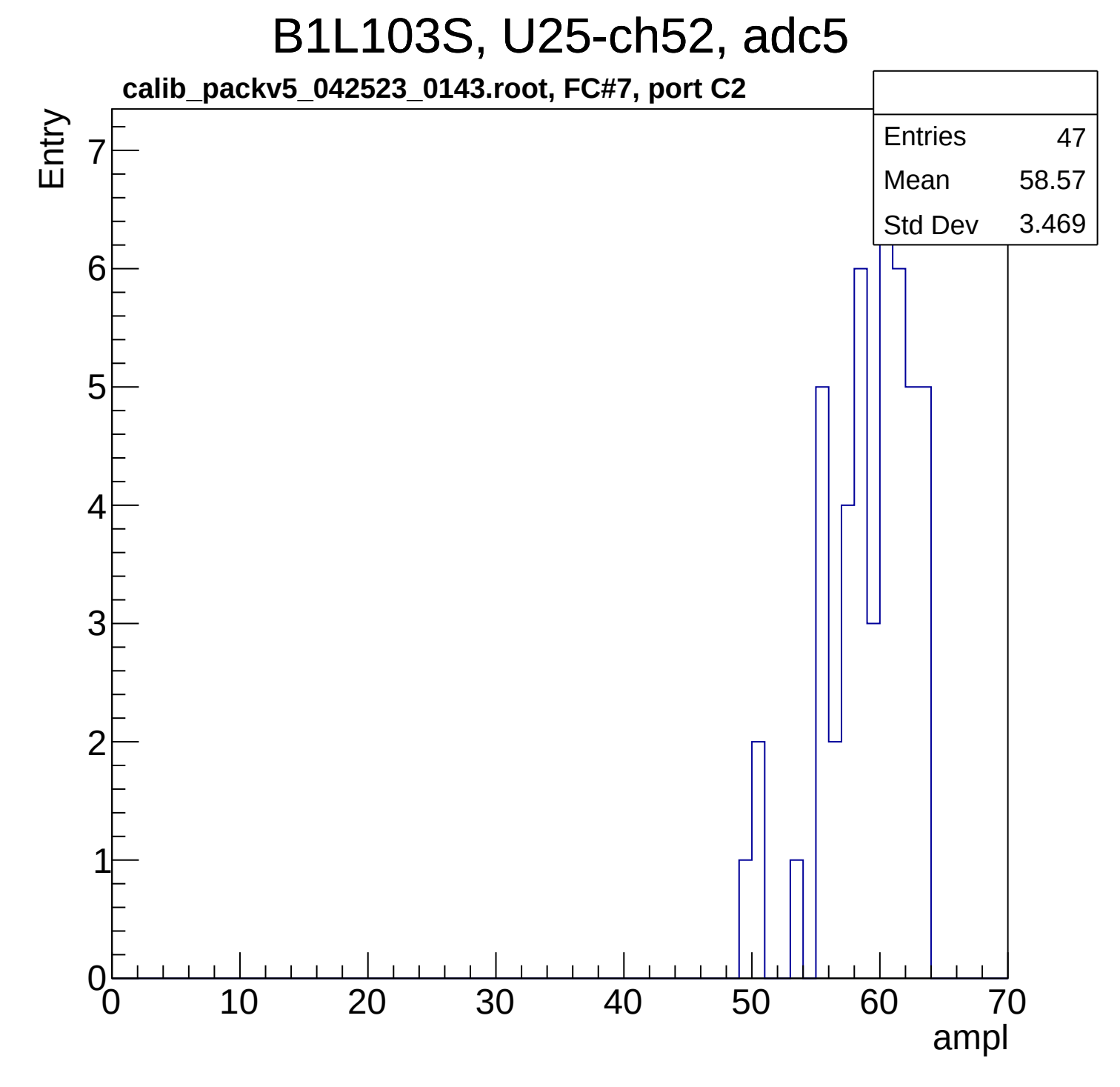
B1L103S, U25-ch52, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.57
Std Dev 3.469
Entry
ampl
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1
2
3
4
5
6
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0
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40
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60
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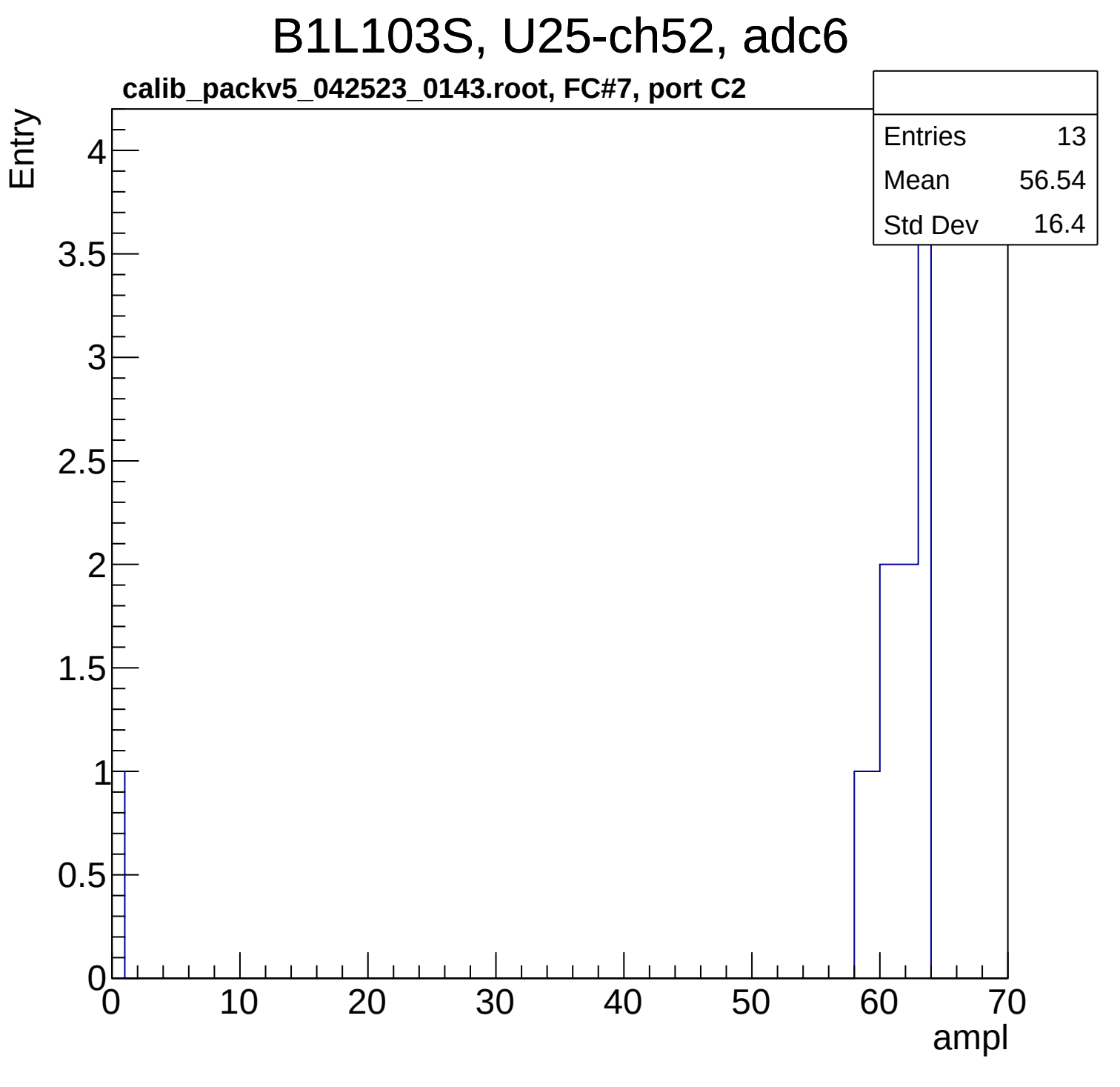
B1L103S, U25-ch52, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.54
Std Dev 16.4
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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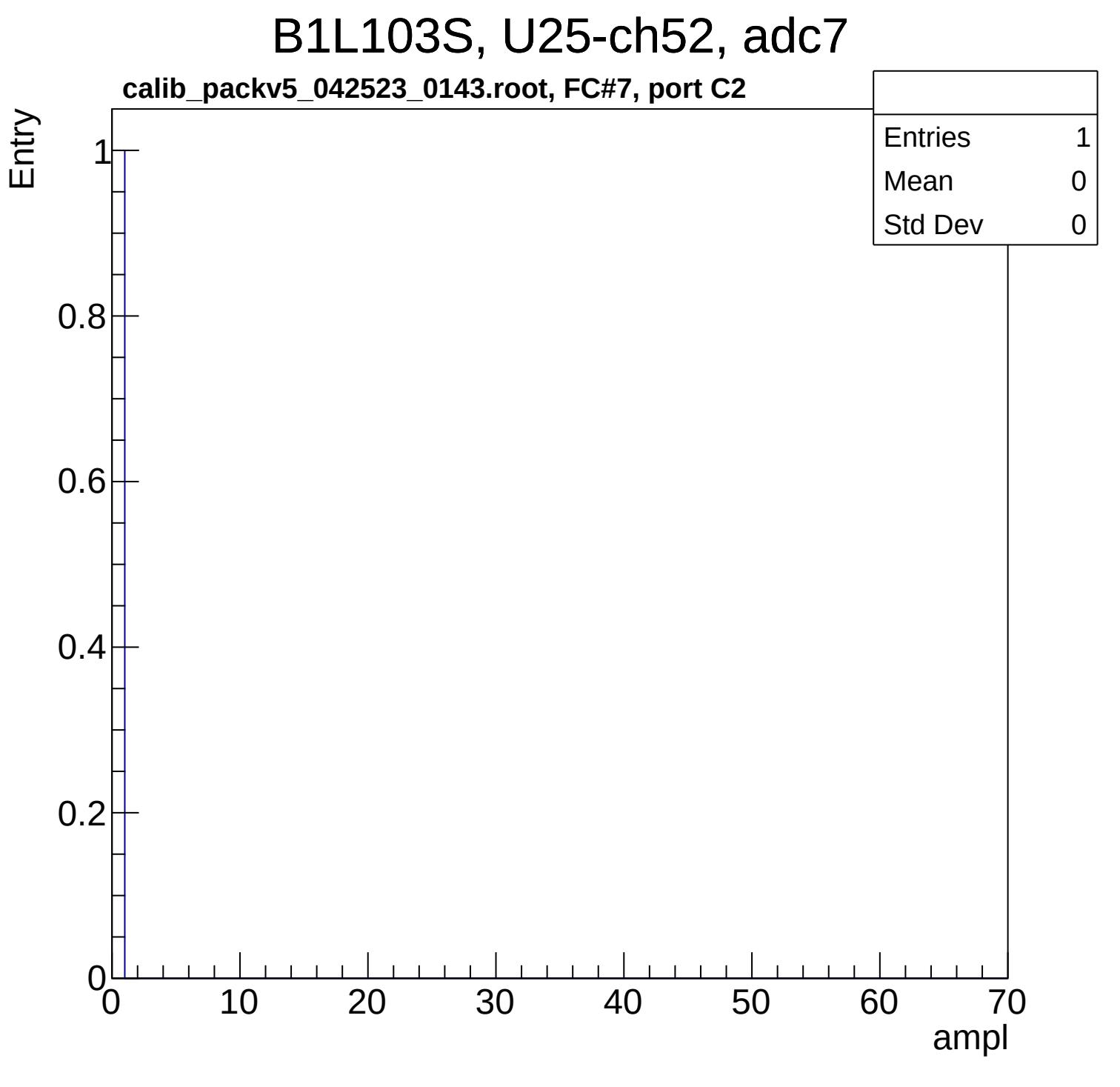
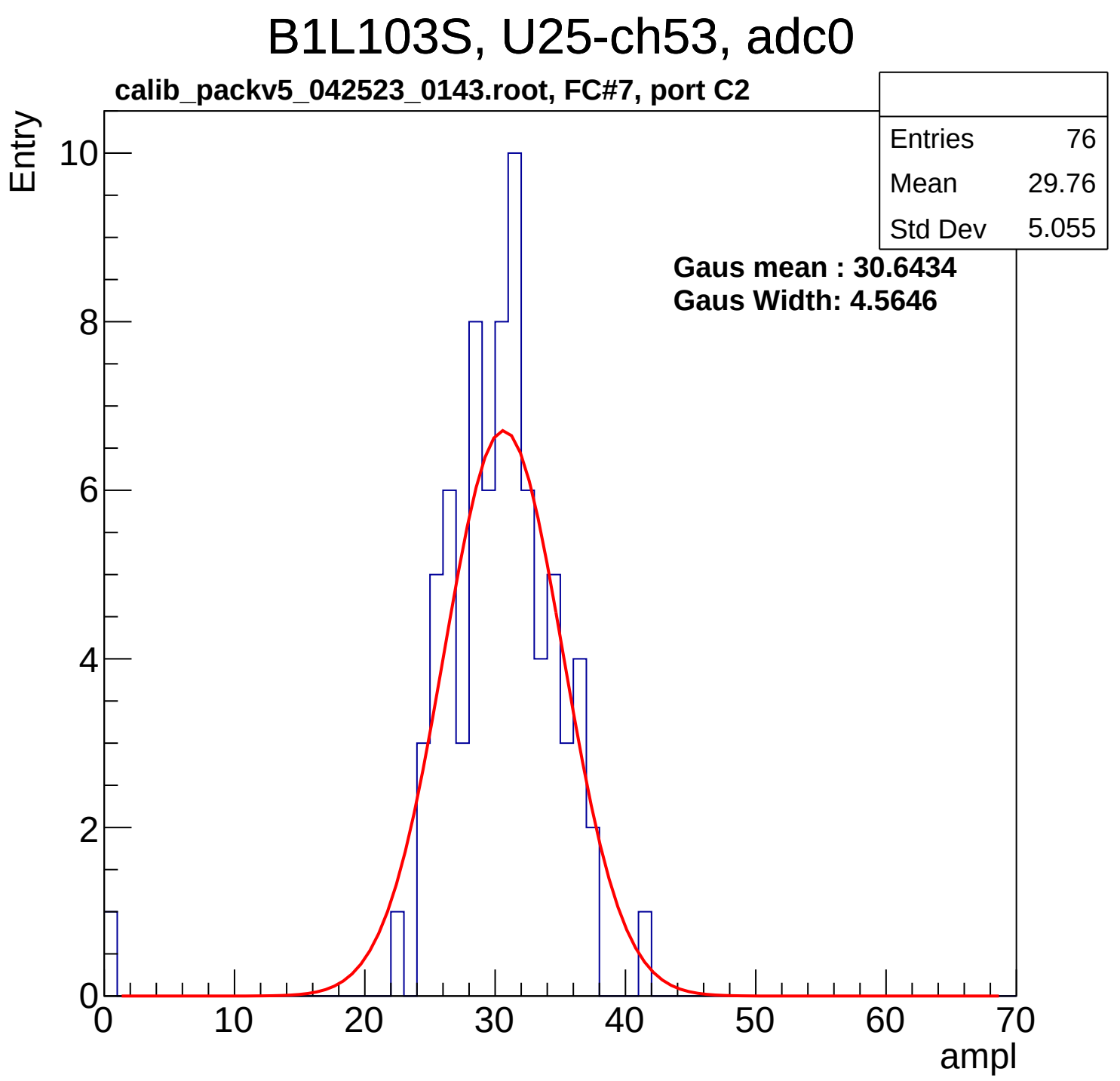
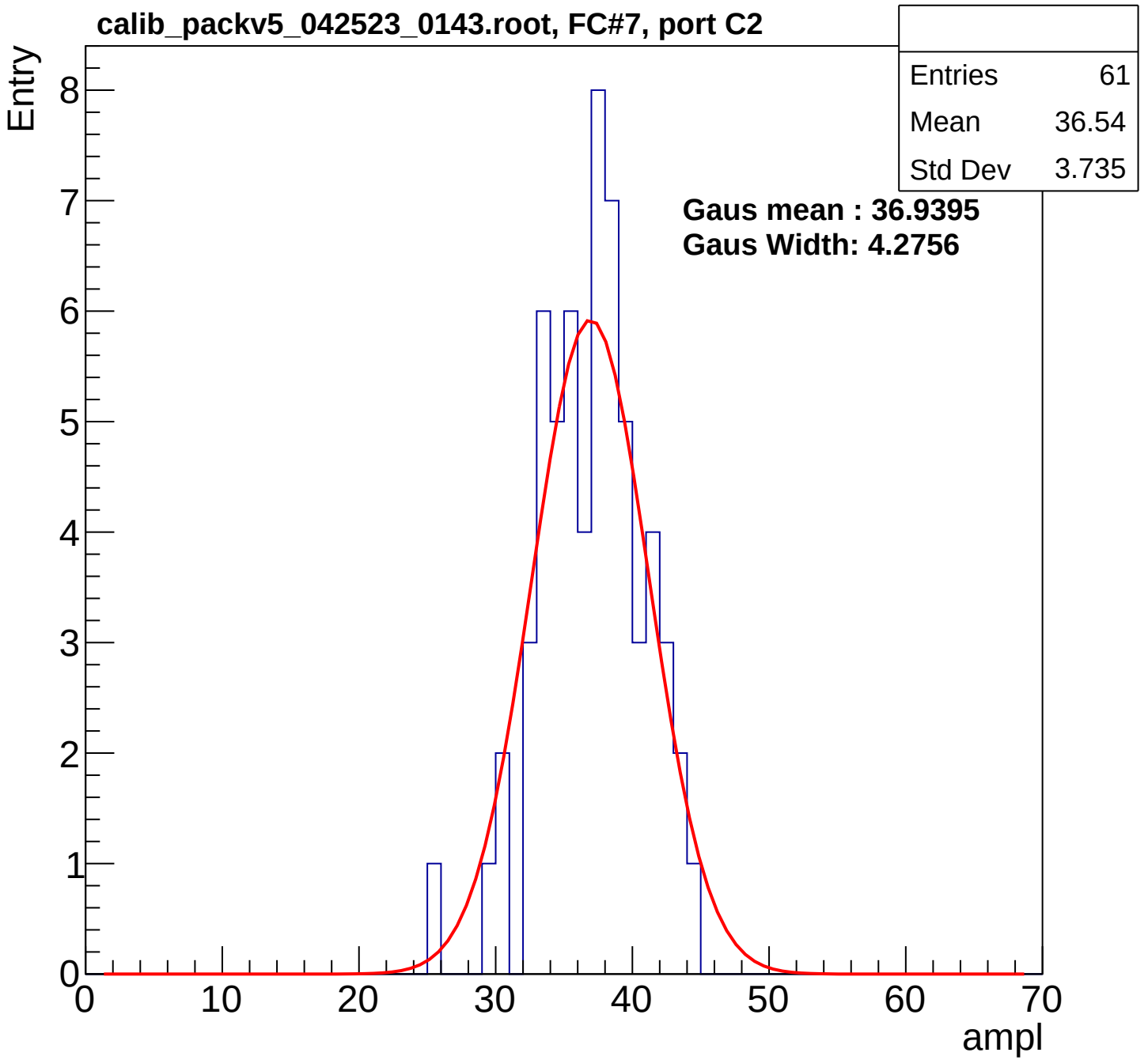

B1L103S, U25-ch52, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch53, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 29.76
Std Dev 5.055
Gaus mean : 30.6434
Gaus Width: 4.5646
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 36.54
Std Dev 3.735
Gaus mean : 36.9395
Gaus Width: 4.2756
Entry
ampl
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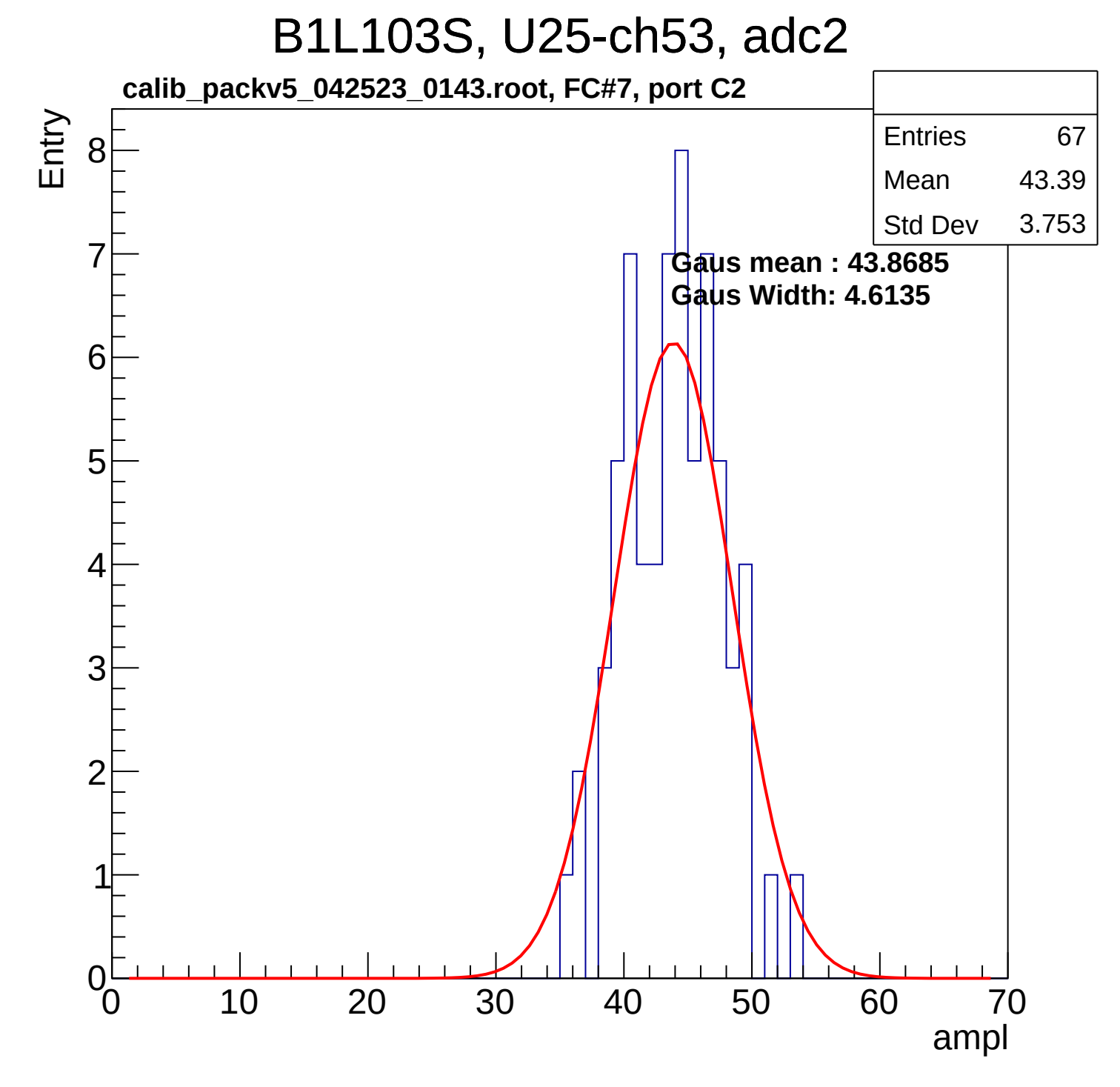
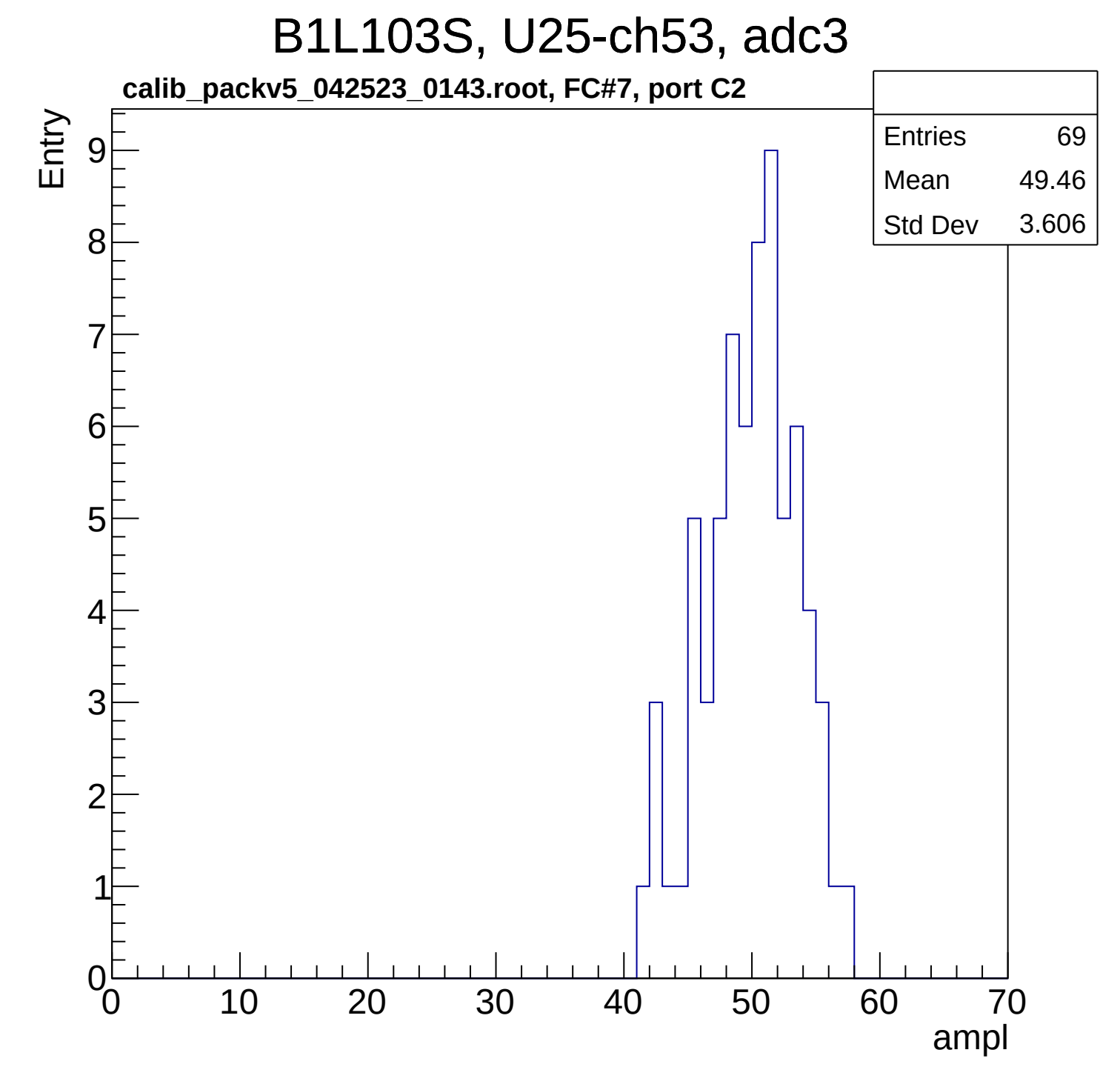
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 43.39
Std Dev 3.753
Gaus mean : 43.8685
Gaus Width: 4.6135
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 49.46
Std Dev 3.606
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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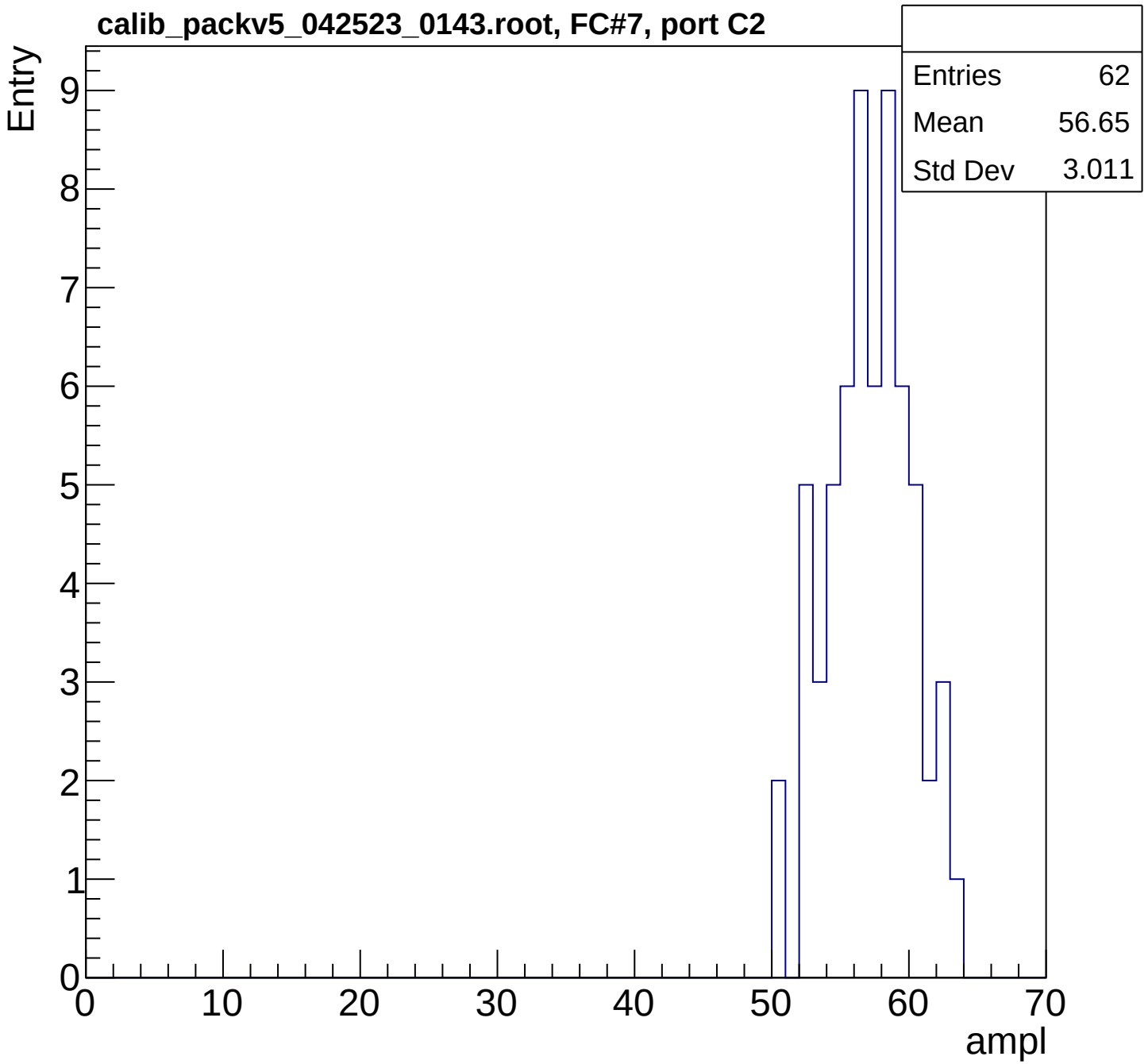
B1L103S, U25-ch53, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 56.65
Std Dev 3.011
Entry
ampl
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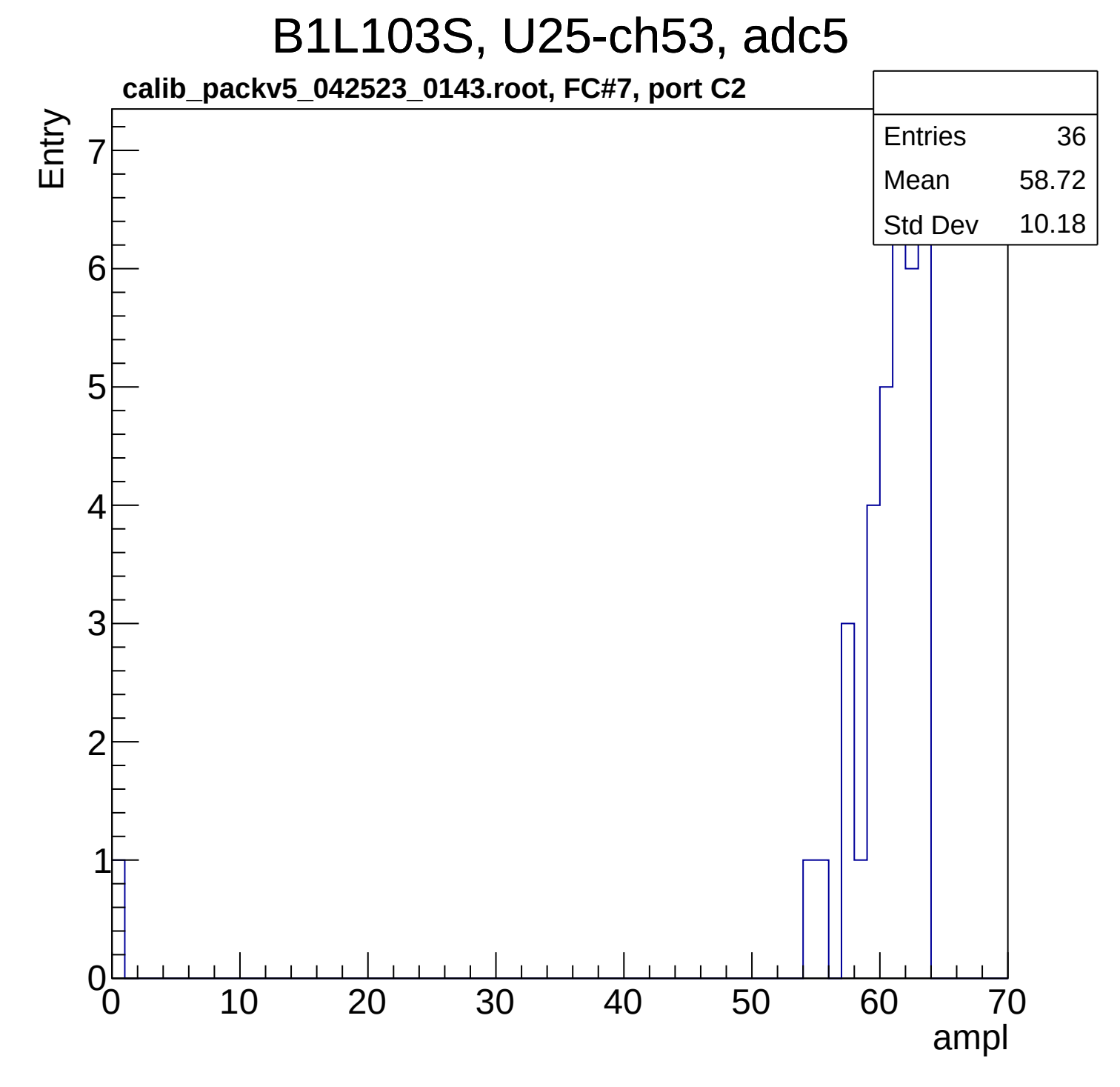
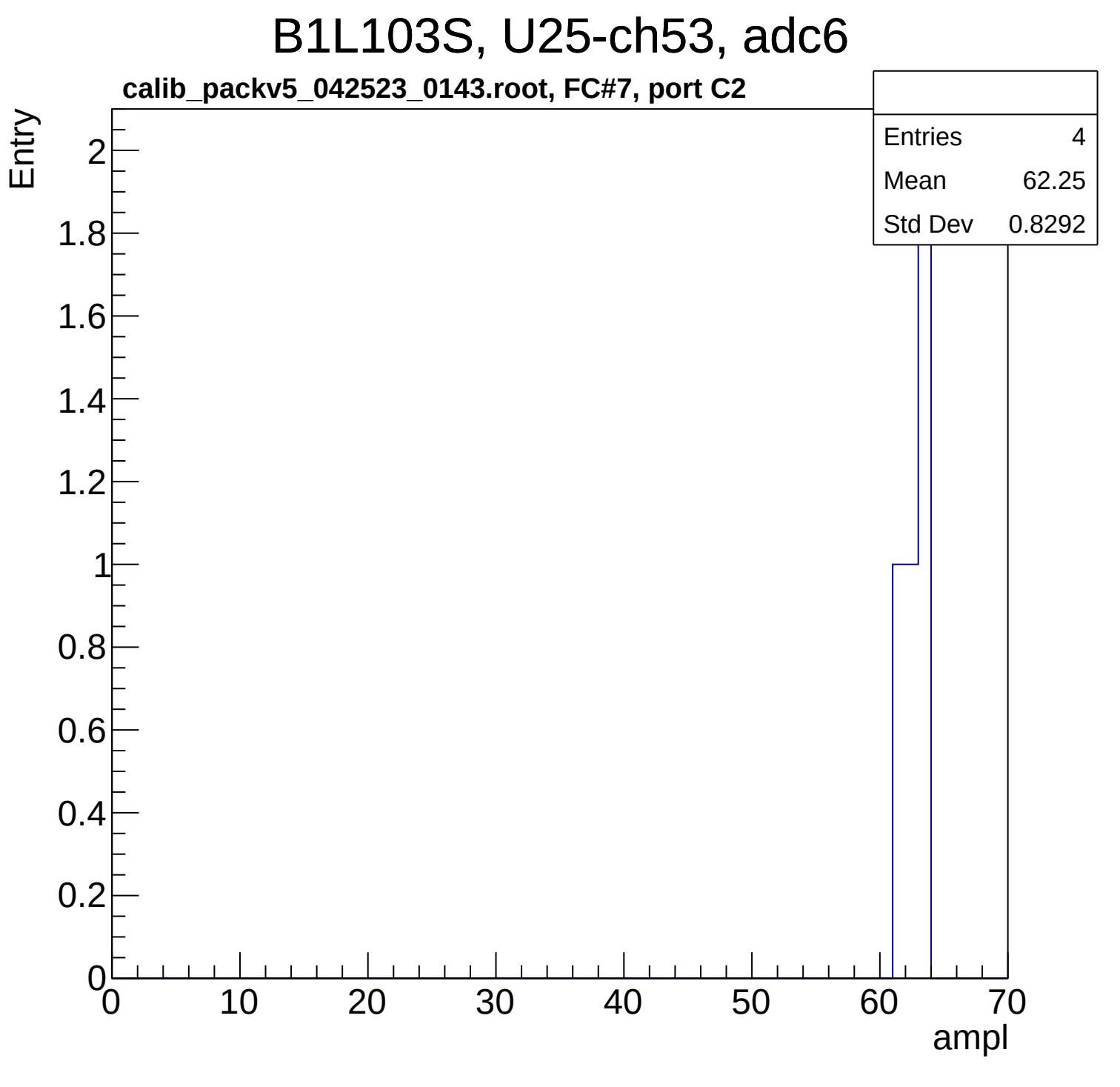
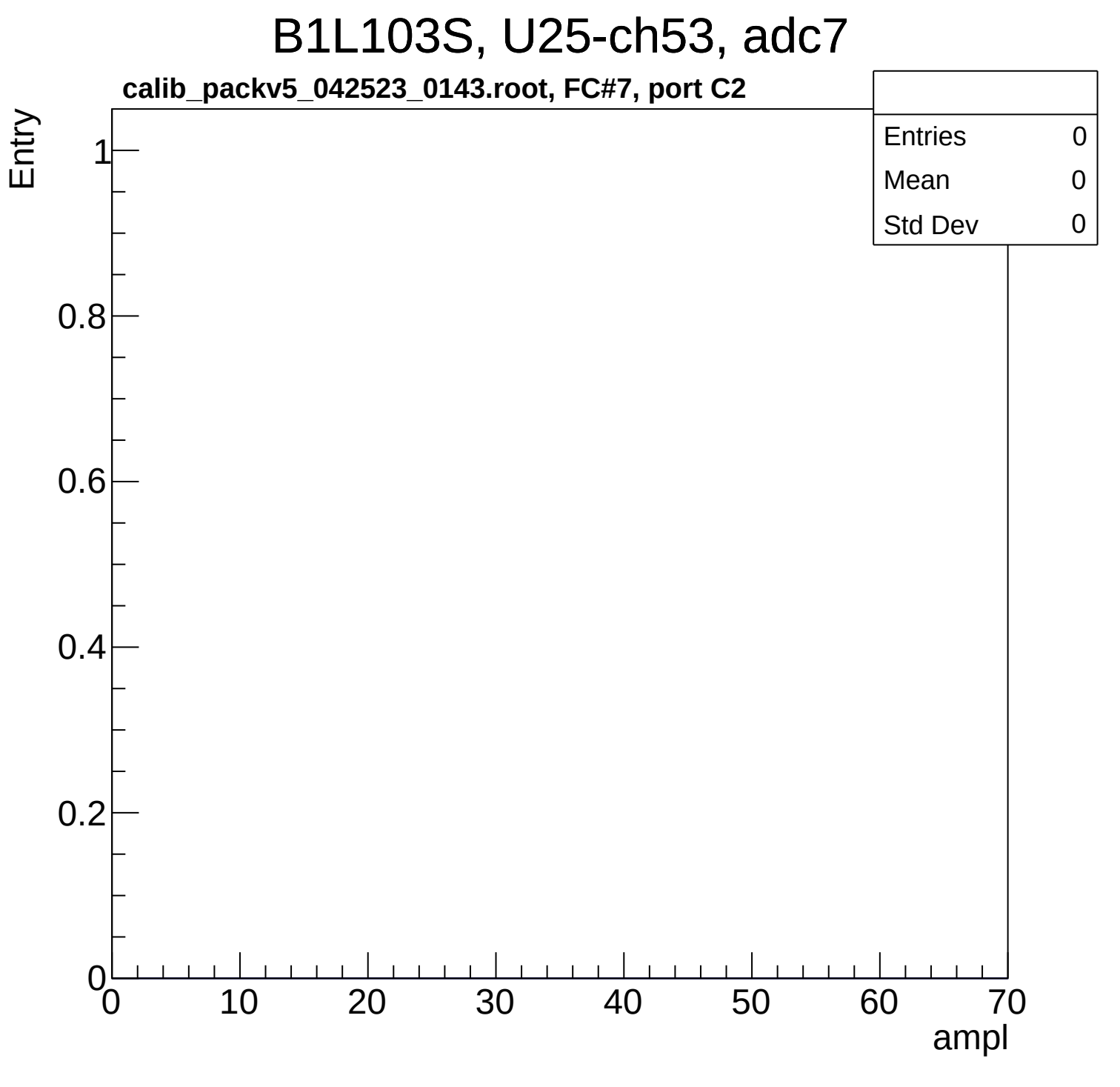
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.72
Std Dev 10.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch53, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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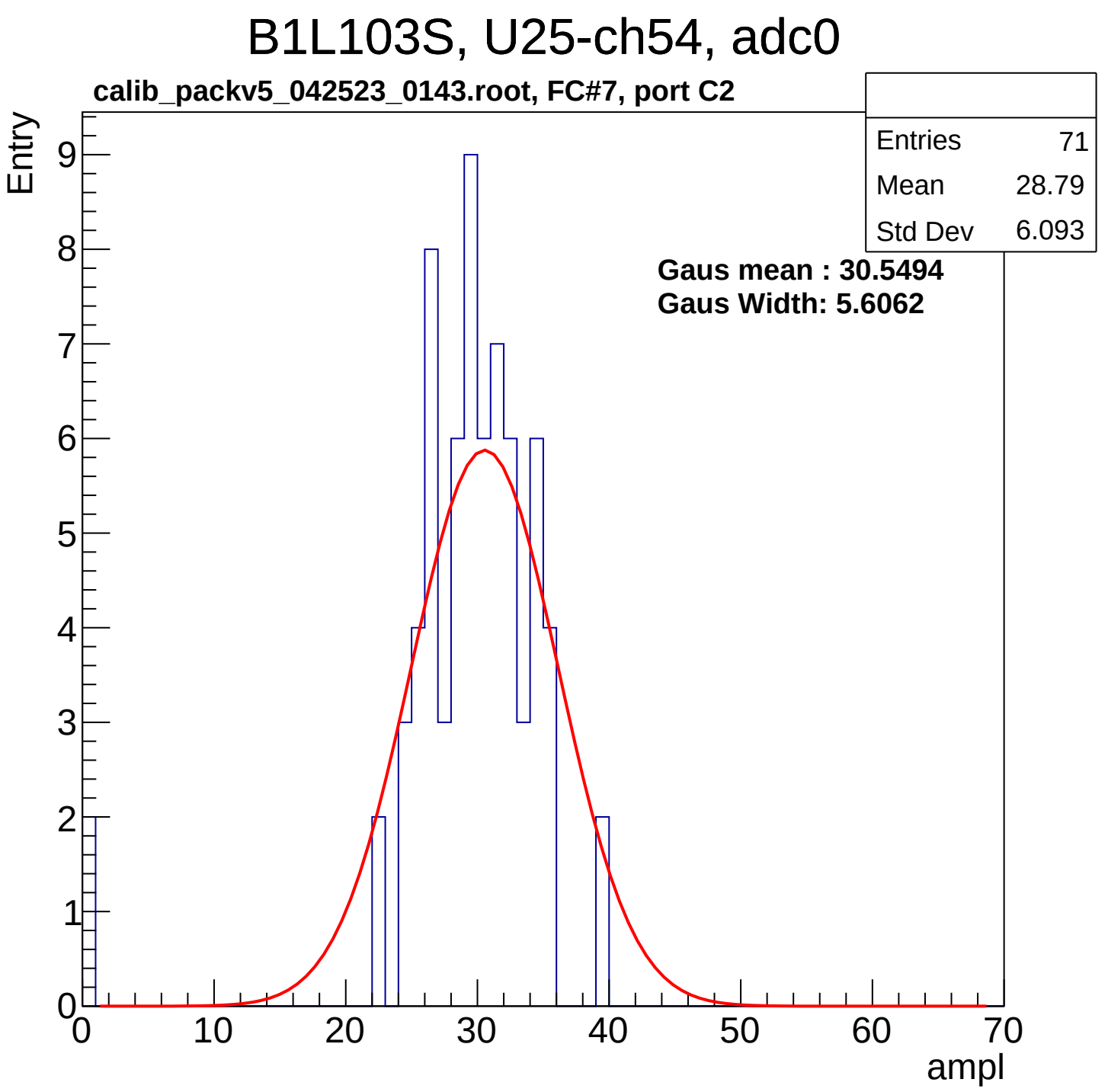
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch54, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 28.79
Std Dev 6.093
Gaus mean : 30.5494
Gaus Width: 5.6062
Entry
ampl
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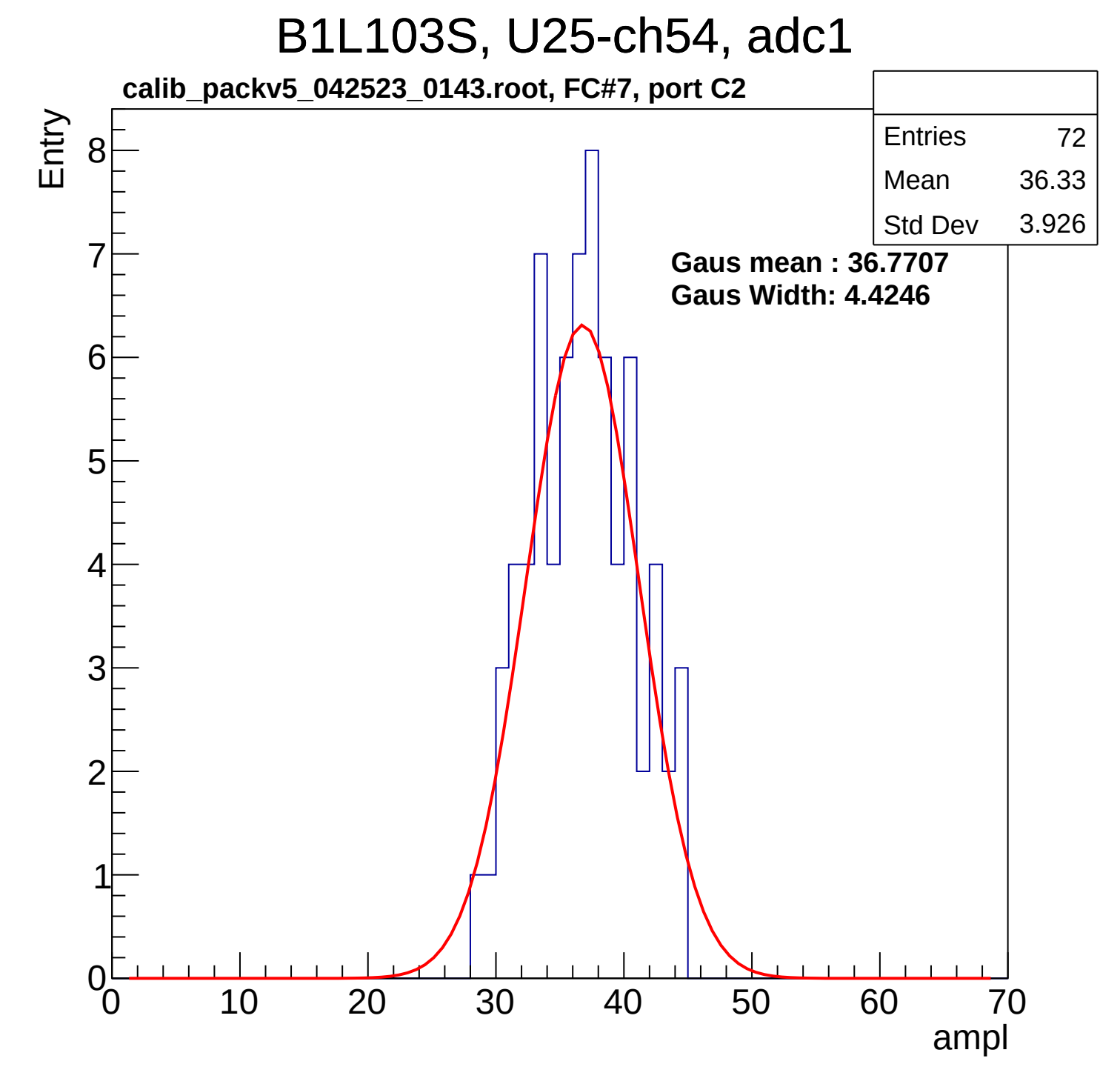
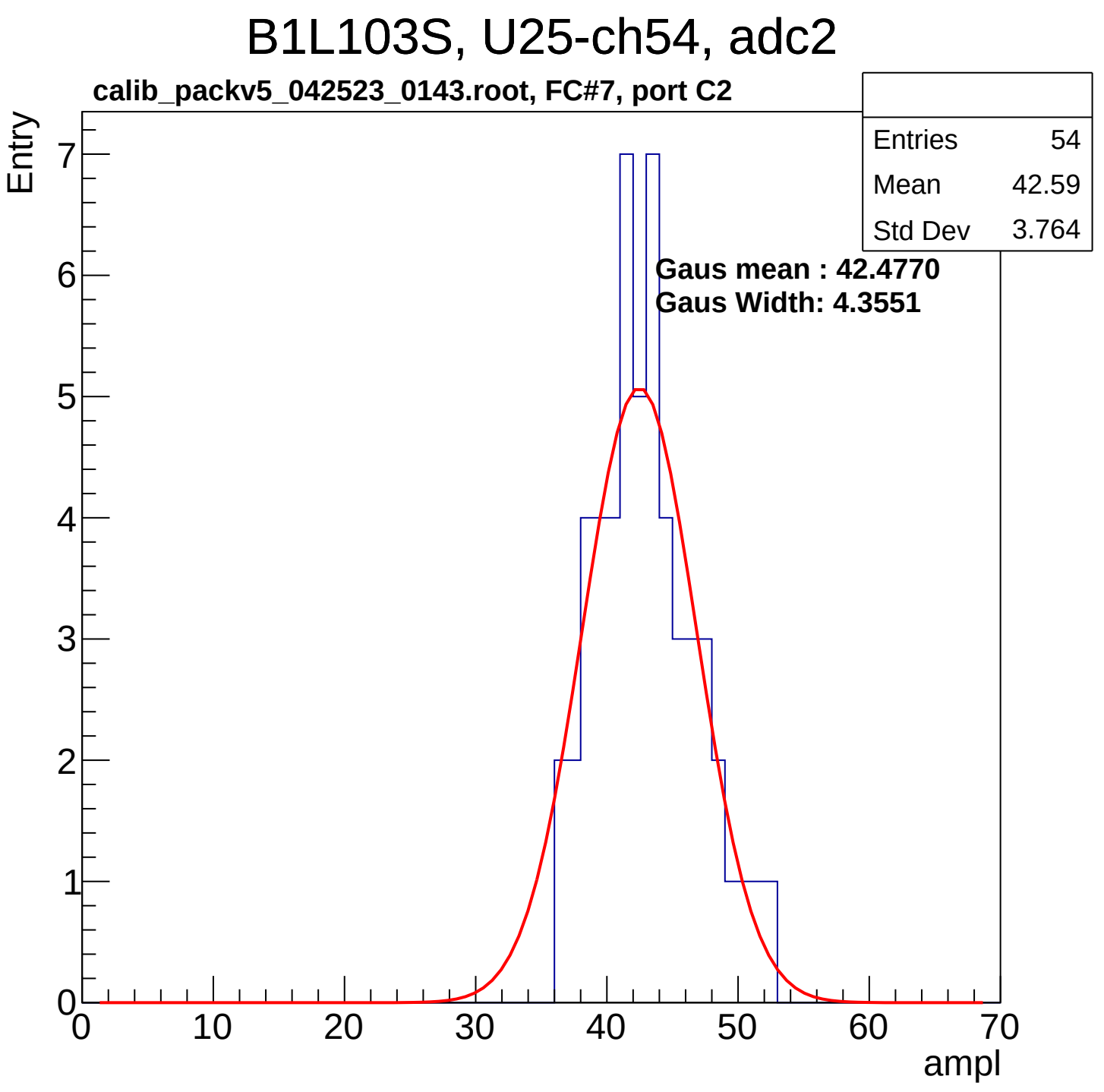
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch54, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 36.33
Std Dev 3.926
Gaus mean : 36.7707
Gaus Width: 4.4246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch54, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 42.59
Std Dev 3.764
Gaus mean : 42.4770
Gaus Width: 4.3551
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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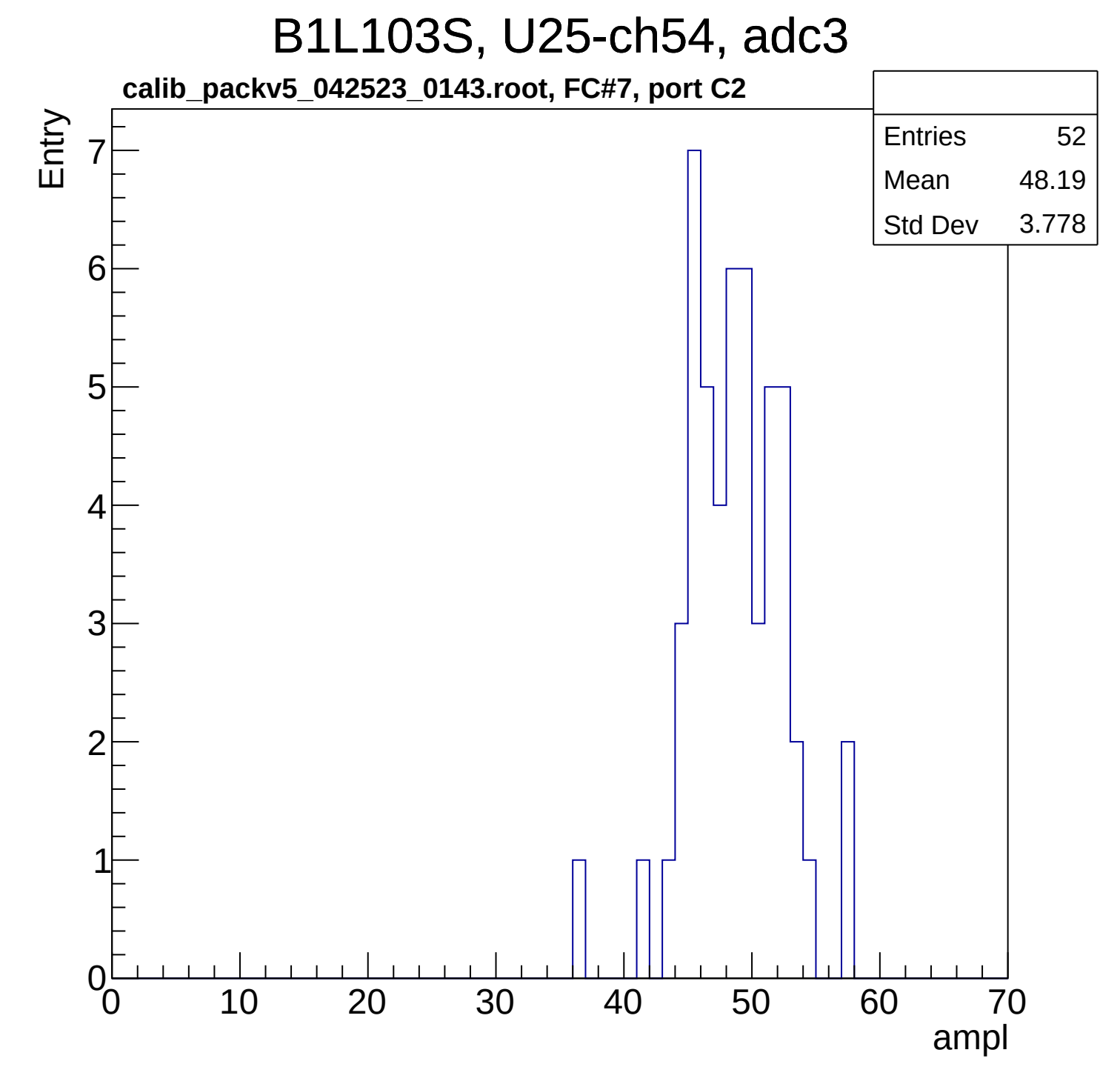
B1L103S, U25-ch54, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 48.19
Std Dev 3.778
Entry
ampl
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1
2
3
4
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6
7
0
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60
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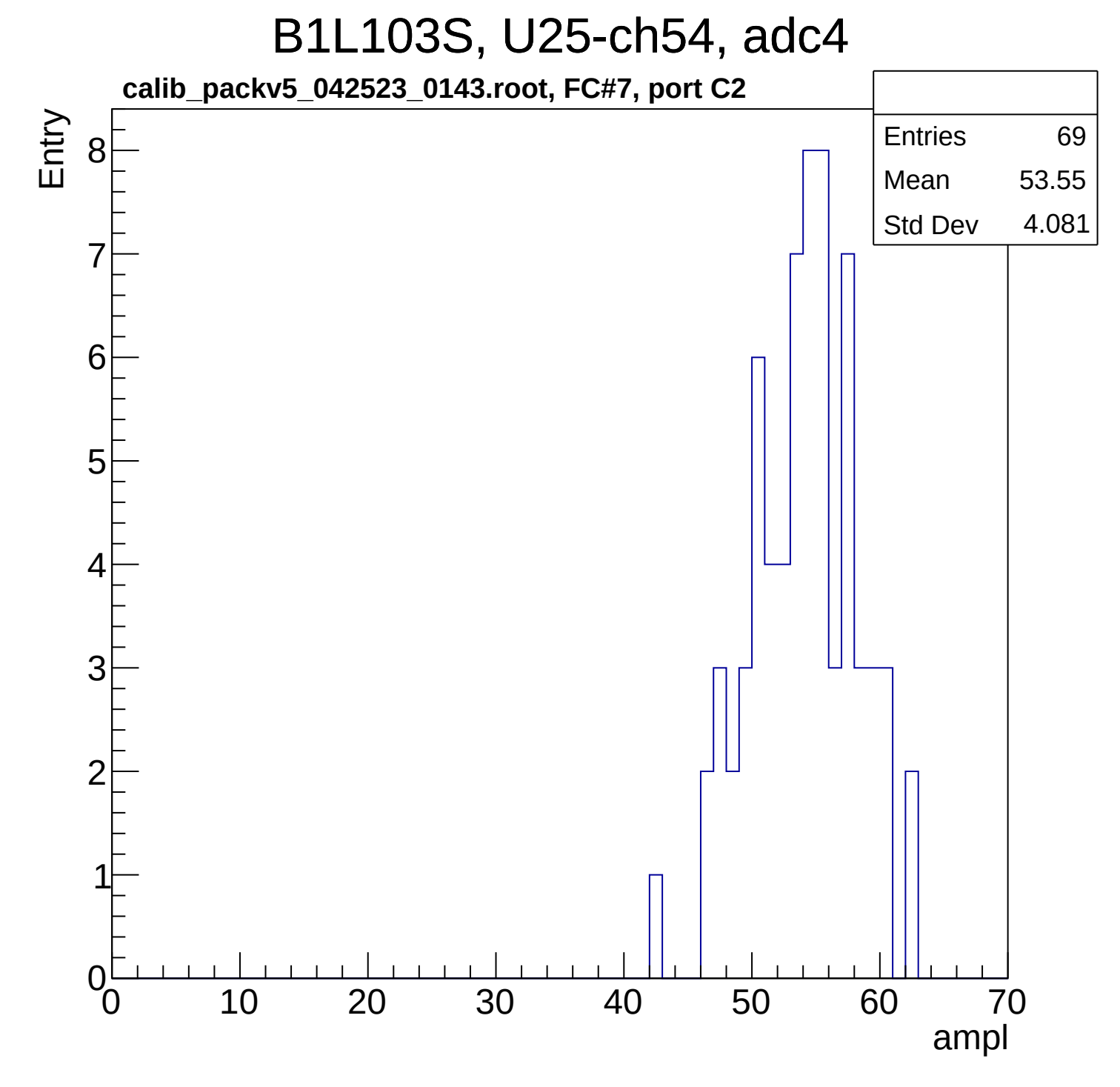
B1L103S, U25-ch54, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 53.55
Std Dev 4.081
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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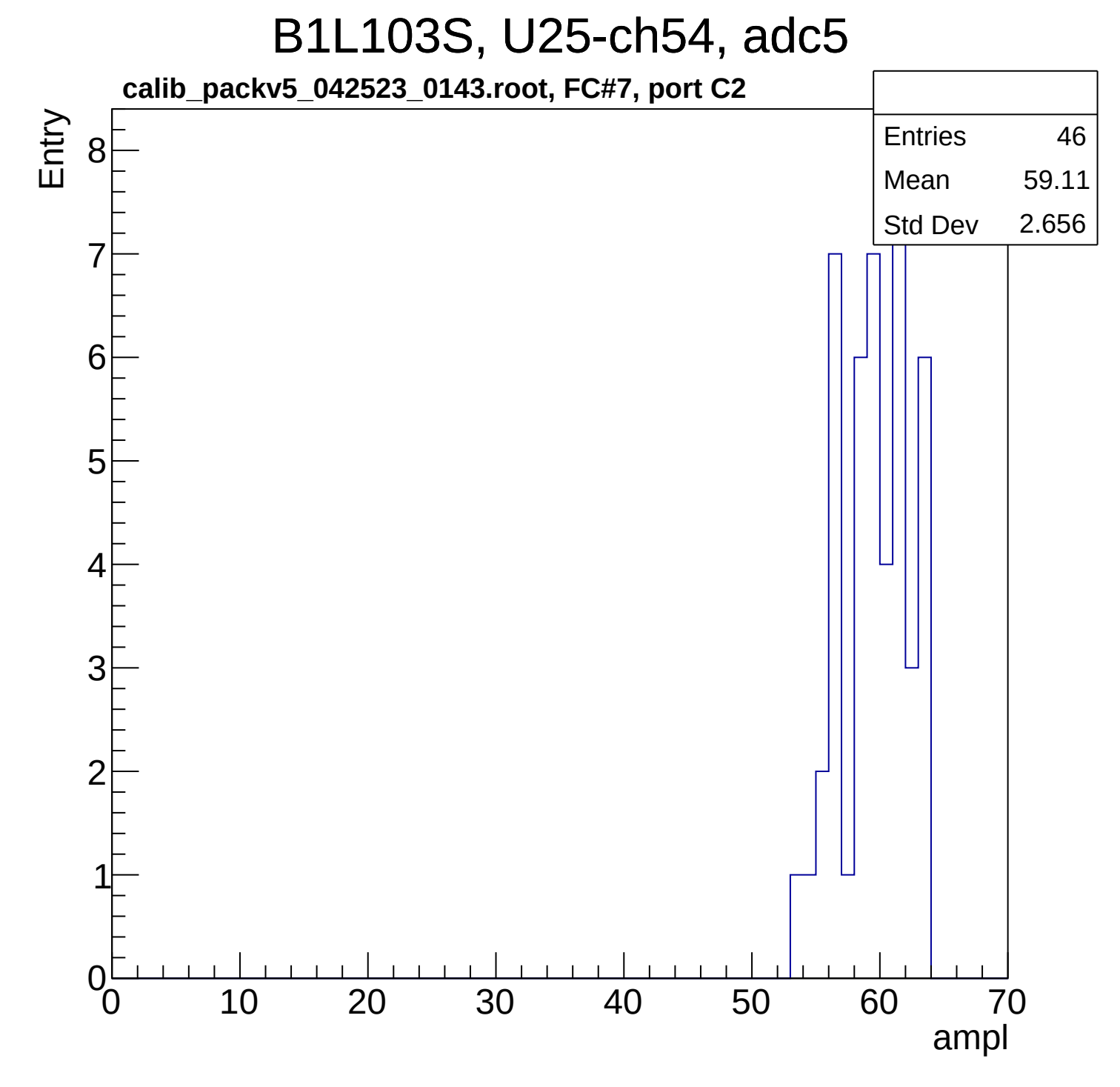
B1L103S, U25-ch54, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.11
Std Dev 2.656
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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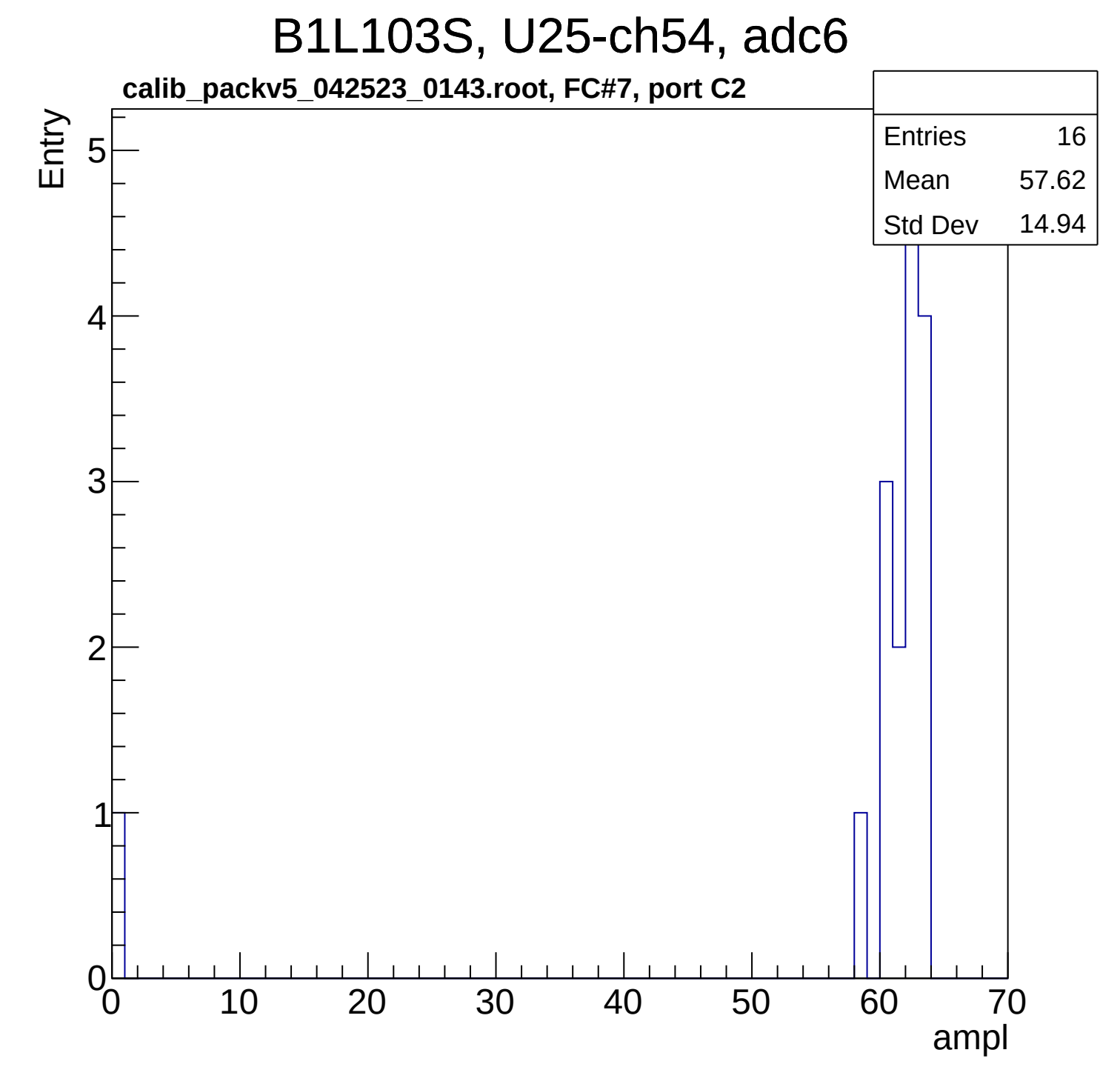
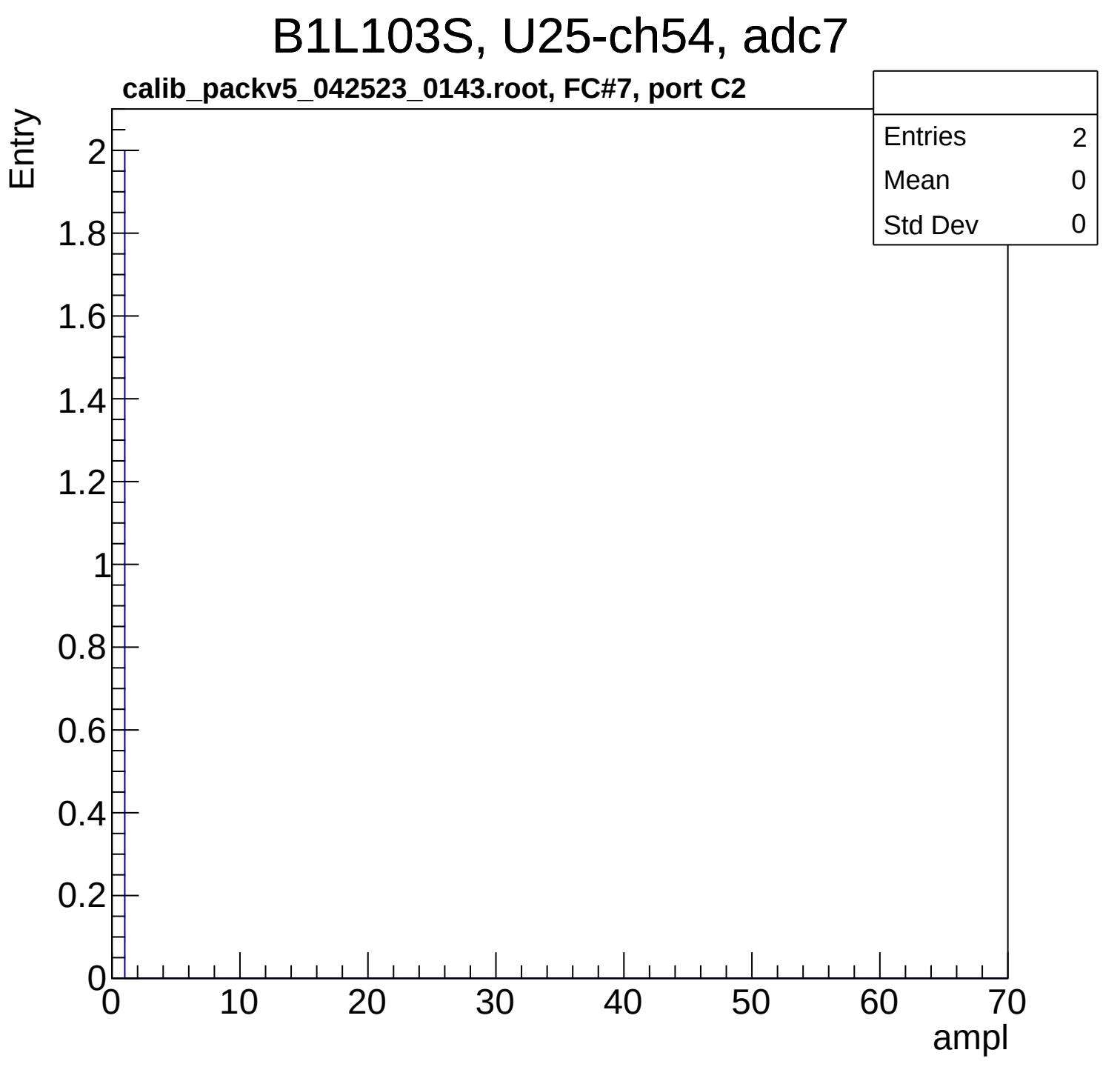
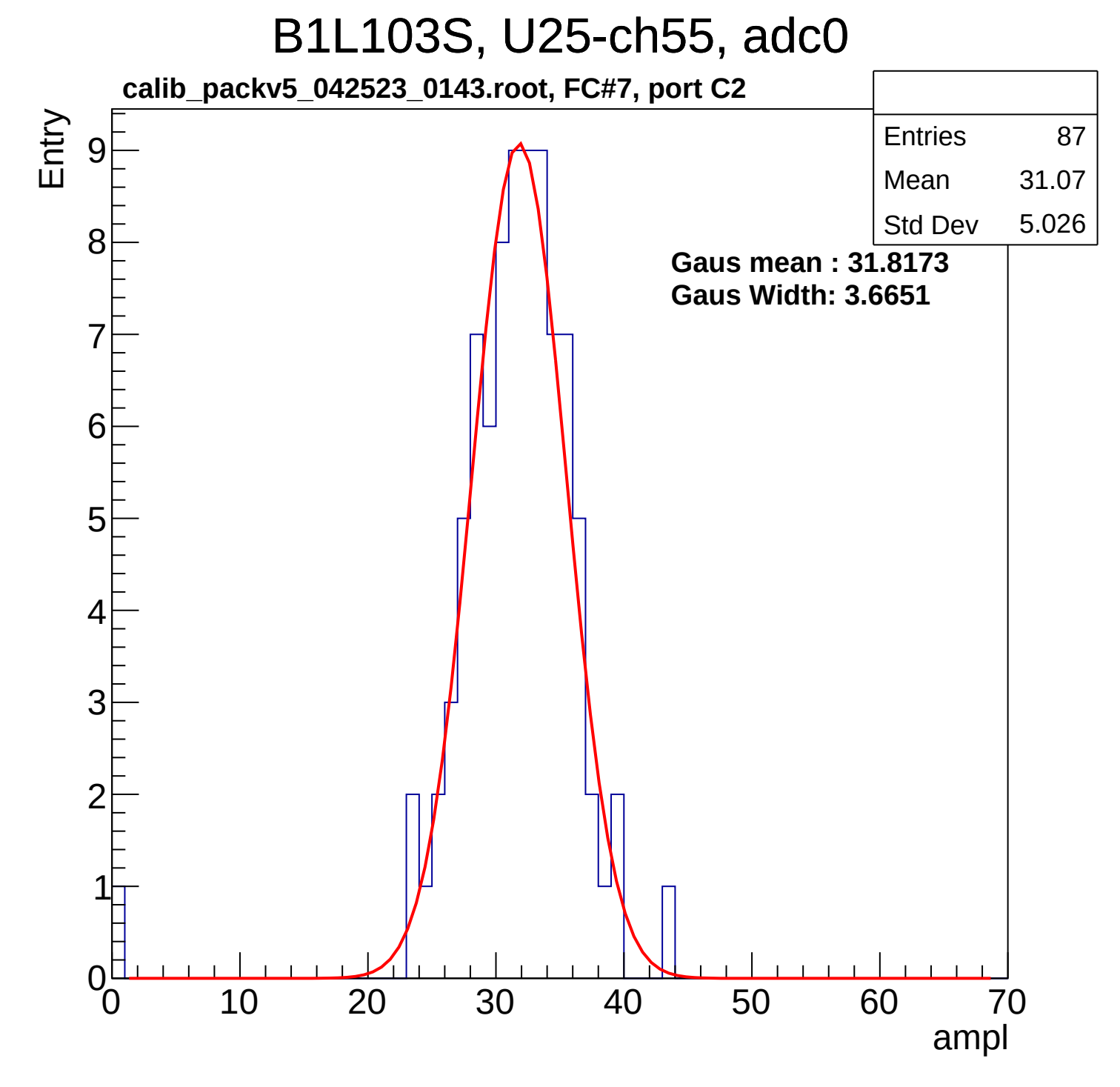
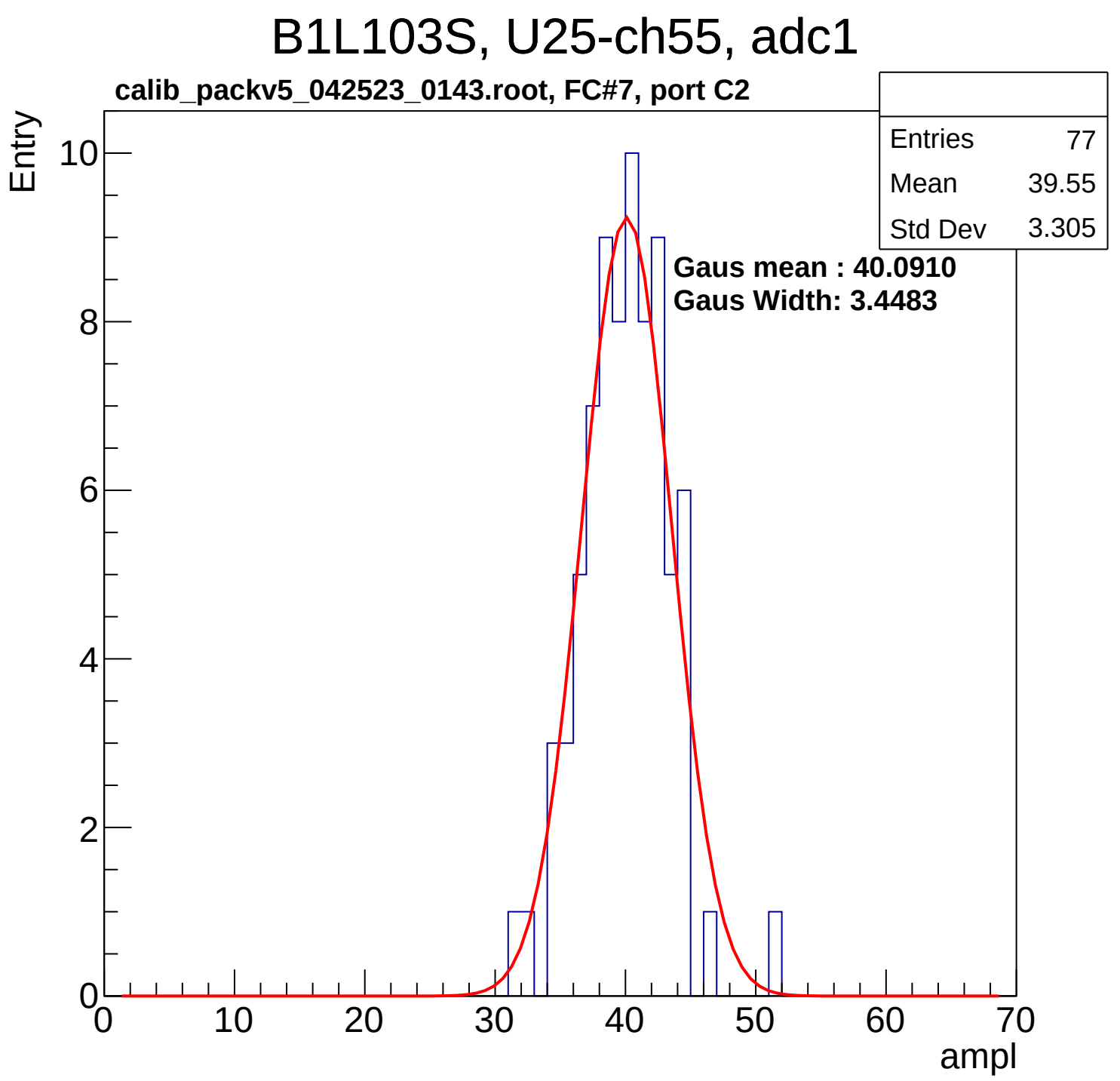

B1L103S, U25-ch54, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 57.62
Std Dev 14.94
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch54, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch55, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 31.07
Std Dev 5.026
Gaus mean : 31.8173
Gaus Width: 3.6651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch55, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 39.55
Std Dev 3.305
Gaus mean : 40.0910
Gaus Width: 3.4483
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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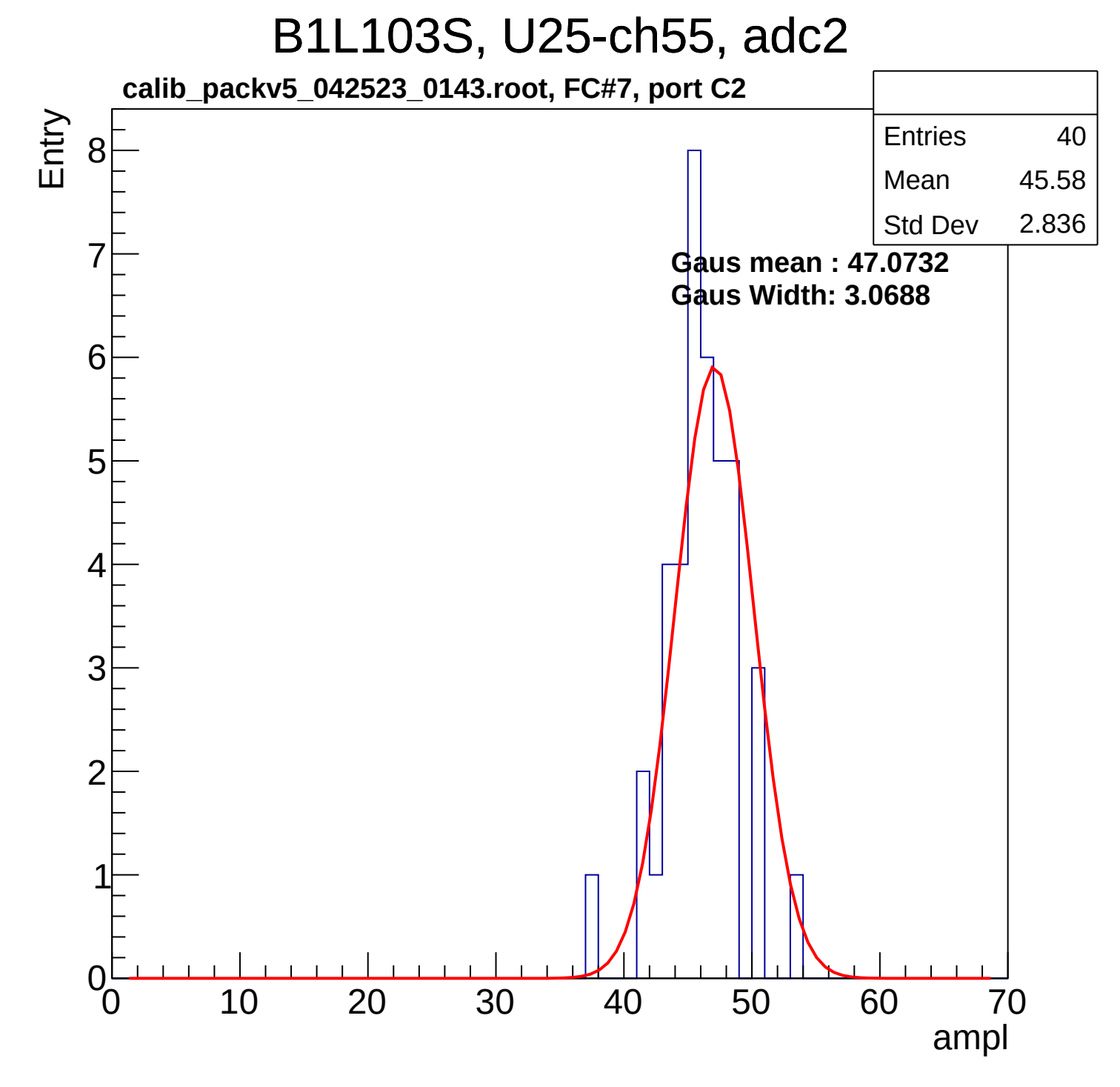
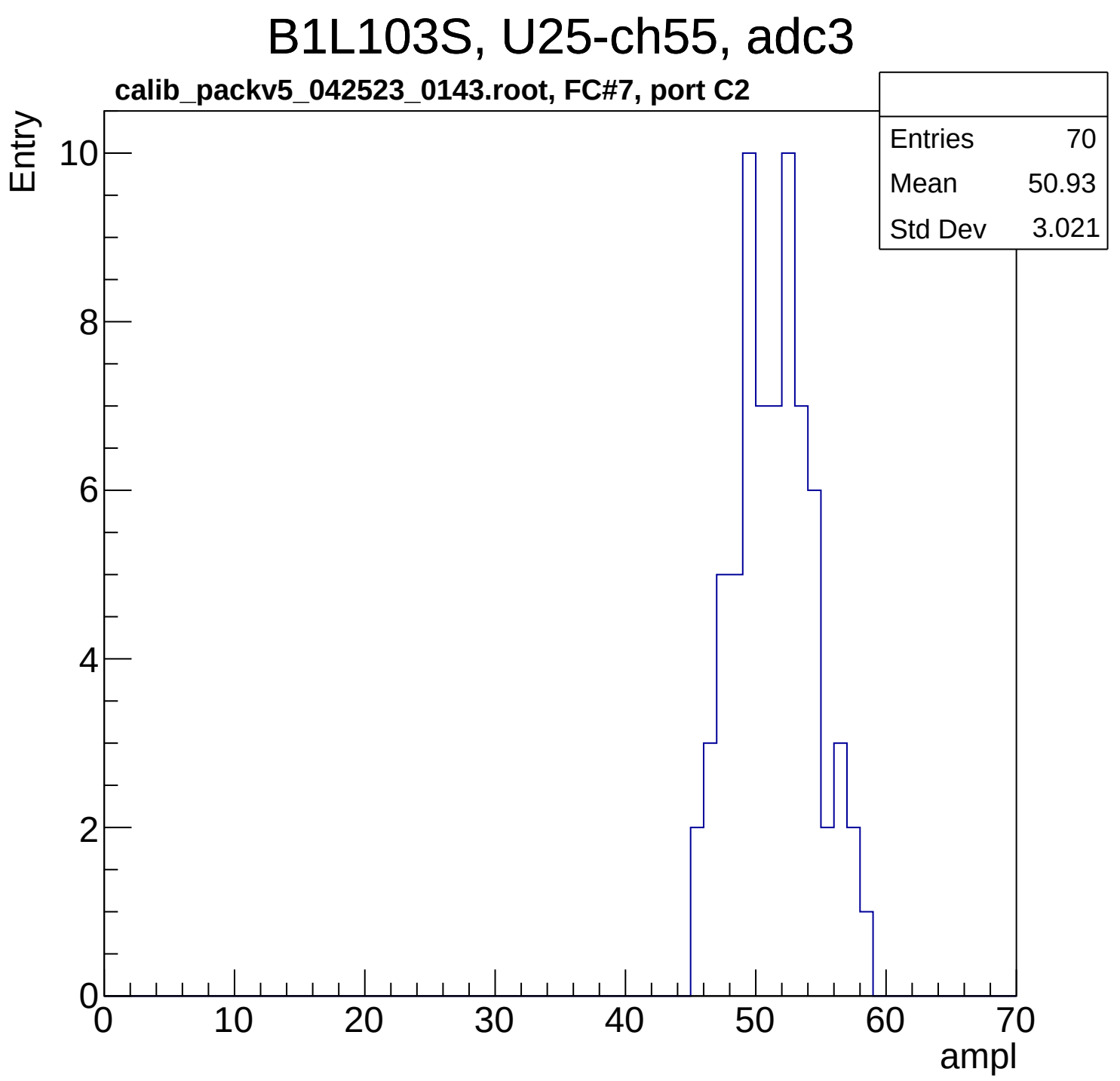
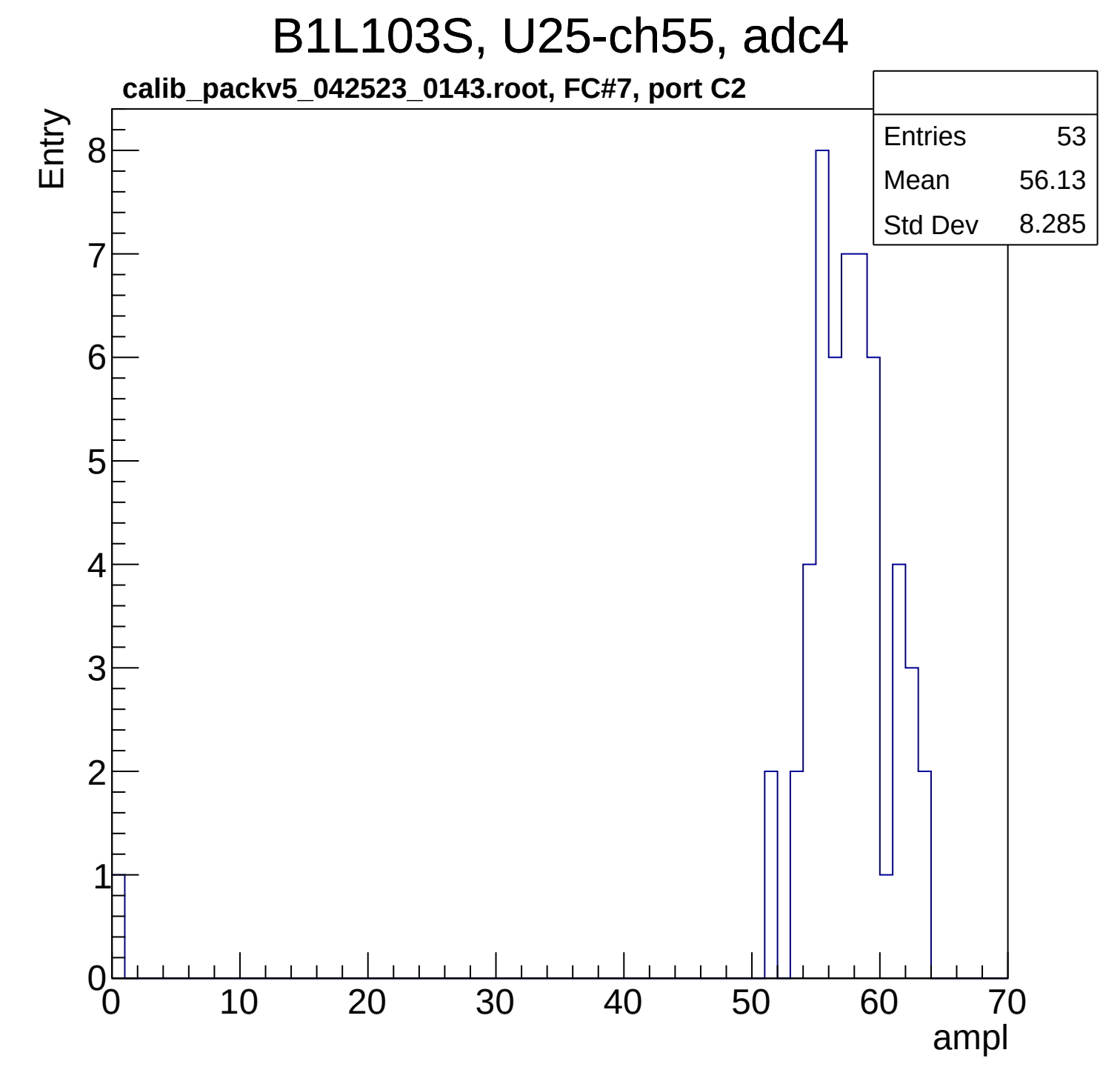
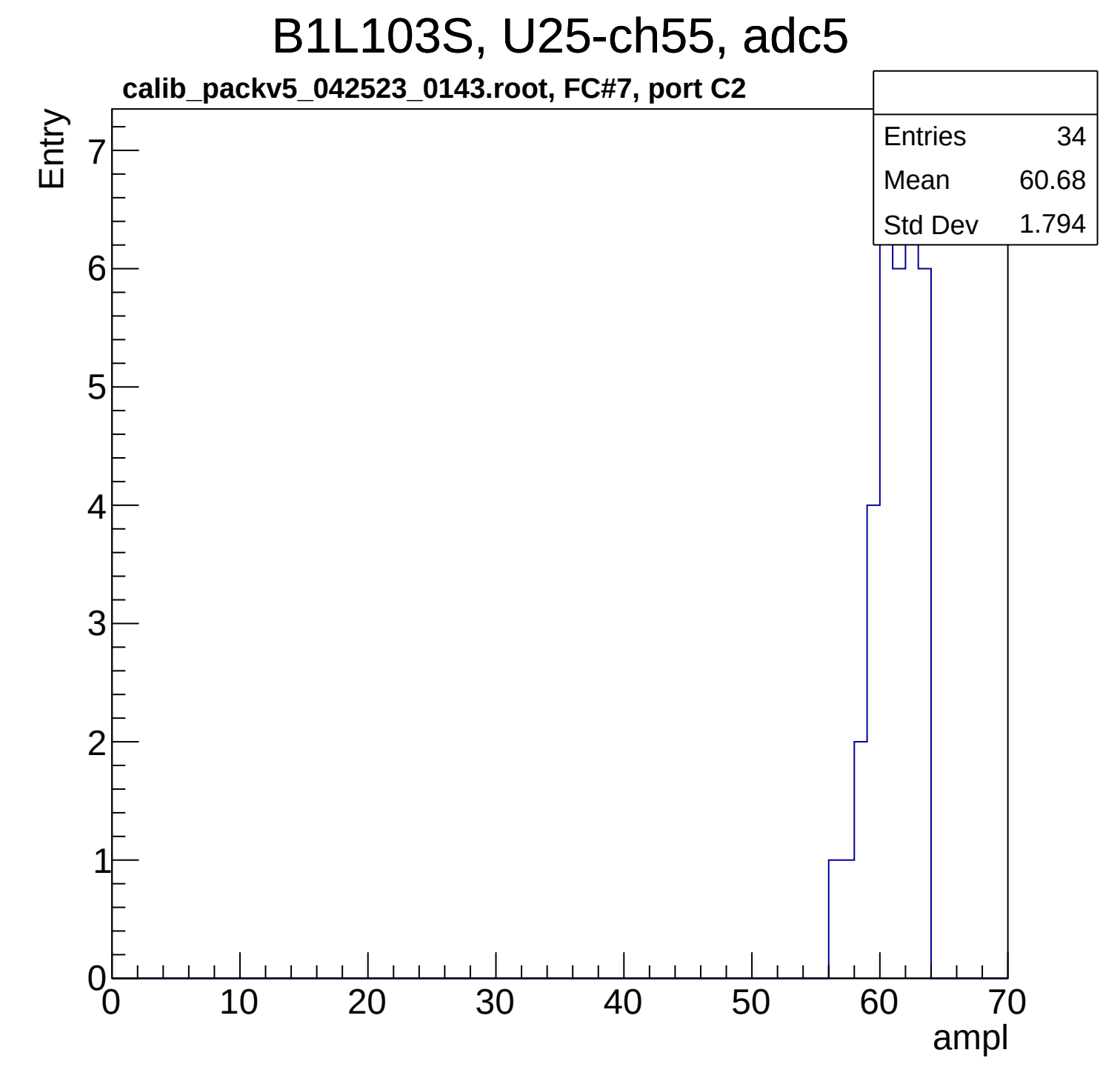
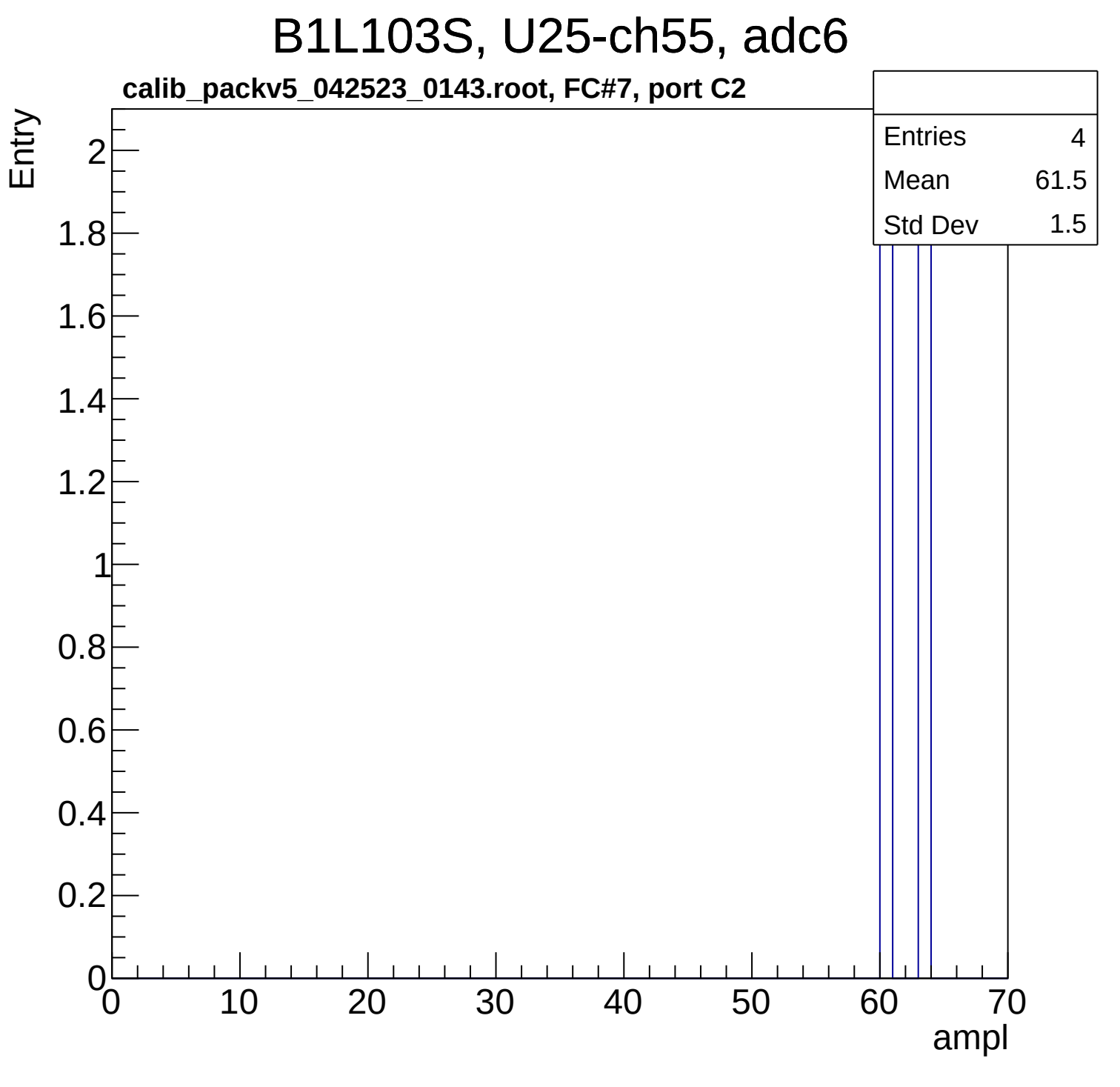
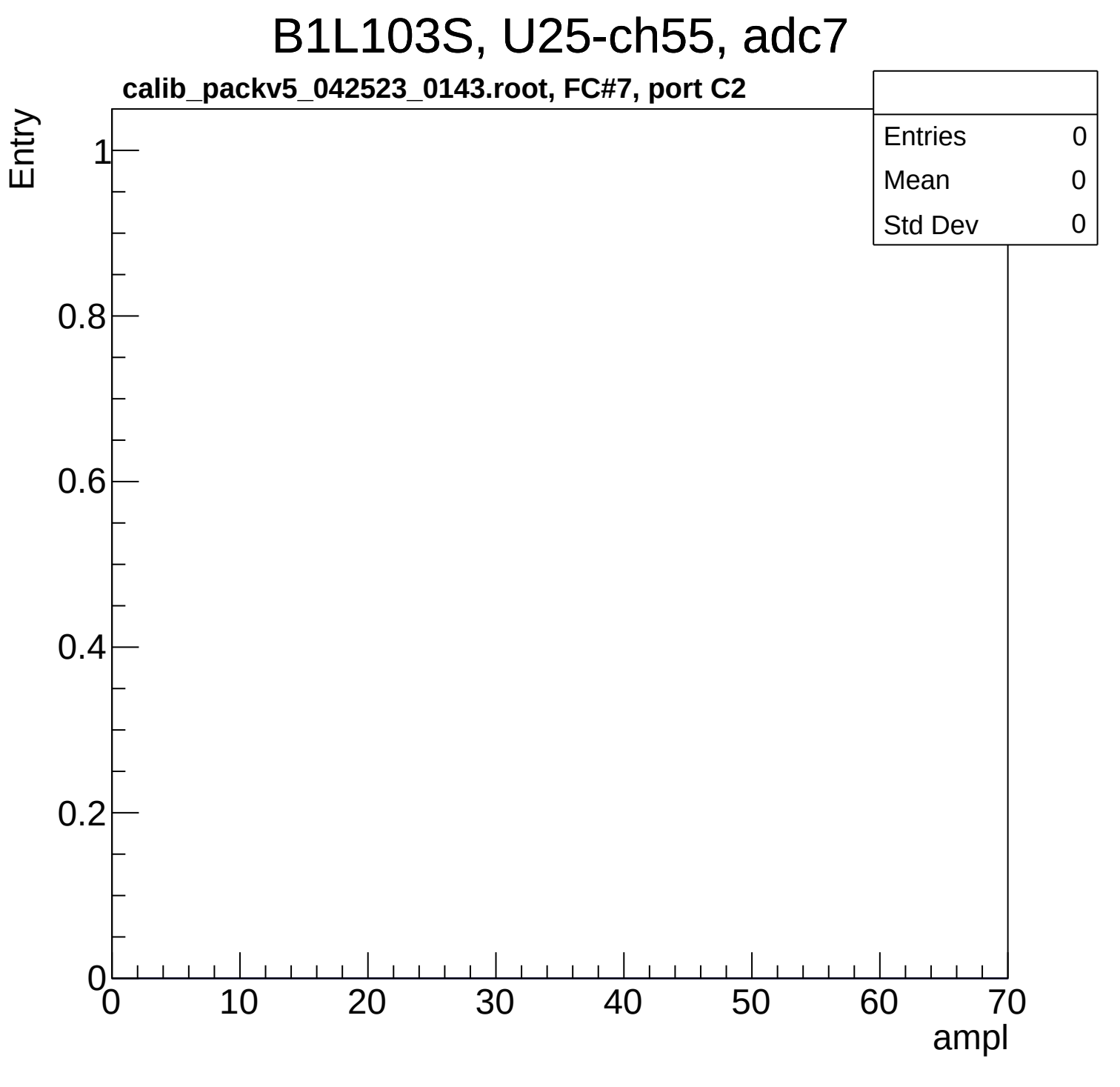

B1L103S, U25-ch55, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 45.58
Std Dev 2.836
Gaus mean : 47.0732
Gaus Width: 3.0688
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

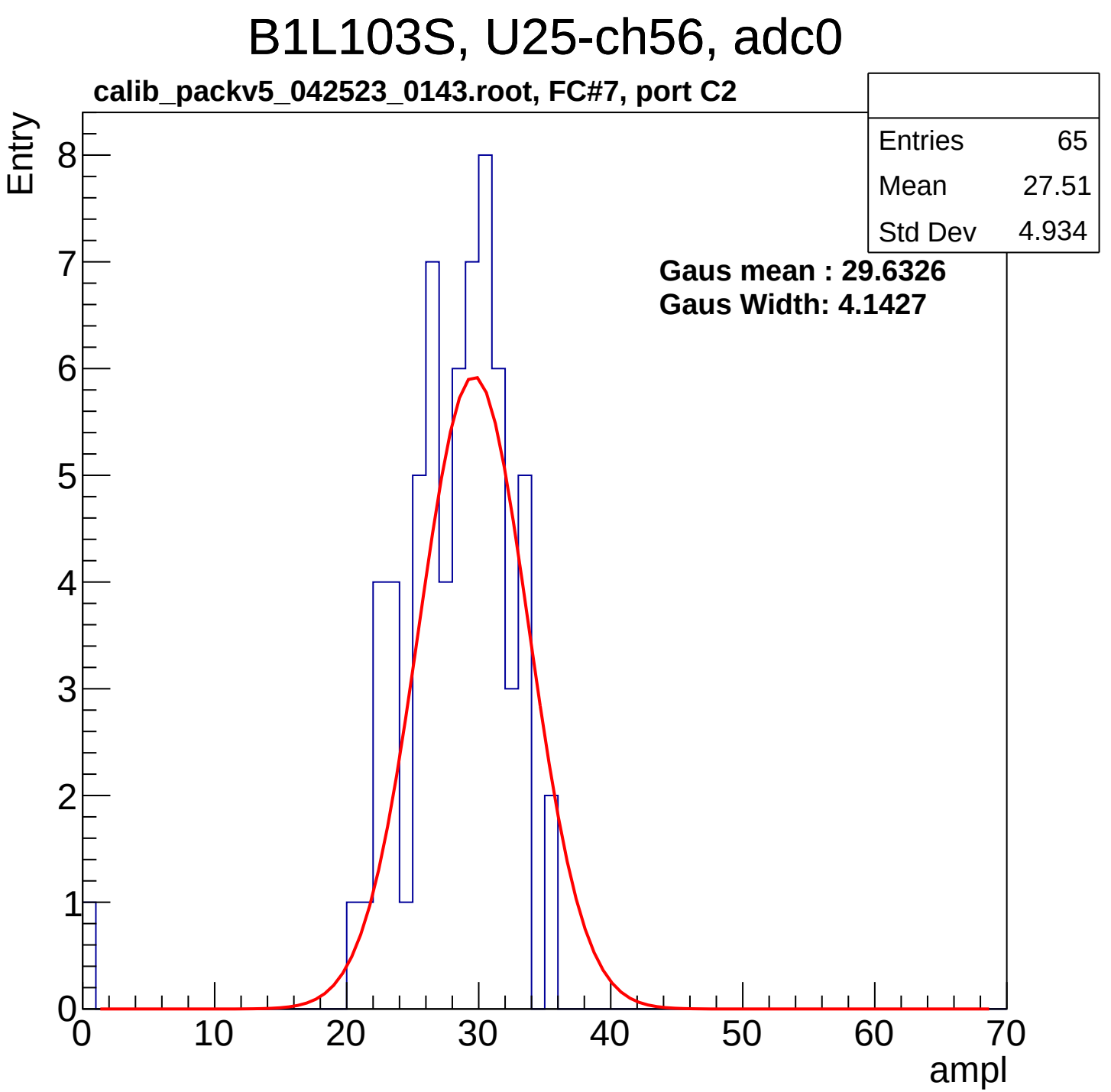

B1L103S, U25-ch55, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 50.93
Std Dev 3.021
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

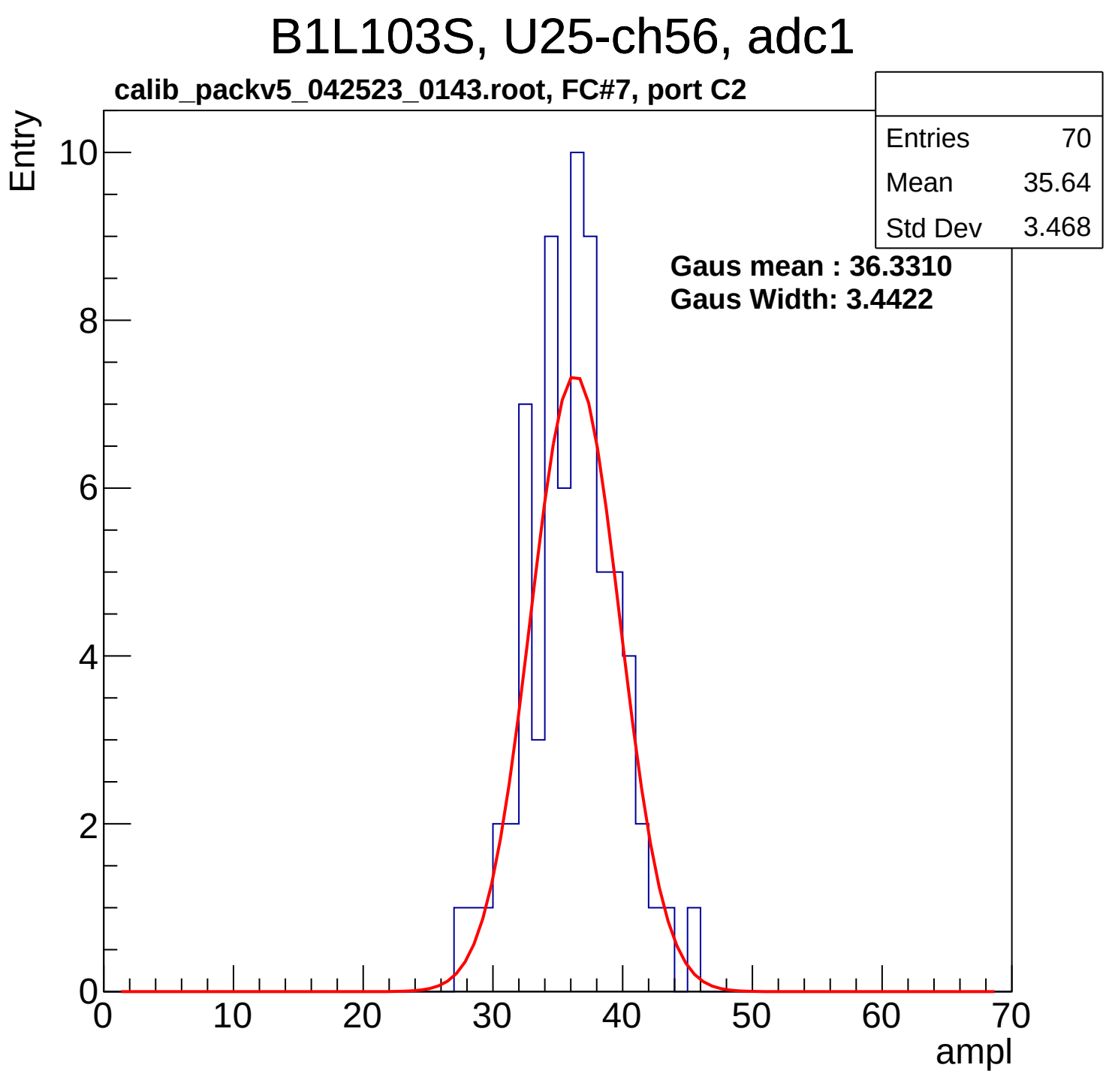

B1L103S, U25-ch55, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 56.13
Std Dev 8.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

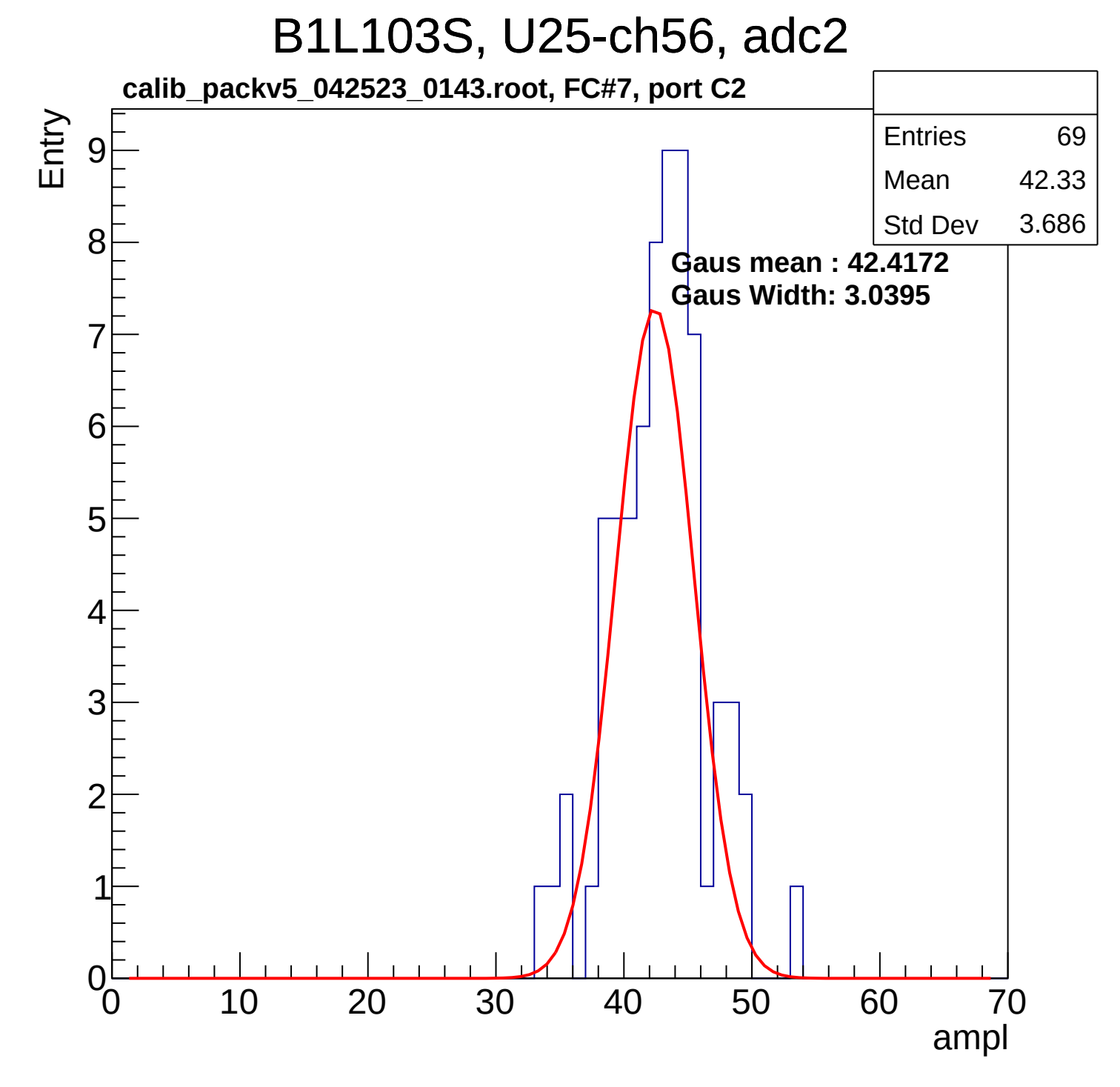

B1L103S, U25-ch55, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 60.68
Std Dev 1.794
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

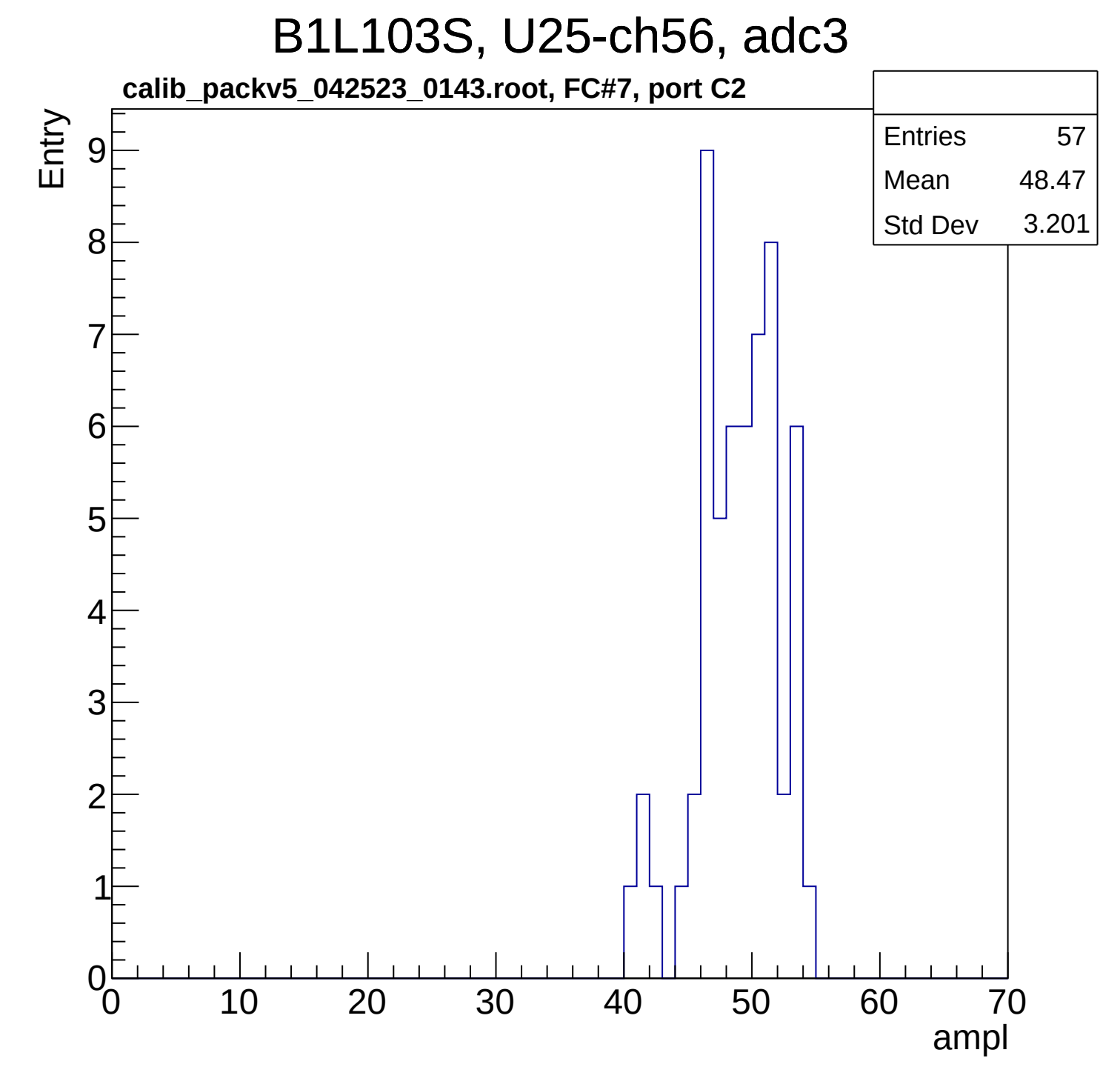

B1L103S, U25-ch55, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

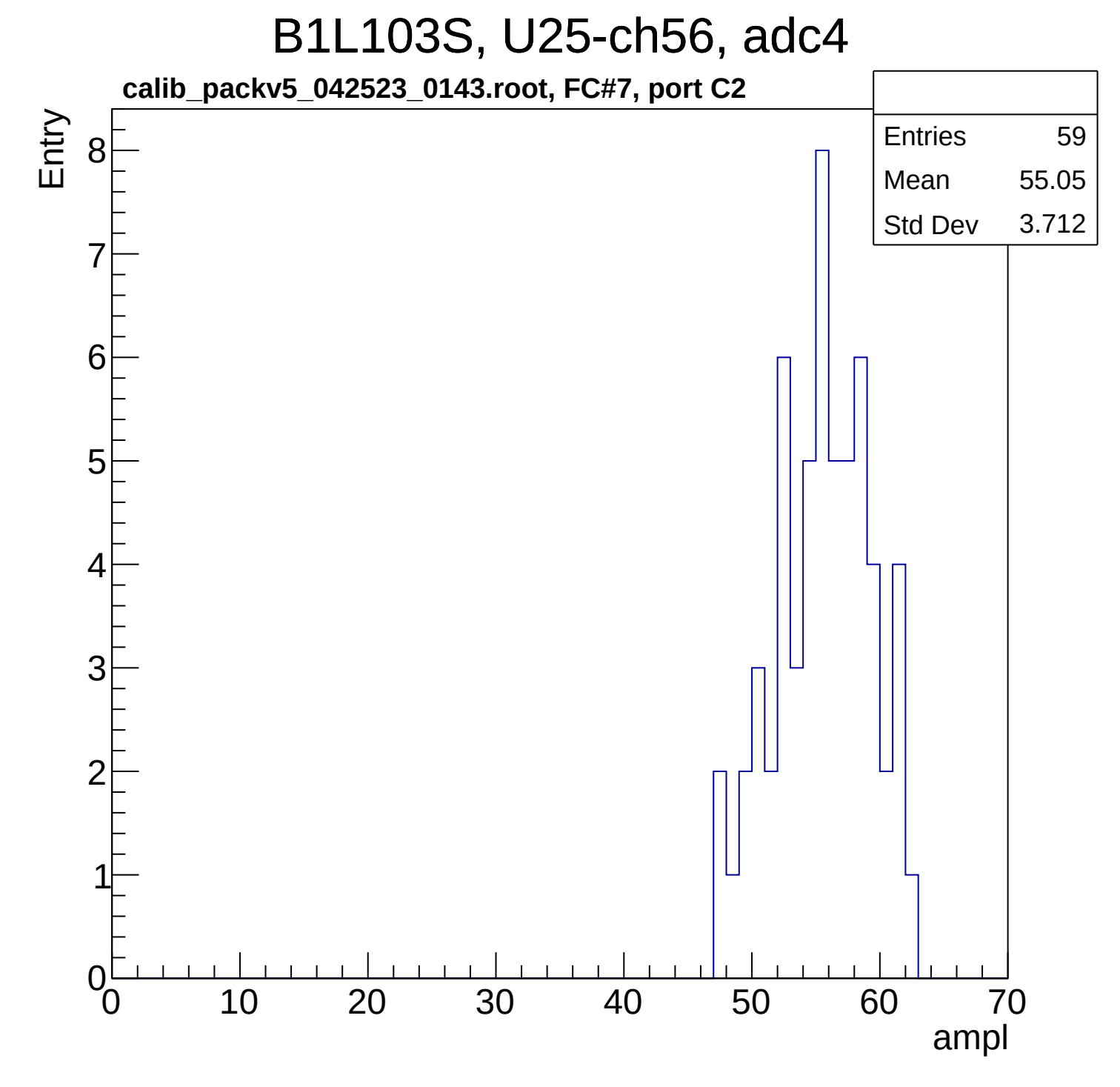

B1L103S, U25-ch55, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

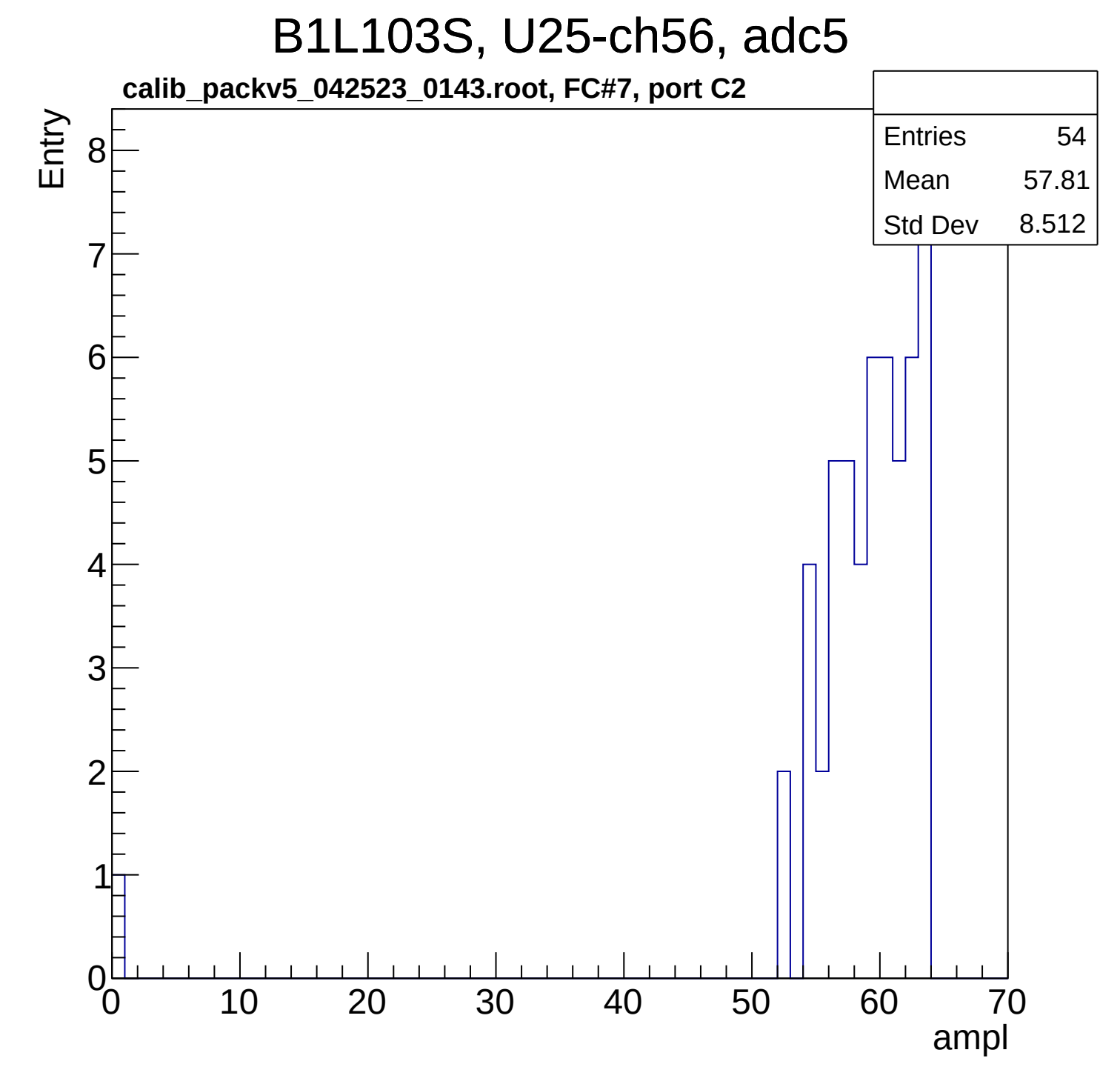

B1L103S, U25-ch56, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 27.51
Std Dev 4.934
Gaus mean : 29.6326
Gaus Width: 4.1427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 35.64
Std Dev 3.468
Gaus mean : 36.3310
Gaus Width: 3.4422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 42.33
Std Dev 3.686
Gaus mean : 42.4172
Gaus Width: 3.0395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 48.47
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 55.05
Std Dev 3.712
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.81
Std Dev 8.512
Entry
ampl
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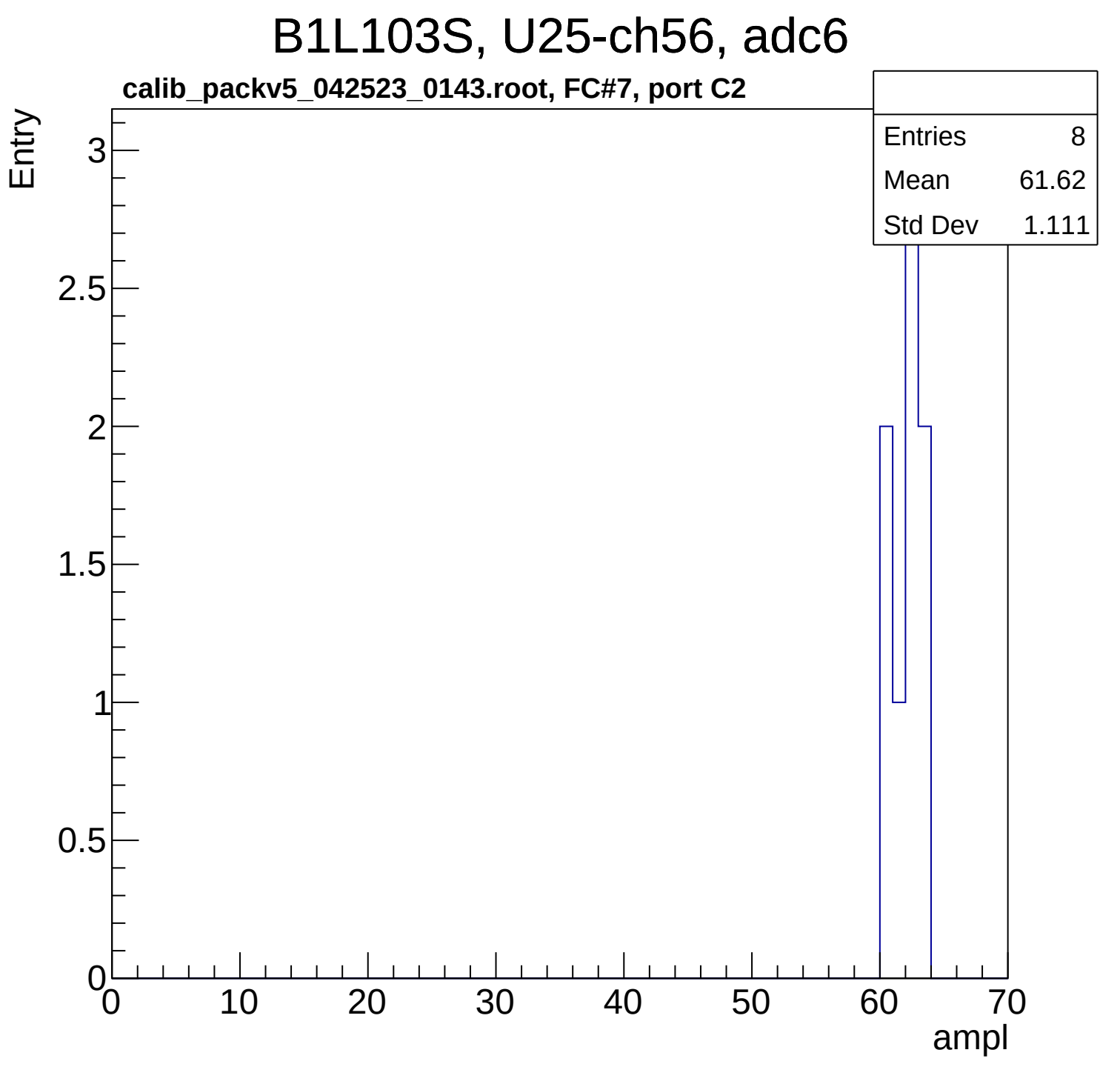
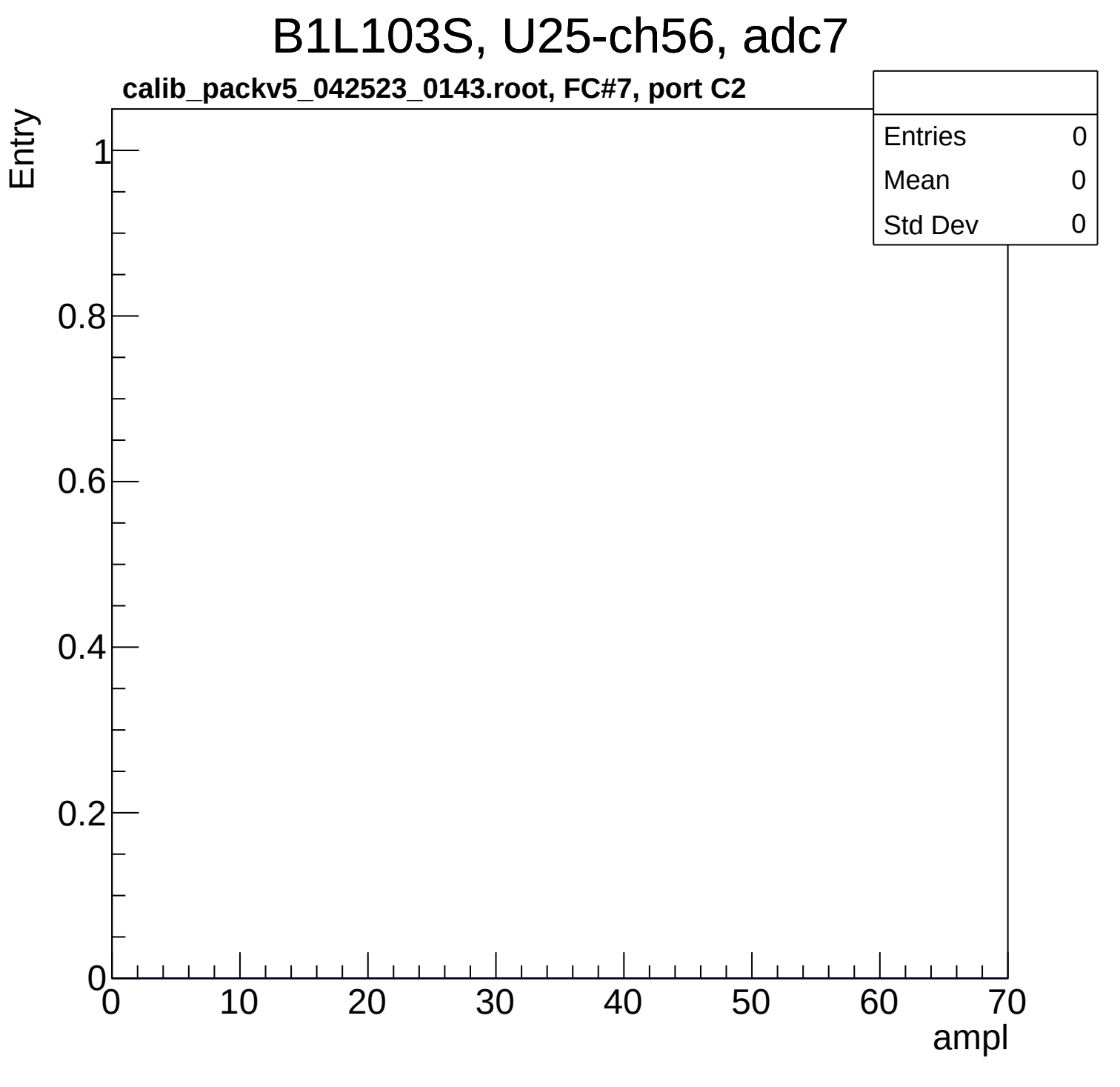
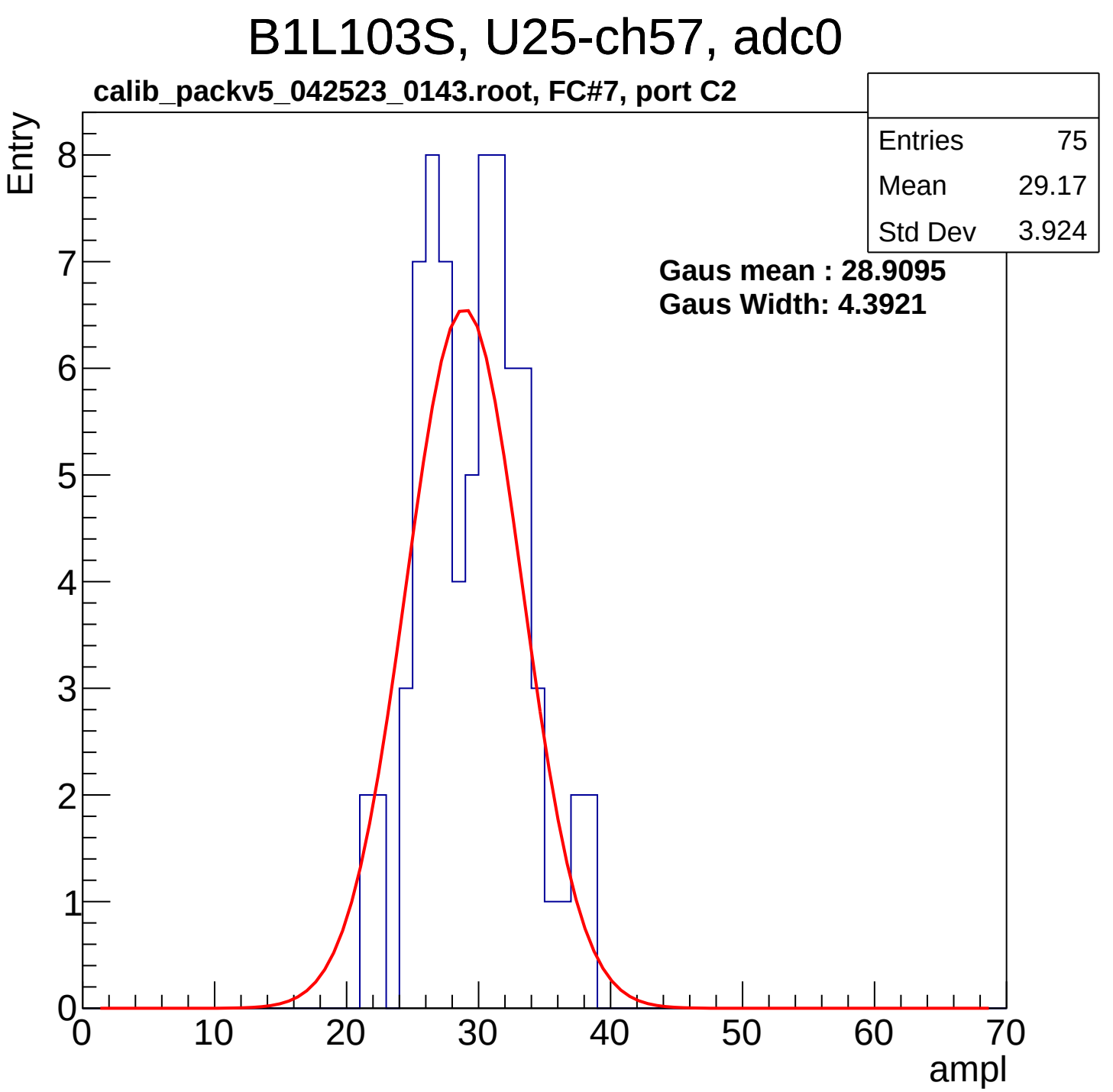
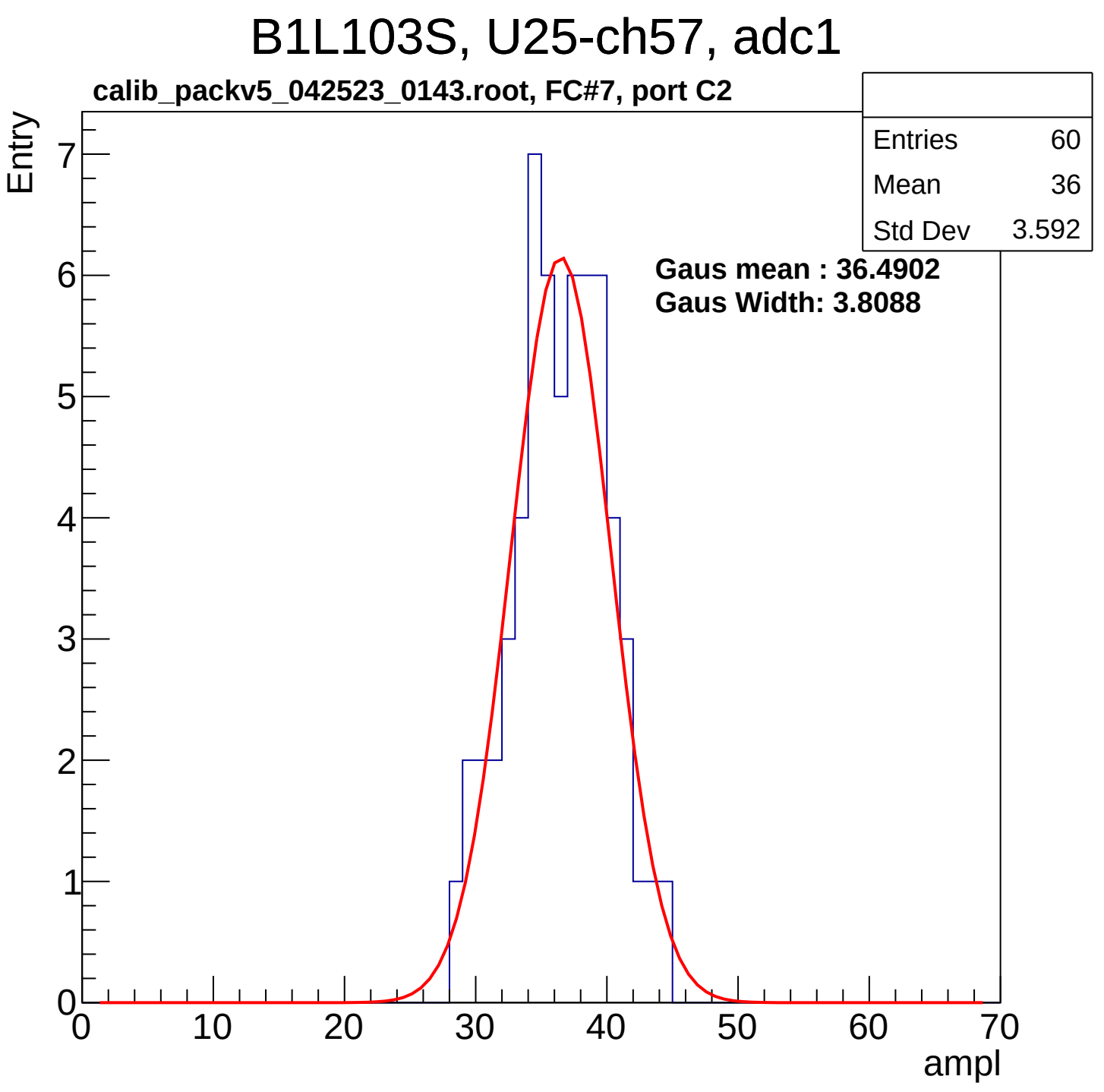
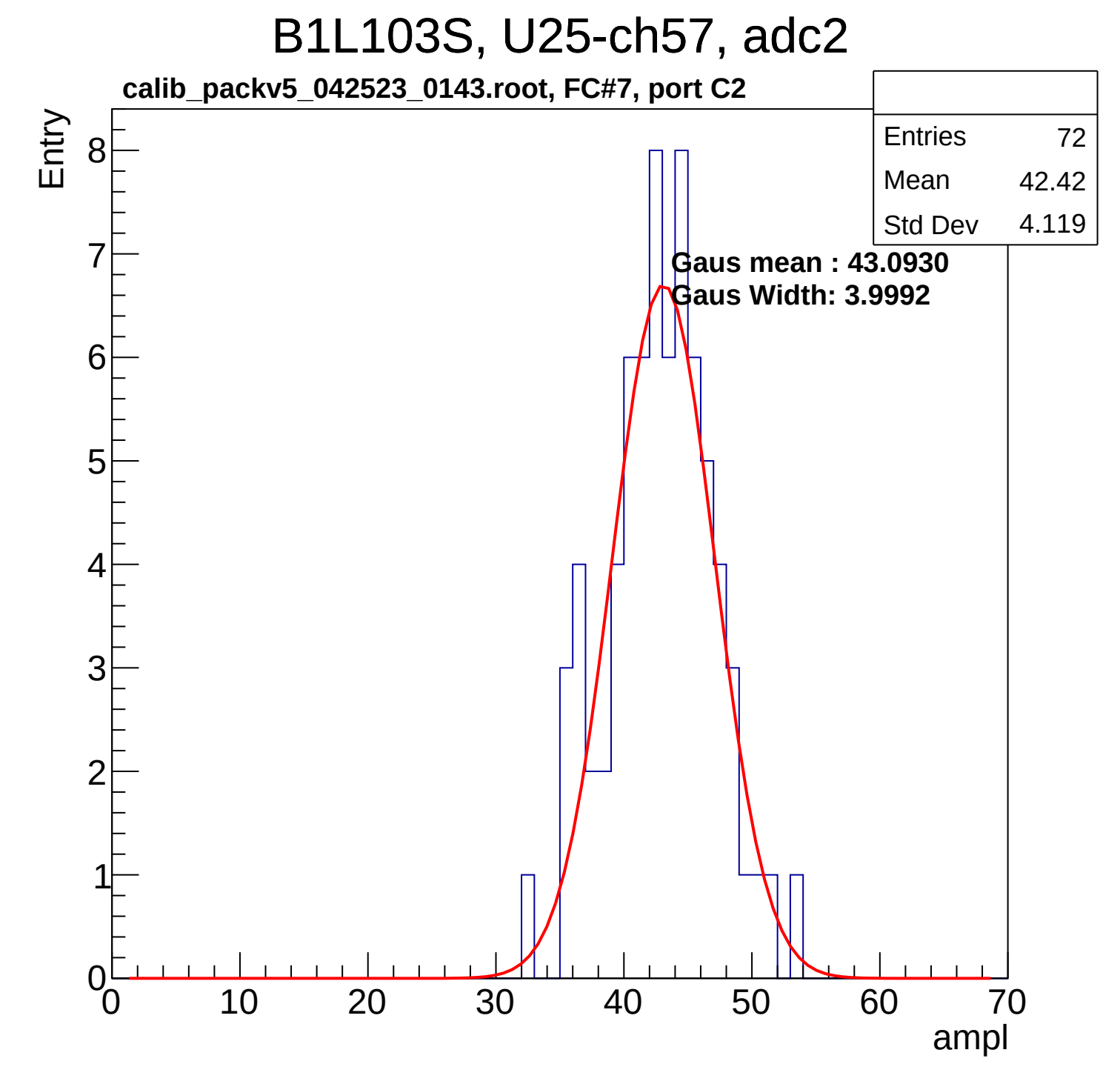
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

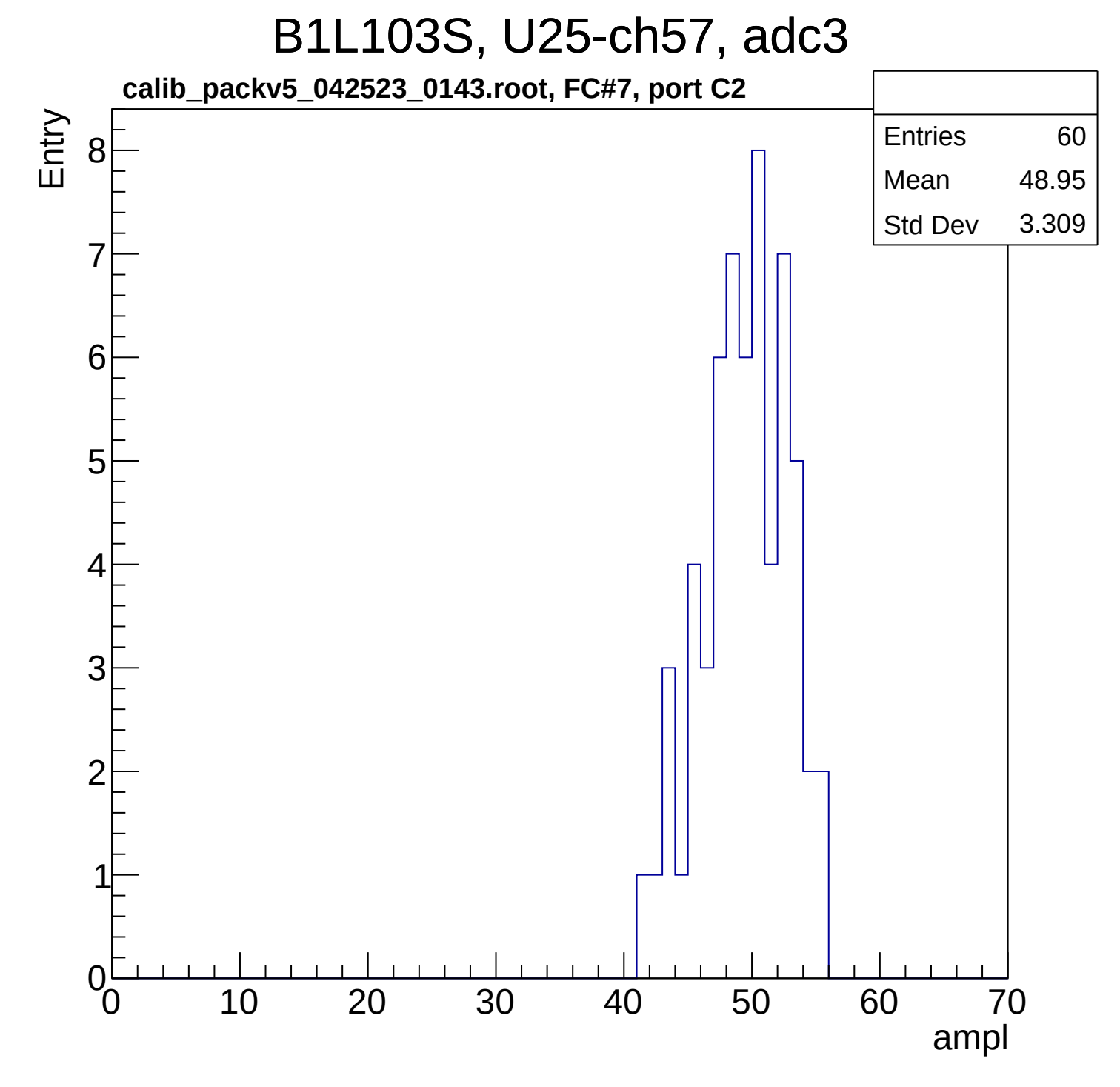

B1L103S, U25-ch56, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.62
Std Dev 1.111
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch56, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch57, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.17
Std Dev 3.924
Gaus mean : 28.9095
Gaus Width: 4.3921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch57, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 36
Std Dev 3.592
Gaus mean : 36.4902
Gaus Width: 3.8088
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch57, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 42.42
Std Dev 4.119
Gaus mean : 43.0930
Gaus Width: 3.9992
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch57, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.95
Std Dev 3.309
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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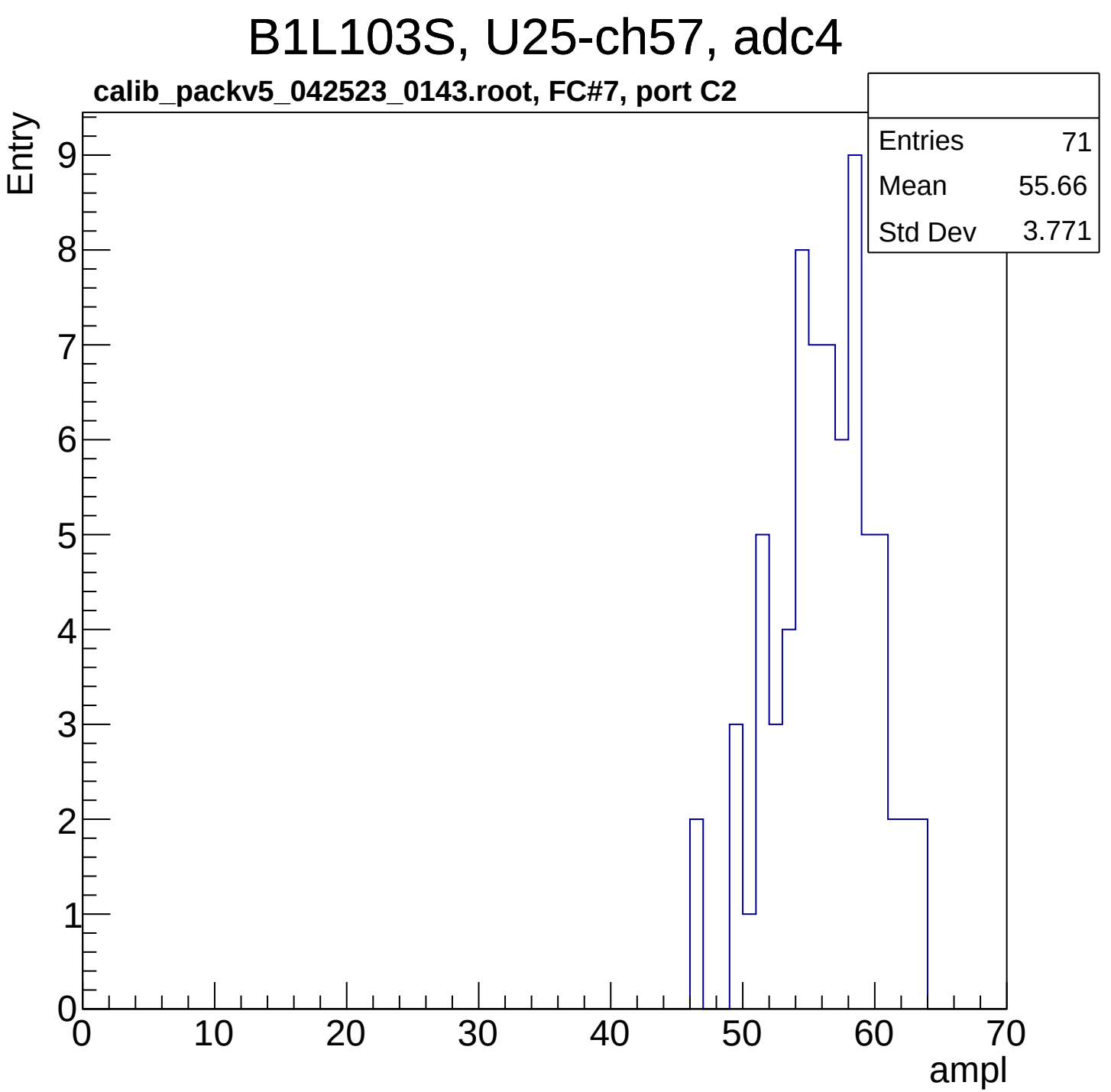
B1L103S, U25-ch57, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 55.66
Std Dev 3.771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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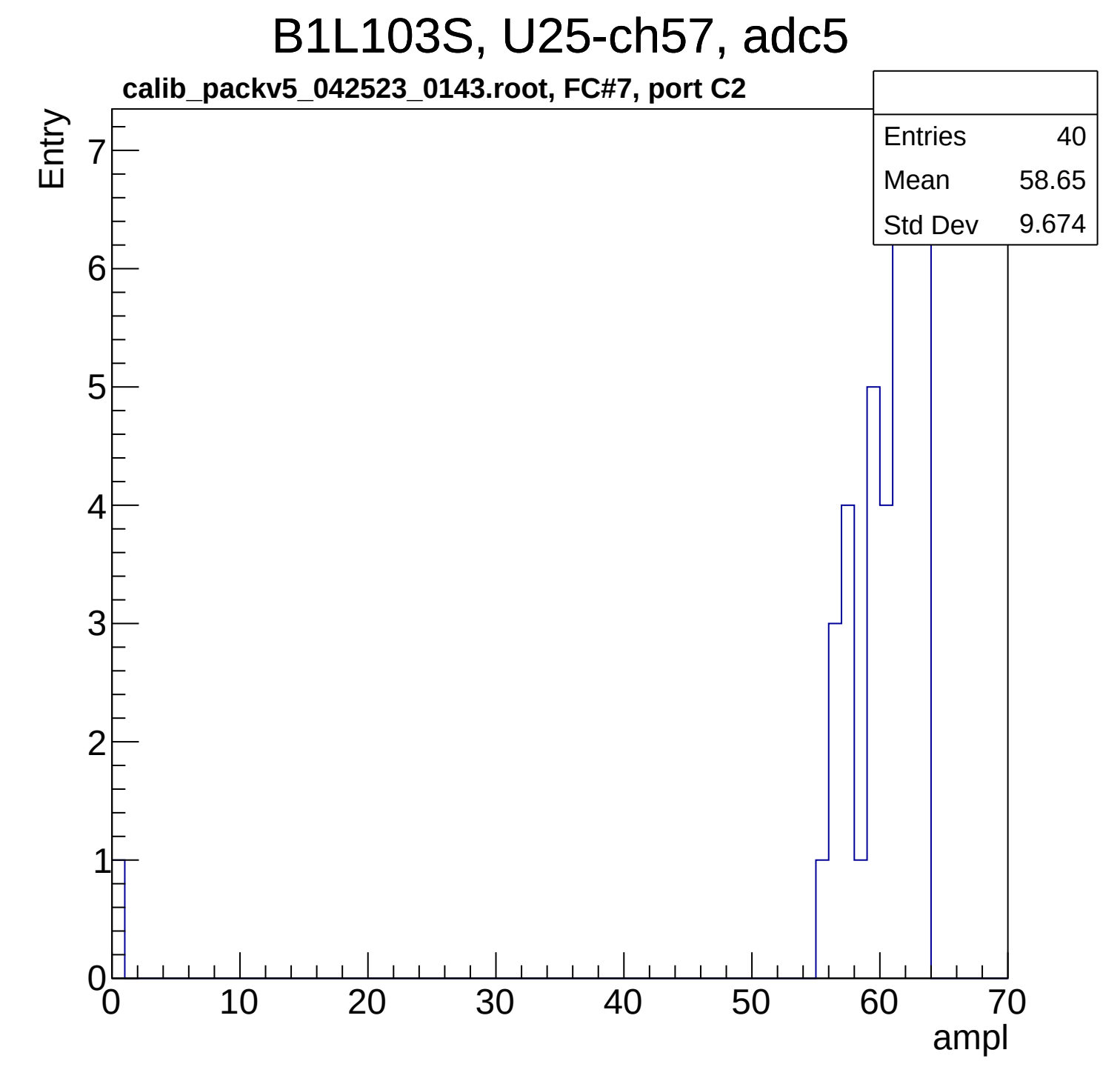
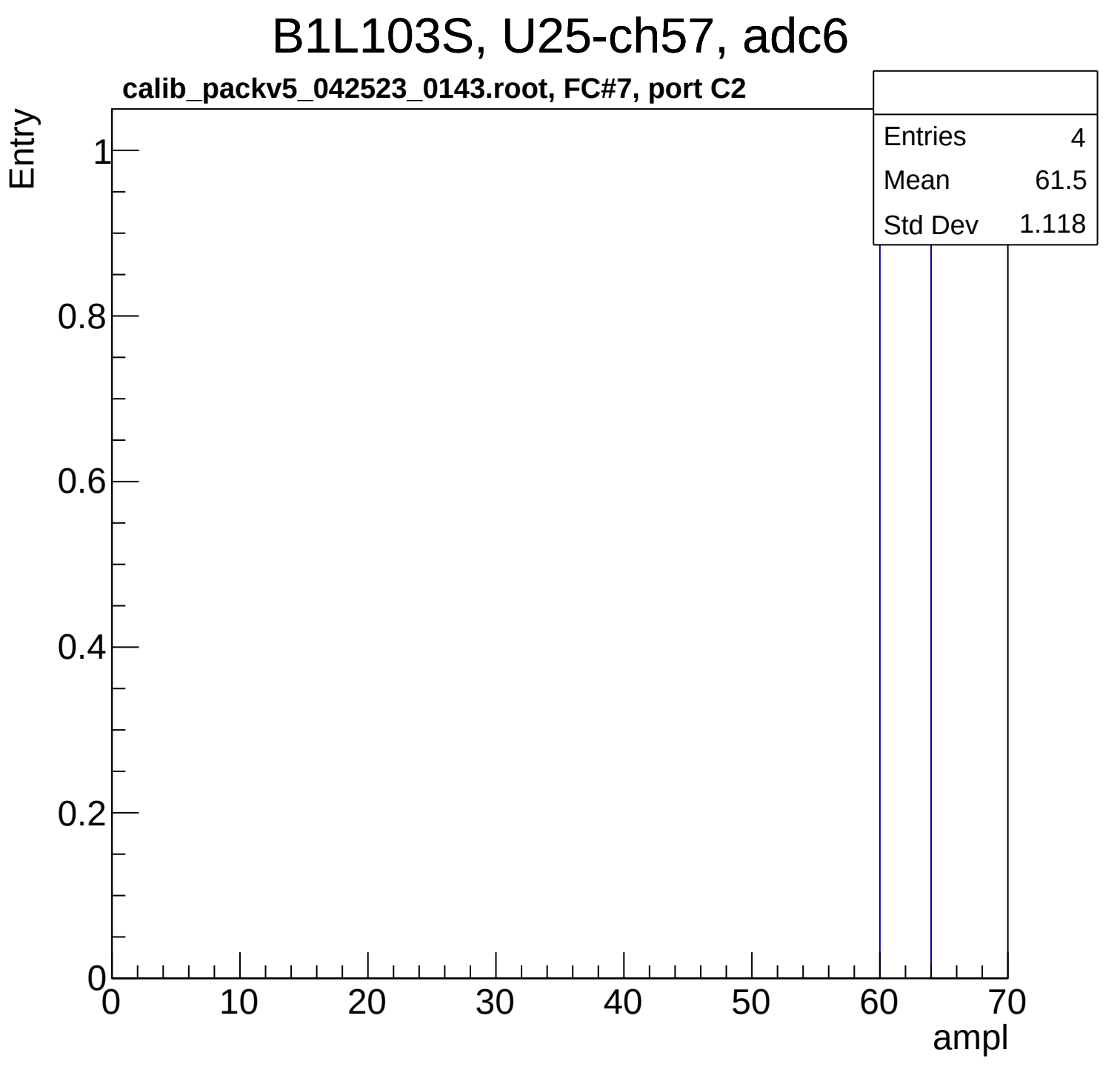
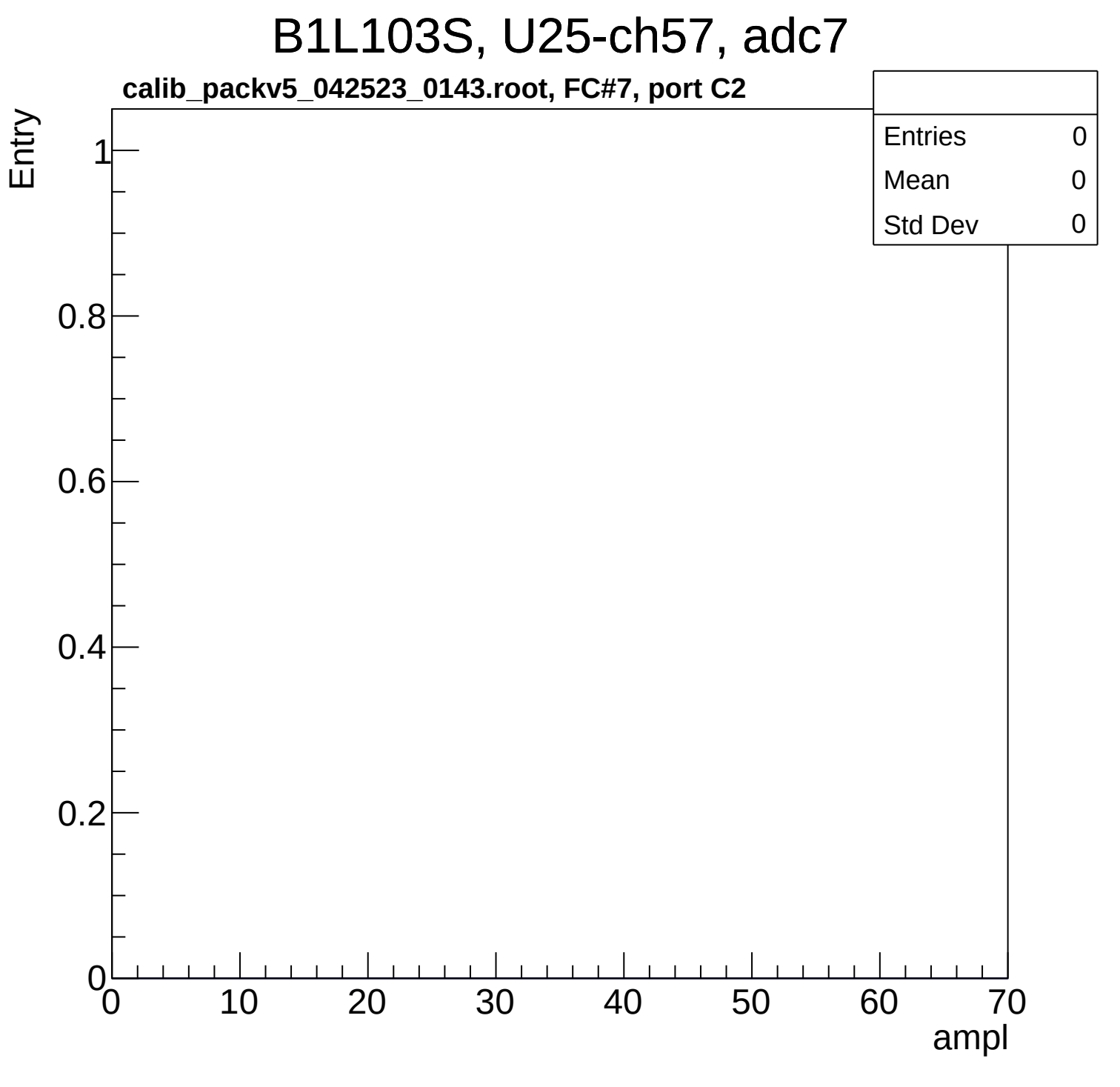

B1L103S, U25-ch57, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 58.65
Std Dev 9.674
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch57, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.5
Std Dev 1.118
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch57, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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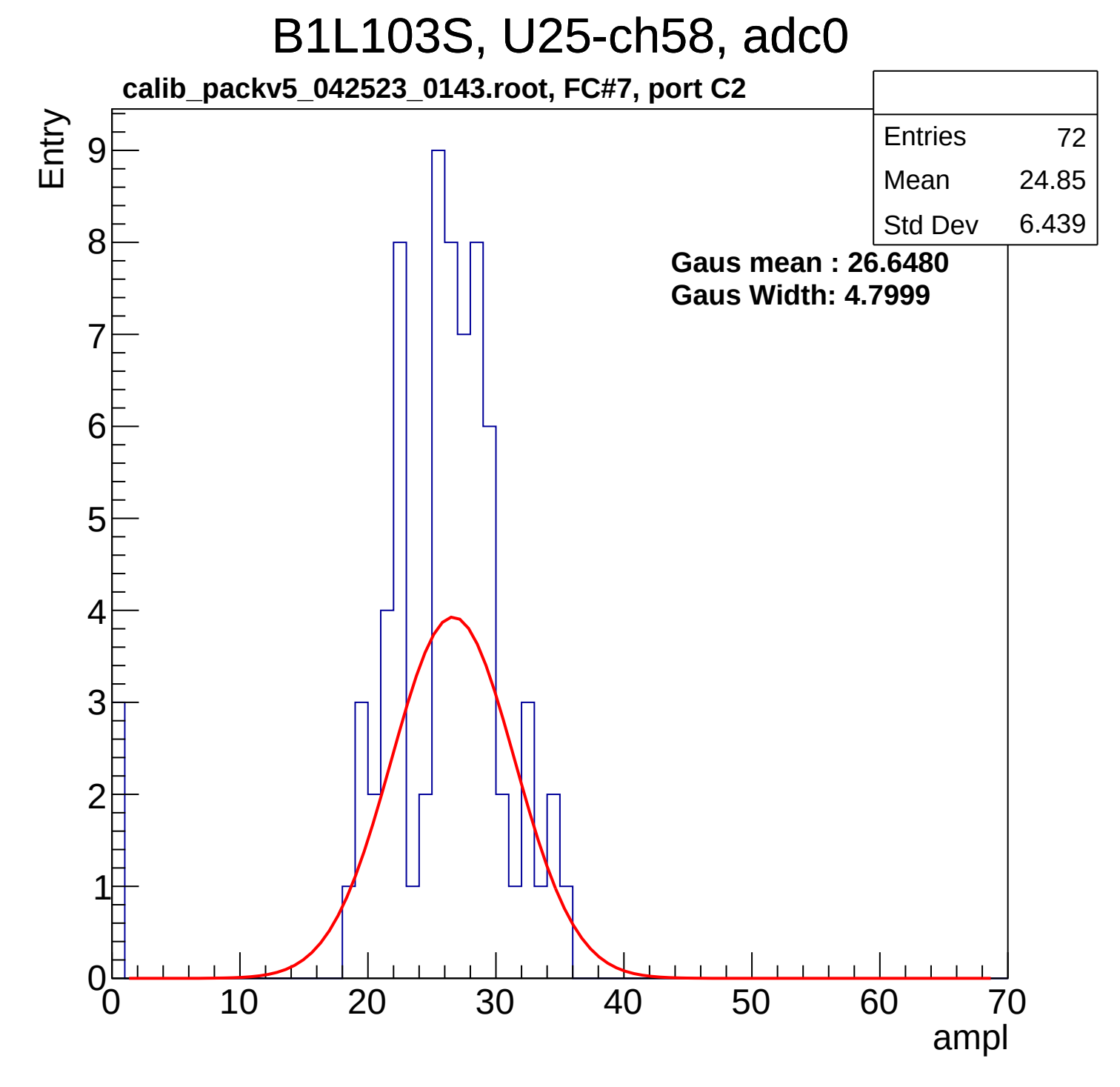
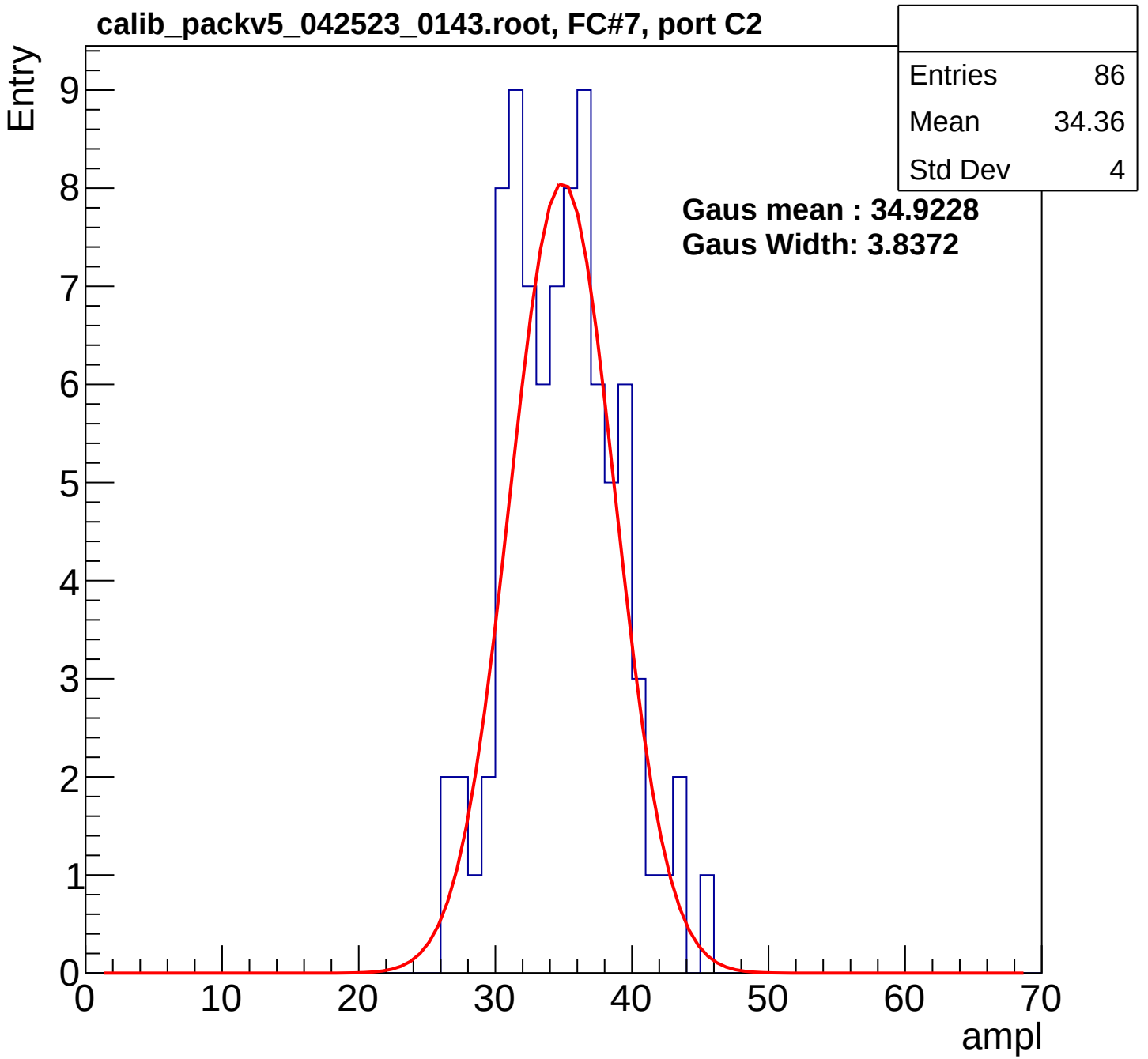
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch58, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 24.85
Std Dev 6.439
Gaus mean : 26.6480
Gaus Width: 4.7999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch58, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 34.36
Std Dev 4
Gaus mean : 34.9228
Gaus Width: 3.8372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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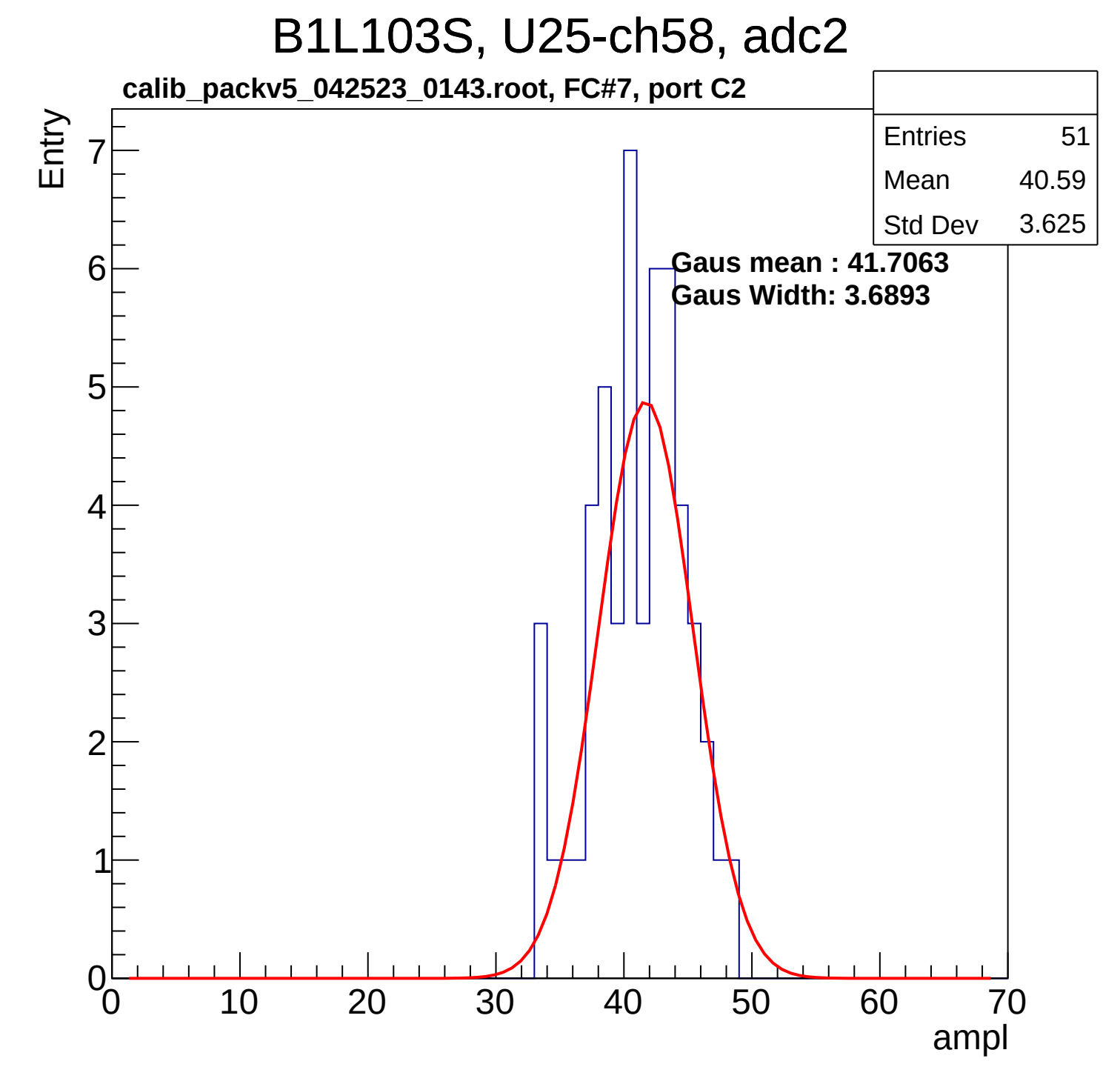
B1L103S, U25-ch58, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 40.59
Std Dev 3.625
Gaus mean : 41.7063
Gaus Width: 3.6893
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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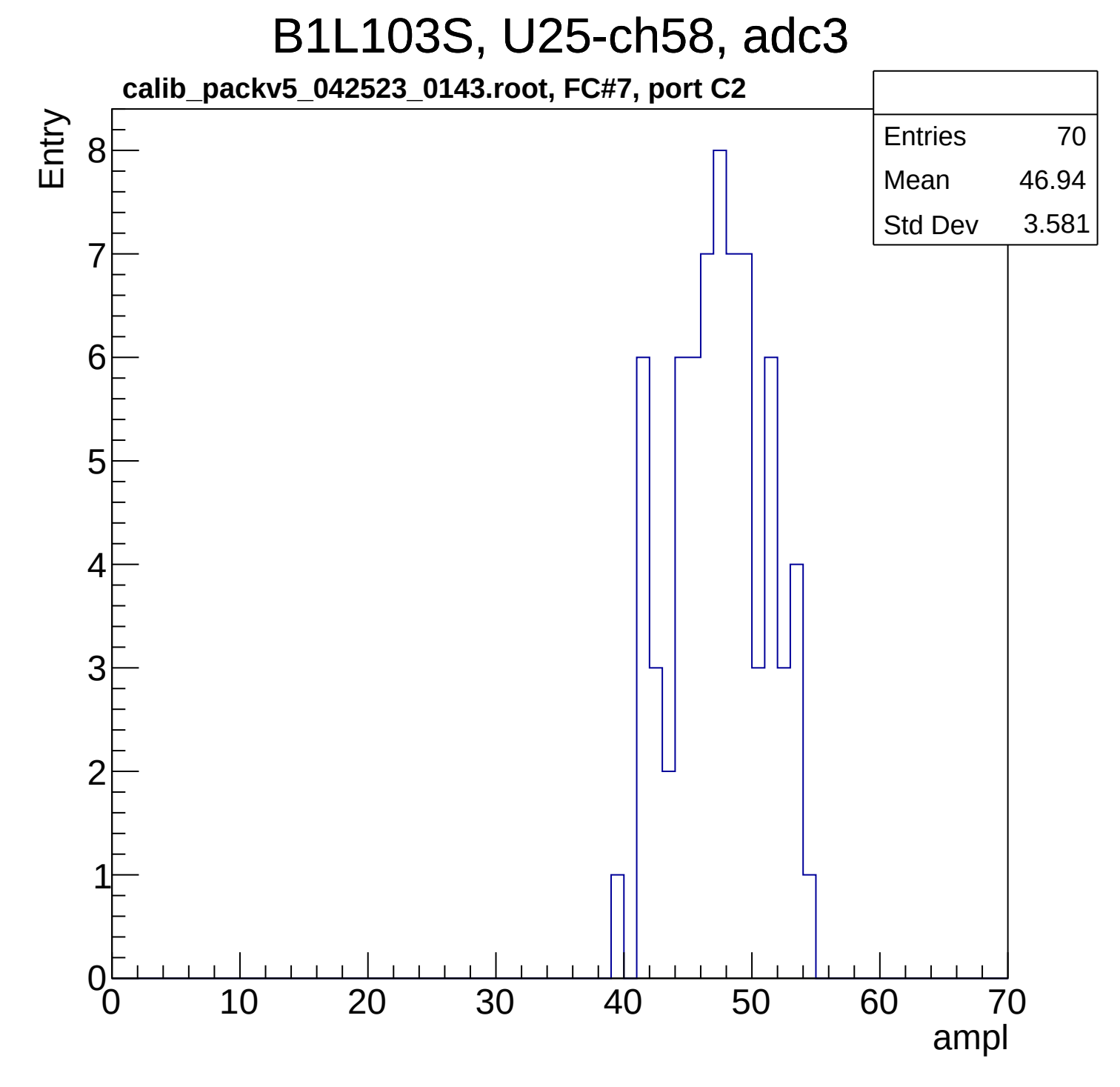
B1L103S, U25-ch58, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 46.94
Std Dev 3.581
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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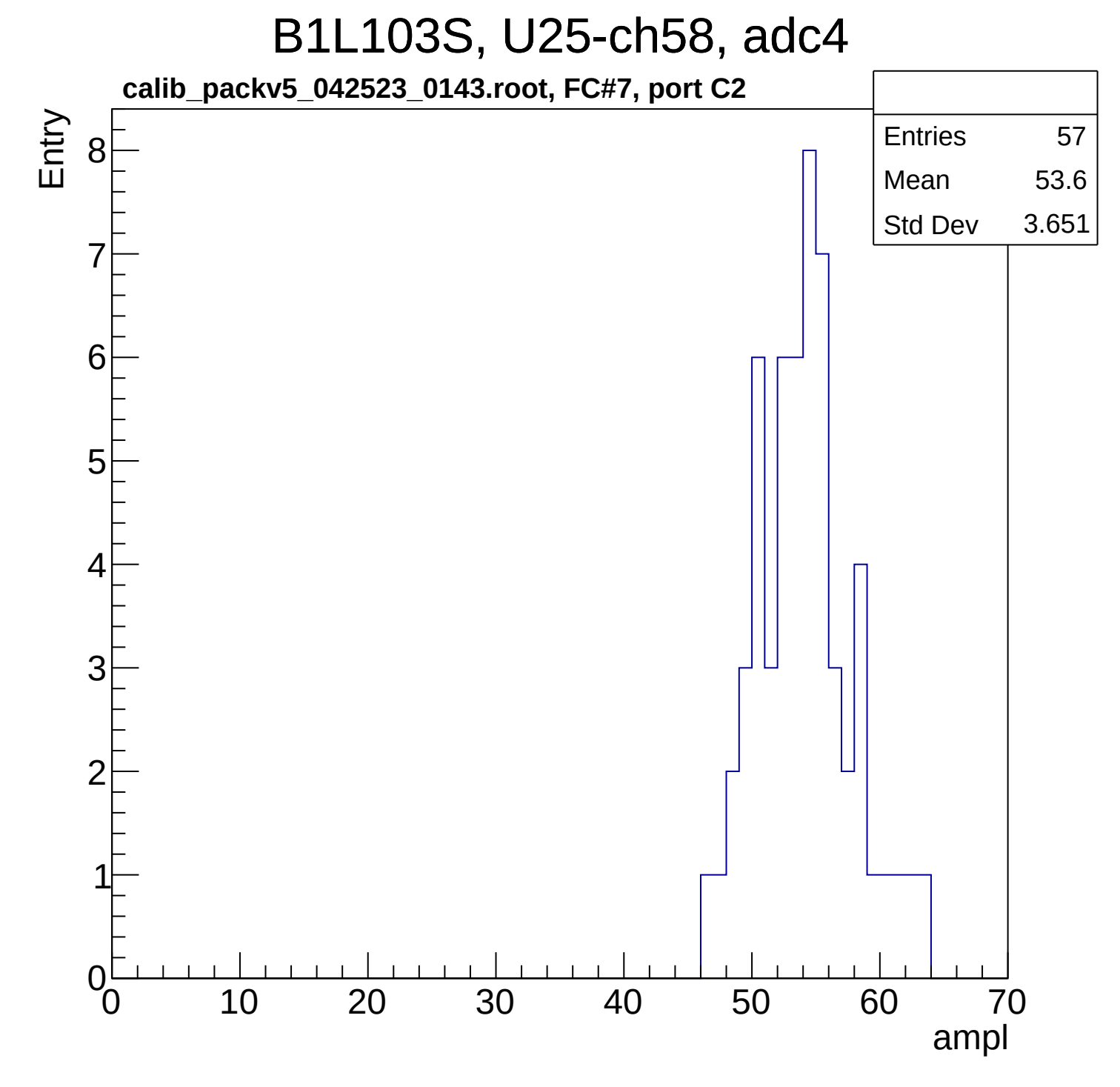
B1L103S, U25-ch58, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 53.6
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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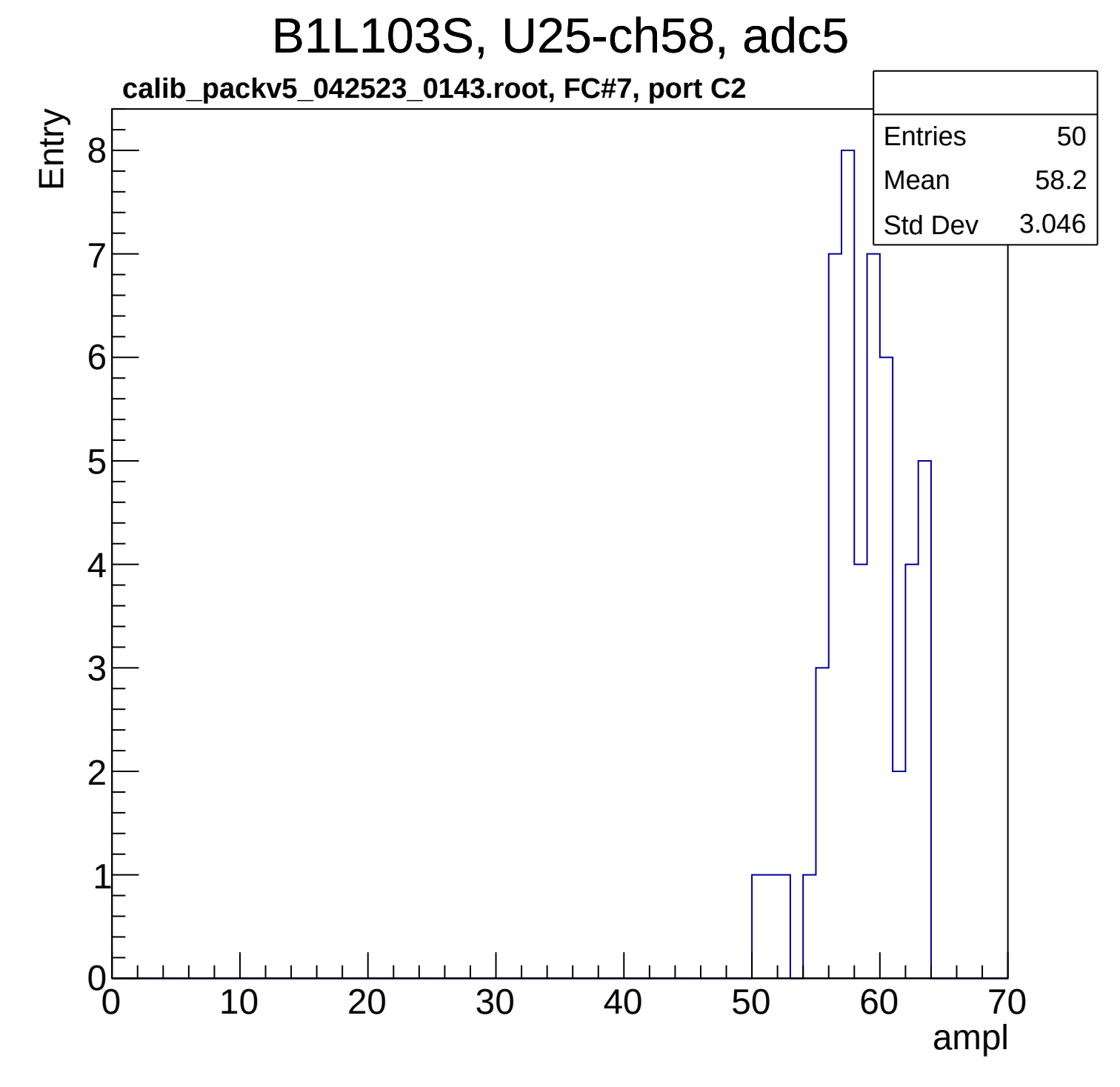
B1L103S, U25-ch58, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 58.2
Std Dev 3.046
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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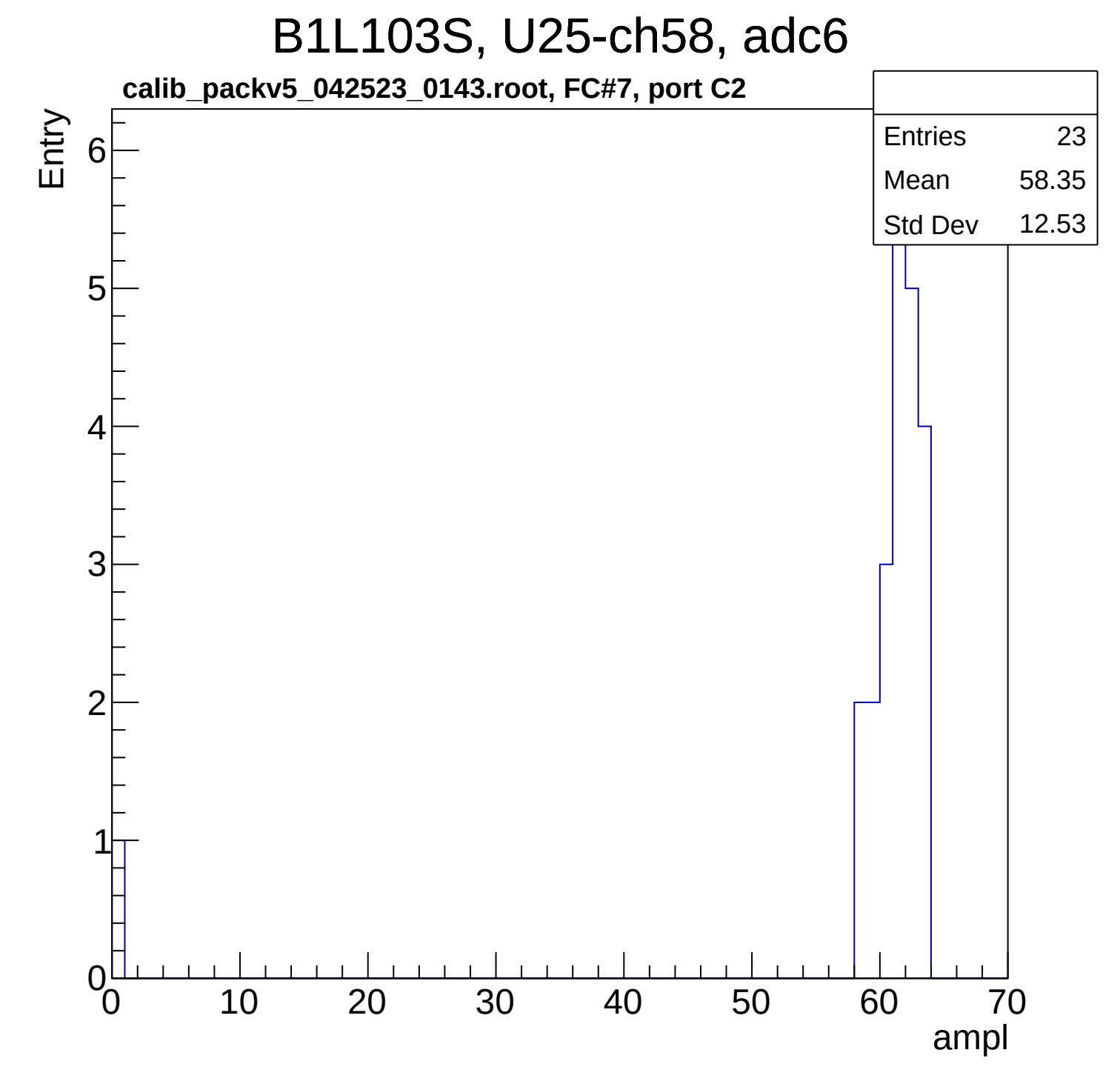
B1L103S, U25-ch58, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 23
Mean 58.35
Std Dev 12.53
Entry
ampl
0
1
2
3
4
5
6
0
10
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60
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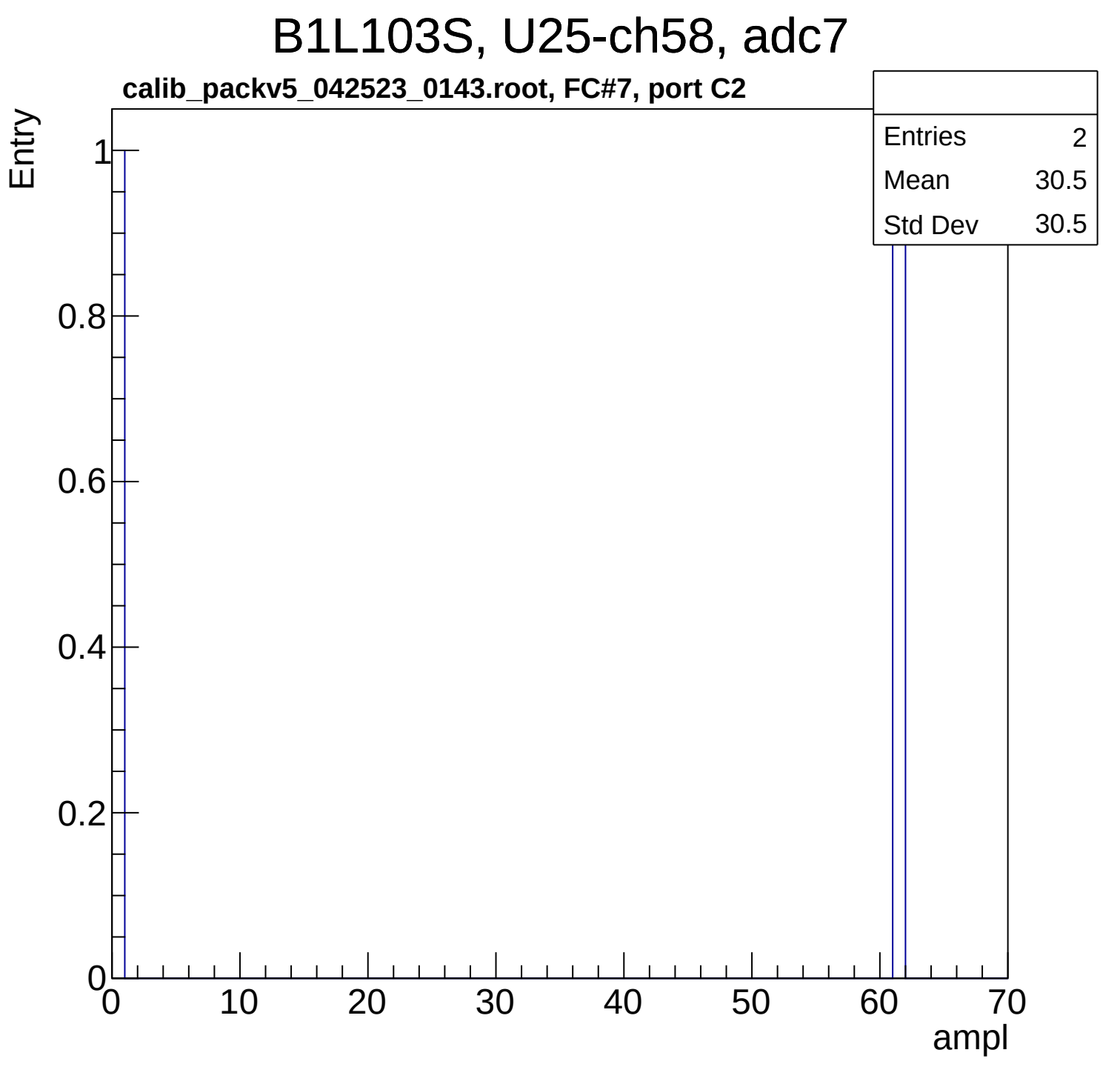
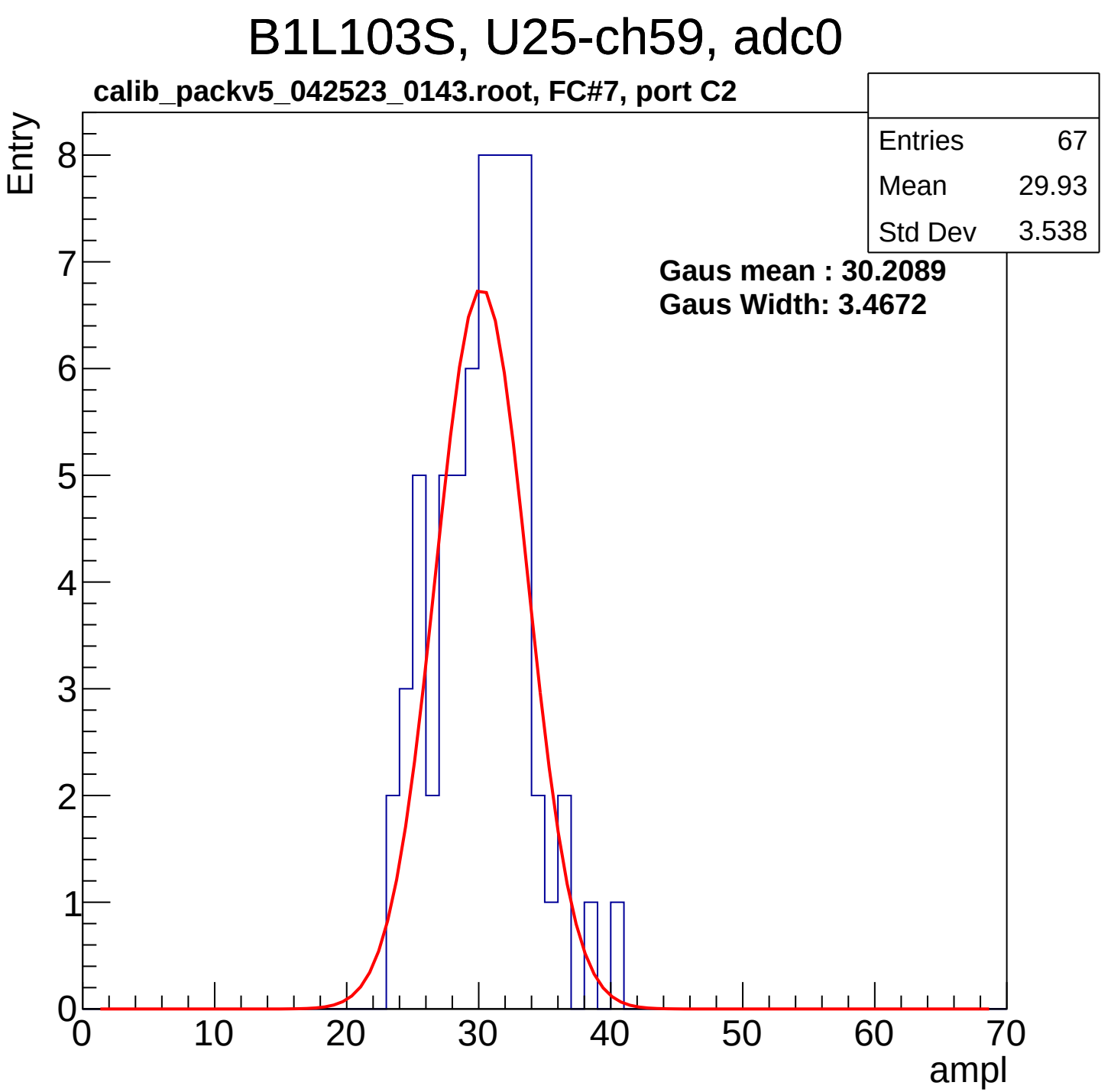

B1L103S, U25-ch58, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch59, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 29.93
Std Dev 3.538
Gaus mean : 30.2089
Gaus Width: 3.4672
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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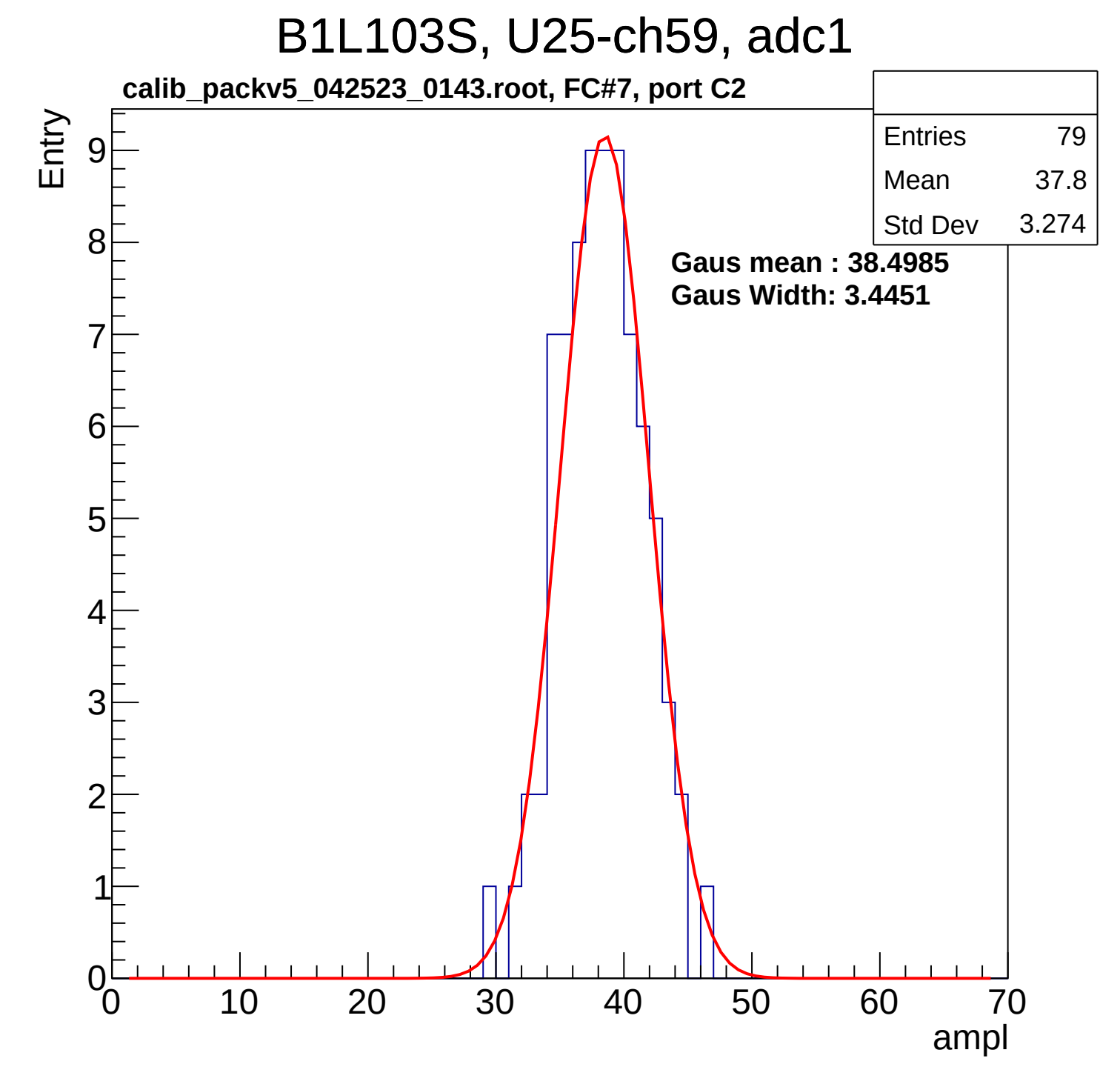
B1L103S, U25-ch59, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 37.8
Std Dev 3.274
Gaus mean : 38.4985
Gaus Width: 3.4451
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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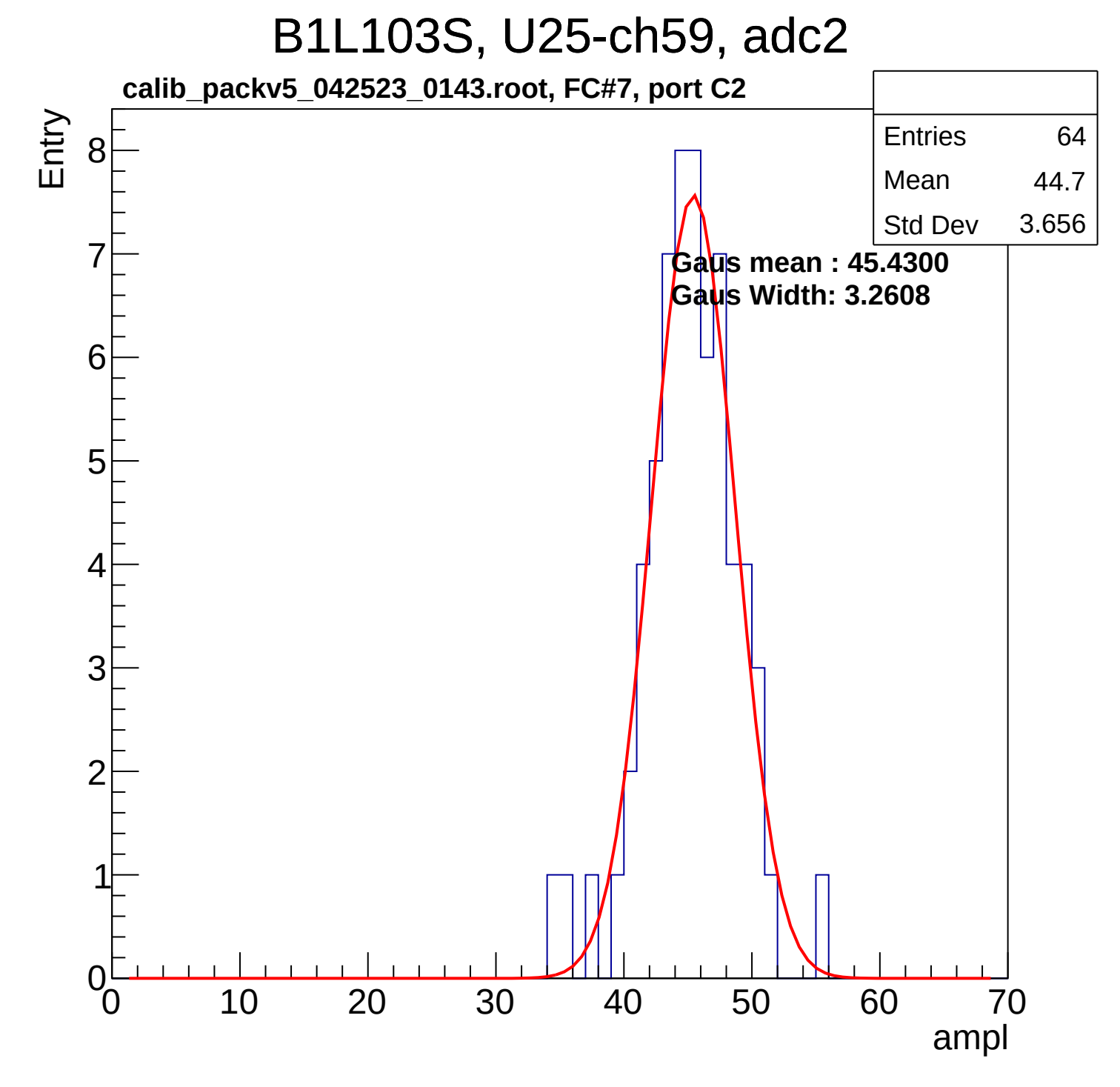
B1L103S, U25-ch59, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.7
Std Dev 3.656
Gaus mean : 45.4300
Gaus Width: 3.2608
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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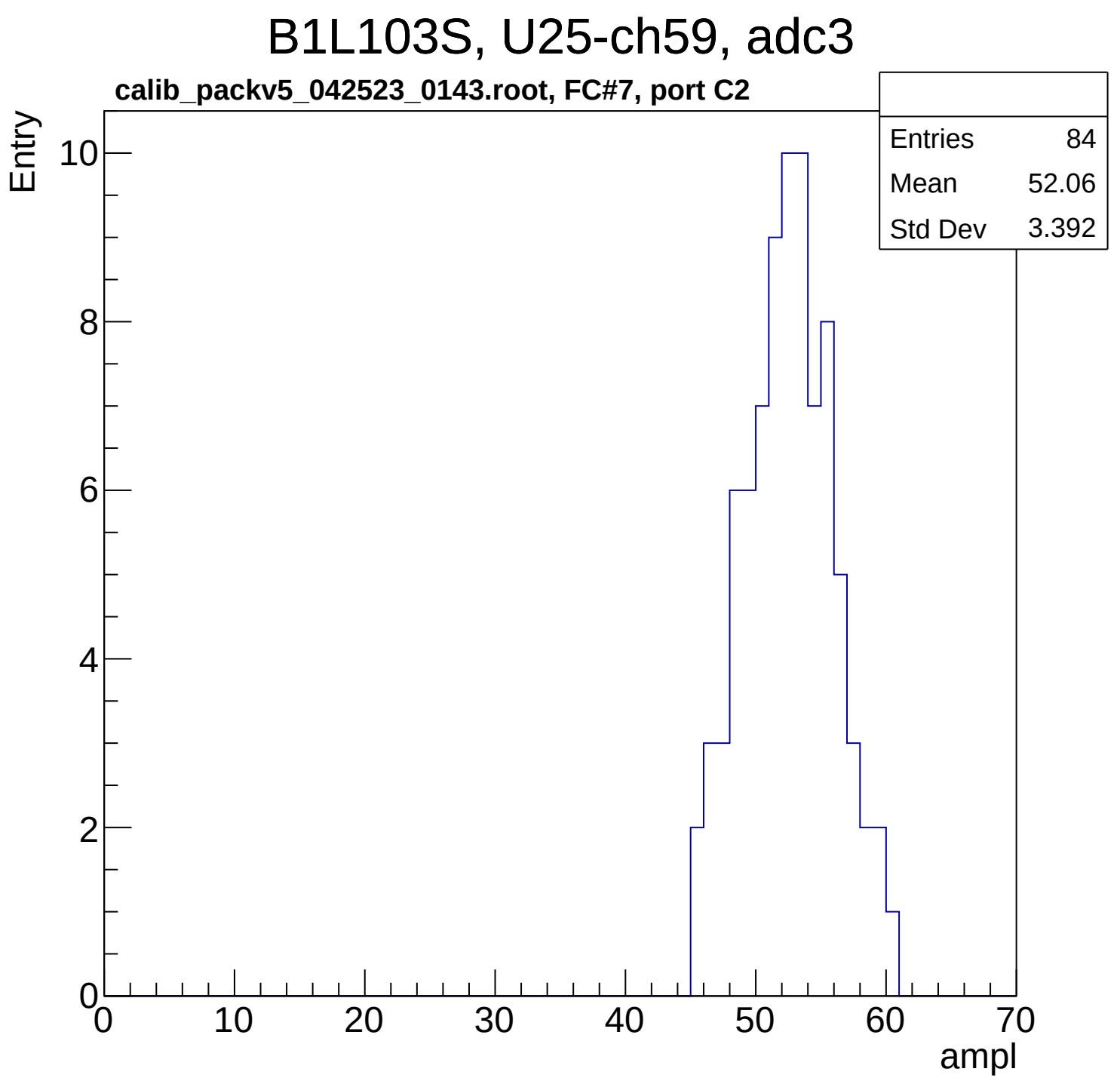
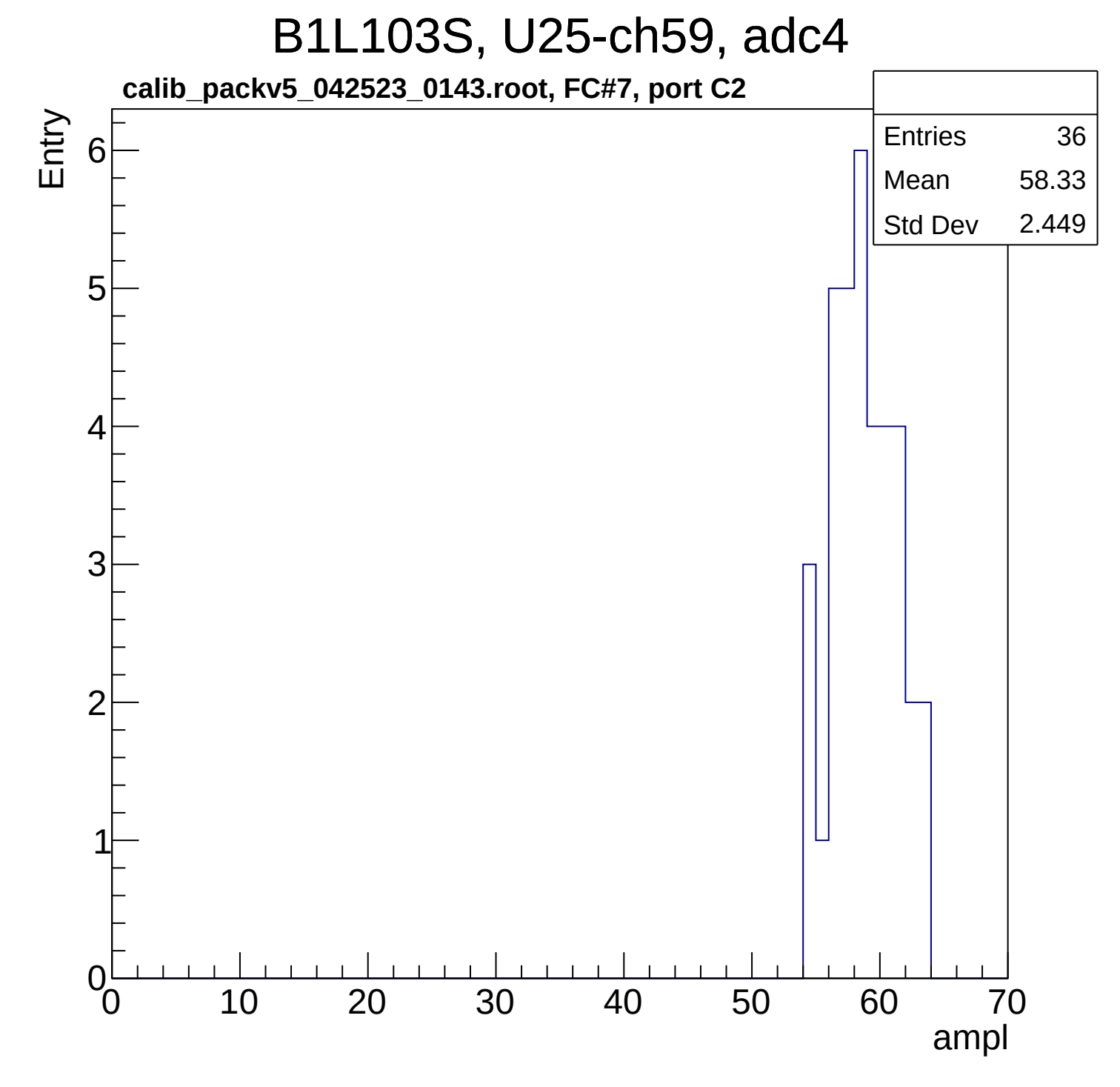
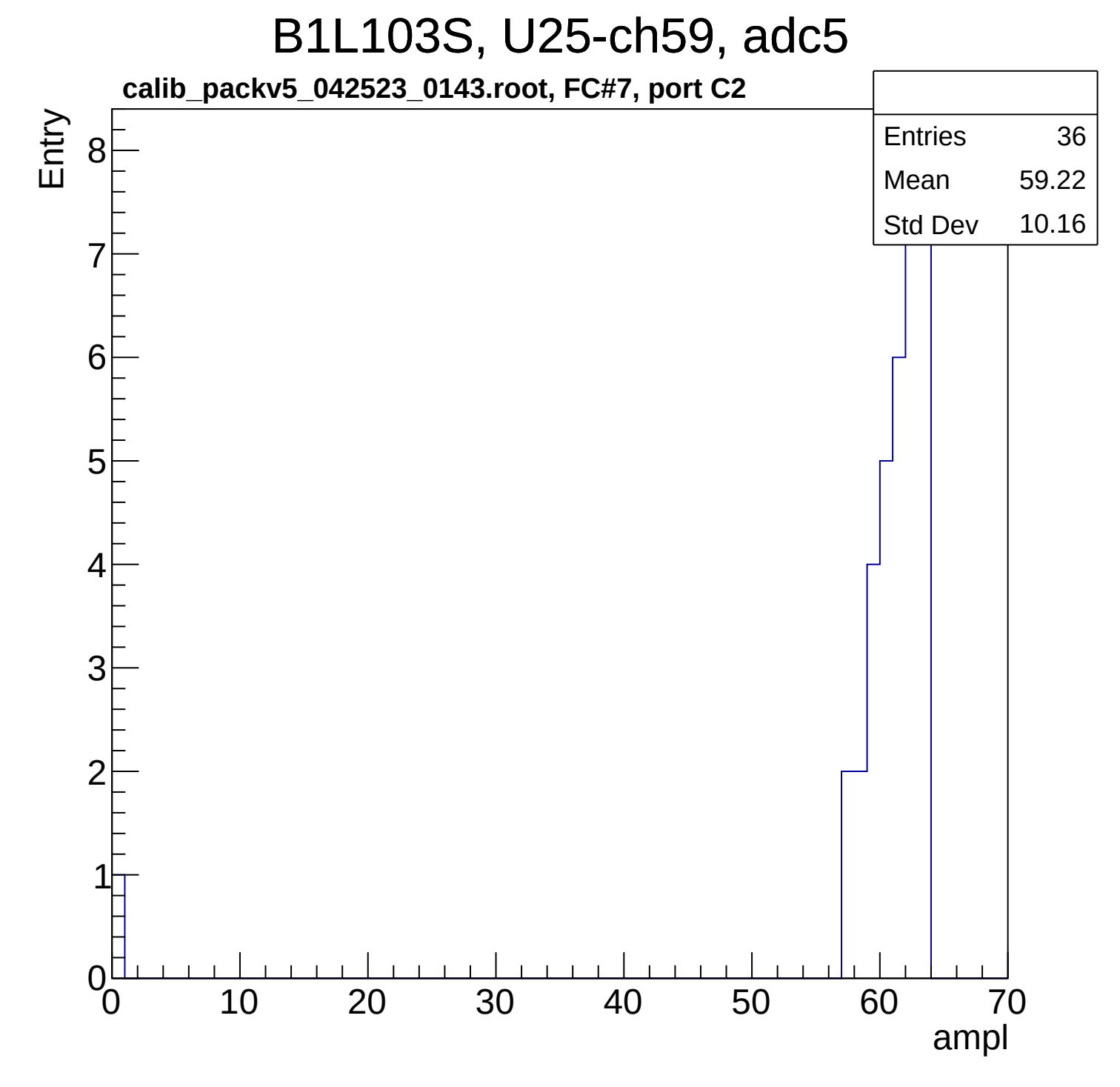

B1L103S, U25-ch59, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 52.06
Std Dev 3.392
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch59, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.33
Std Dev 2.449
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch59, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 59.22
Std Dev 10.16
Entry
ampl
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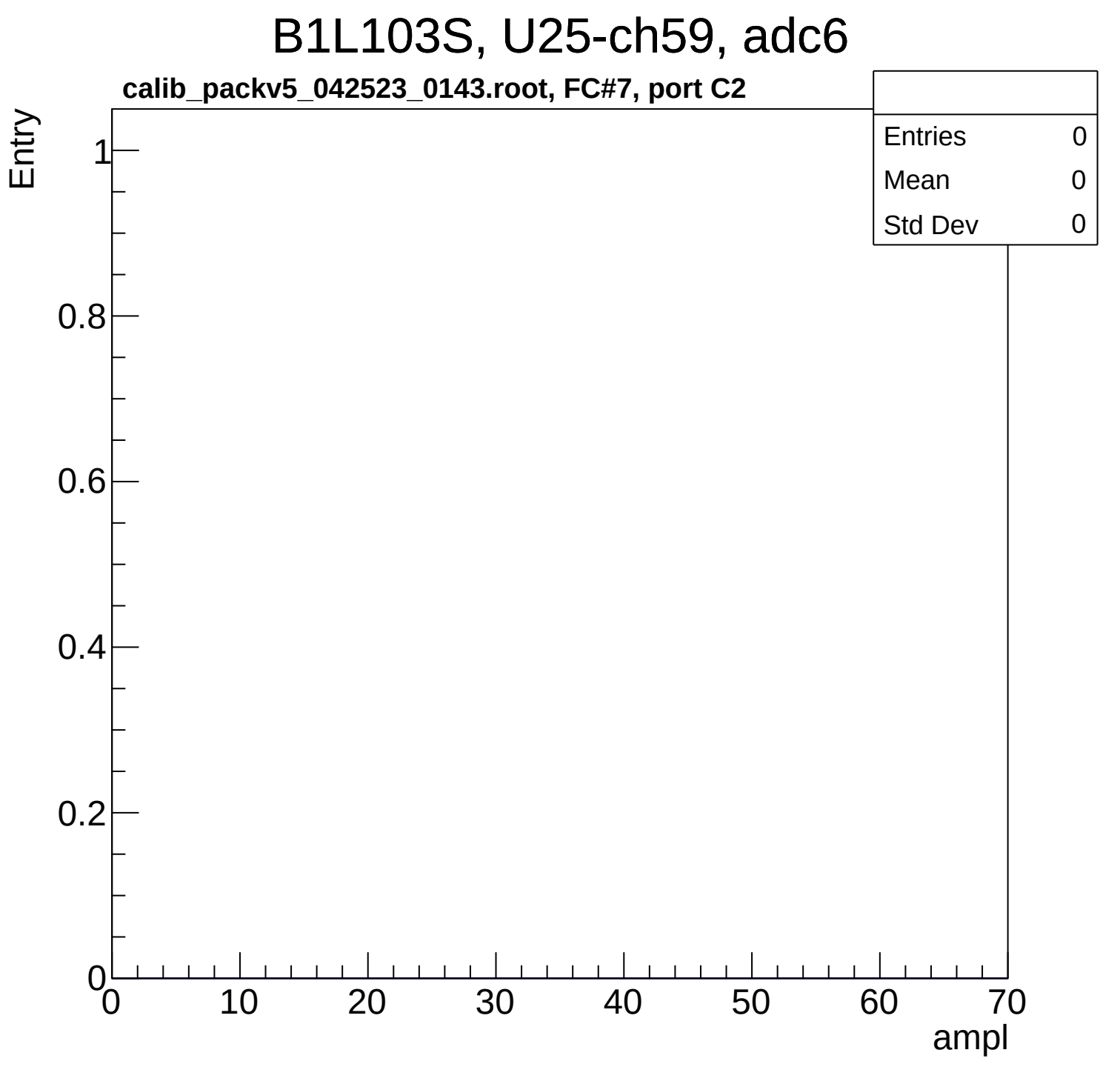
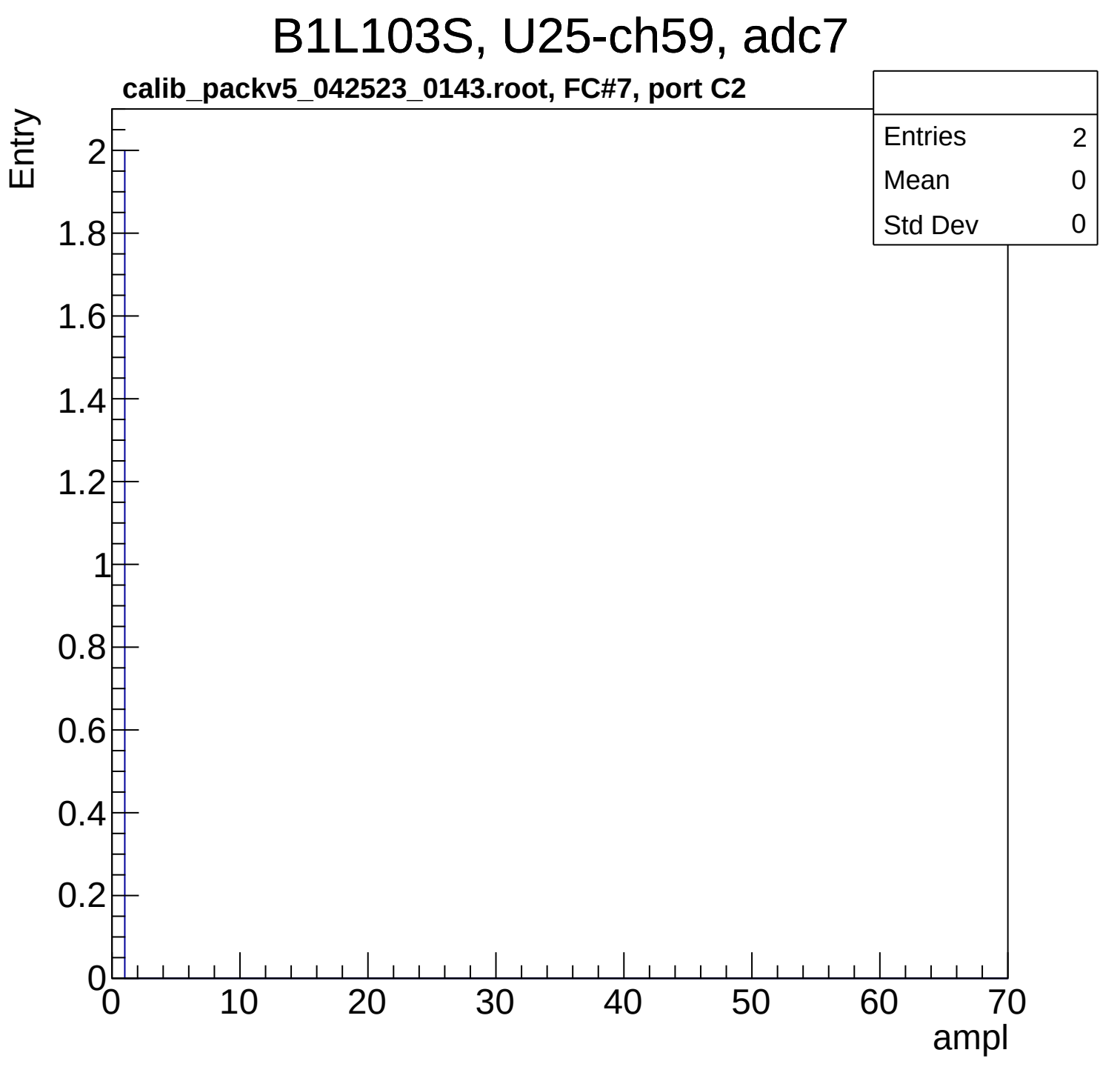
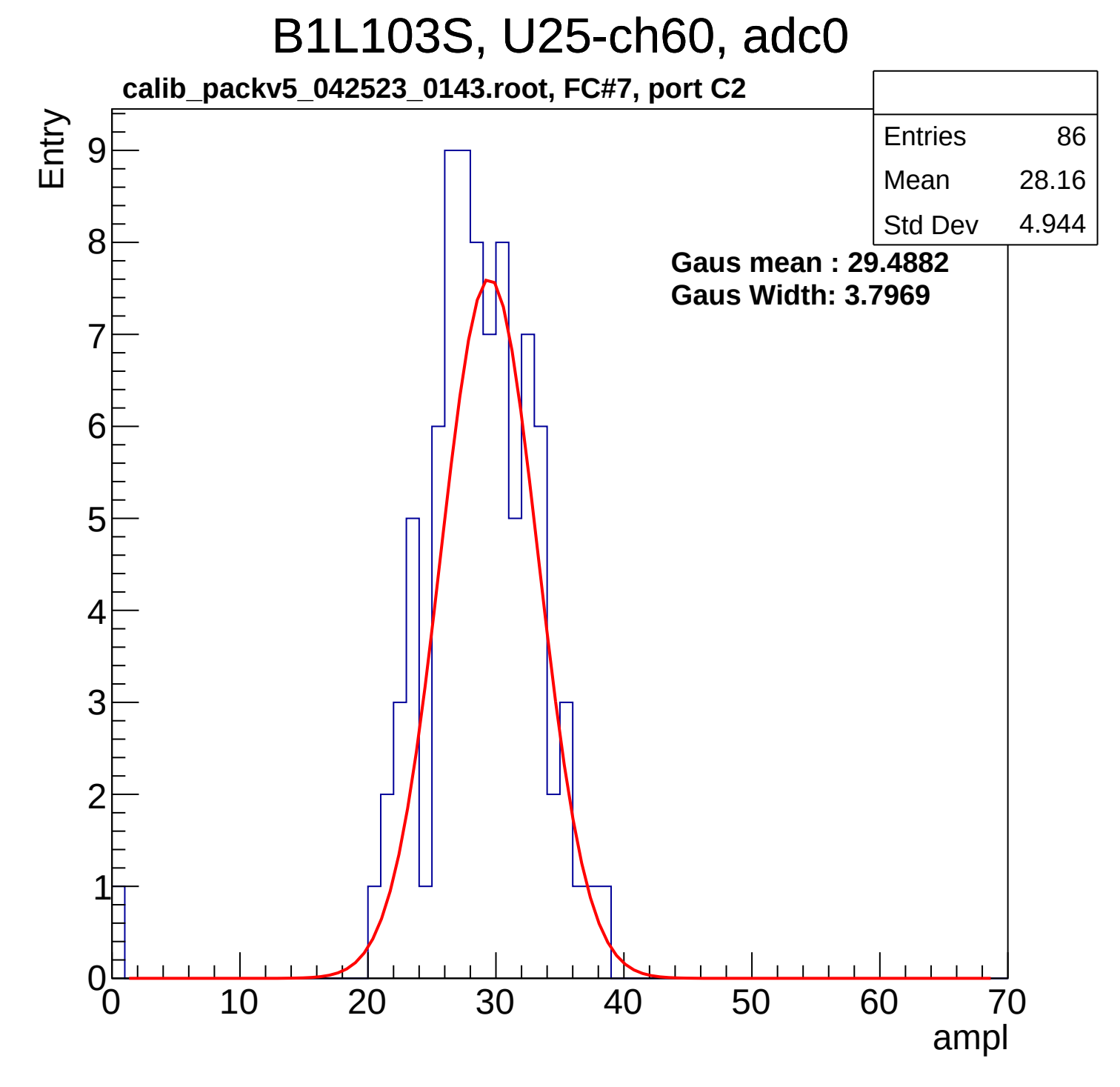
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch59, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch59, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch60, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 28.16
Std Dev 4.944
Gaus mean : 29.4882
Gaus Width: 3.7969
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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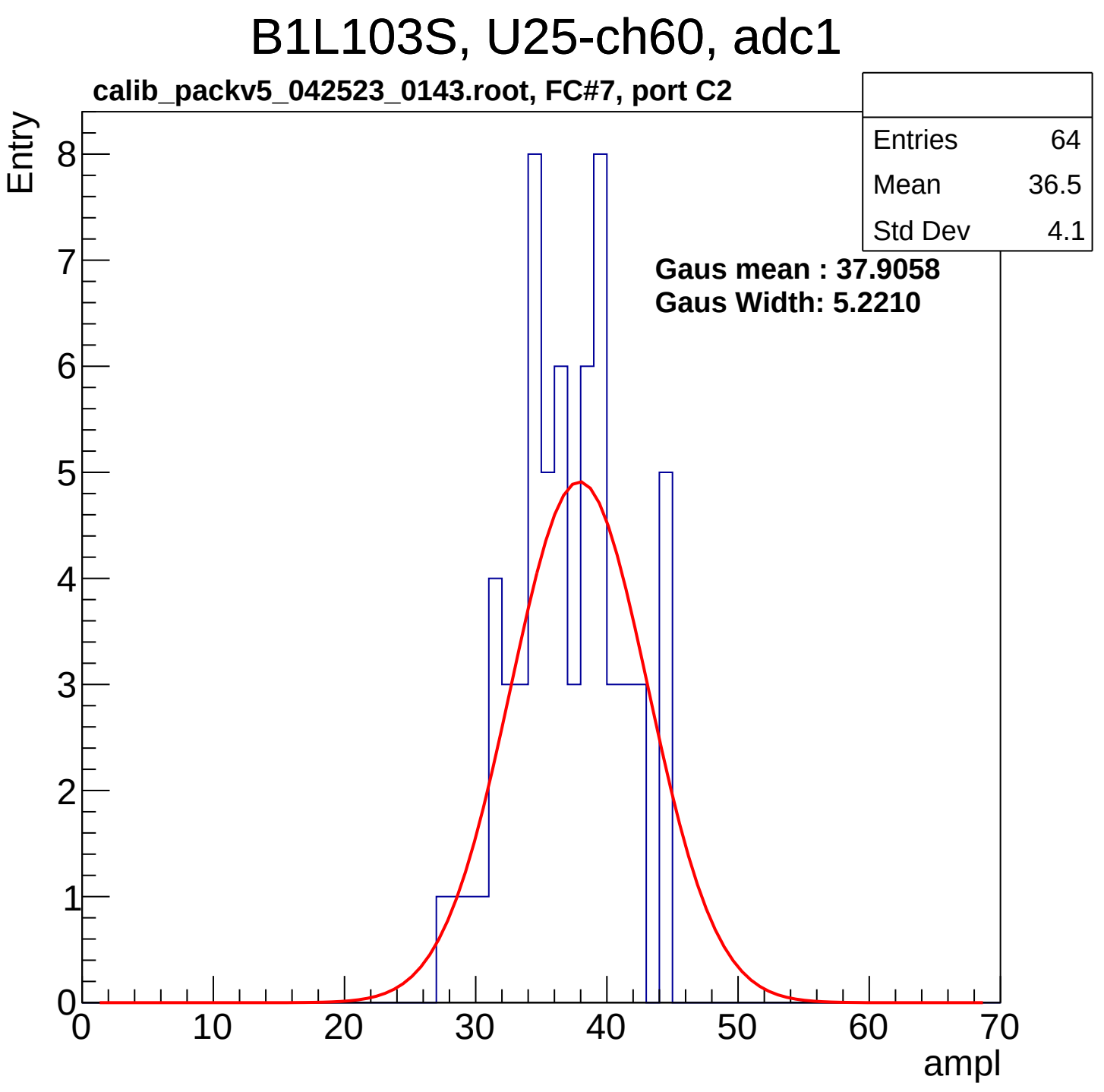
B1L103S, U25-ch60, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 36.5
Std Dev 4.1
Gaus mean : 37.9058
Gaus Width: 5.2210
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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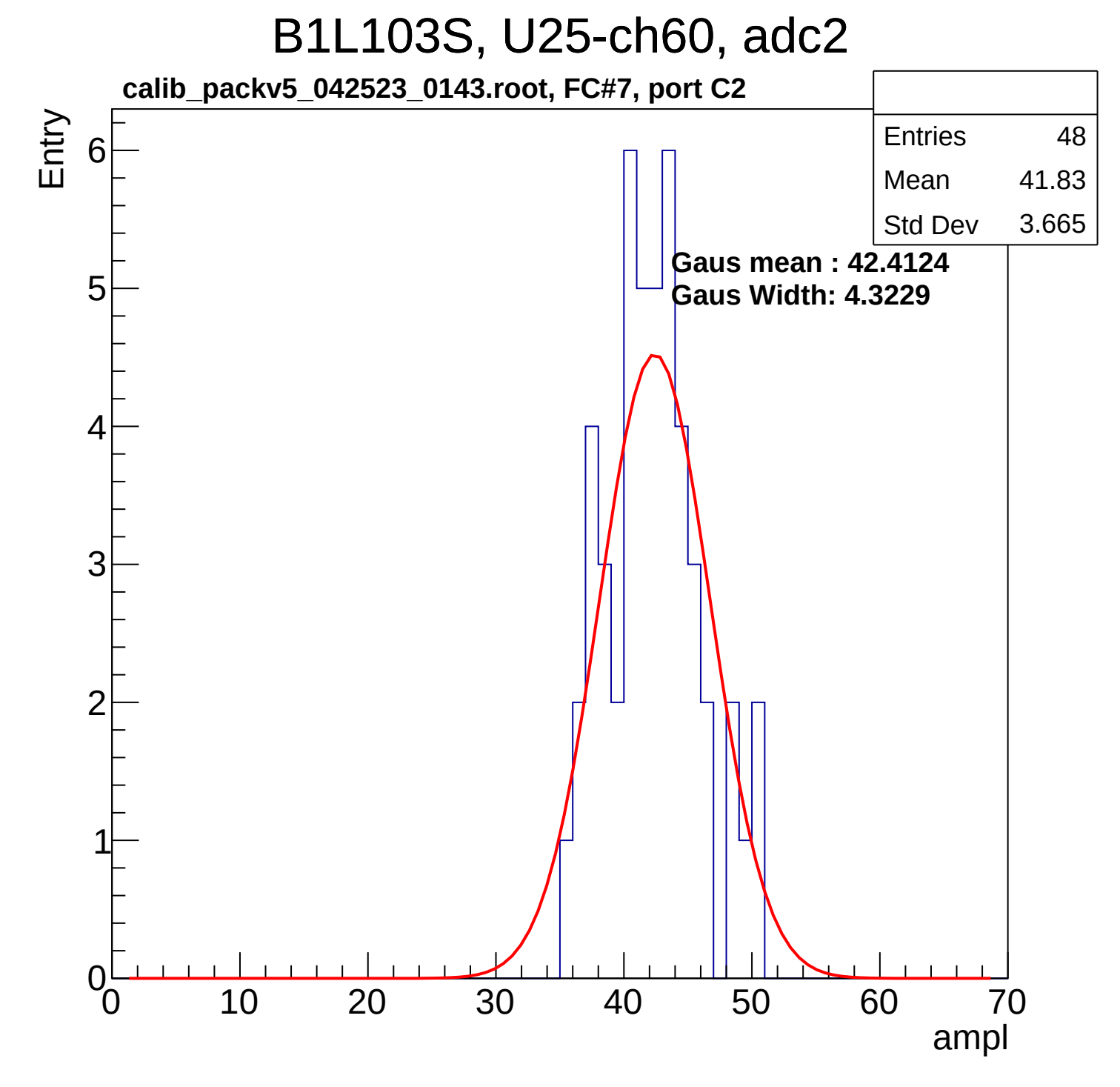
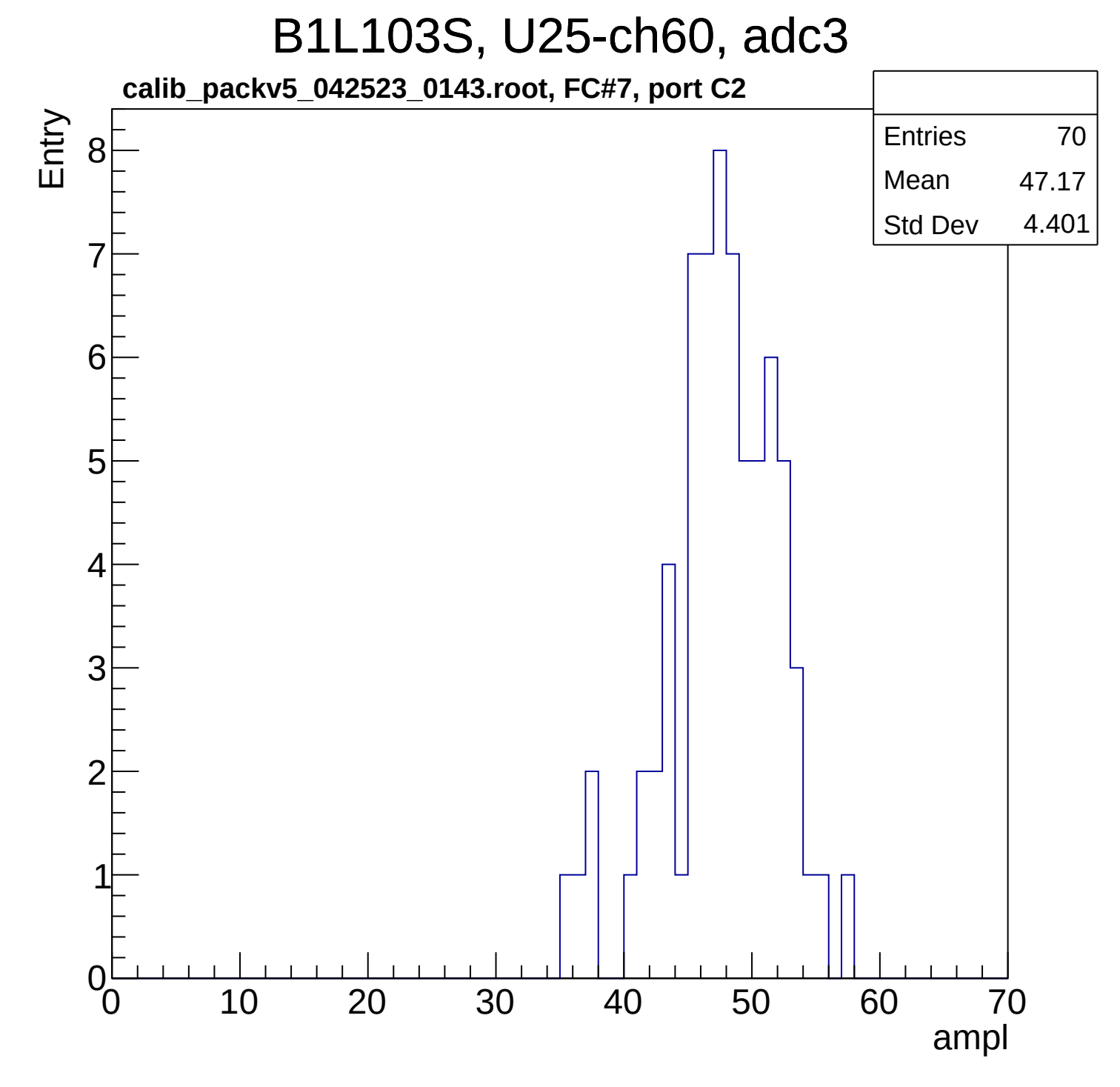
60
70


B1L103S, U25-ch60, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 41.83
Std Dev 3.665
Gaus mean : 42.4124
Gaus Width: 4.3229
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch60, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 47.17
Std Dev 4.401
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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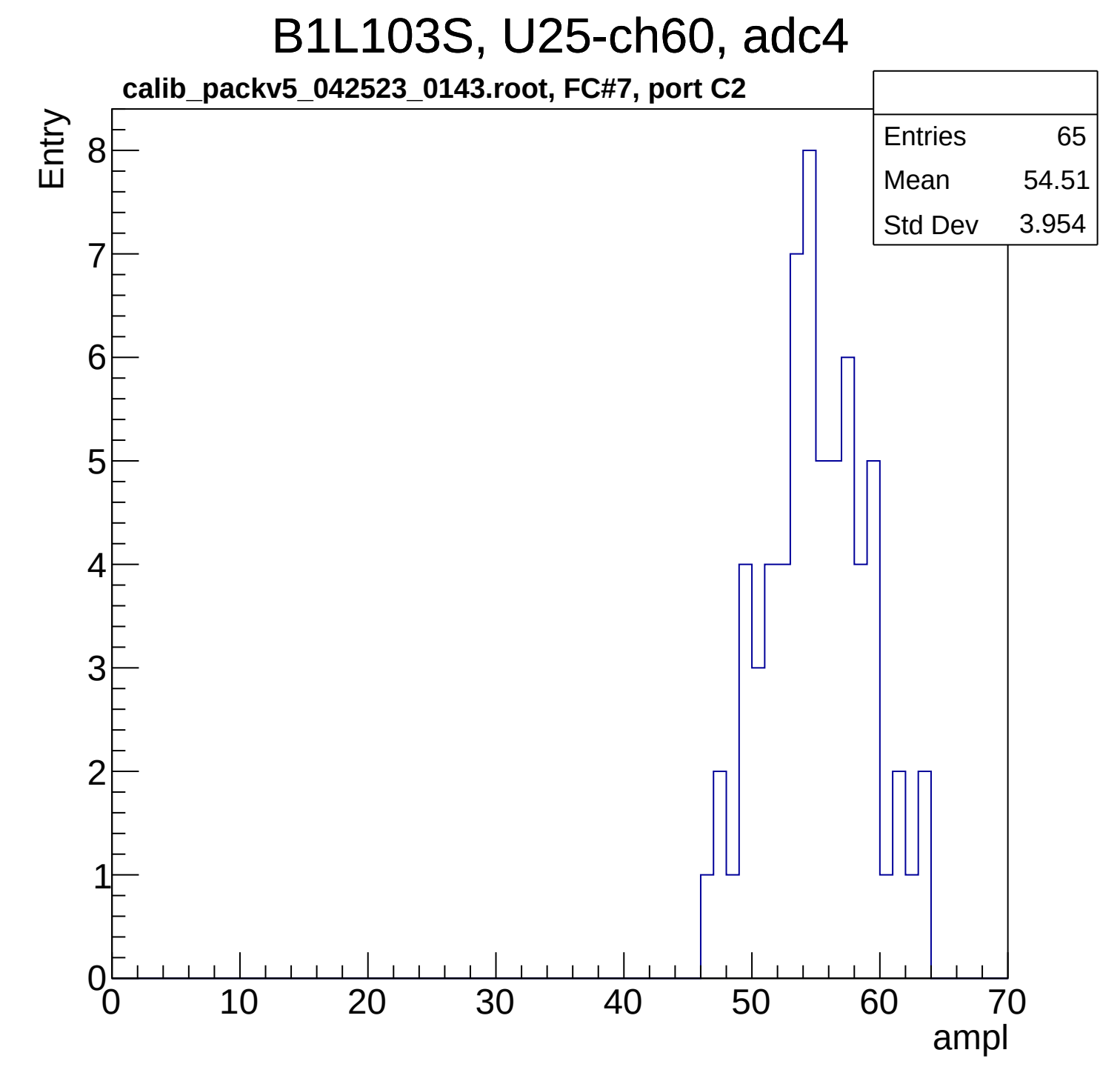
B1L103S, U25-ch60, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 54.51
Std Dev 3.954
Entry
ampl
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4
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6
7
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0
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60
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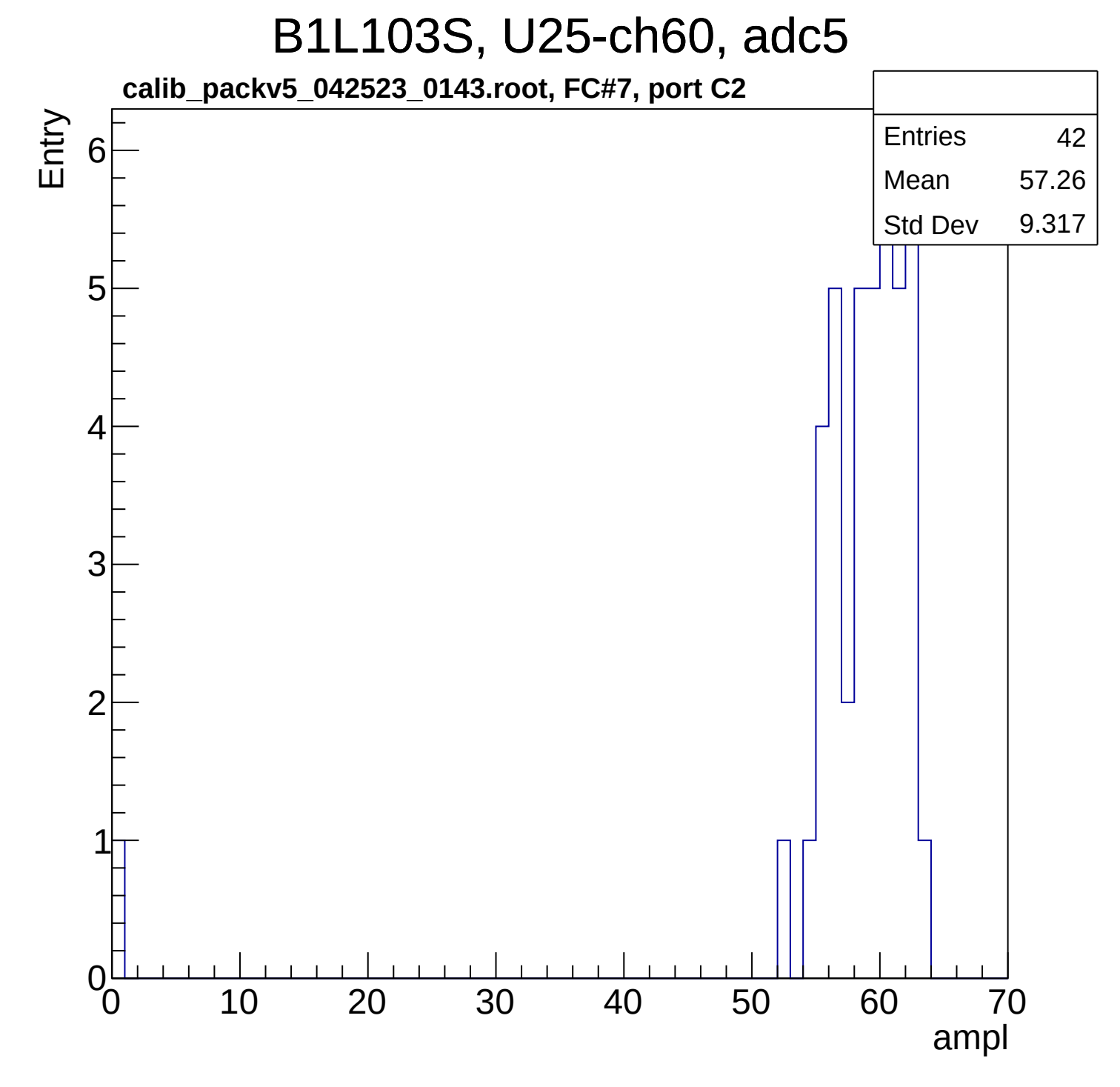
B1L103S, U25-ch60, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 57.26
Std Dev 9.317
Entry
ampl
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3
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0
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20
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40
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60
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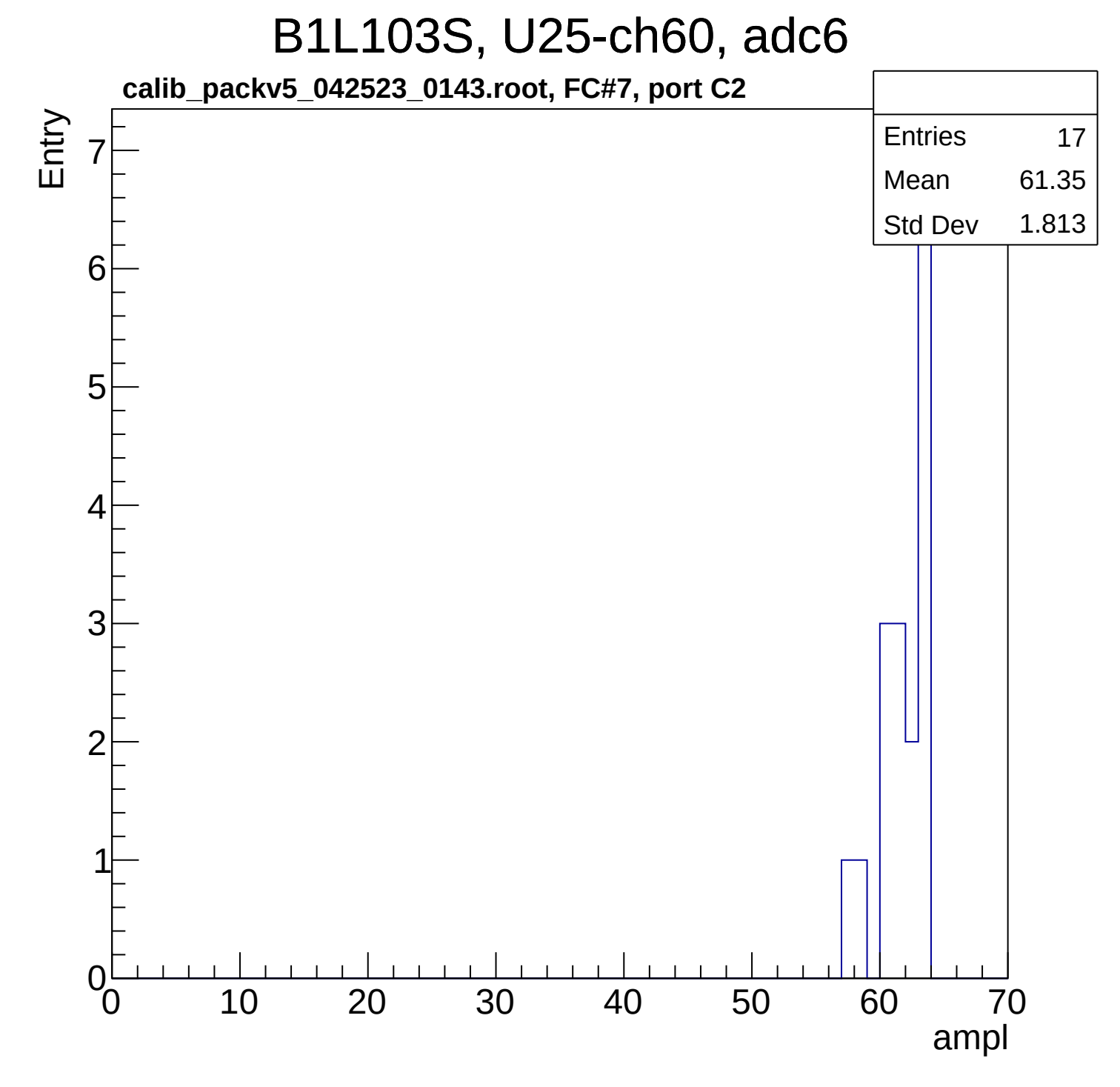
B1L103S, U25-ch60, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 61.35
Std Dev 1.813
Entry
ampl
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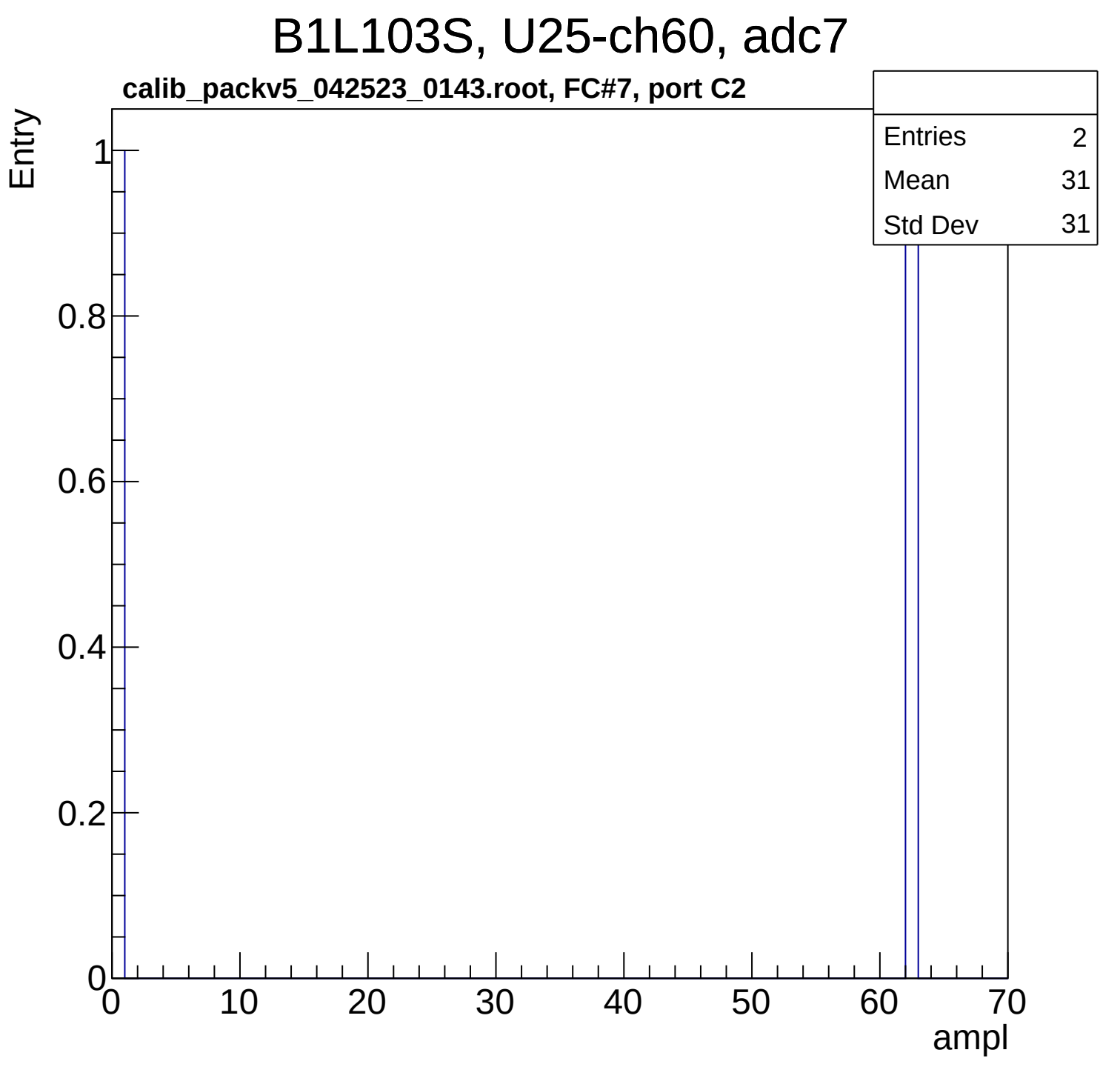
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch60, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
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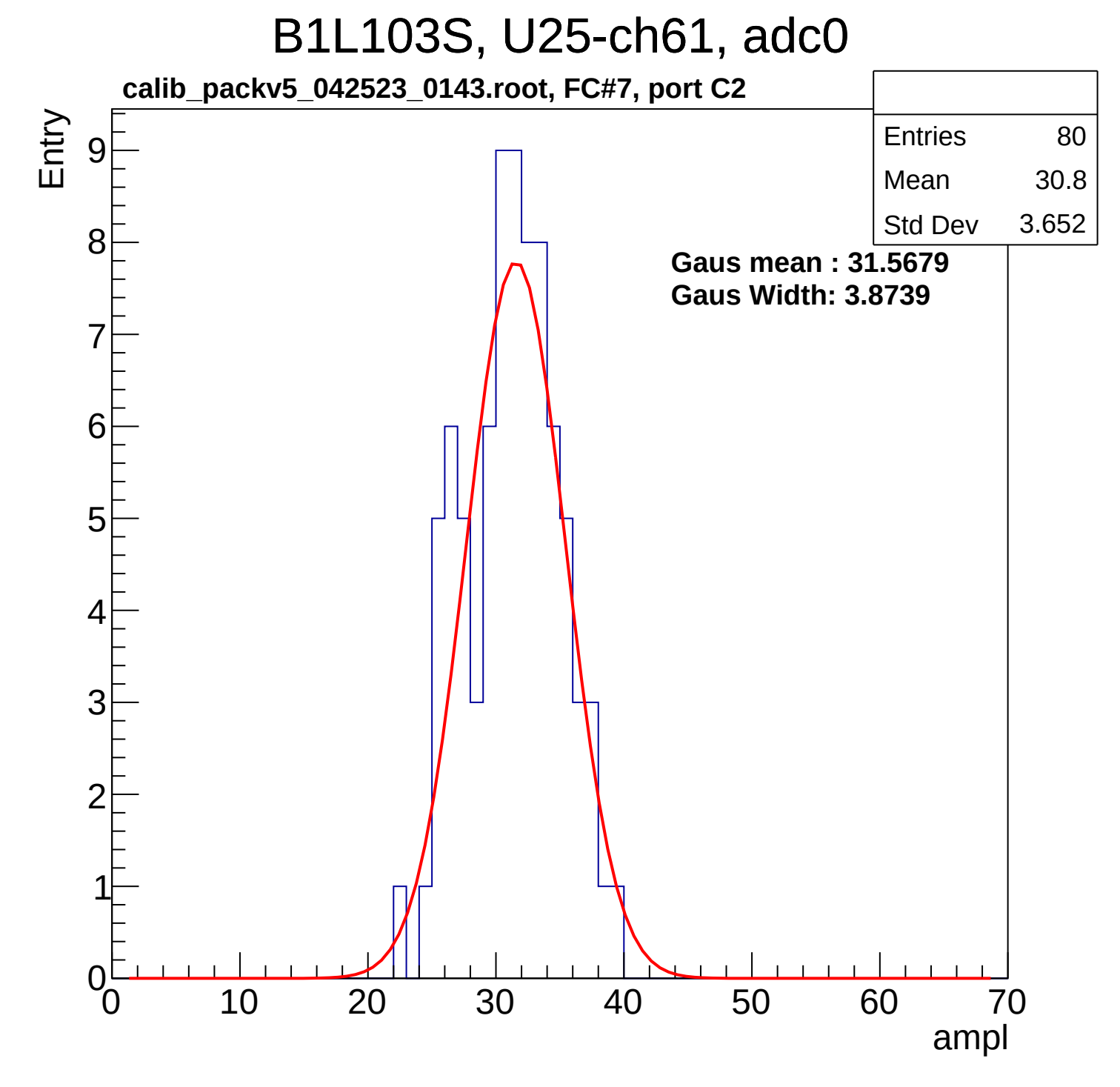
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch61, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 30.8
Std Dev 3.652
Gaus mean : 31.5679
Gaus Width: 3.8739
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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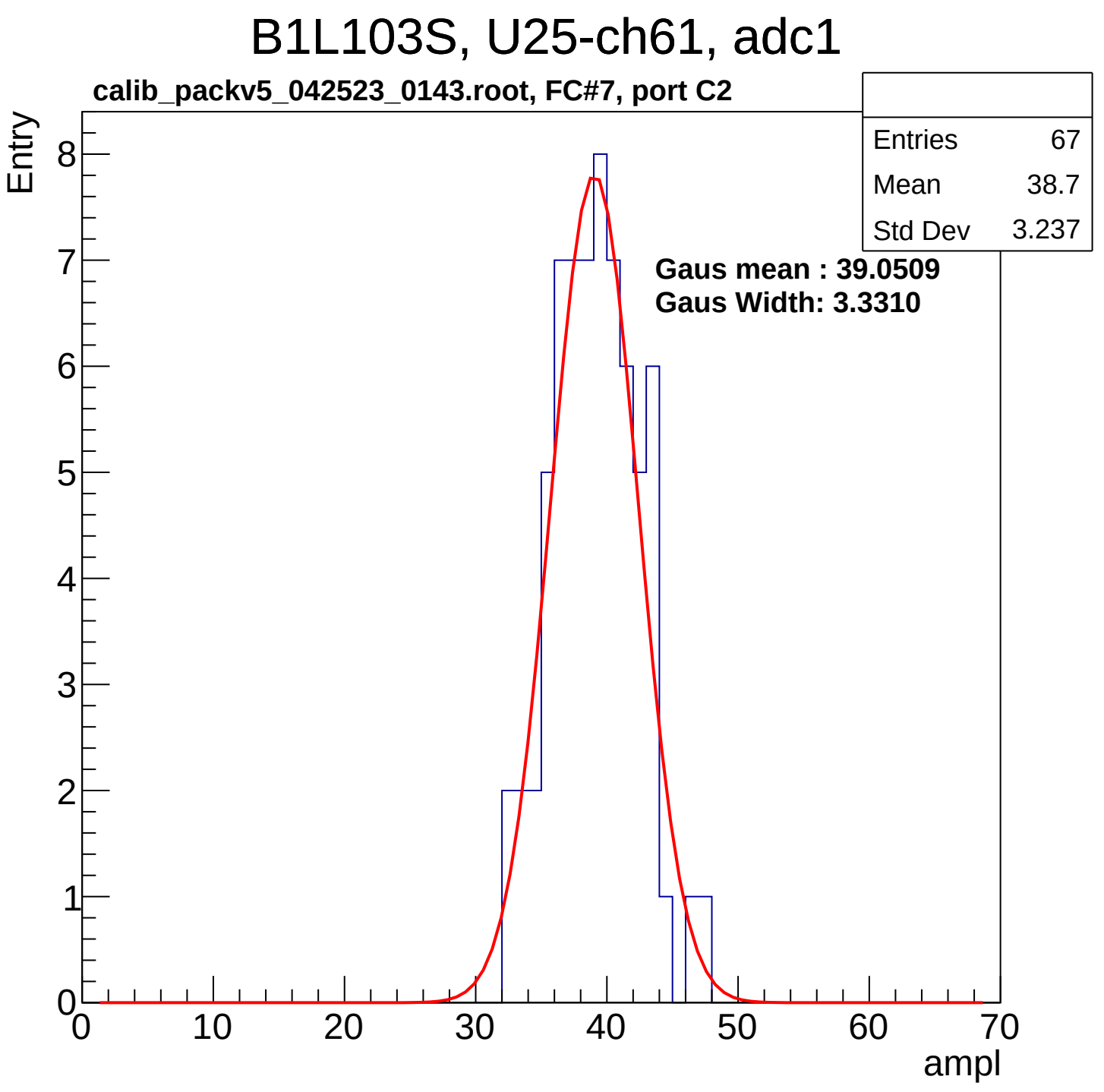
B1L103S, U25-ch61, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 38.7
Std Dev 3.237
Gaus mean : 39.0509
Gaus Width: 3.3310
Entry
ampl
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1
2
3
4
5
6
7
8
0
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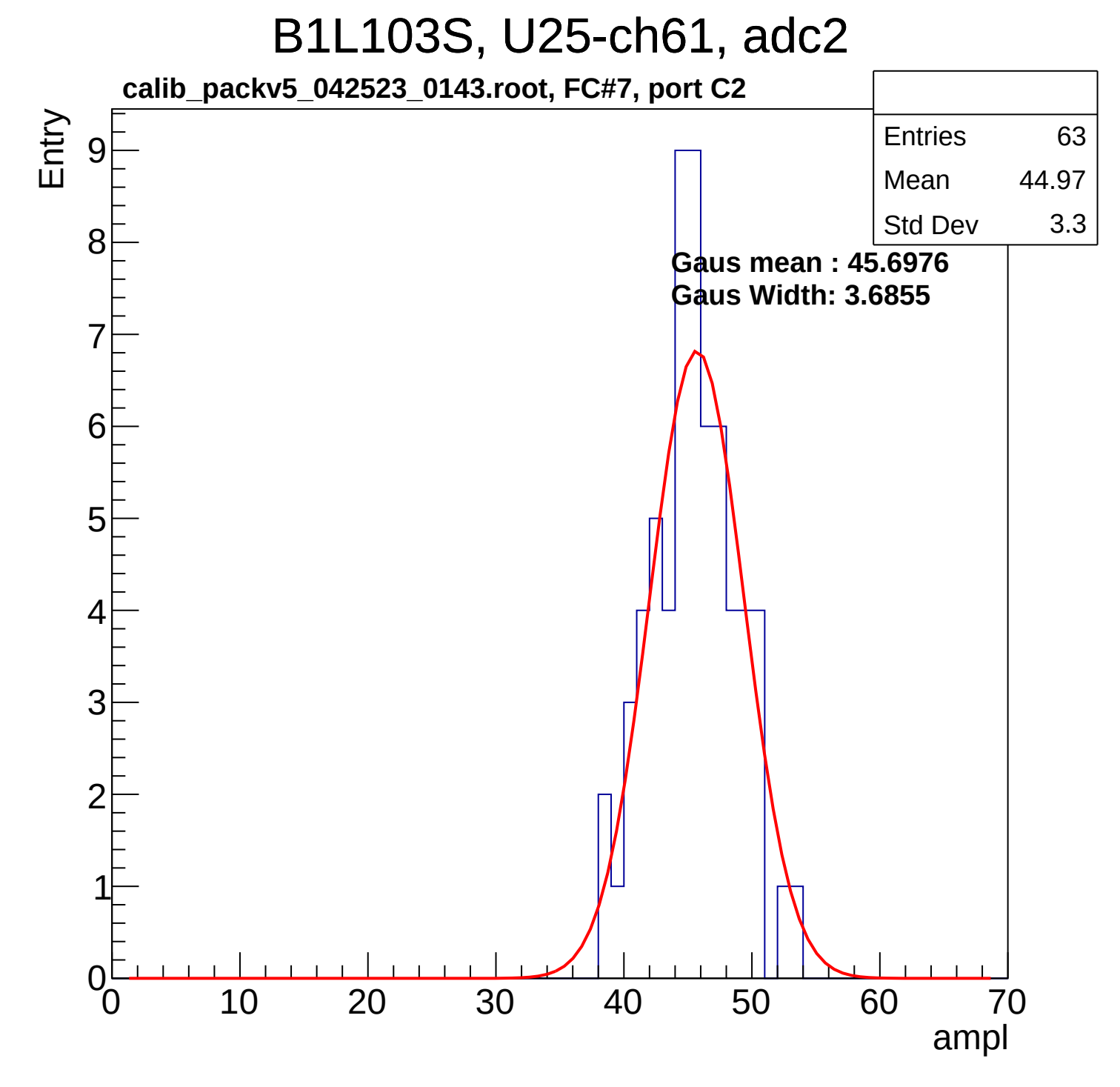
B1L103S, U25-ch61, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 44.97
Std Dev 3.3
Gaus mean : 45.6976
Gaus Width: 3.6855
Entry
ampl
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2
3
4
5
6
7
8
9
0
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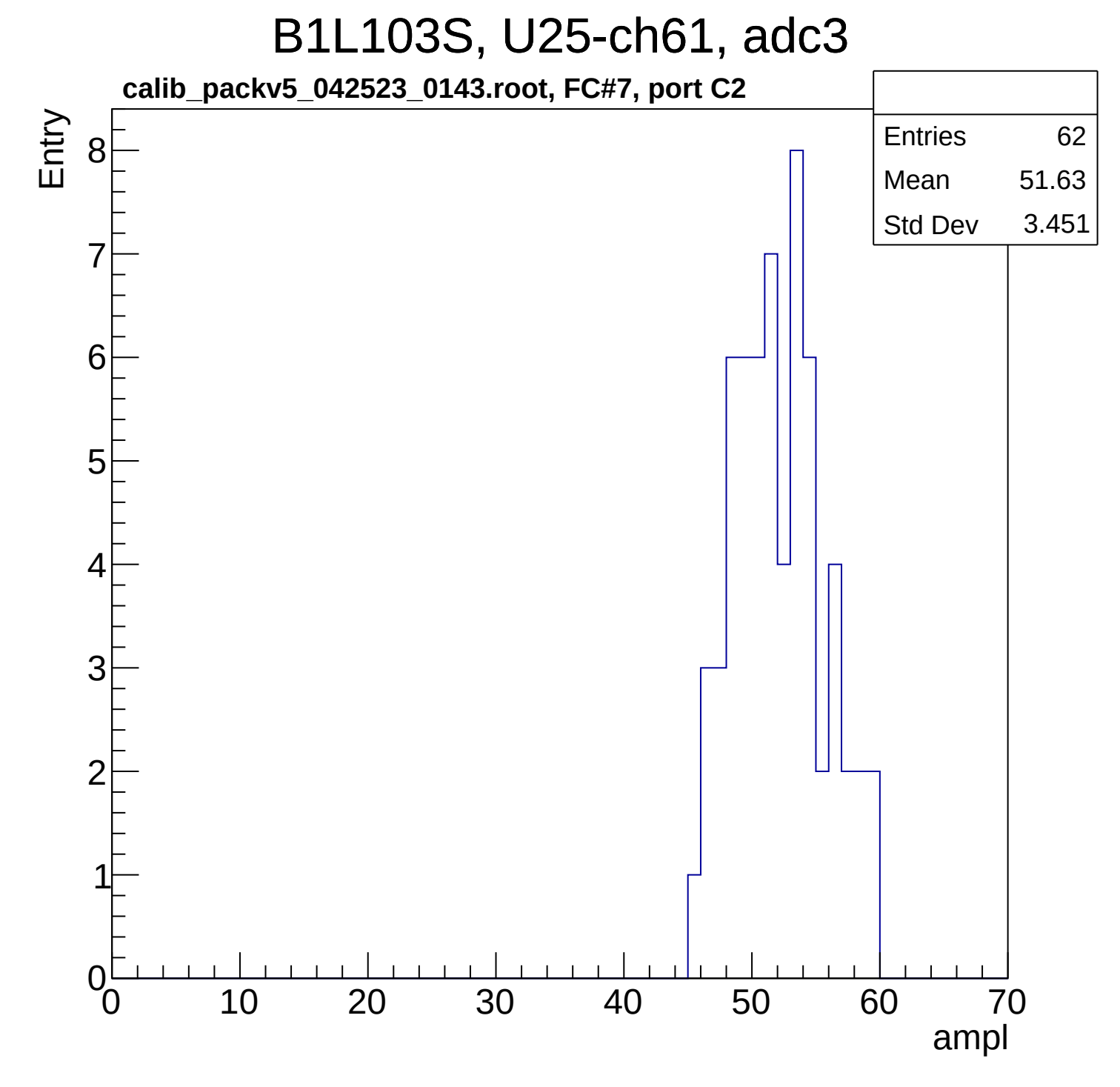
B1L103S, U25-ch61, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 51.63
Std Dev 3.451
Entry
ampl
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60
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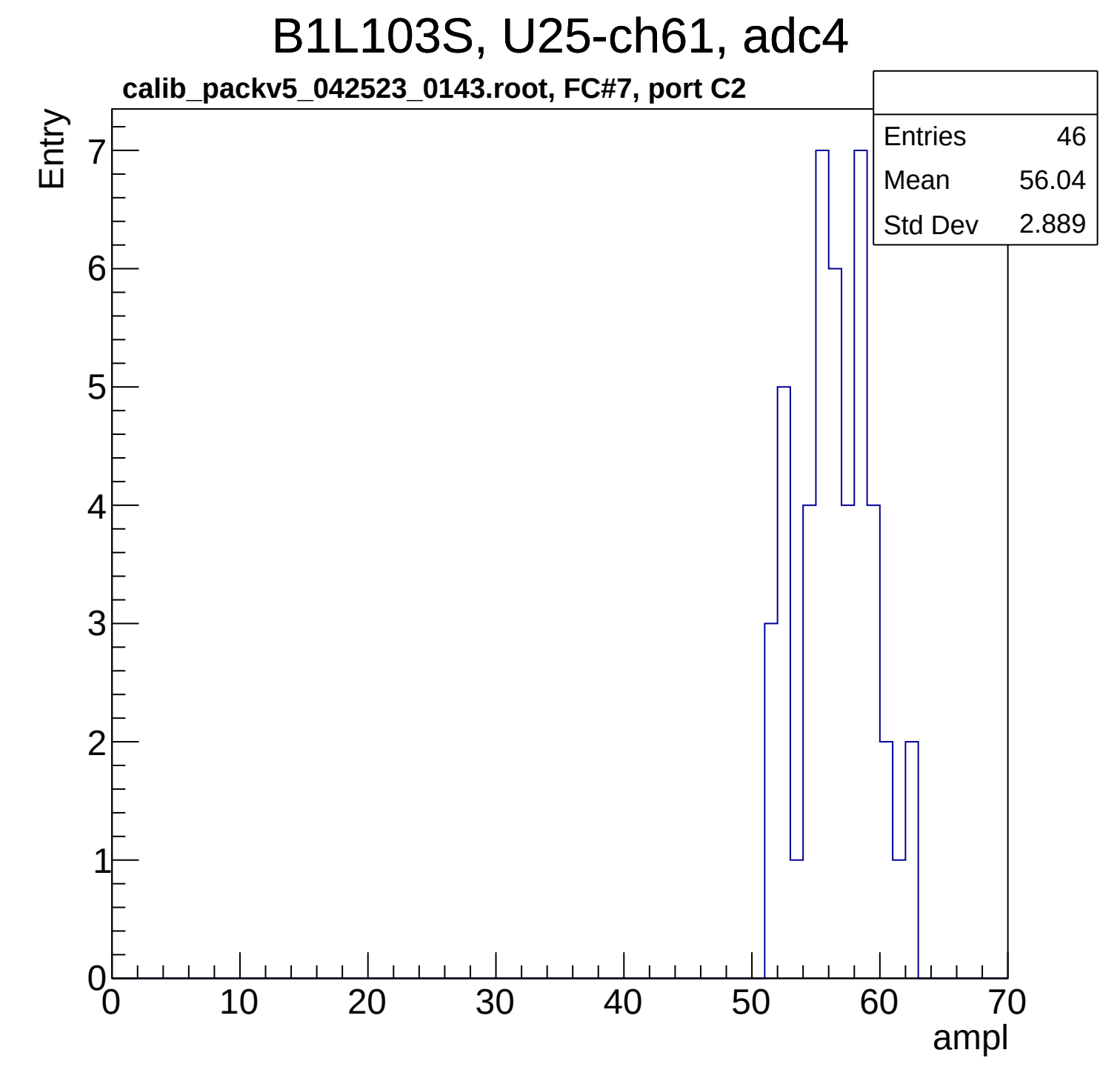
B1L103S, U25-ch61, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.04
Std Dev 2.889
Entry
ampl
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1
2
3
4
5
6
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0
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60
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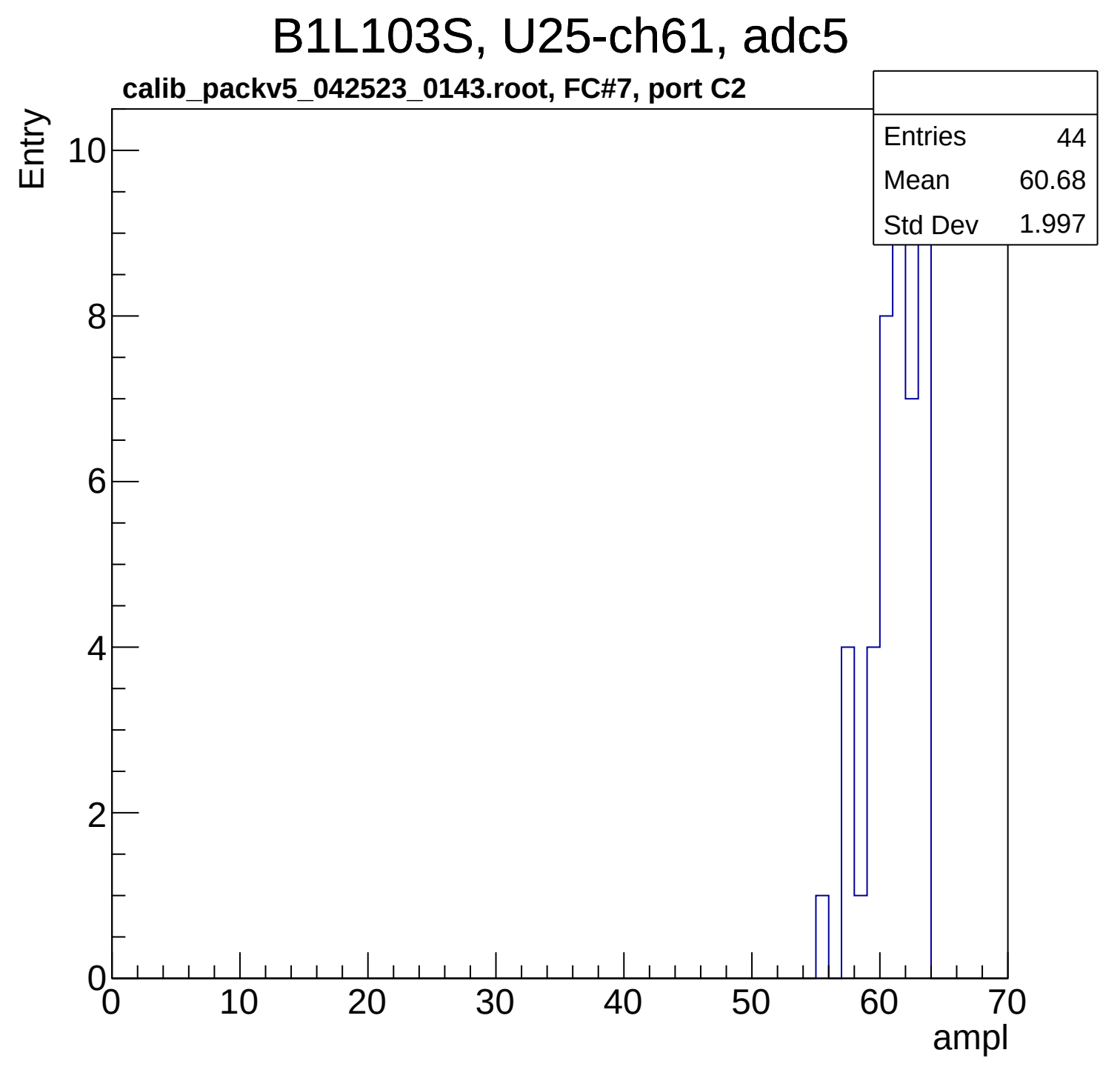
B1L103S, U25-ch61, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 60.68
Std Dev 1.997
Entry
ampl
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30
40
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60
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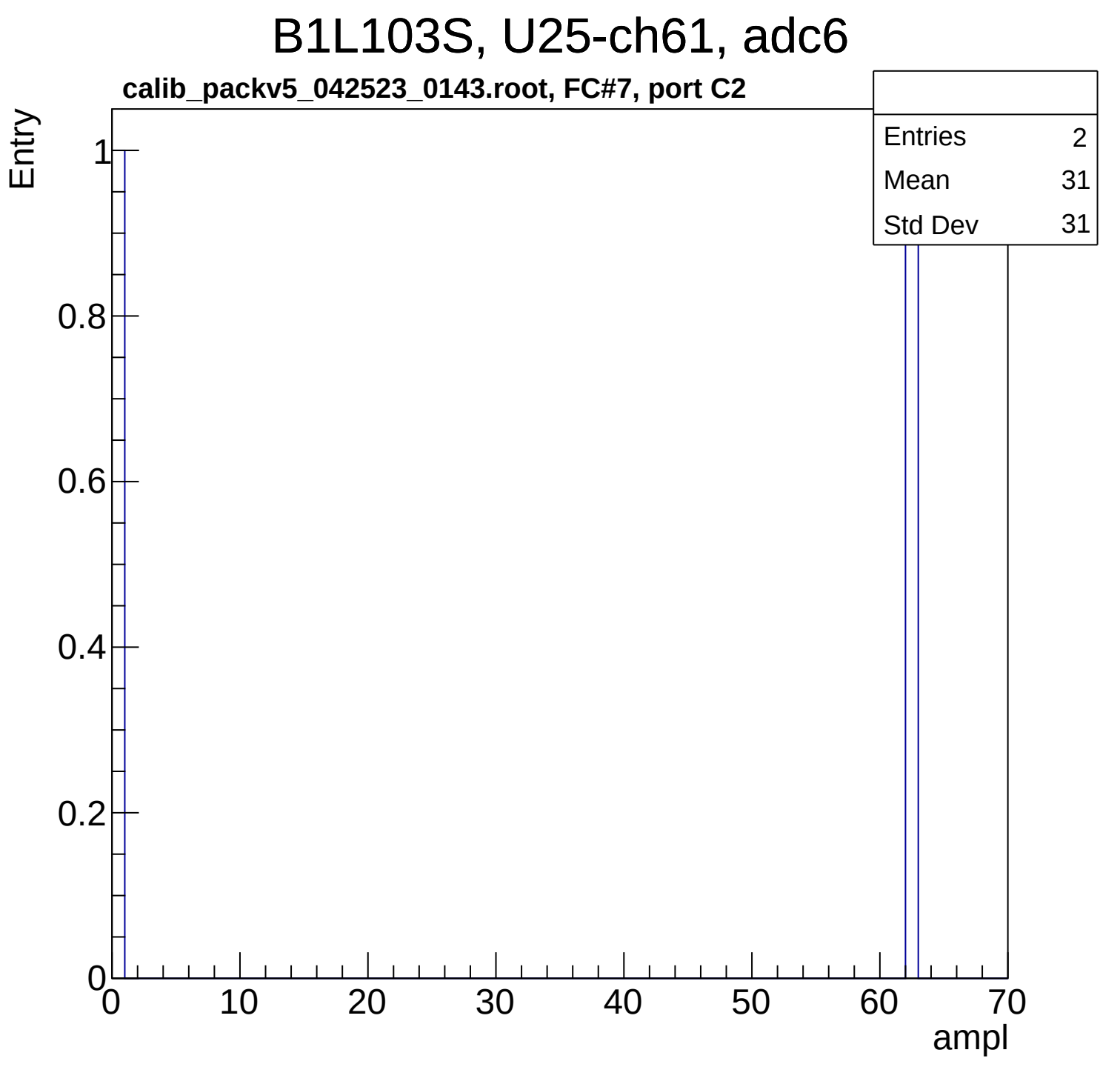
B1L103S, U25-ch61, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
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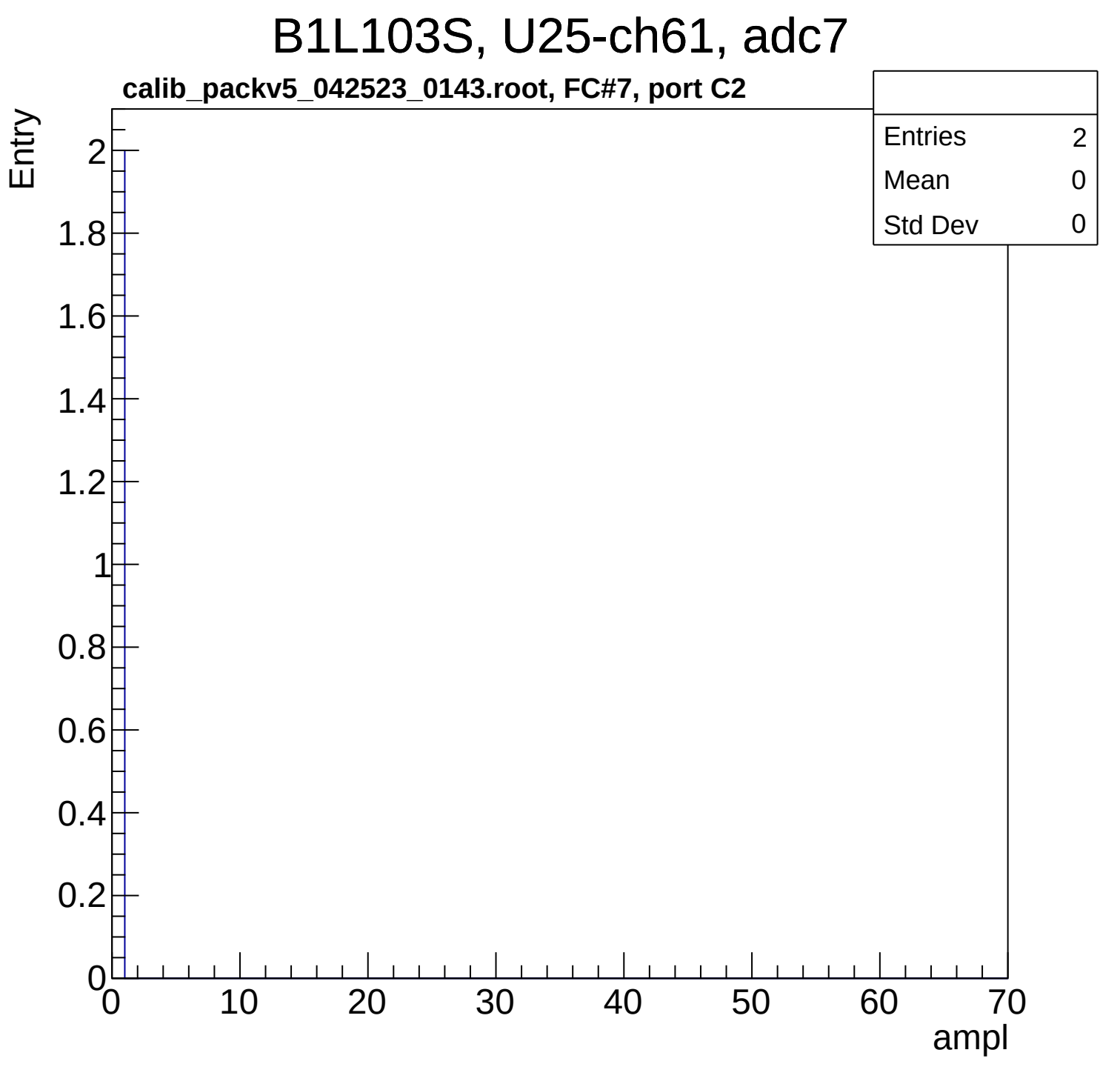
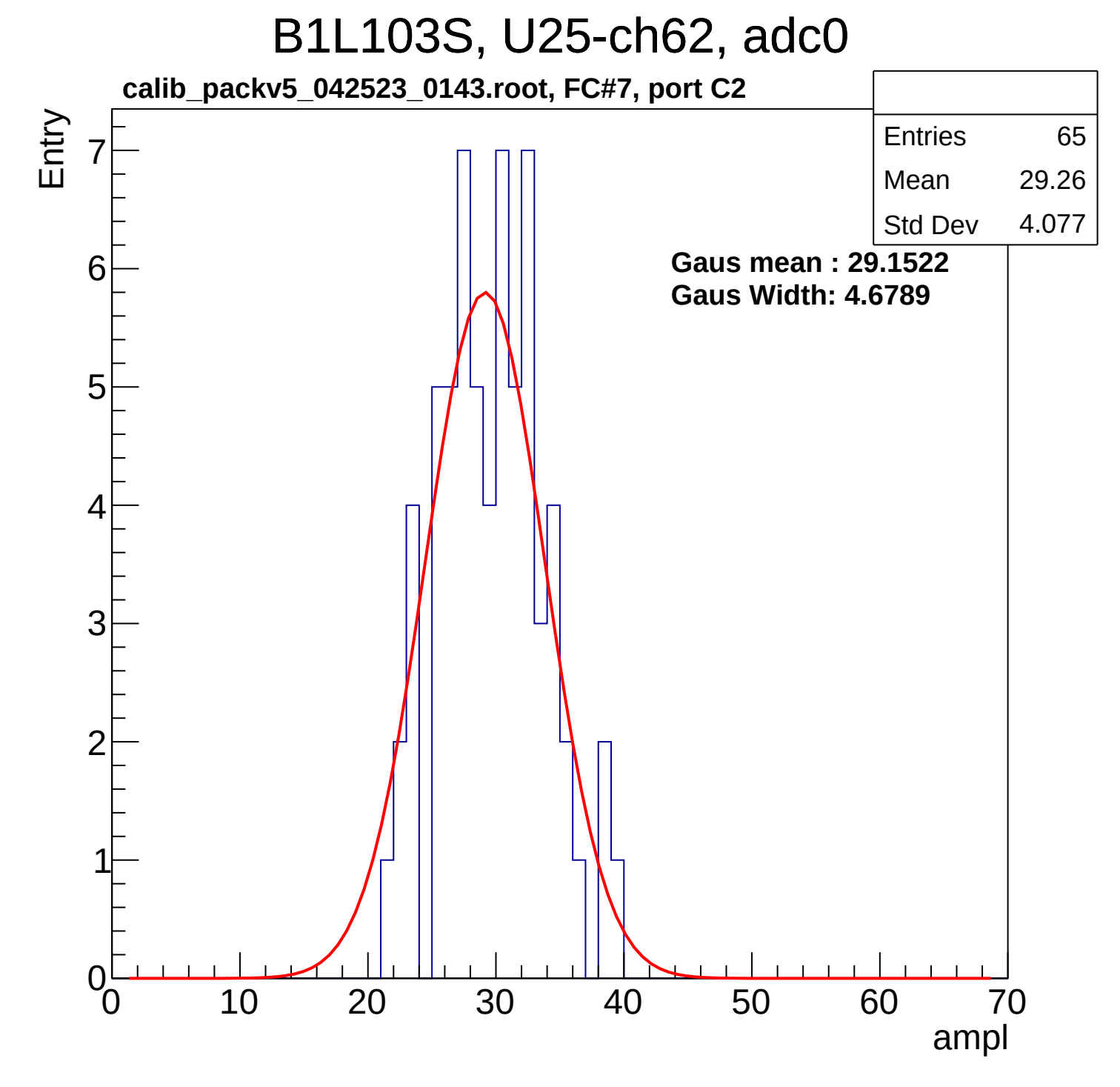
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch61, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch62, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 29.26
Std Dev 4.077
Gaus mean : 29.1522
Gaus Width: 4.6789
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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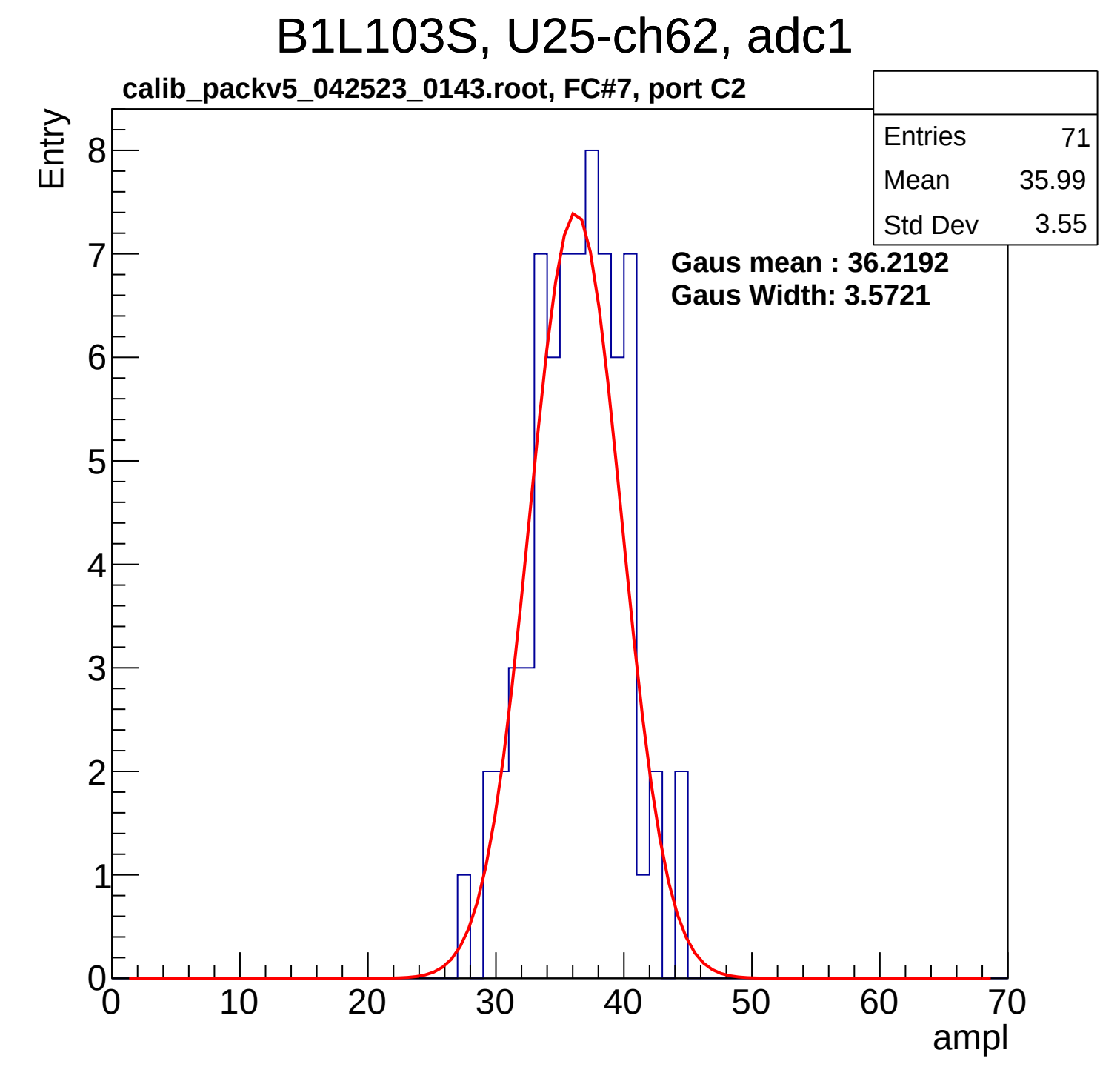
B1L103S, U25-ch62, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 35.99
Std Dev 3.55
Gaus mean : 36.2192
Gaus Width: 3.5721
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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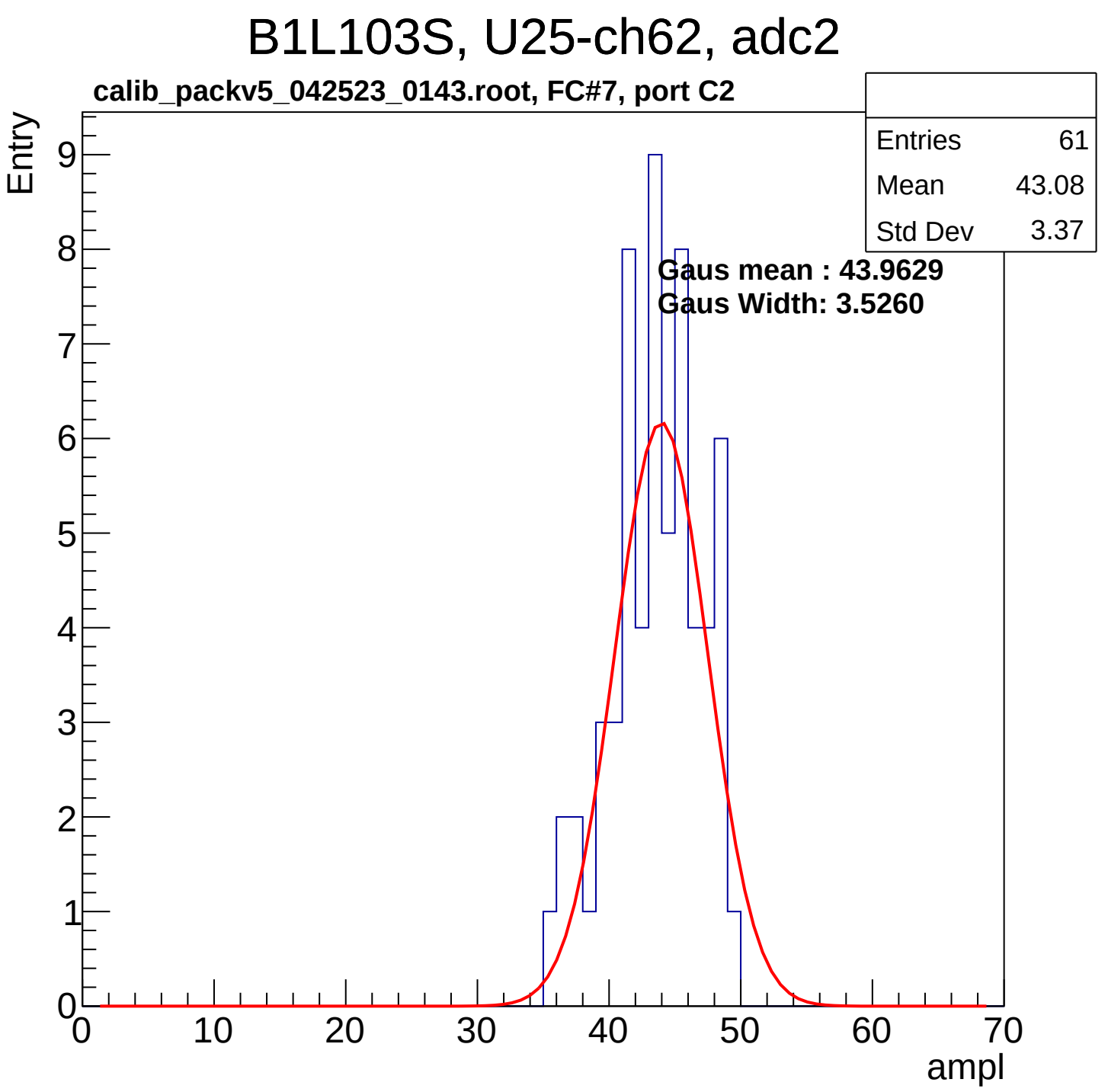
B1L103S, U25-ch62, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 43.08
Std Dev 3.37
Gaus mean : 43.9629
Gaus Width: 3.5260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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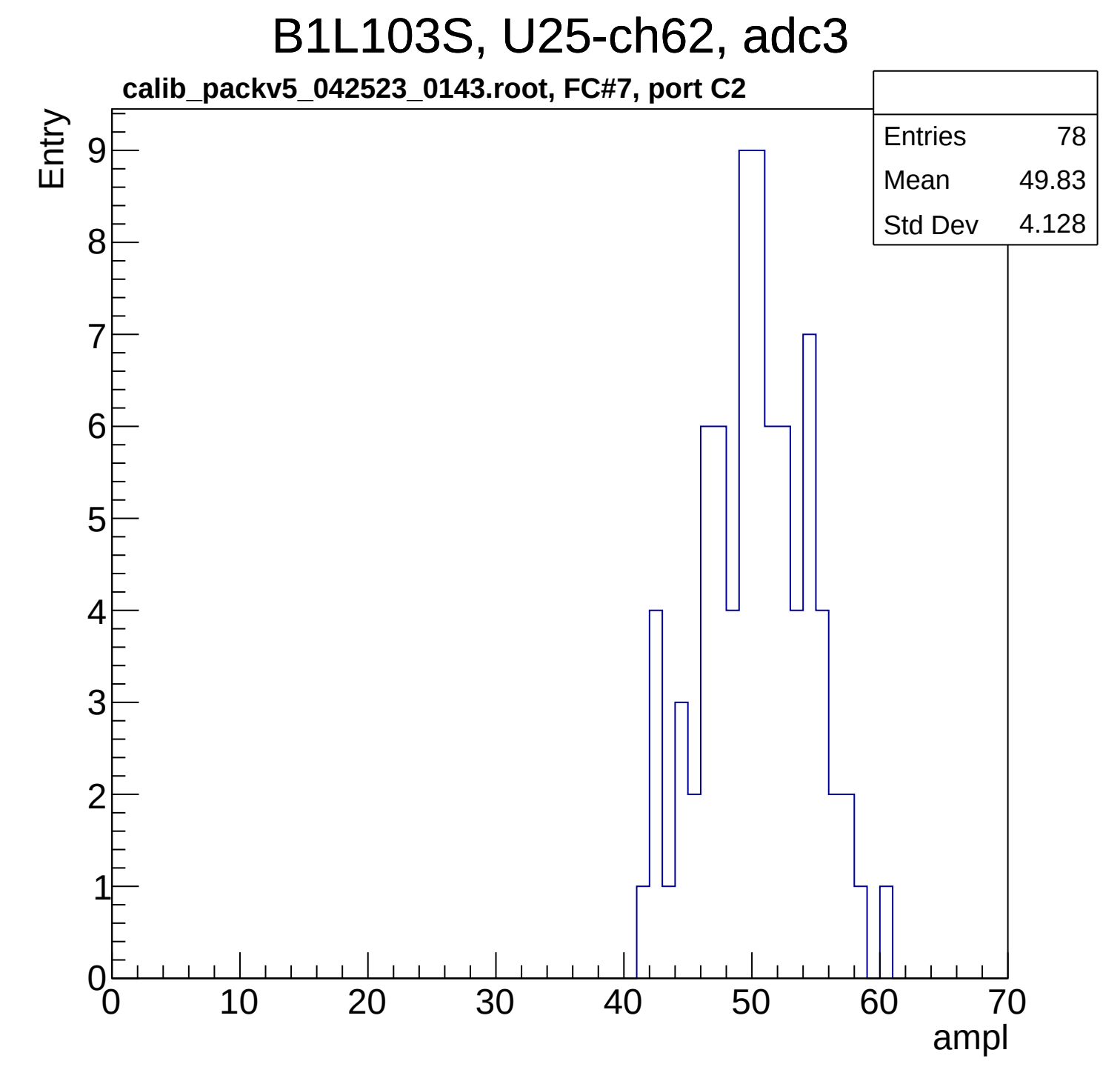
B1L103S, U25-ch62, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 49.83
Std Dev 4.128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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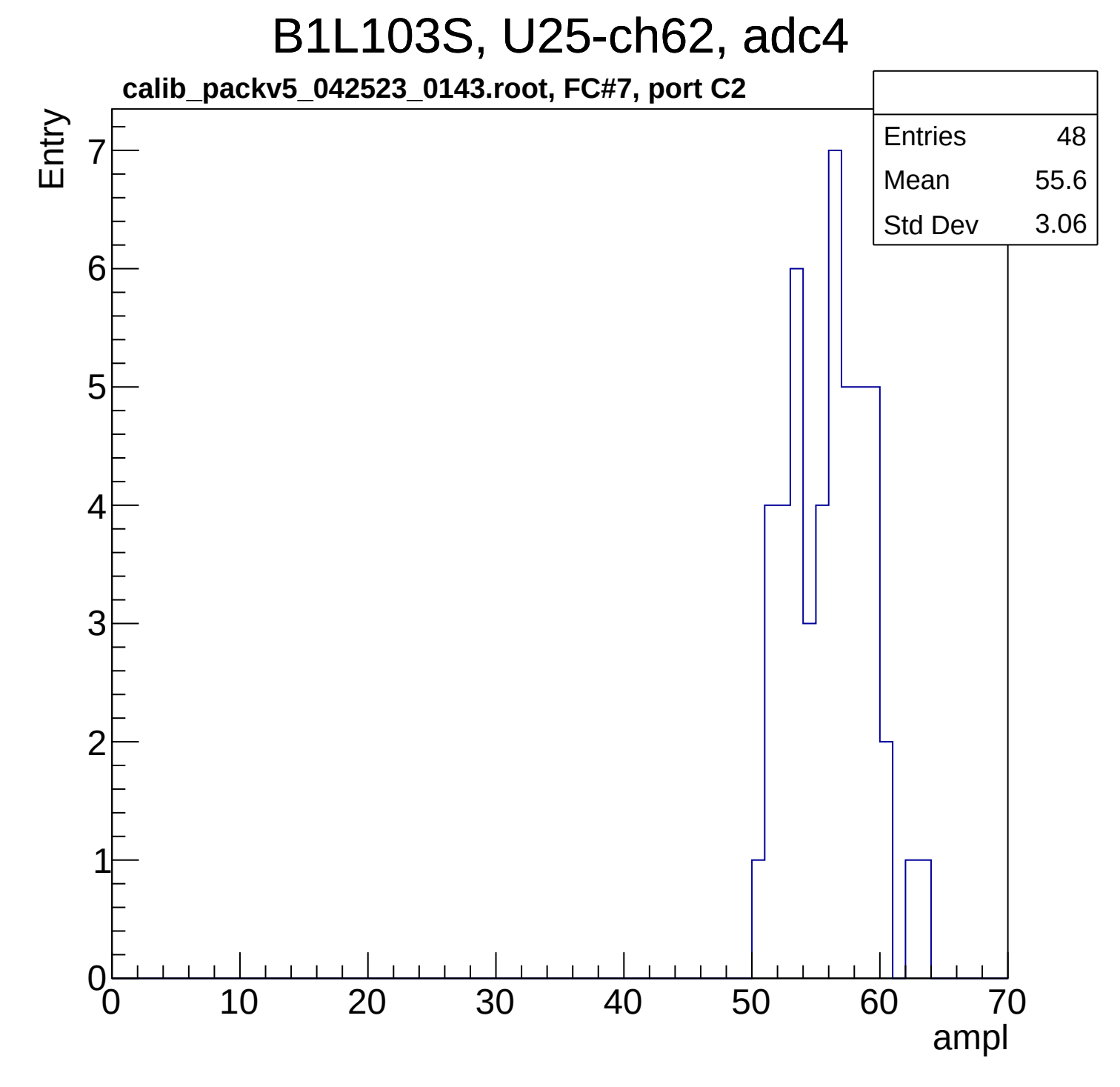
B1L103S, U25-ch62, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 55.6
Std Dev 3.06
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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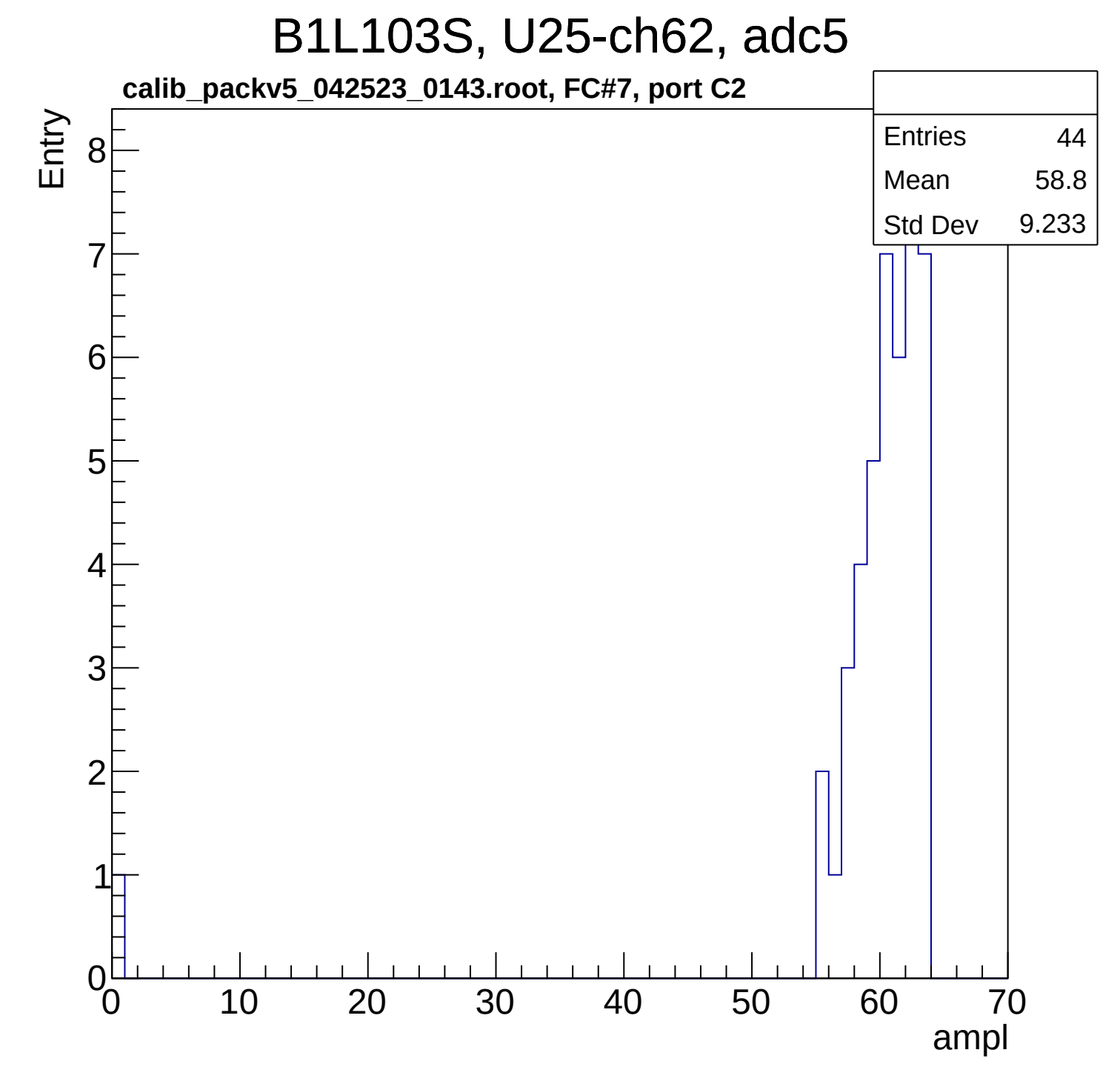
B1L103S, U25-ch62, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.8
Std Dev 9.233
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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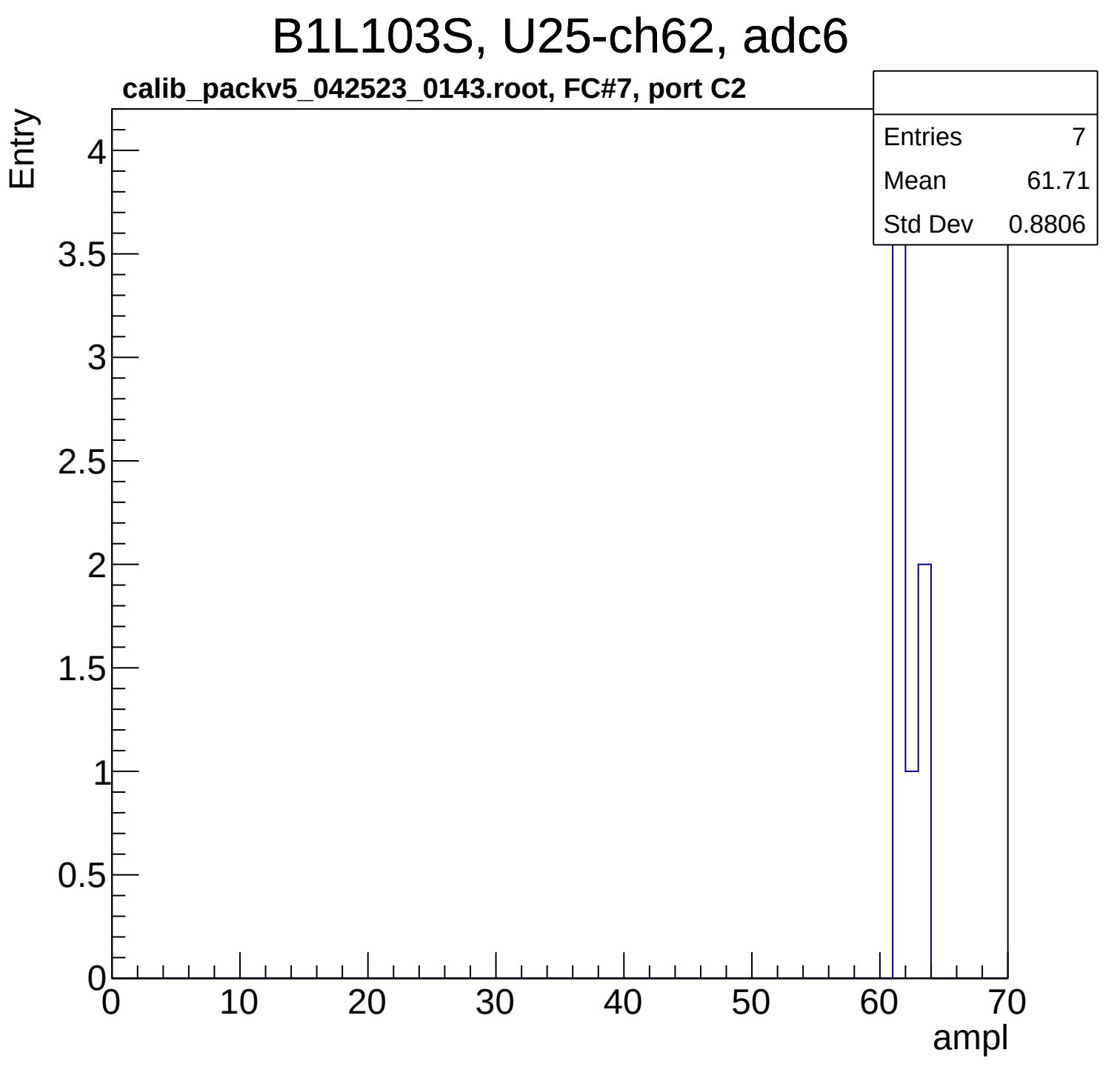
B1L103S, U25-ch62, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 61.71
Std Dev 0.8806
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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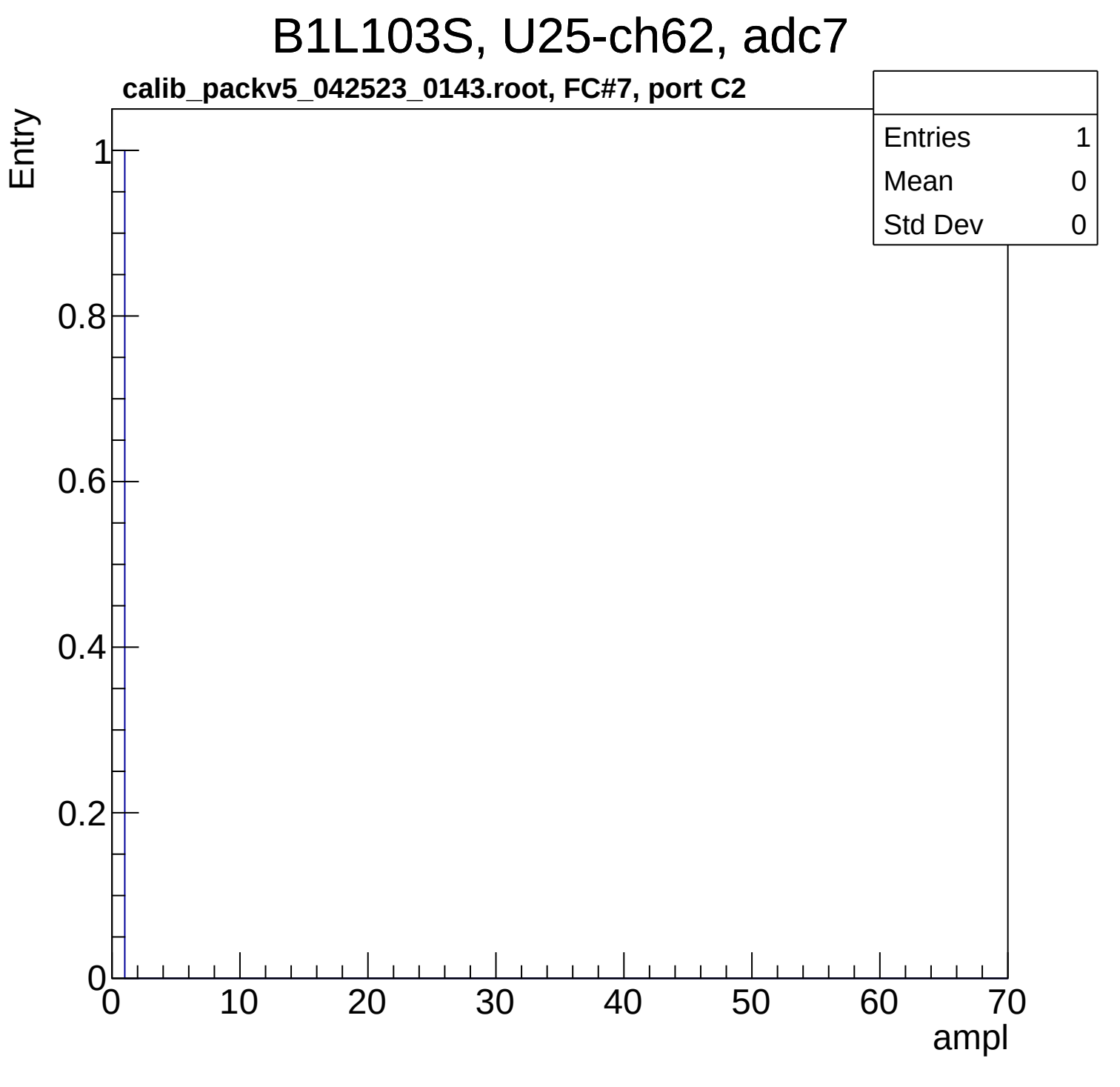
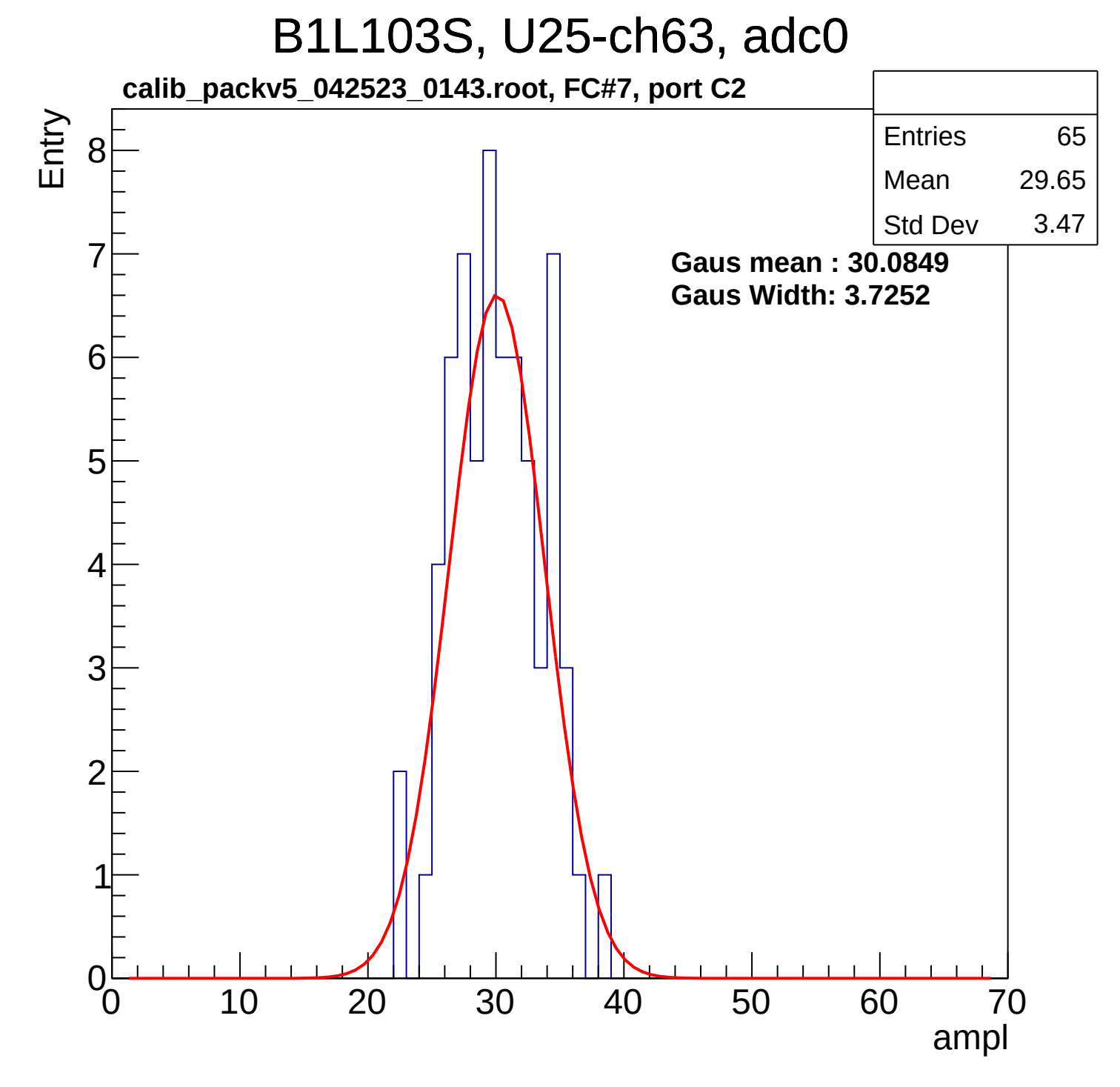
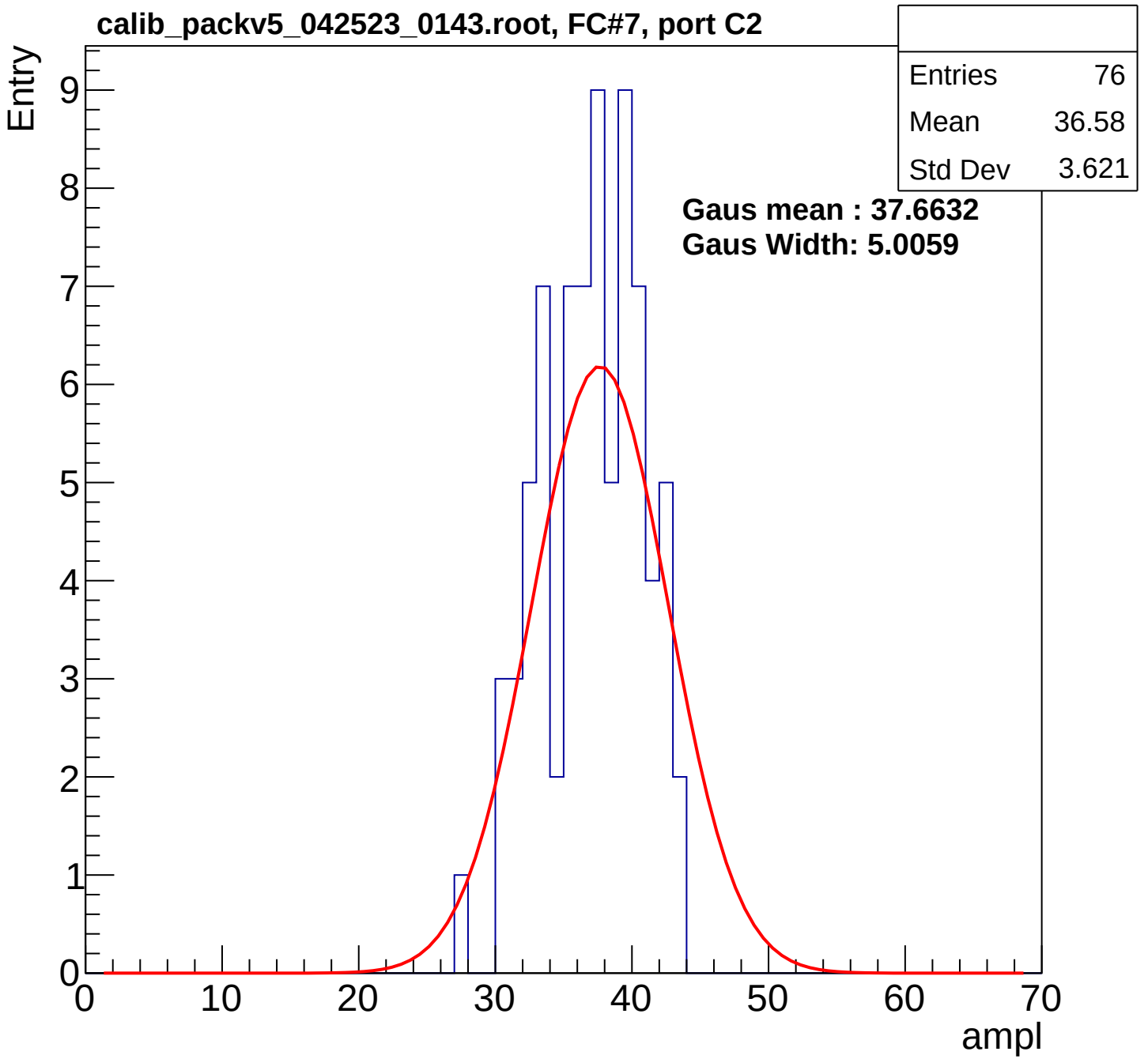

B1L103S, U25-ch62, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch63, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 29.65
Std Dev 3.47
Gaus mean : 30.0849
Gaus Width: 3.7252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch63, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36.58
Std Dev 3.621
Gaus mean : 37.6632
Gaus Width: 5.0059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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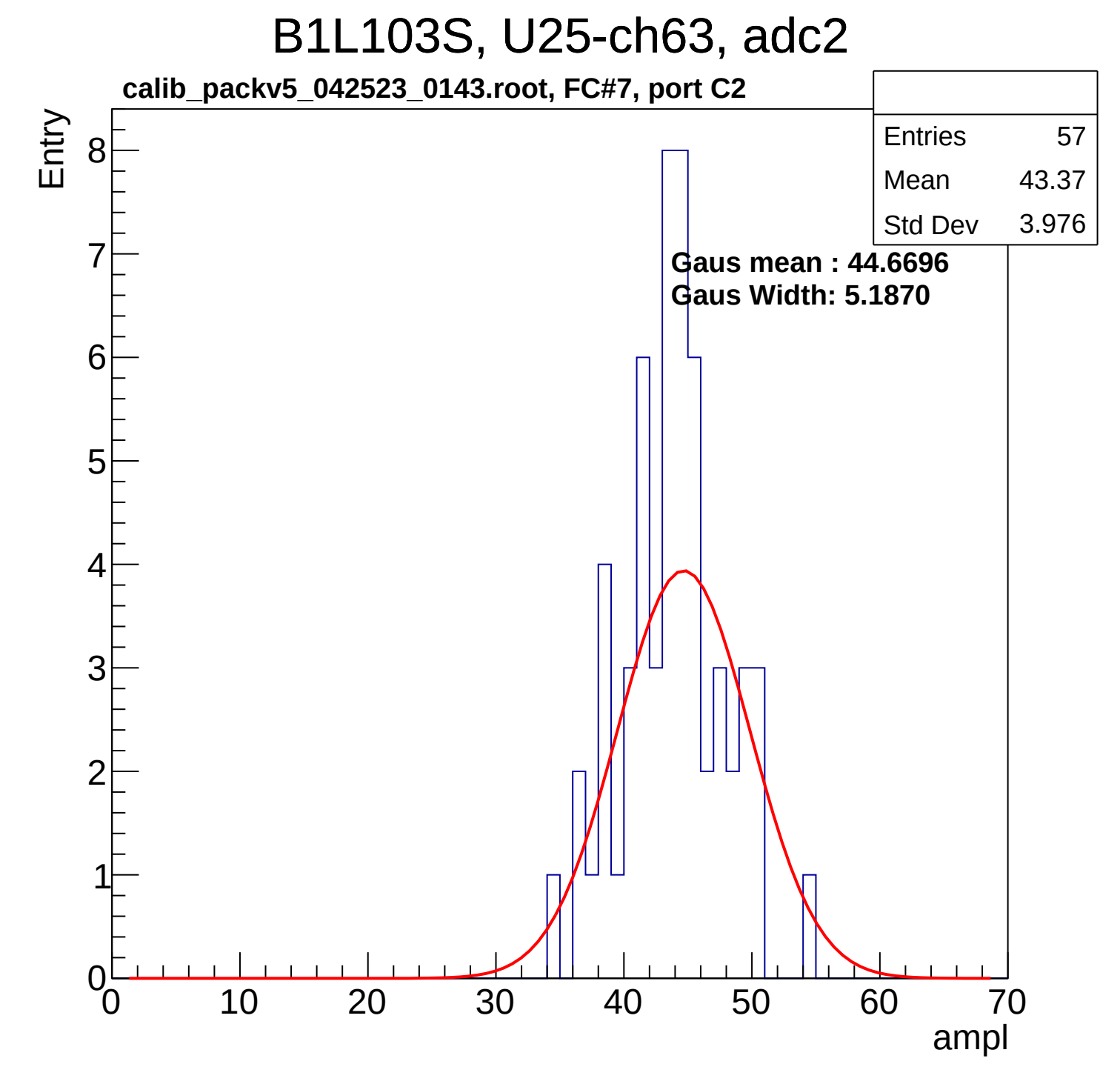
60
70


B1L103S, U25-ch63, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.37
Std Dev 3.976
Gaus mean : 44.6696
Gaus Width: 5.1870
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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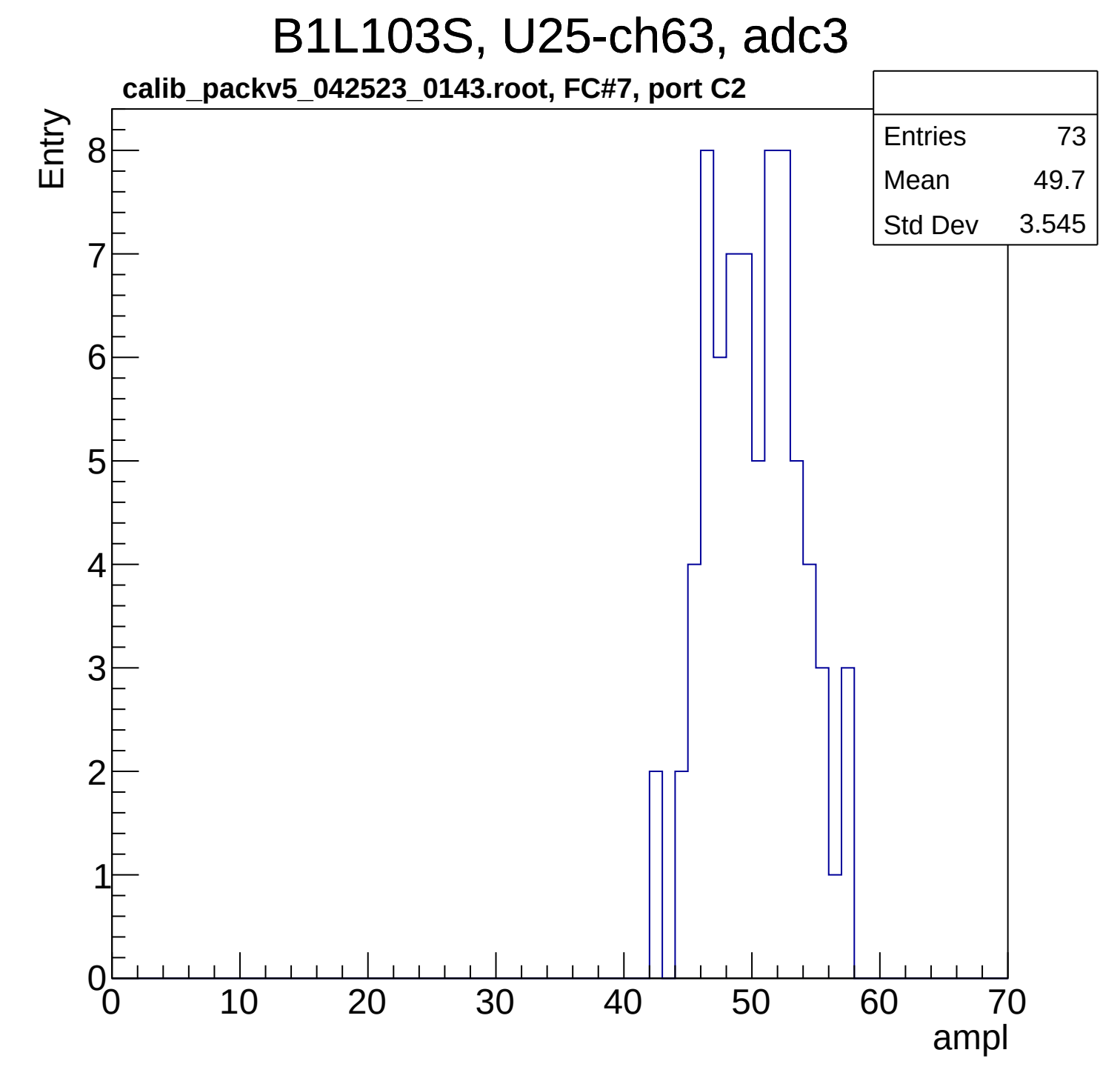
B1L103S, U25-ch63, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.7
Std Dev 3.545
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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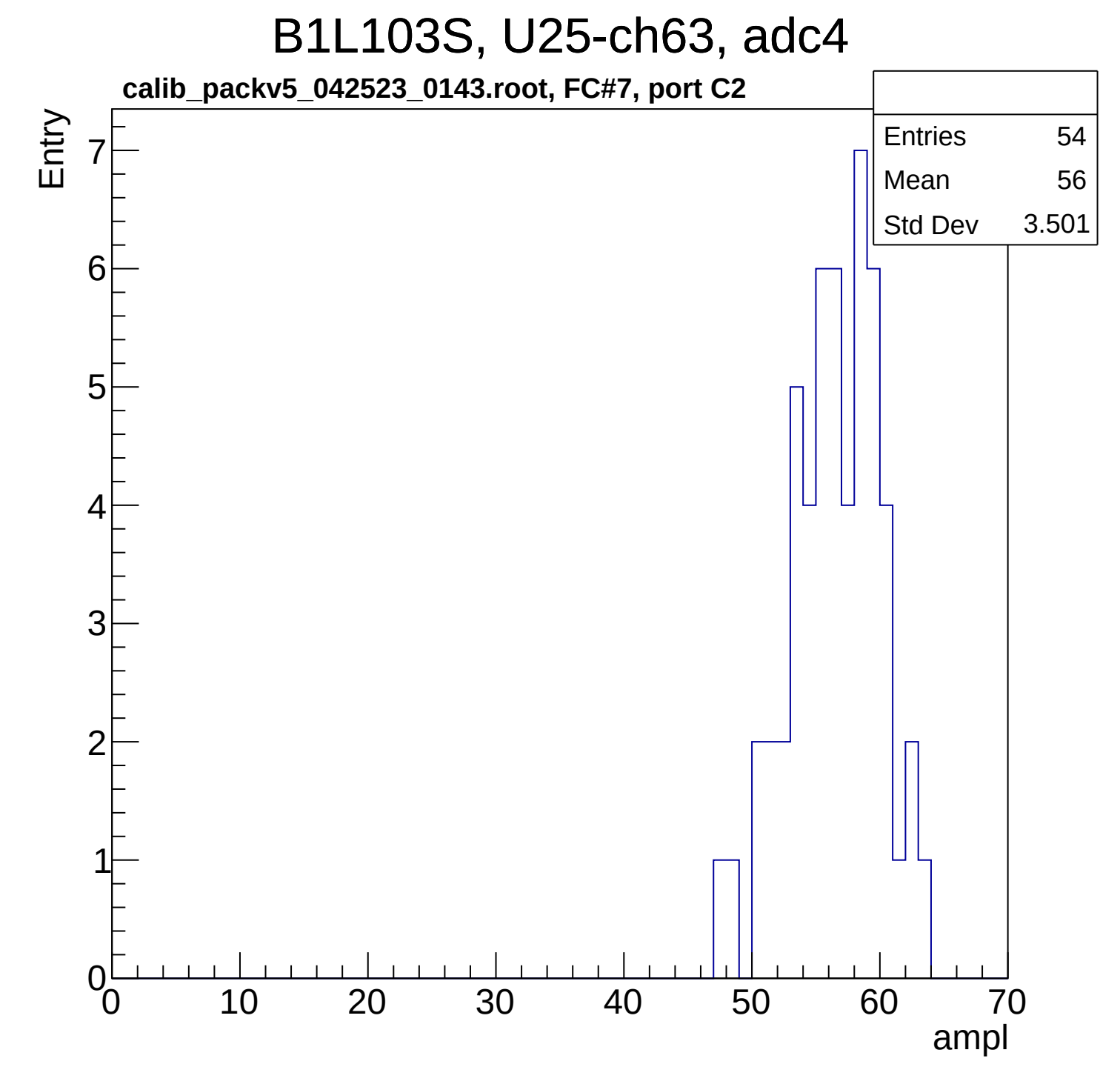
B1L103S, U25-ch63, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56
Std Dev 3.501
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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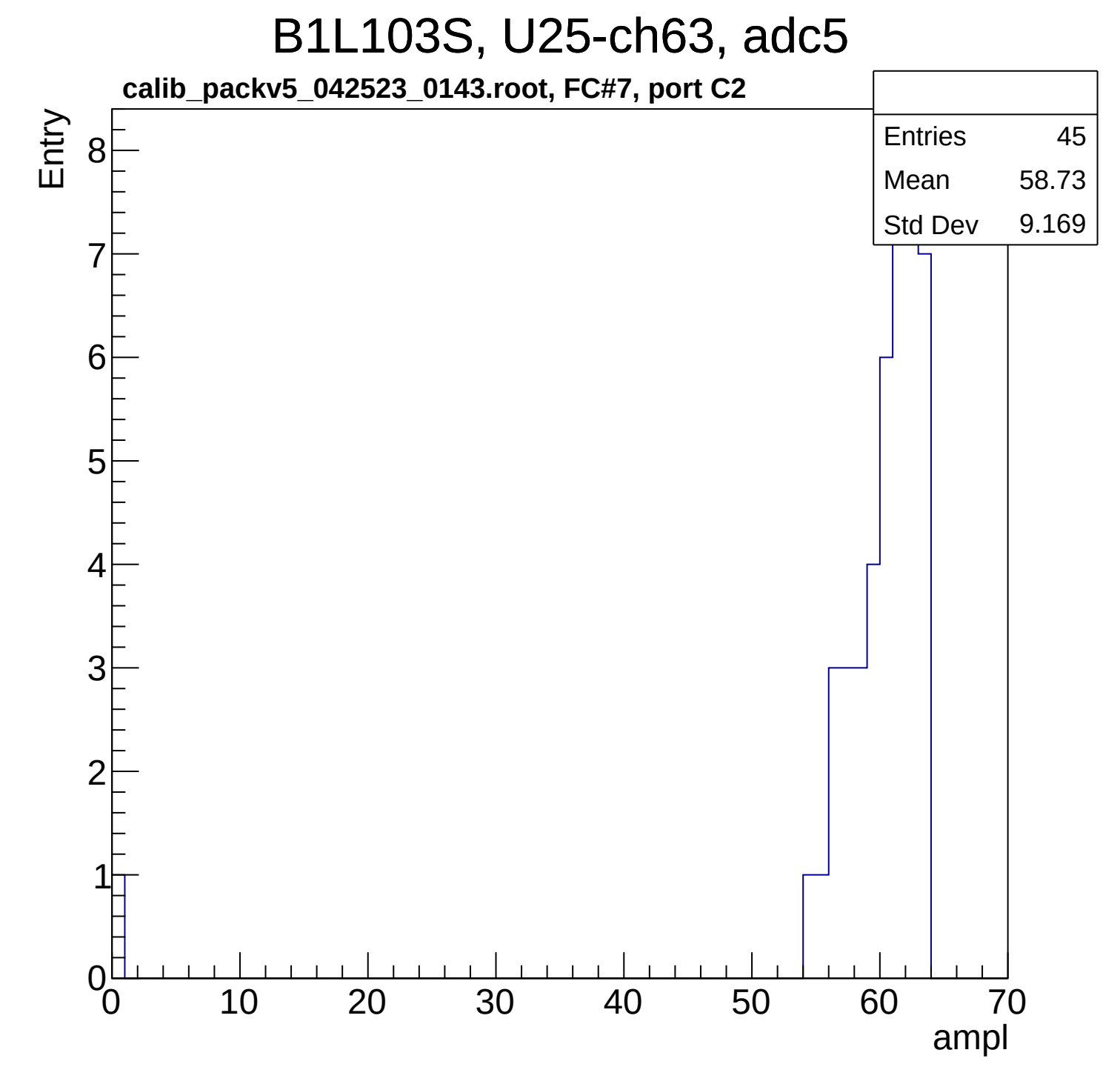
B1L103S, U25-ch63, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.73
Std Dev 9.169
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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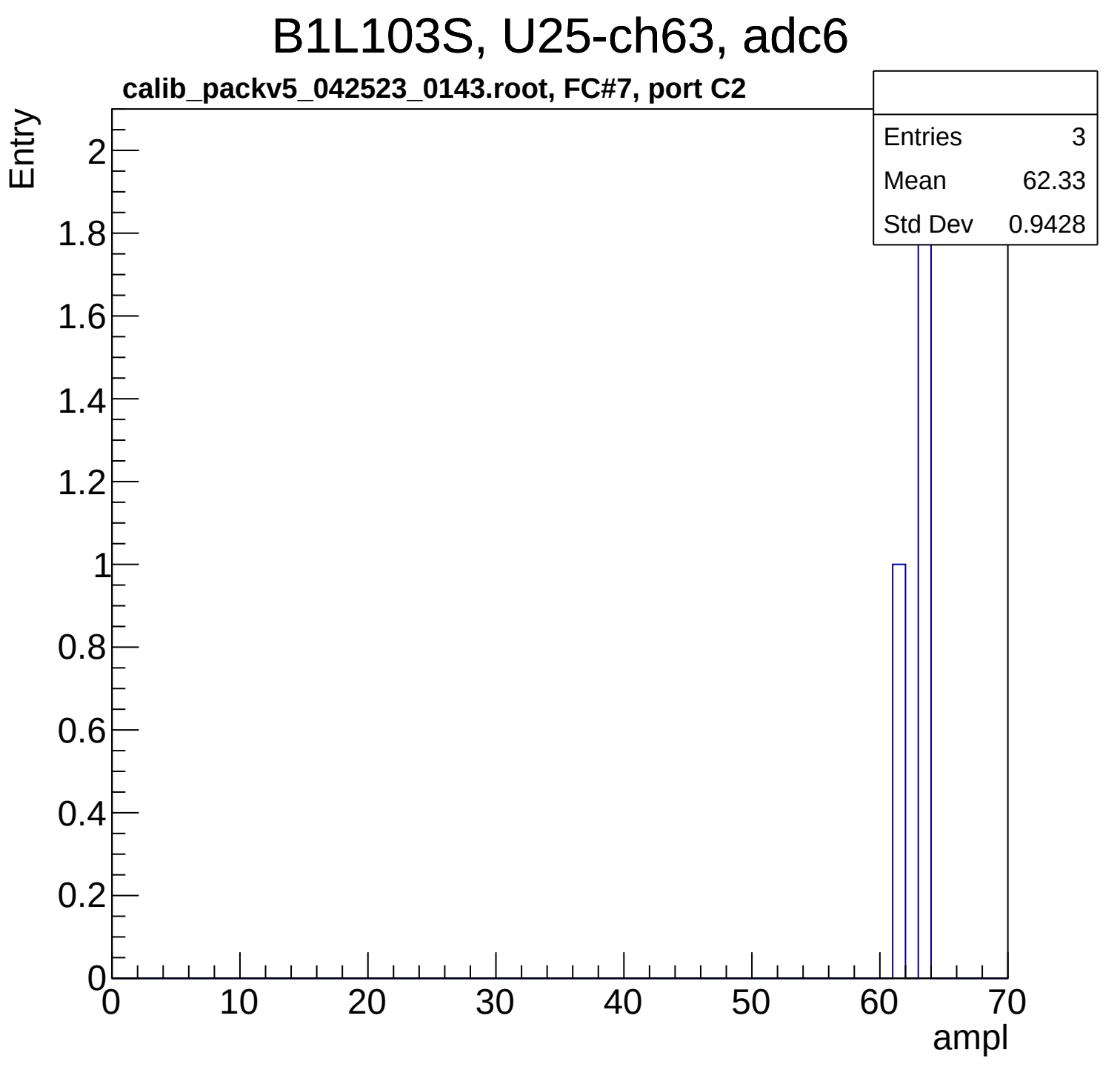
B1L103S, U25-ch63, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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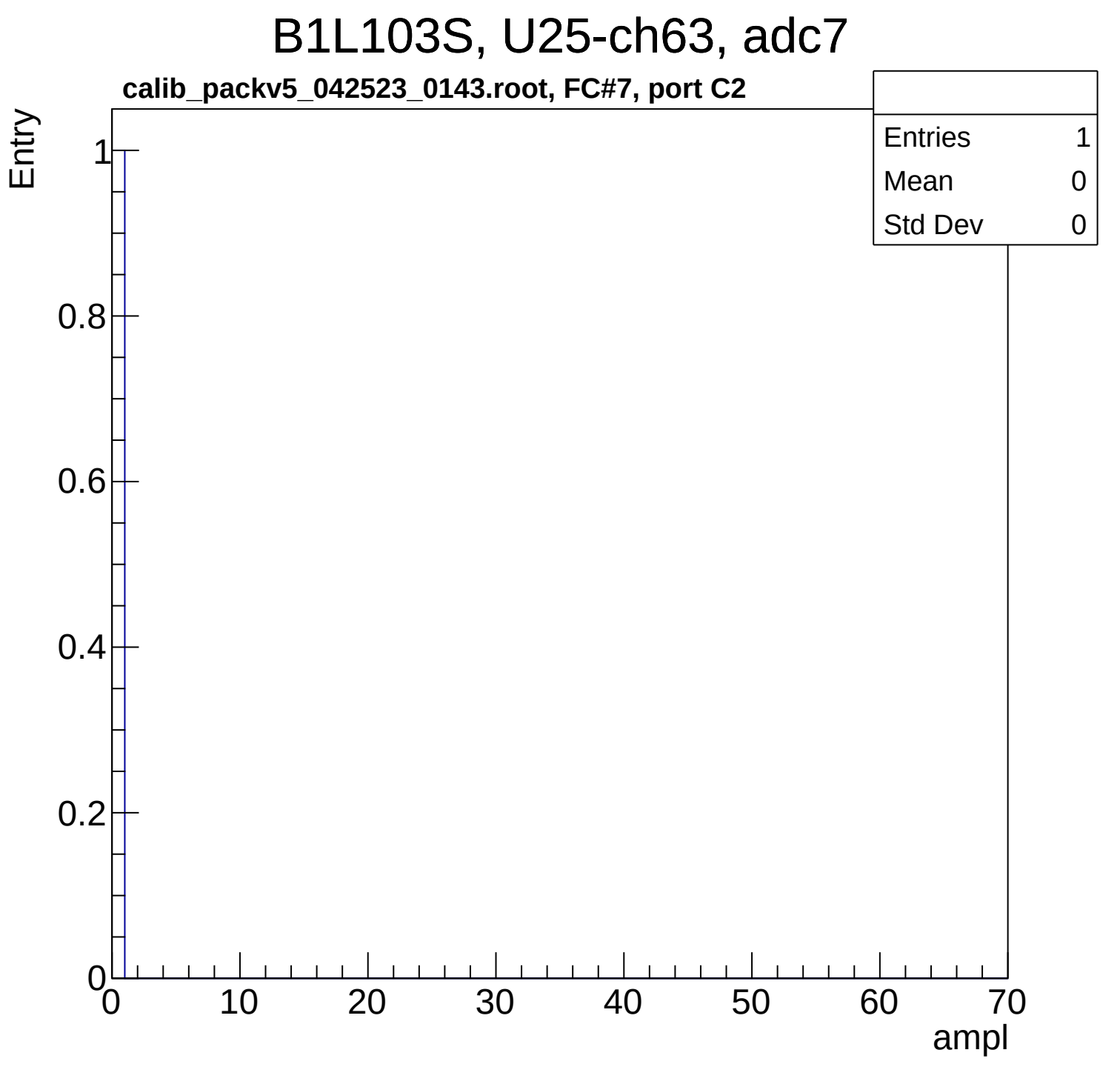
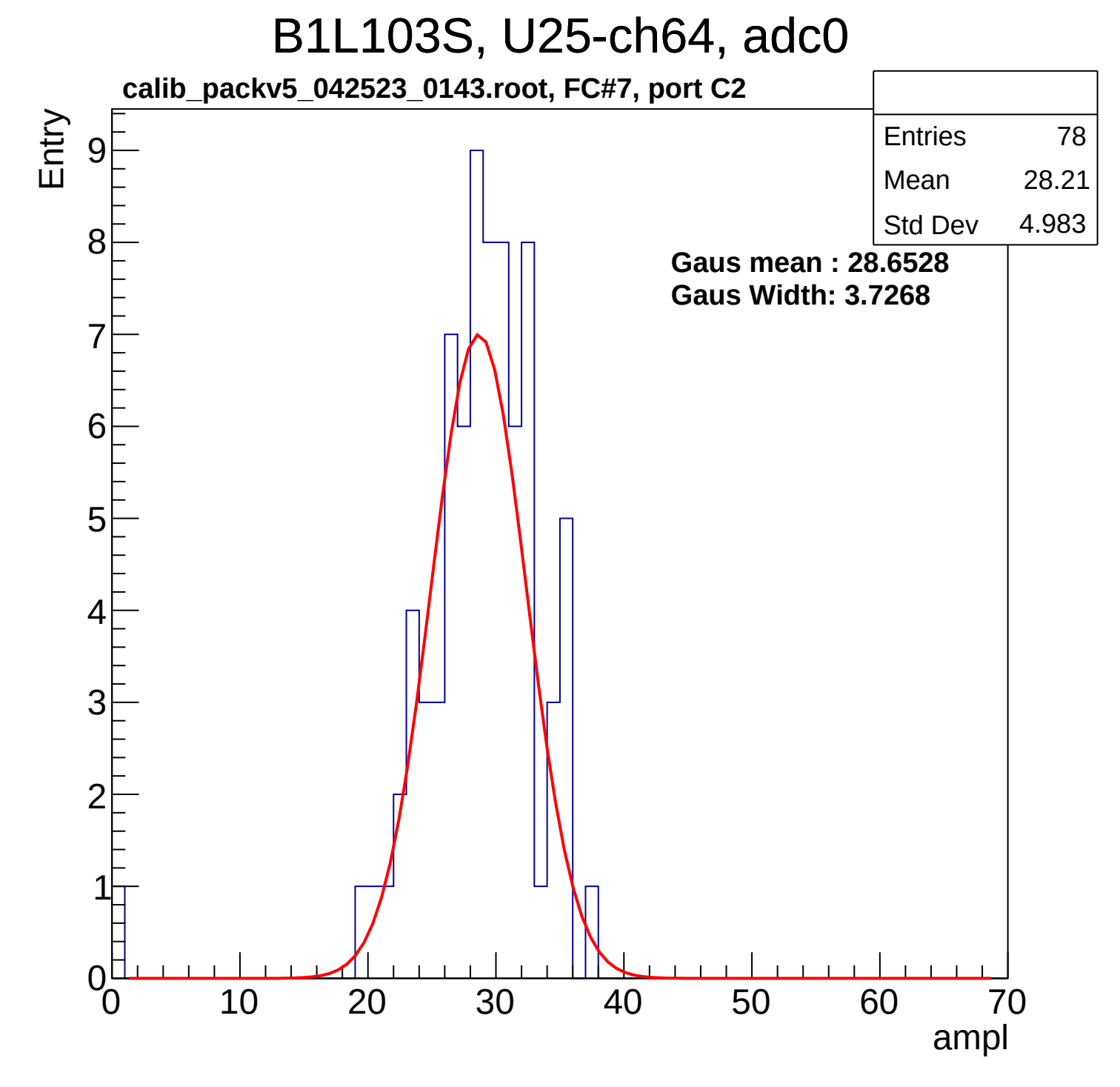
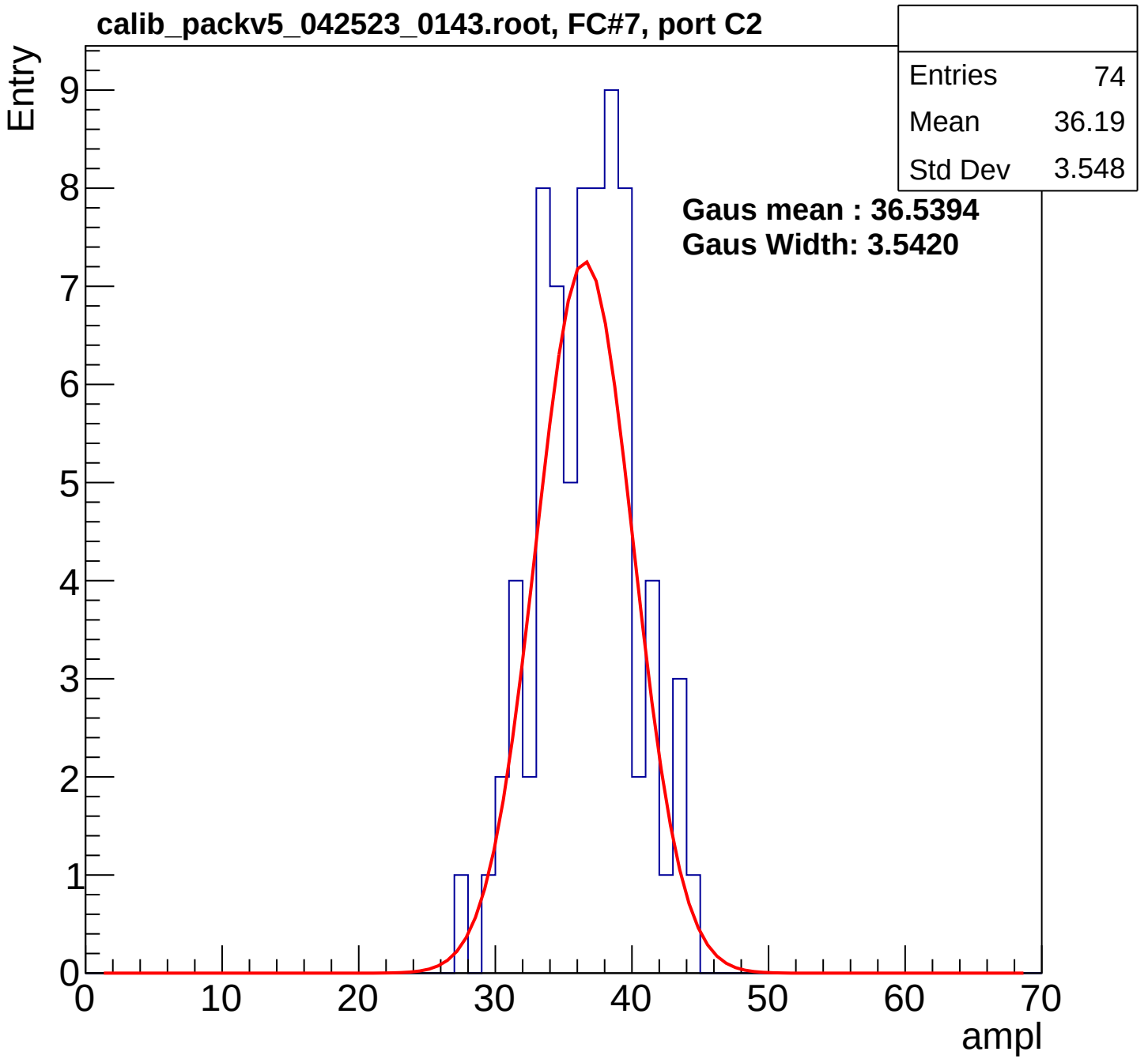

B1L103S, U25-ch63, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch64, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 28.21
Std Dev 4.983
Gaus mean : 28.6528
Gaus Width: 3.7268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch64, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 36.19
Std Dev 3.548
Gaus mean : 36.5394
Gaus Width: 3.5420
Entry
ampl
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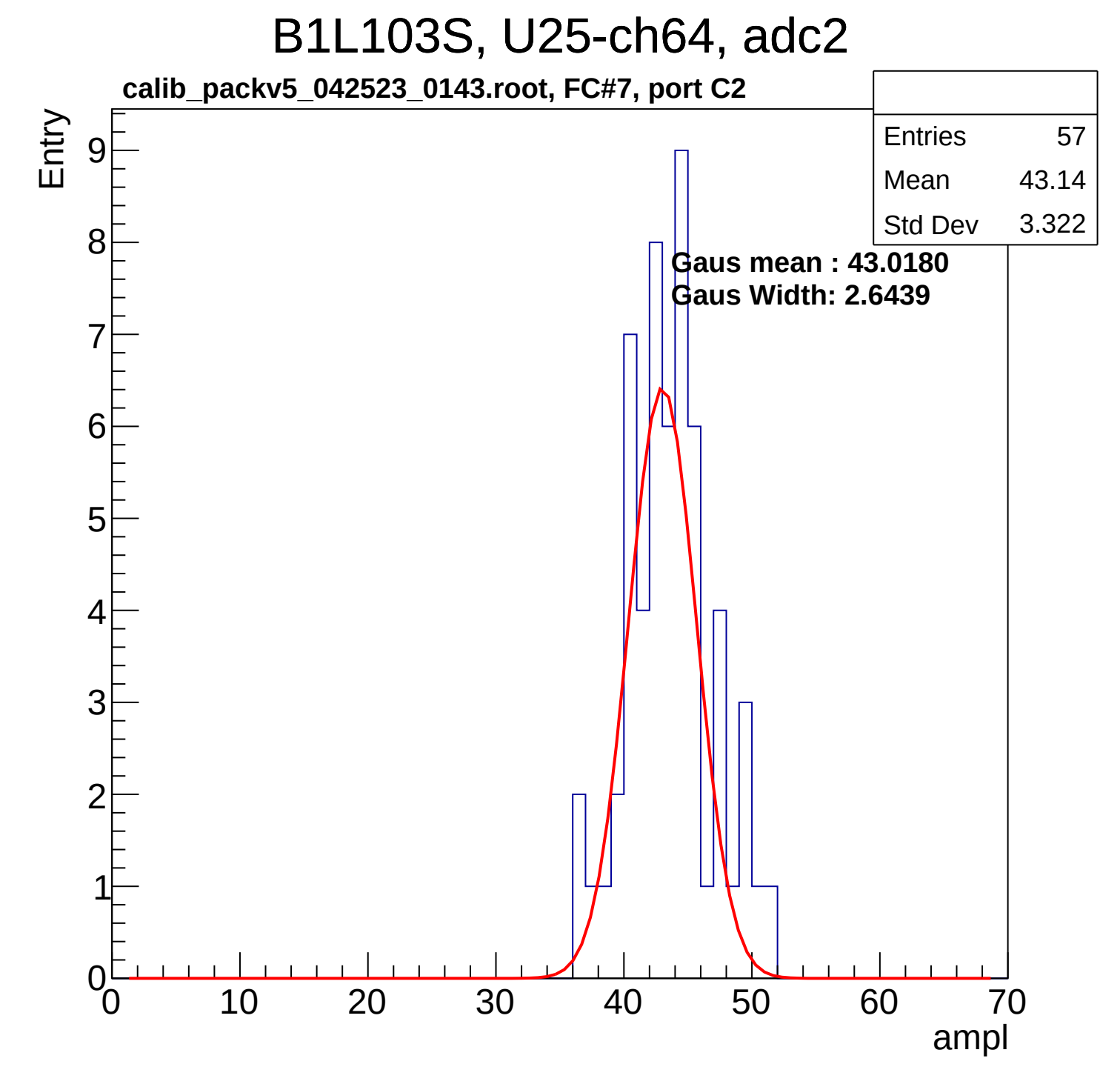
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch64, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.14
Std Dev 3.322
Gaus mean : 43.0180
Gaus Width: 2.6439
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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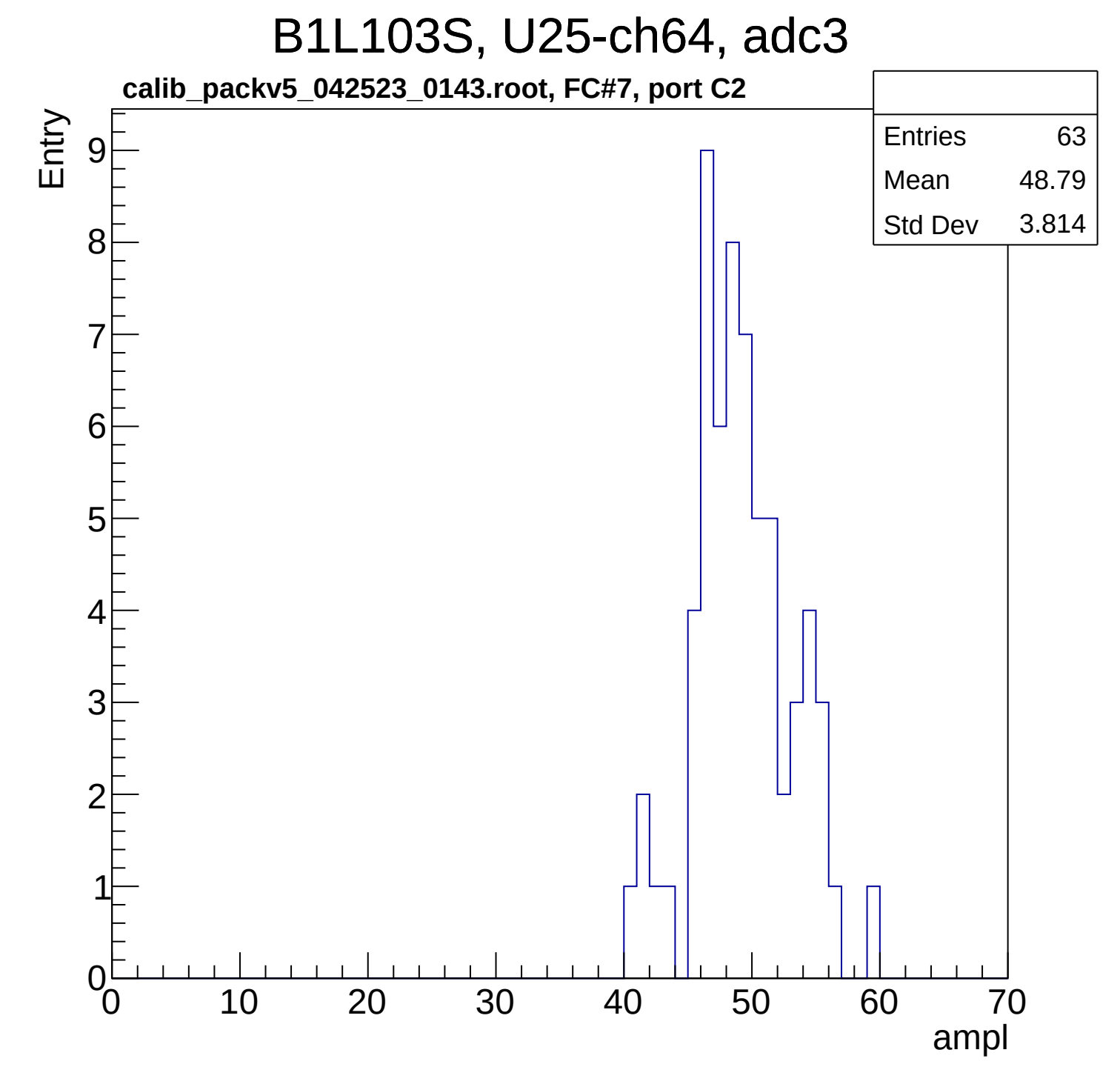
B1L103S, U25-ch64, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.79
Std Dev 3.814
Entry
ampl
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3
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5
6
7
8
9
0
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20
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40
50
60
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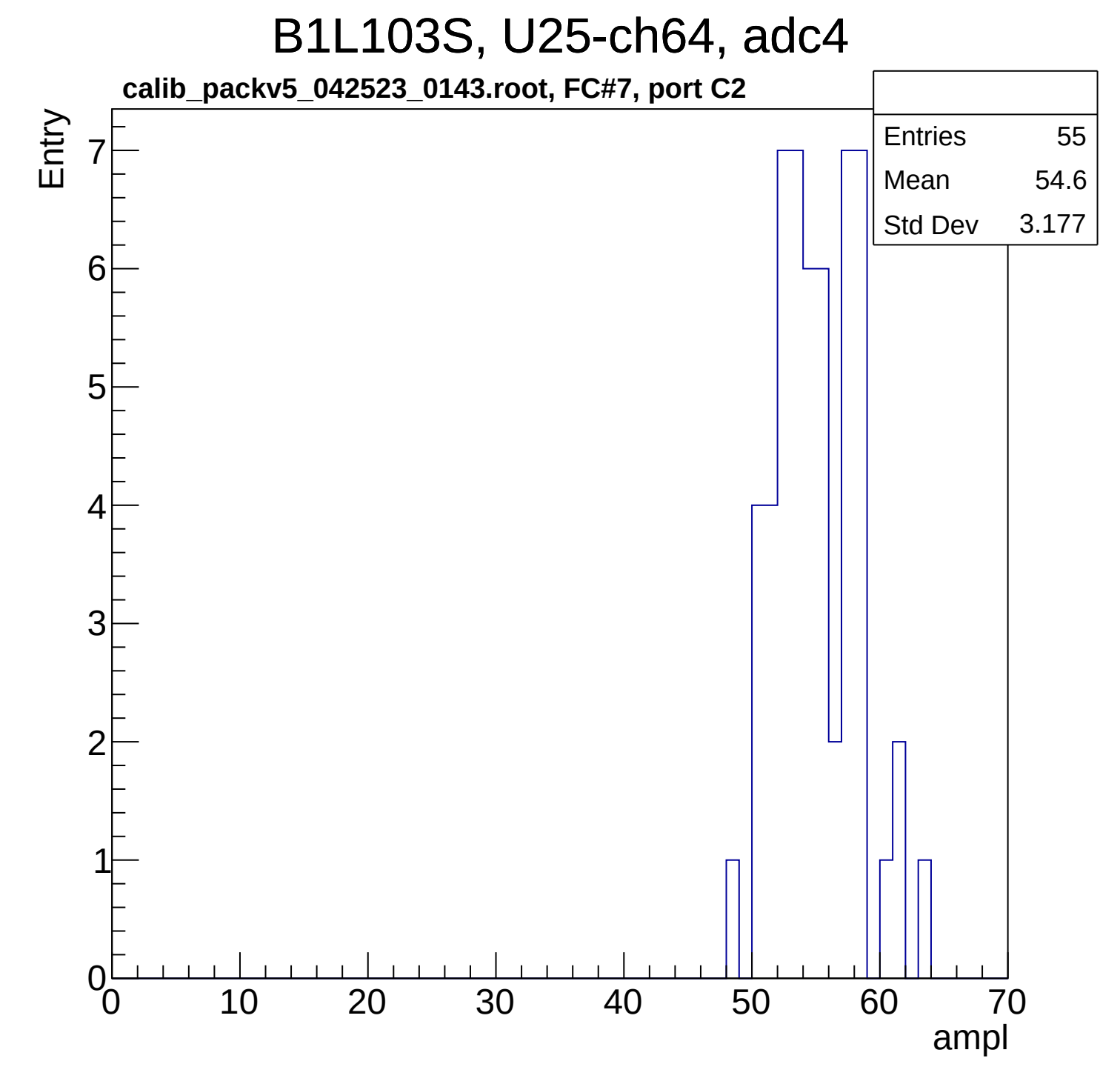
B1L103S, U25-ch64, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 54.6
Std Dev 3.177
Entry
ampl
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3
4
5
6
7
0
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20
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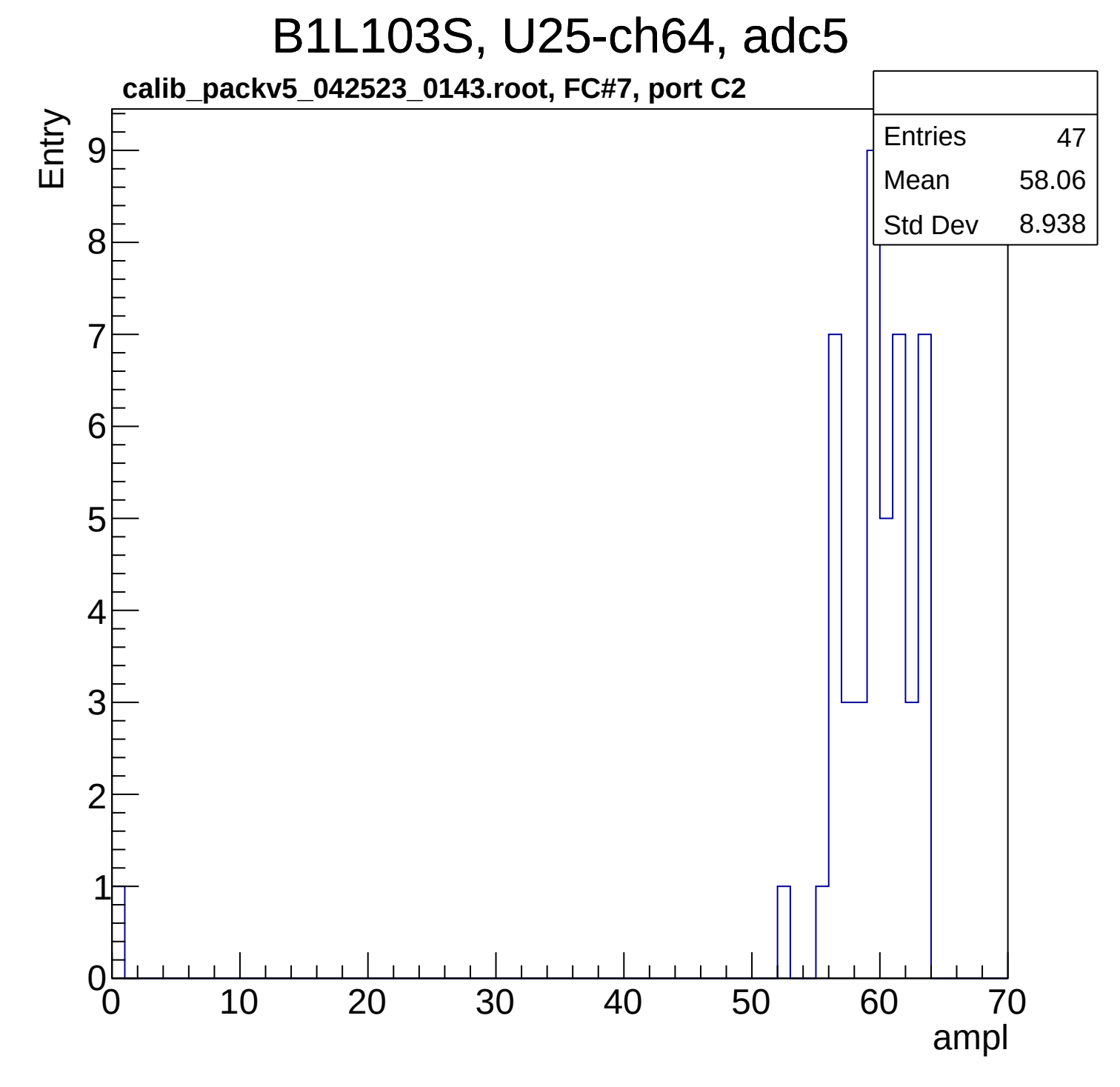
40
50
60
70


B1L103S, U25-ch64, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 58.06
Std Dev 8.938
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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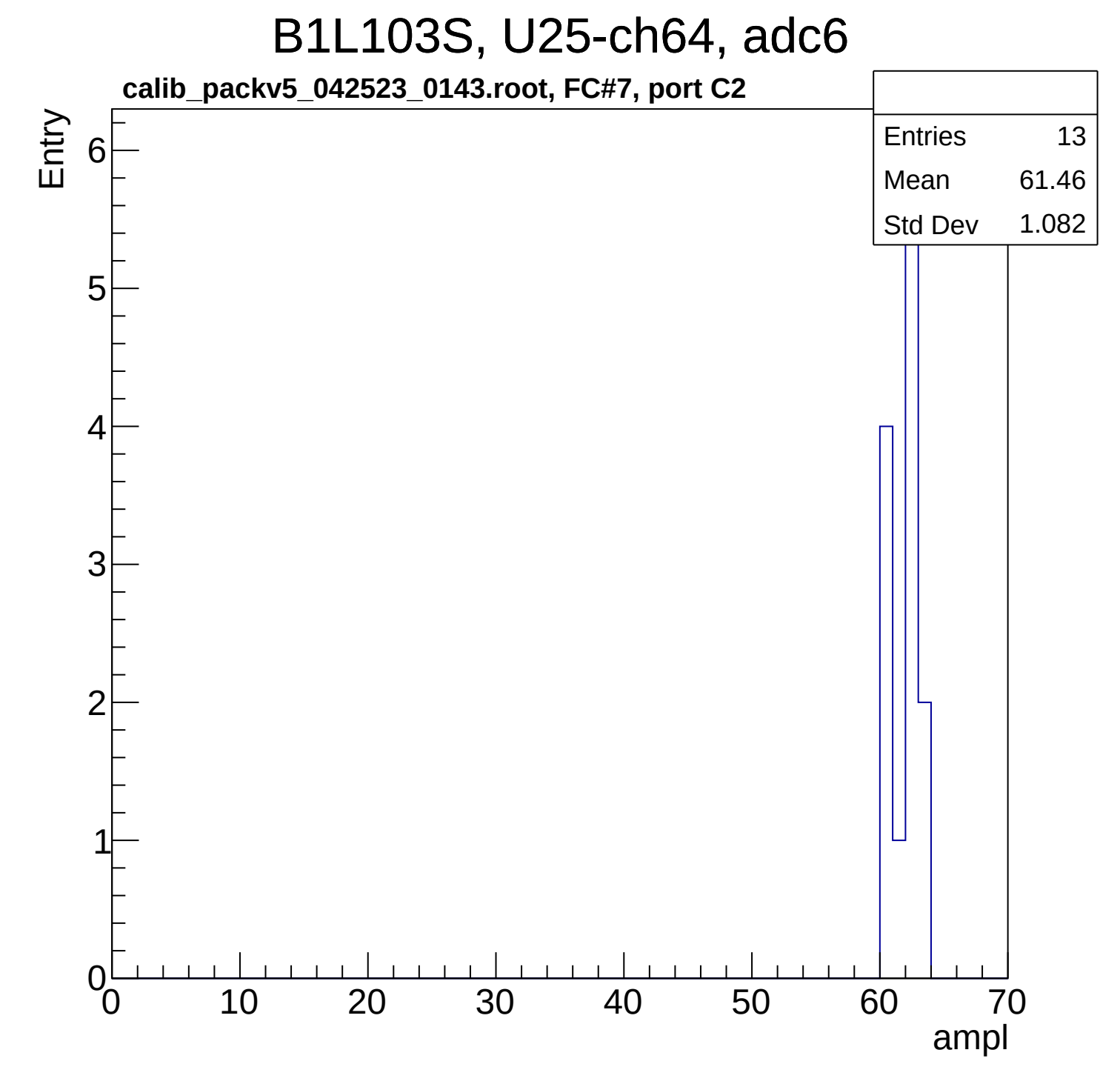
B1L103S, U25-ch64, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.46
Std Dev 1.082
Entry
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2
3
4
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6
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10
20
30
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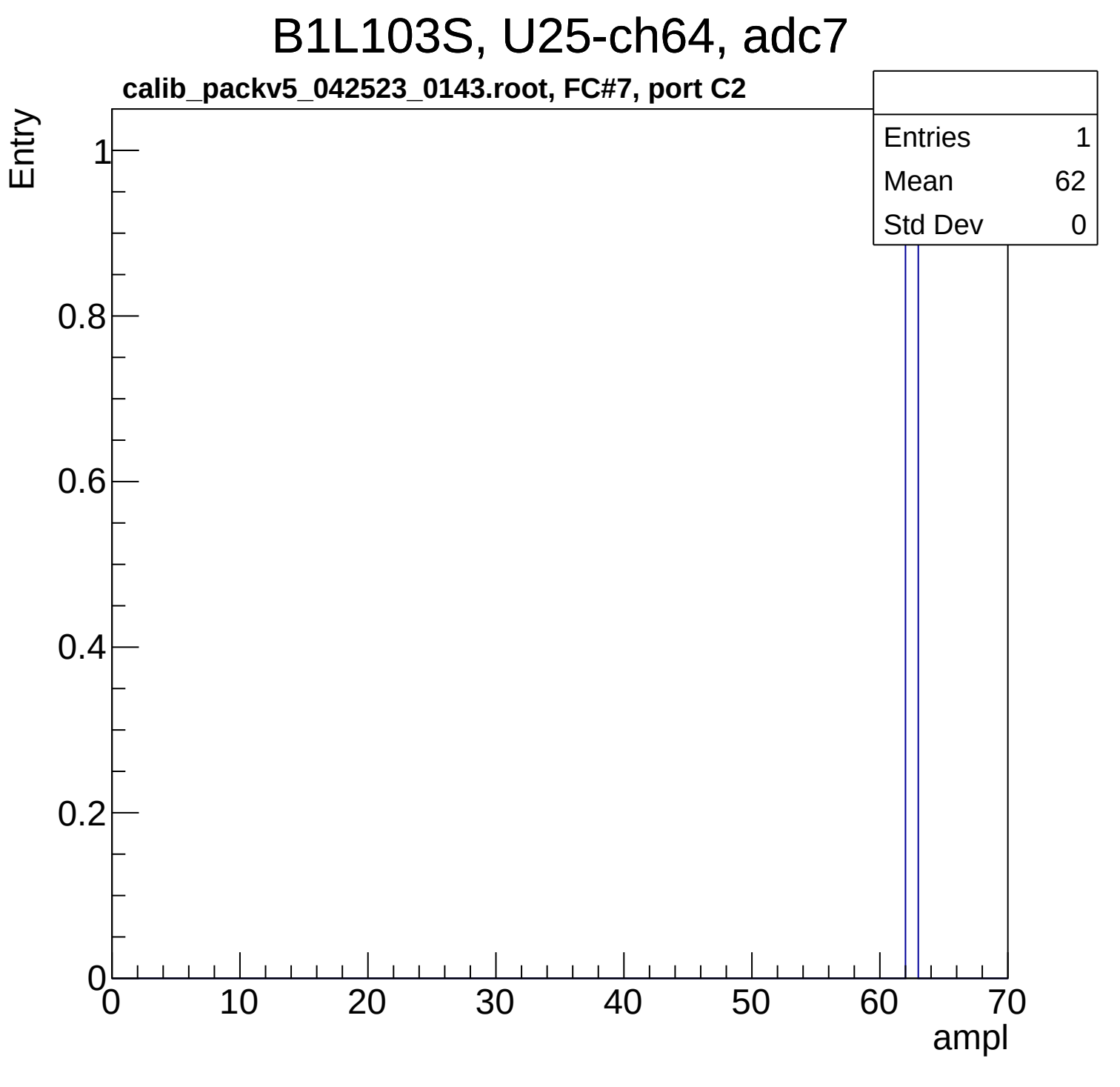
50
60
70
ampl


B1L103S, U25-ch64, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
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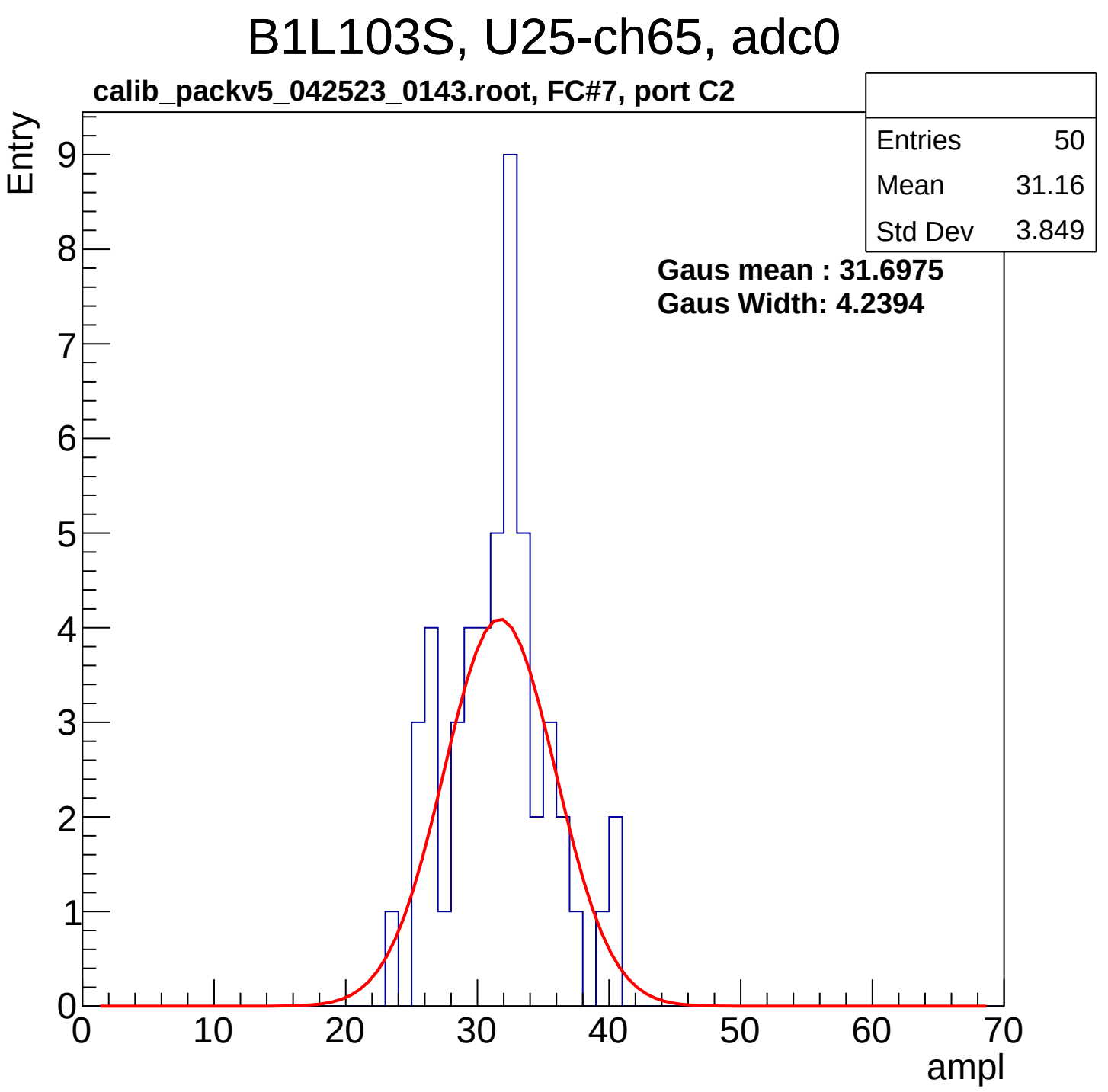
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch65, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 31.16
Std Dev 3.849
Gaus mean : 31.6975
Gaus Width: 4.2394
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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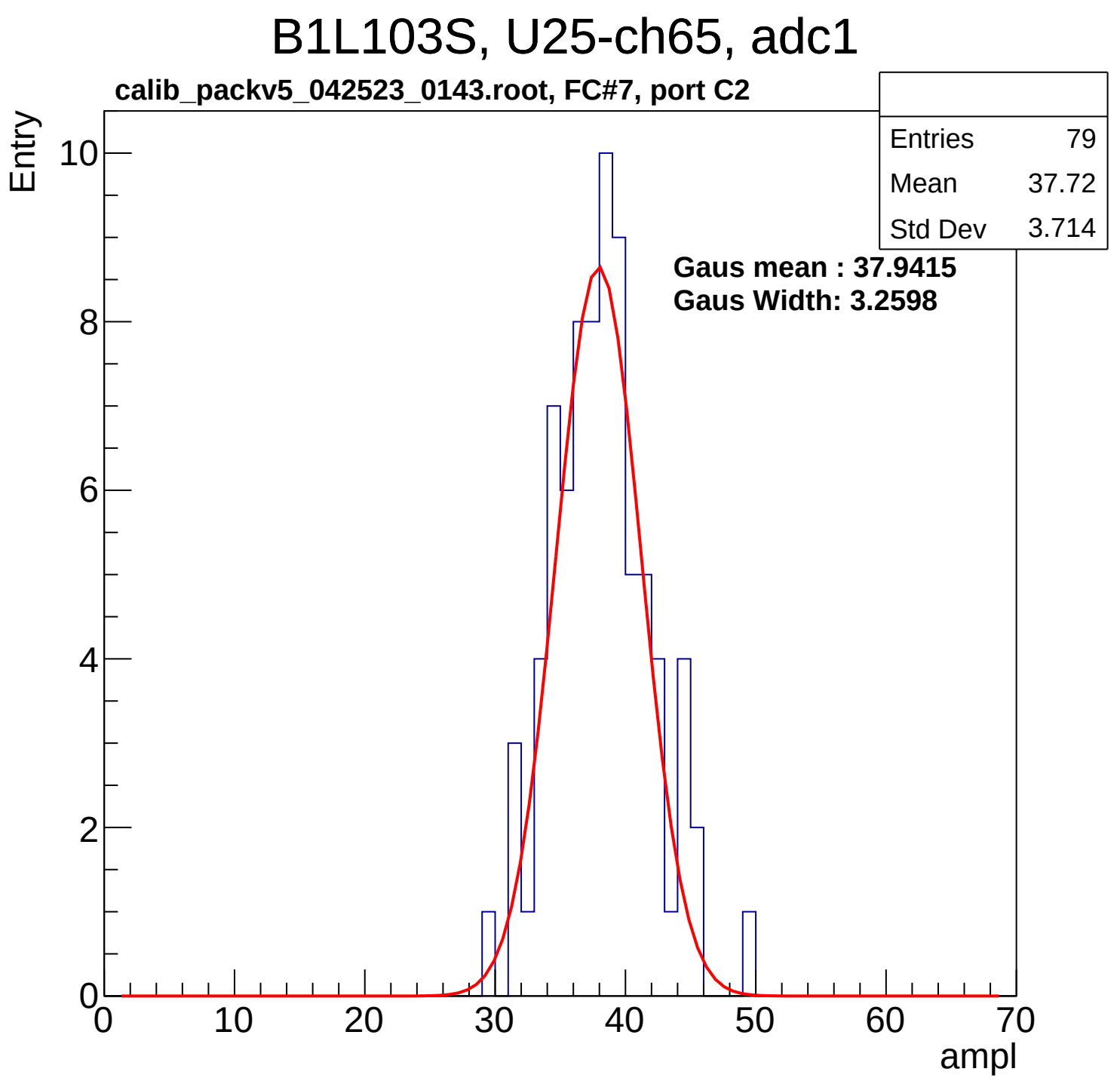
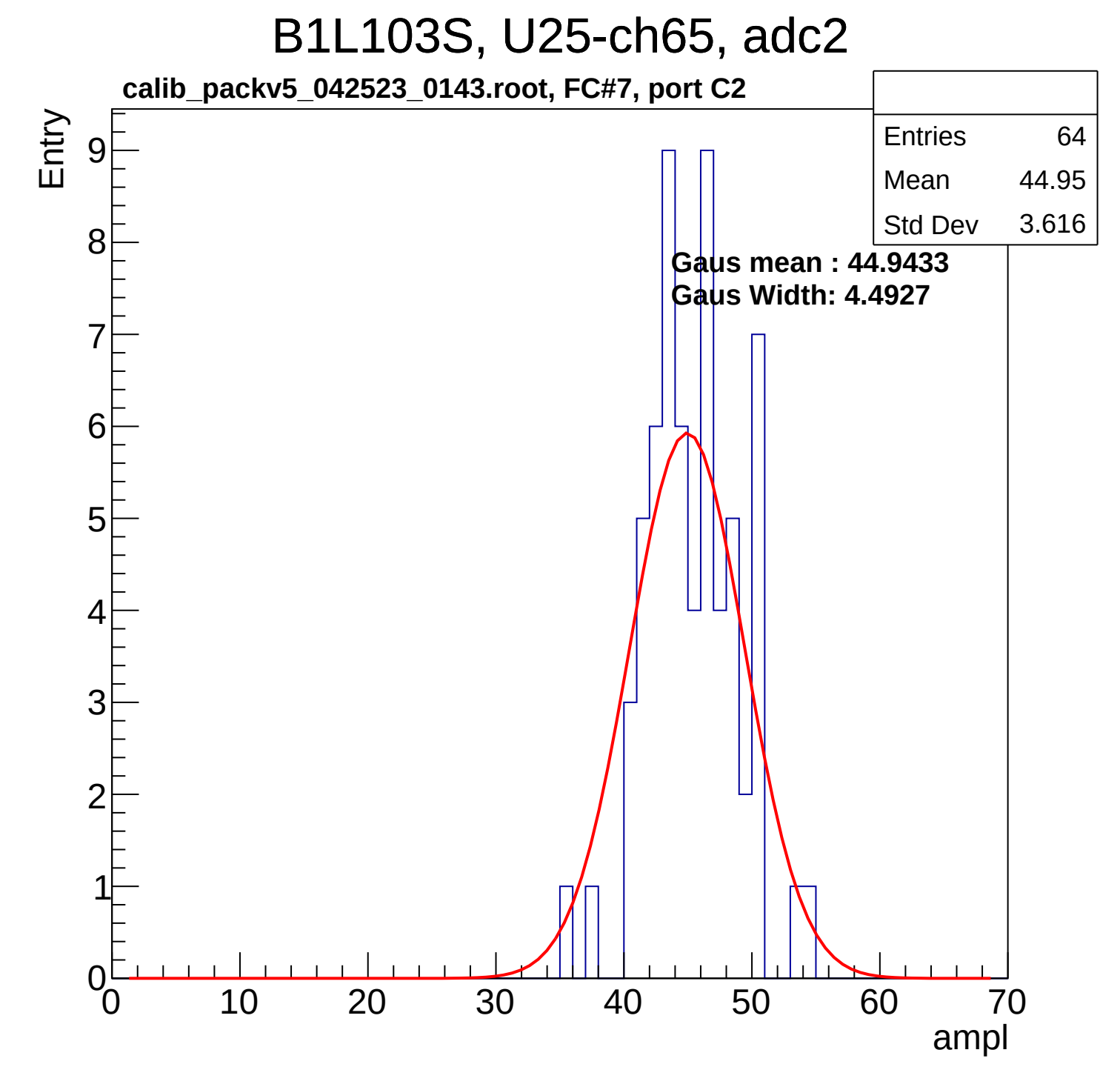

B1L103S, U25-ch65, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 37.72
Std Dev 3.714
Gaus mean : 37.9415
Gaus Width: 3.2598
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch65, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.95
Std Dev 3.616
Gaus mean : 44.9433
Gaus Width: 4.4927
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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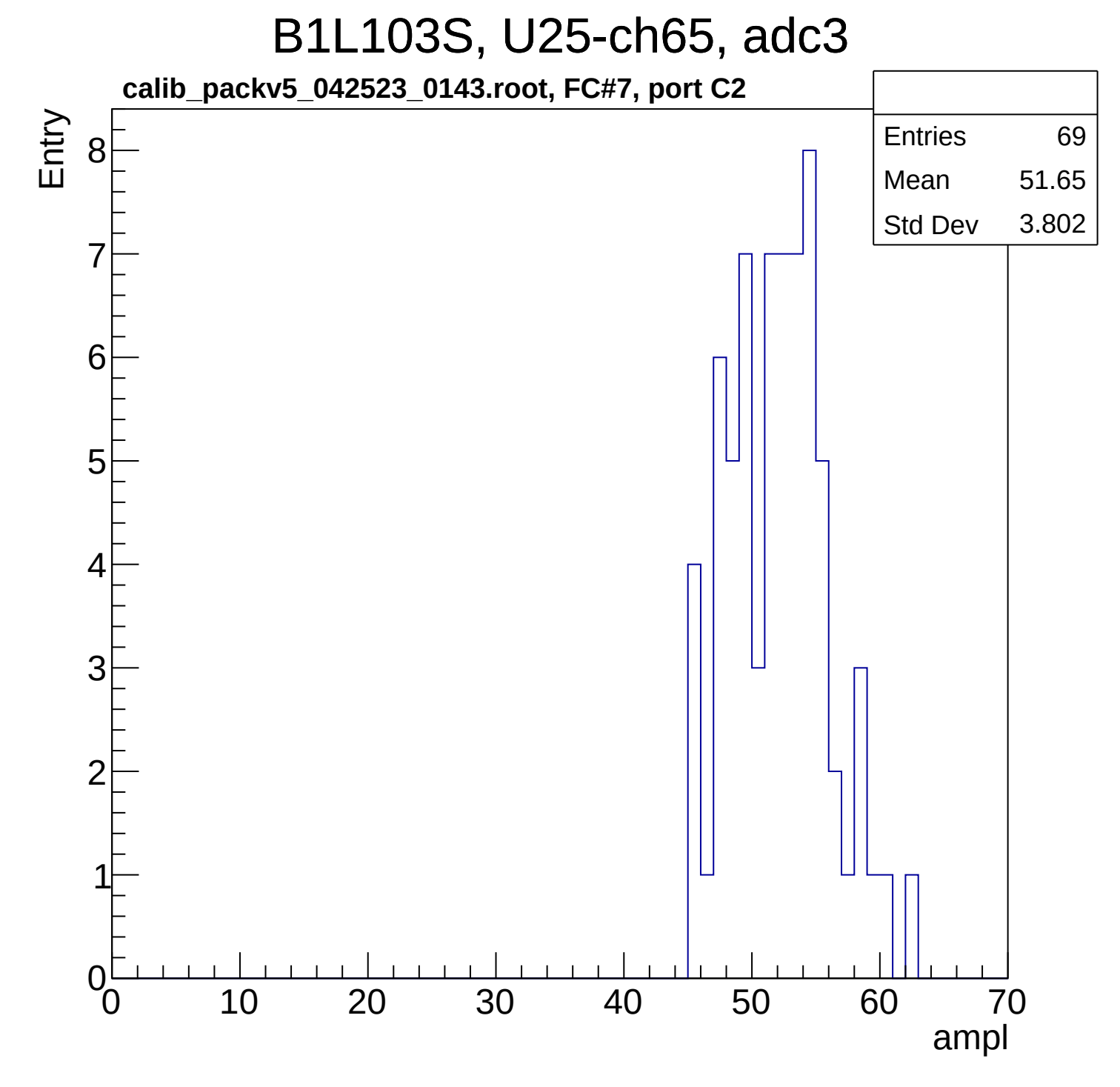
40
50
60
70


B1L103S, U25-ch65, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 51.65
Std Dev 3.802
Entry
ampl
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3
4
5
6
7
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0
10
20
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60
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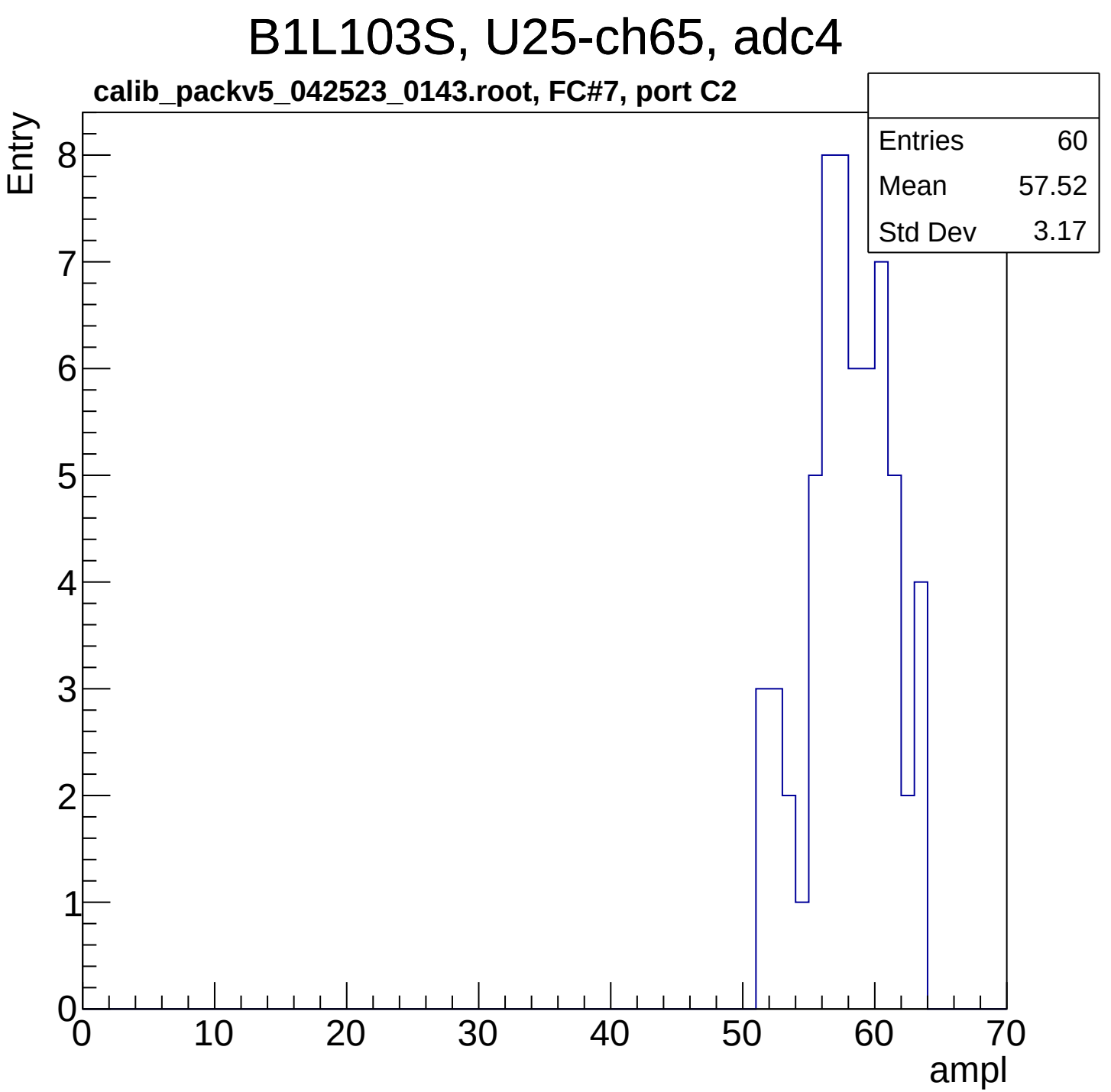
B1L103S, U25-ch65, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 57.52
Std Dev 3.17
Entry
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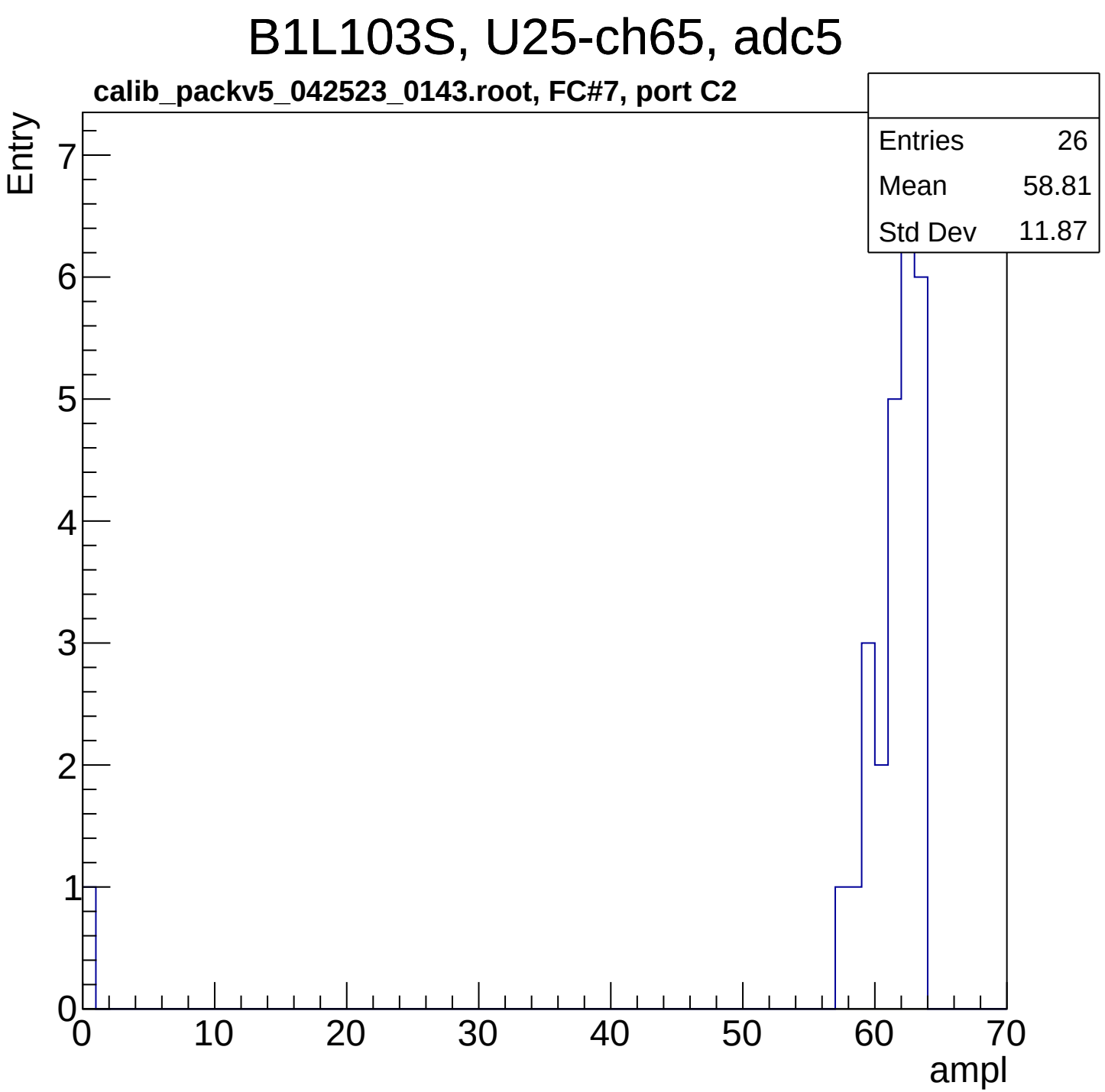
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch65, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 58.81
Std Dev 11.87
Entry
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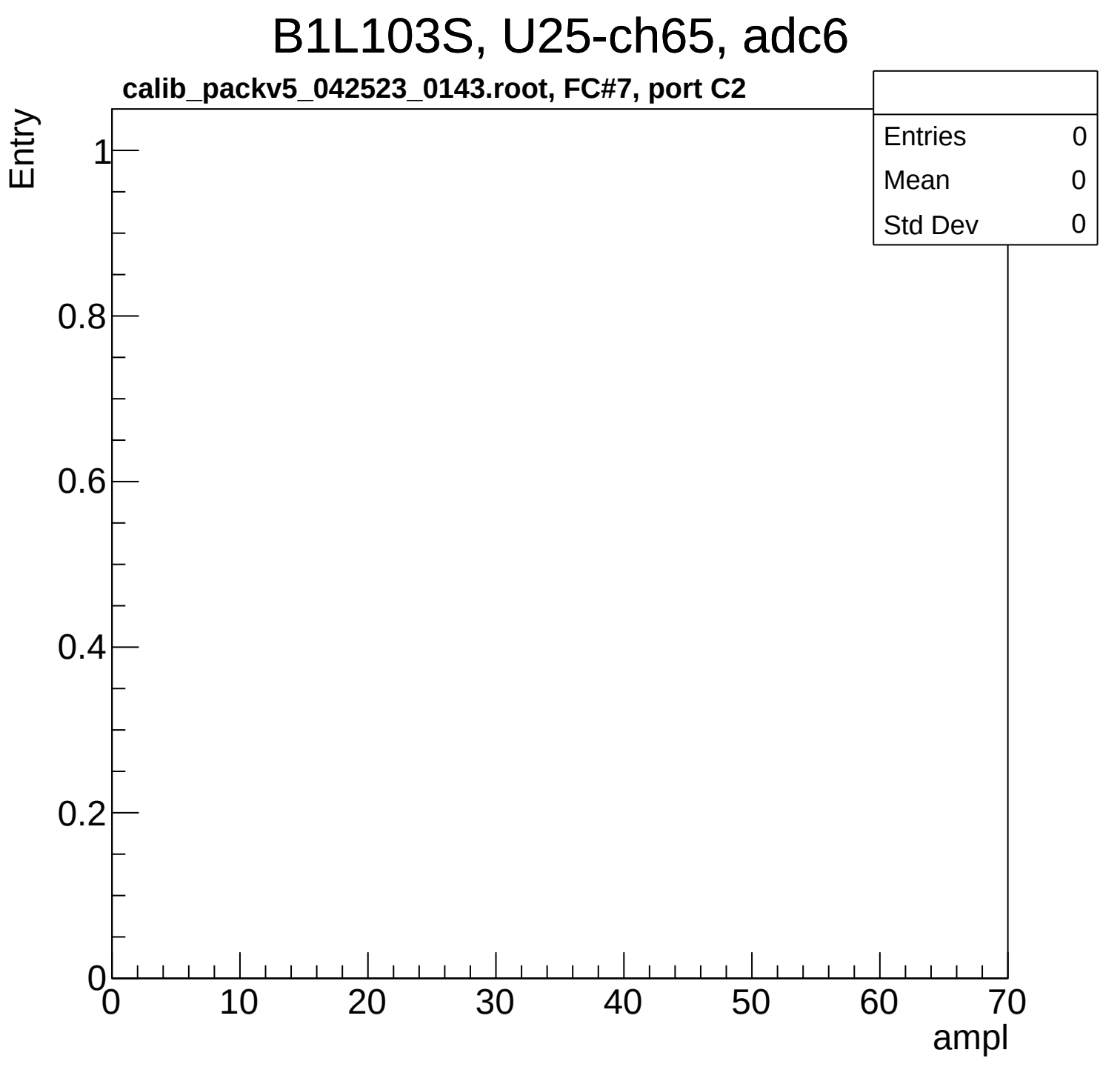
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch65, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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50
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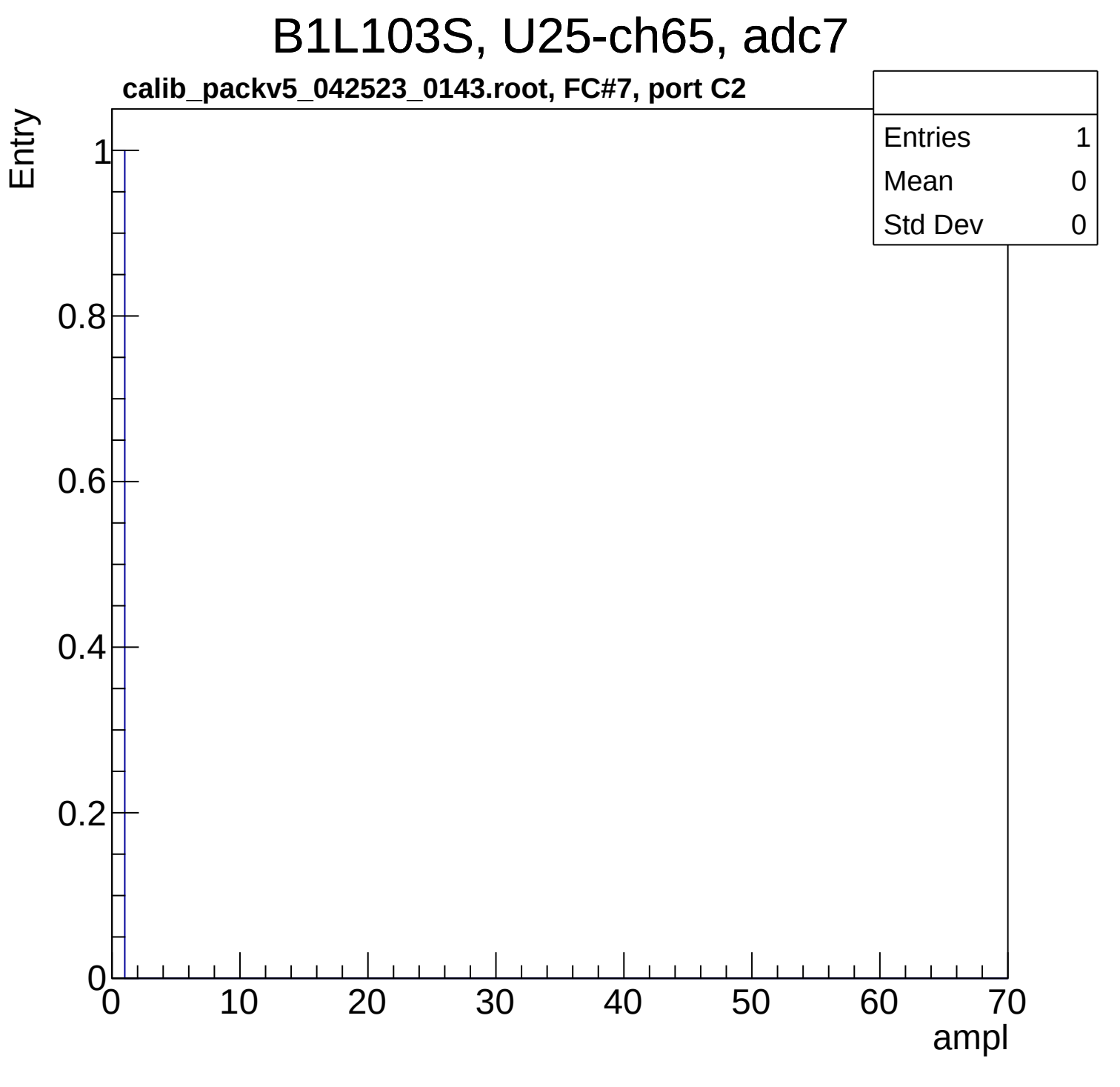
70
ampl


B1L103S, U25-ch65, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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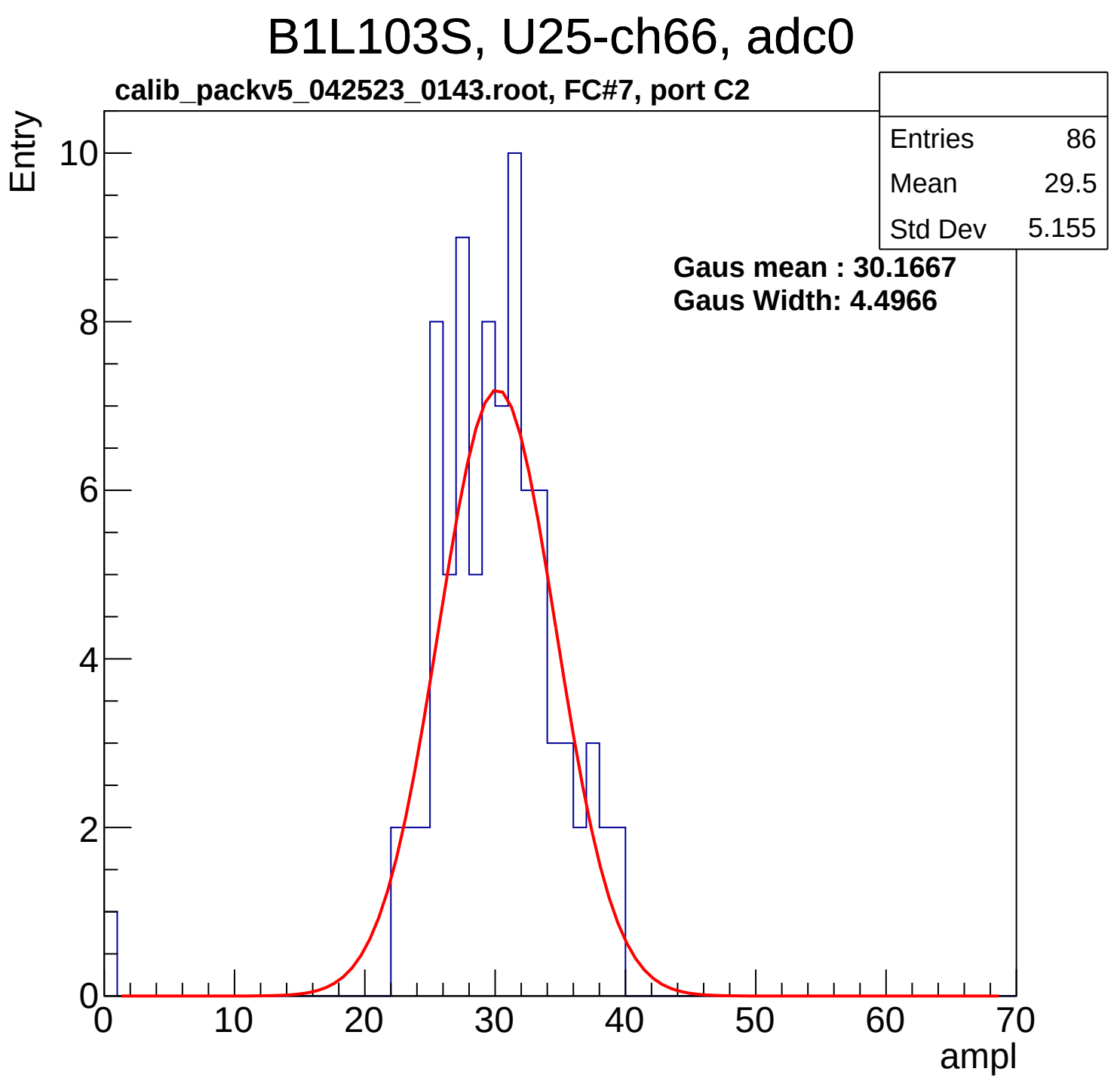
70
ampl


B1L103S, U25-ch66, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 29.5
Std Dev 5.155
Gaus mean : 30.1667
Gaus Width: 4.4966
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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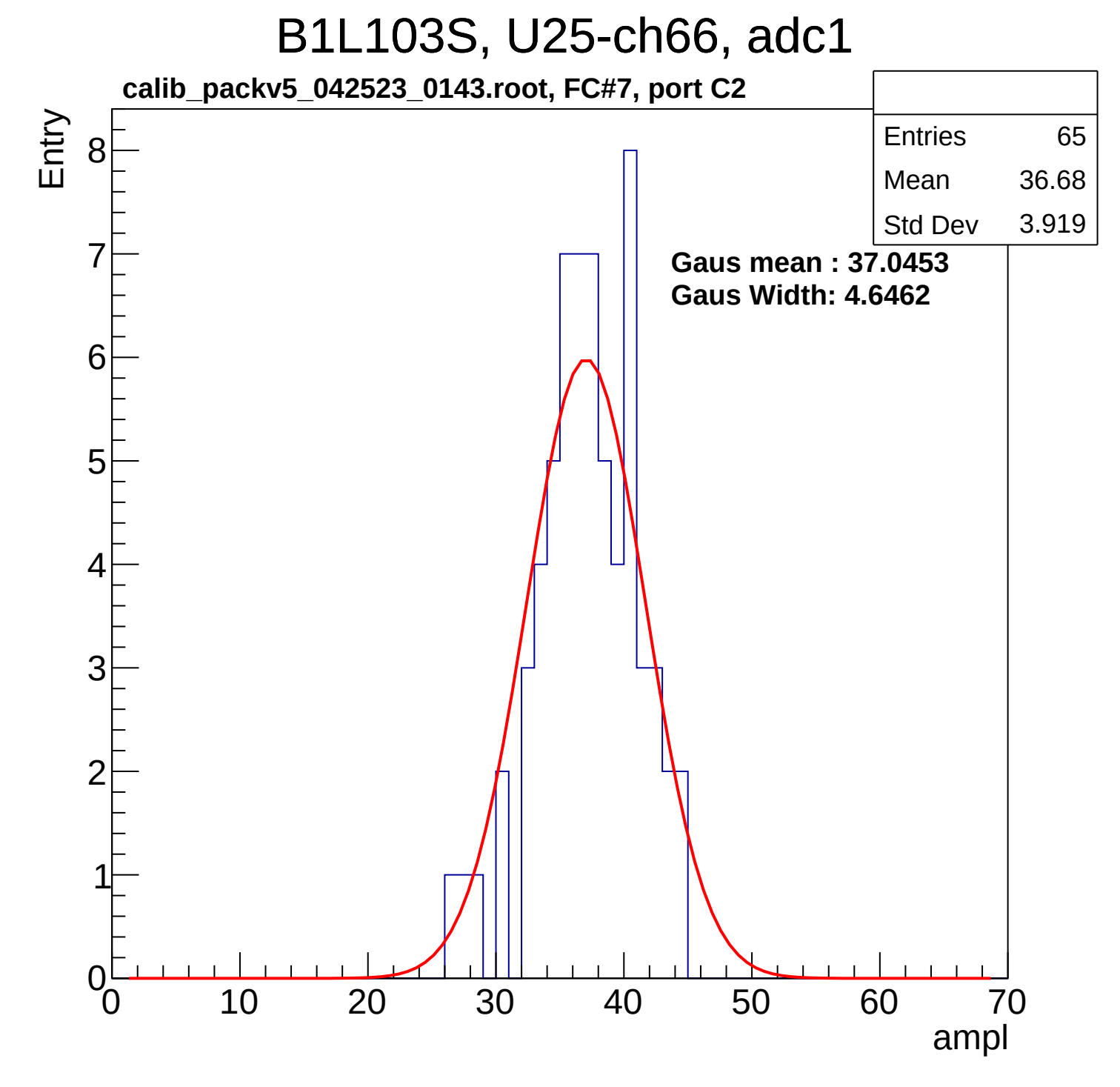
B1L103S, U25-ch66, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 36.68
Std Dev 3.919
Gaus mean : 37.0453
Gaus Width: 4.6462
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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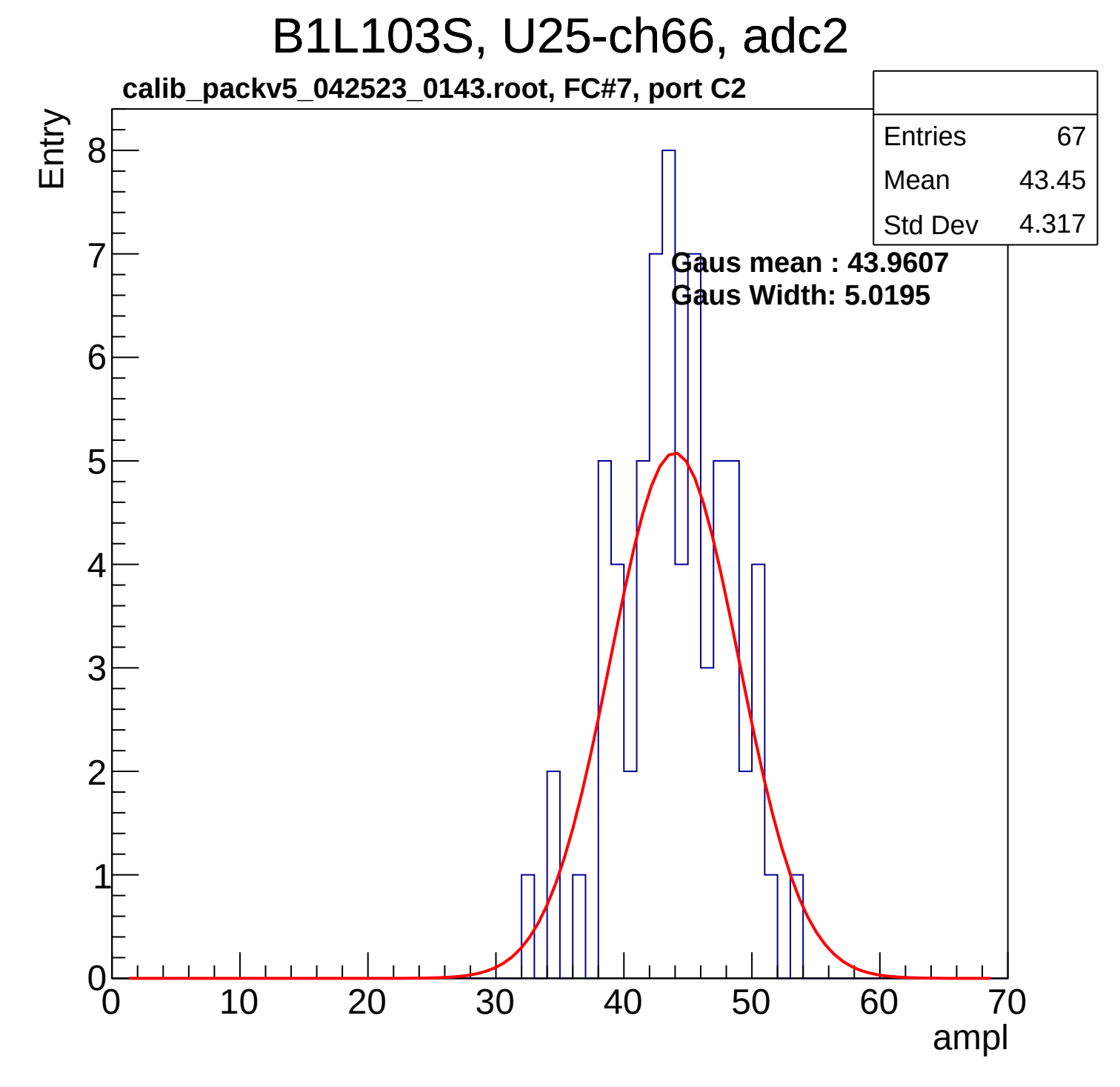
B1L103S, U25-ch66, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 43.45
Std Dev 4.317
Gaus mean : 43.9607
Gaus Width: 5.0195
Entry
ampl
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1
2
3
4
5
6
7
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0
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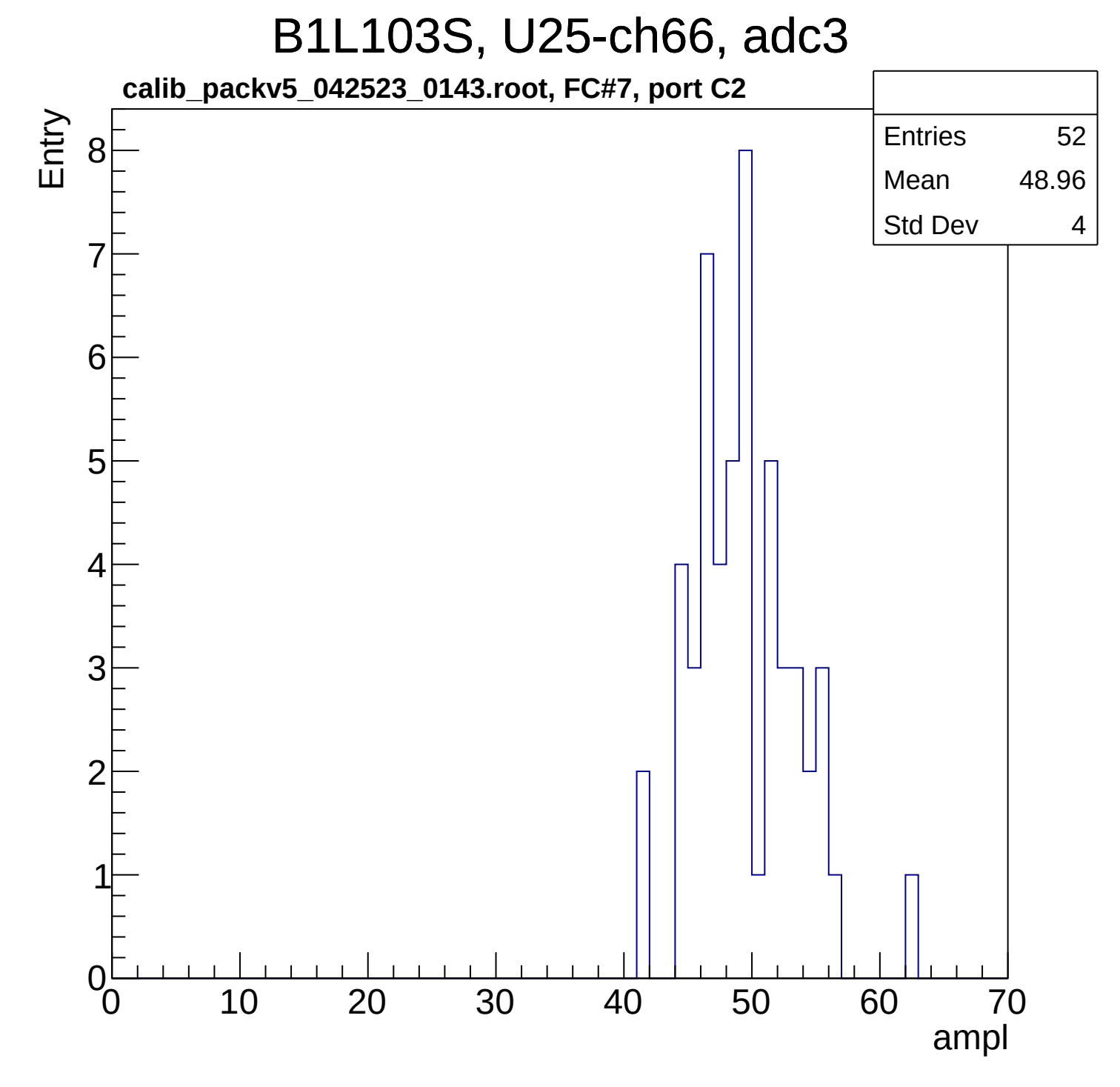
B1L103S, U25-ch66, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 48.96
Std Dev 4
Entry
ampl
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1
2
3
4
5
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7
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0
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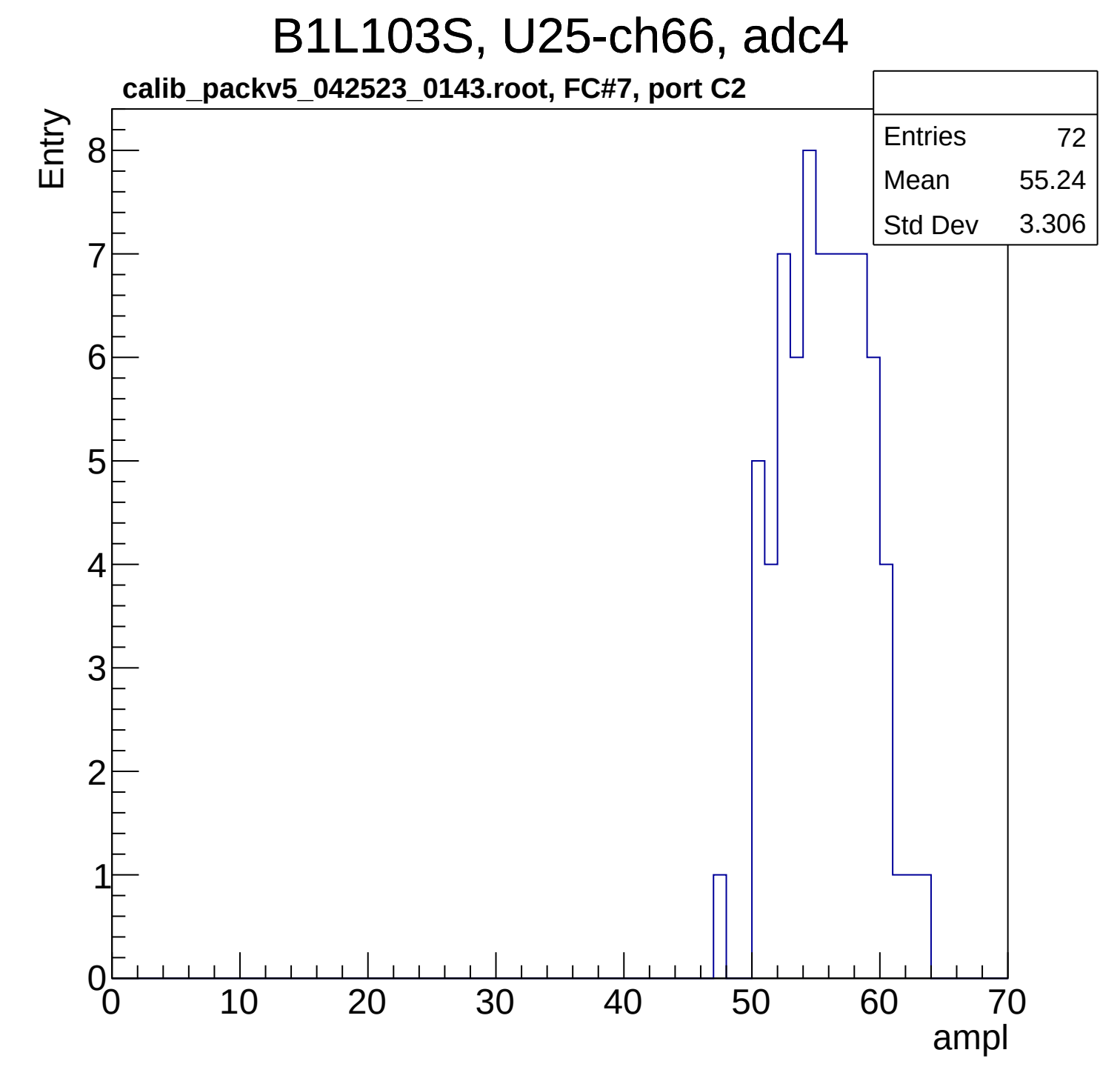
B1L103S, U25-ch66, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 55.24
Std Dev 3.306
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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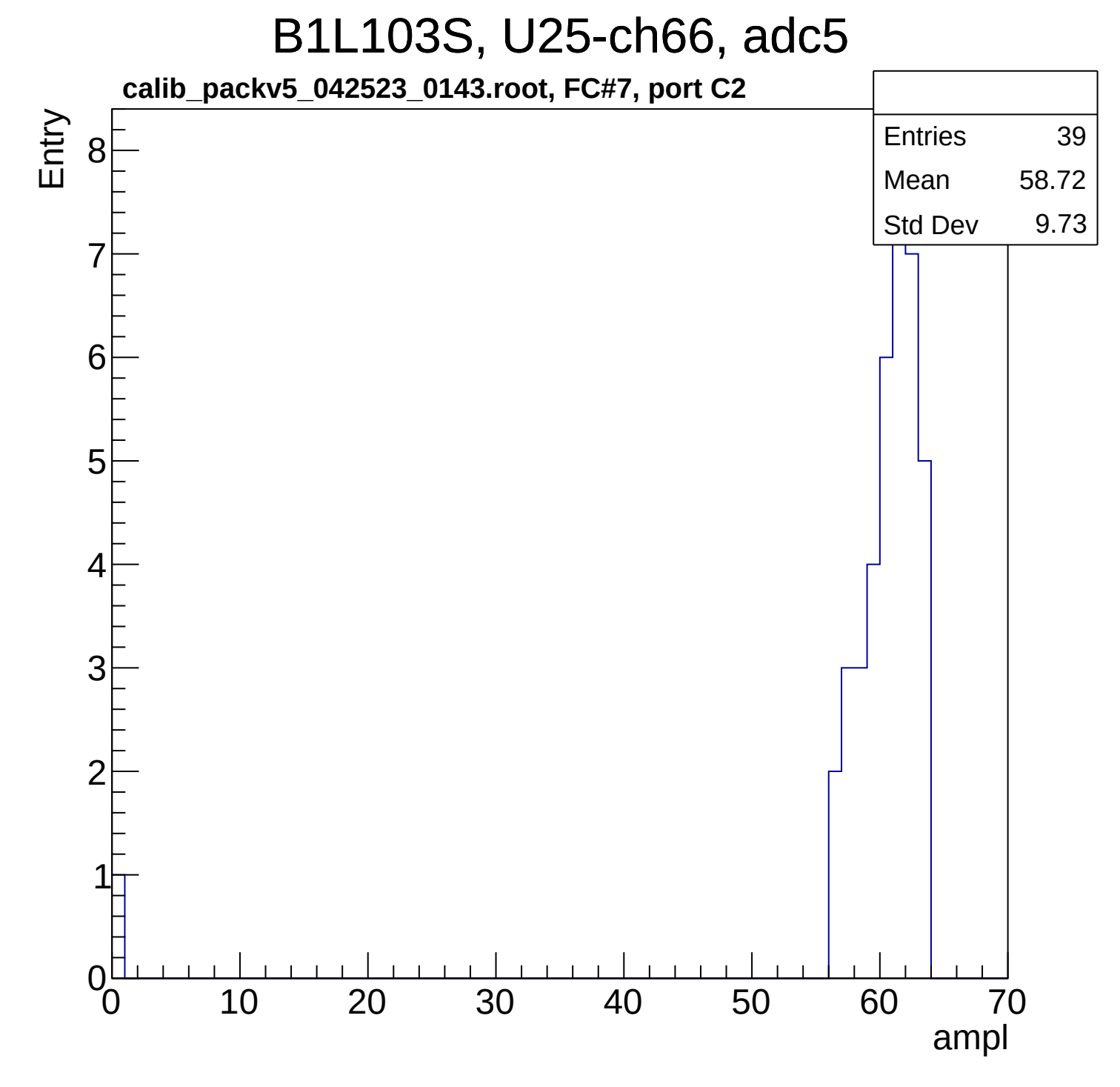
B1L103S, U25-ch66, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.72
Std Dev 9.73
Entry
ampl
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1
2
3
4
5
6
7
8
0
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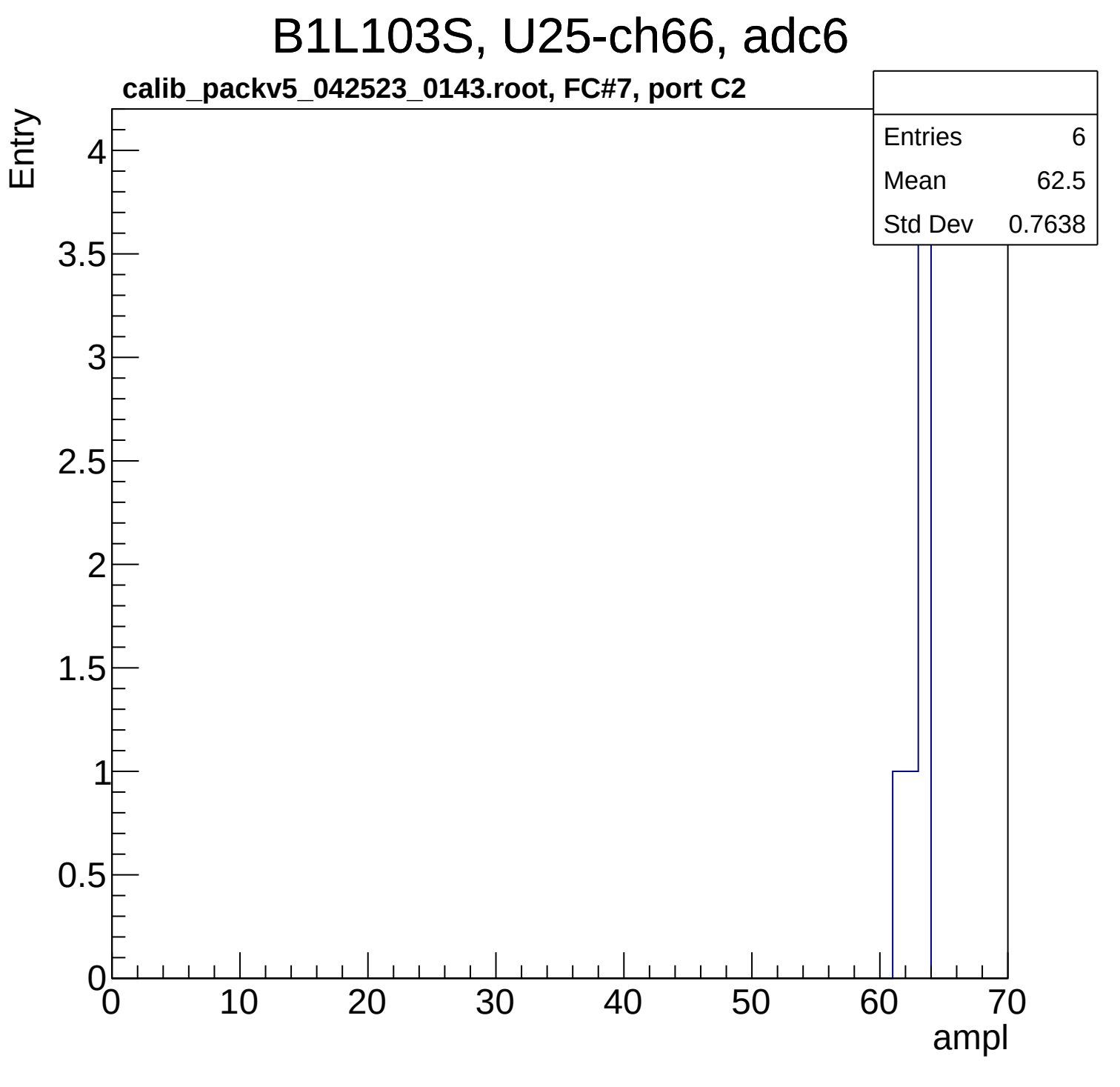
B1L103S, U25-ch66, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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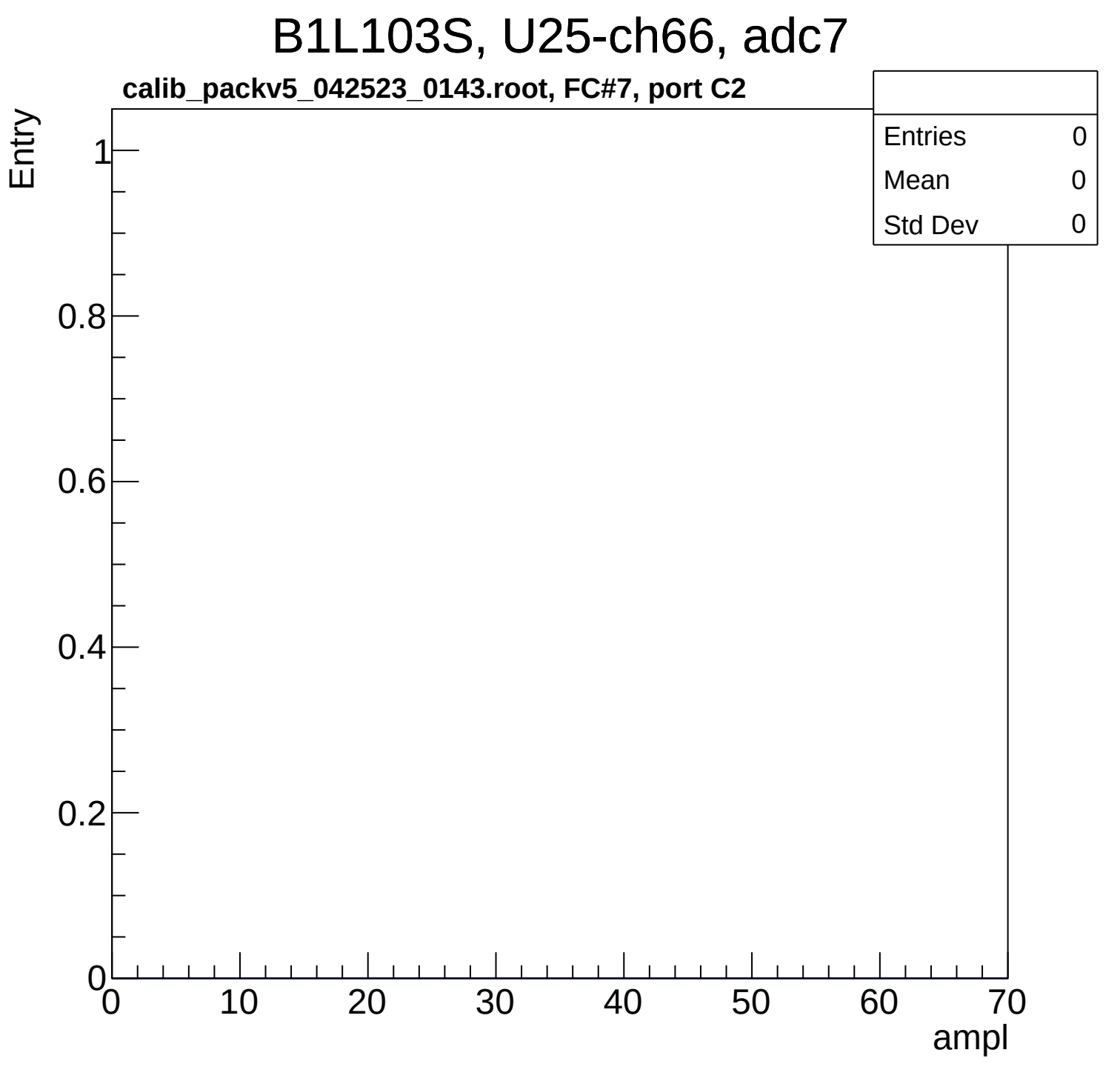
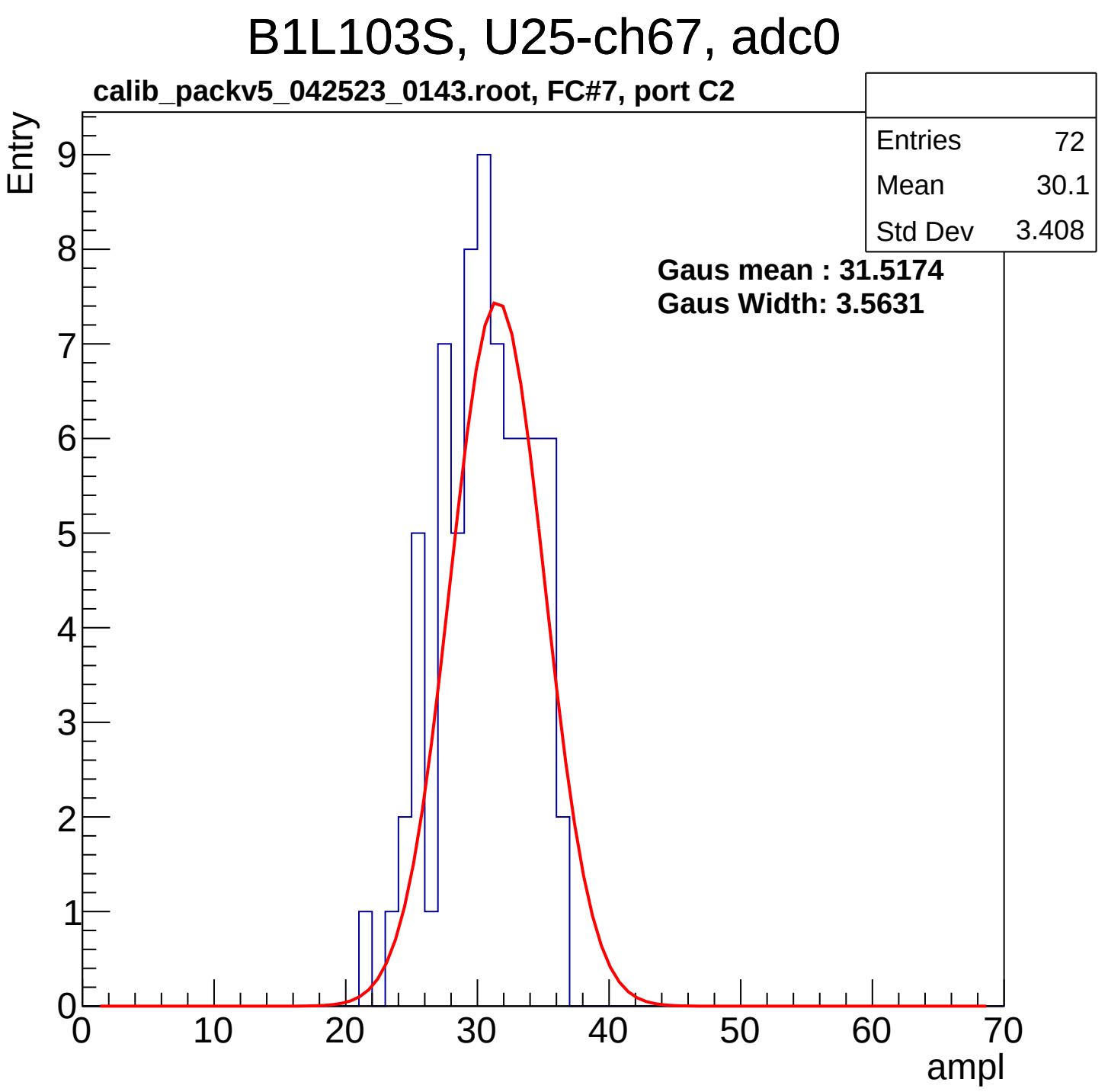

B1L103S, U25-ch66, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch67, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 30.1
Std Dev 3.408
Gaus mean : 31.5174
Gaus Width: 3.5631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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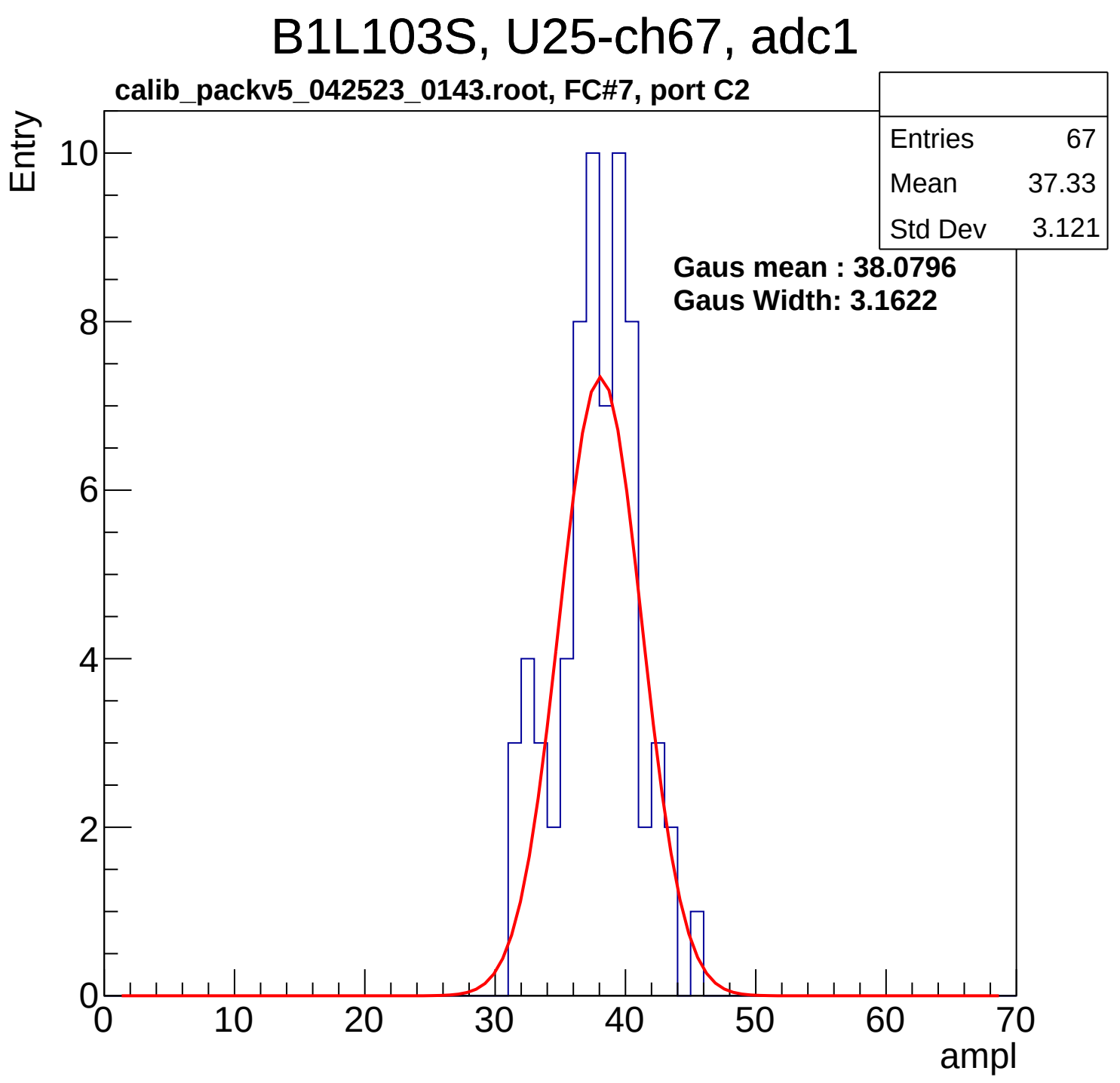
B1L103S, U25-ch67, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.33
Std Dev 3.121
Gaus mean : 38.0796
Gaus Width: 3.1622
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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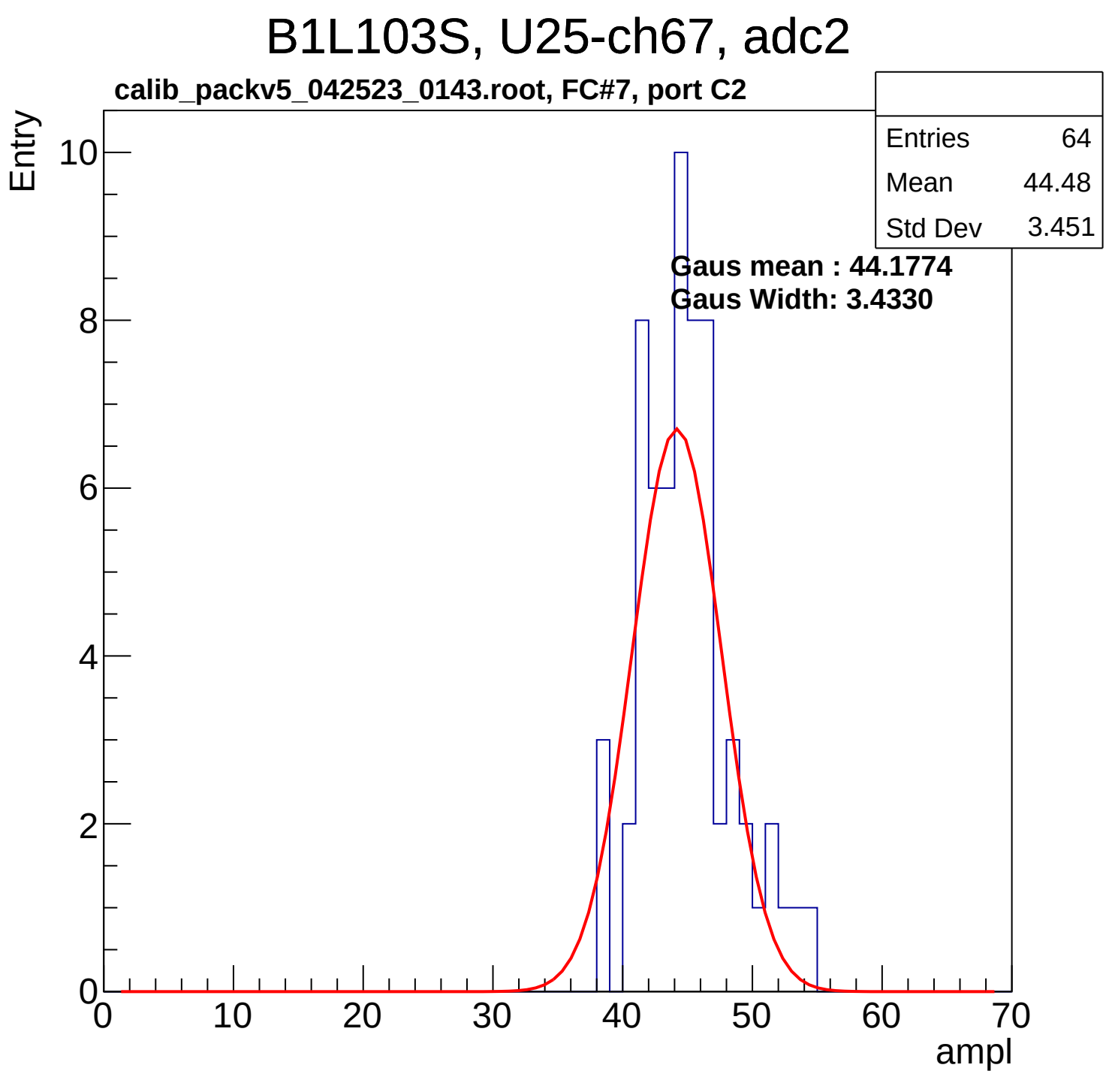
B1L103S, U25-ch67, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.48
Std Dev 3.451
Gaus mean : 44.1774
Gaus Width: 3.4330
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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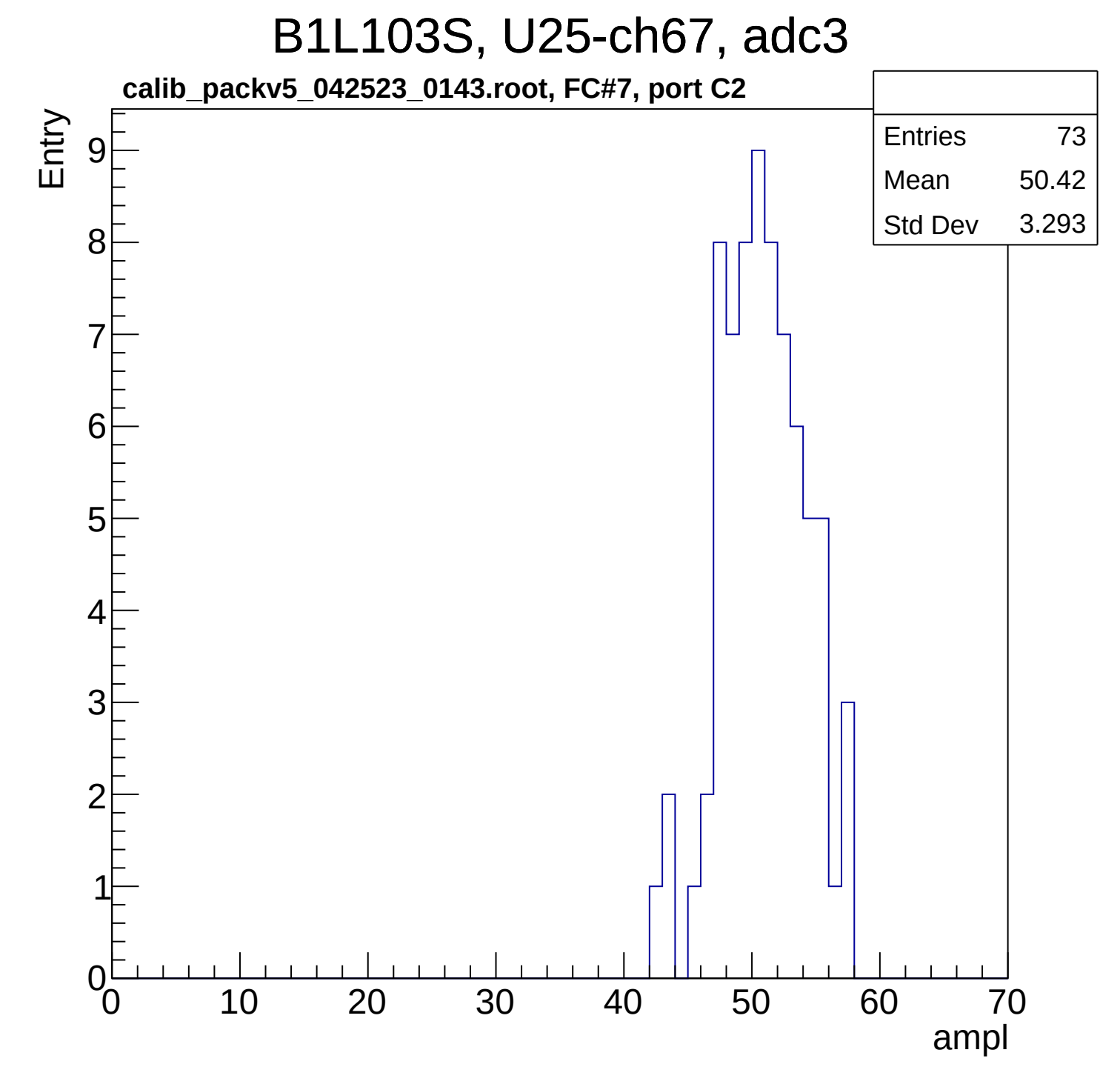
B1L103S, U25-ch67, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50.42
Std Dev 3.293
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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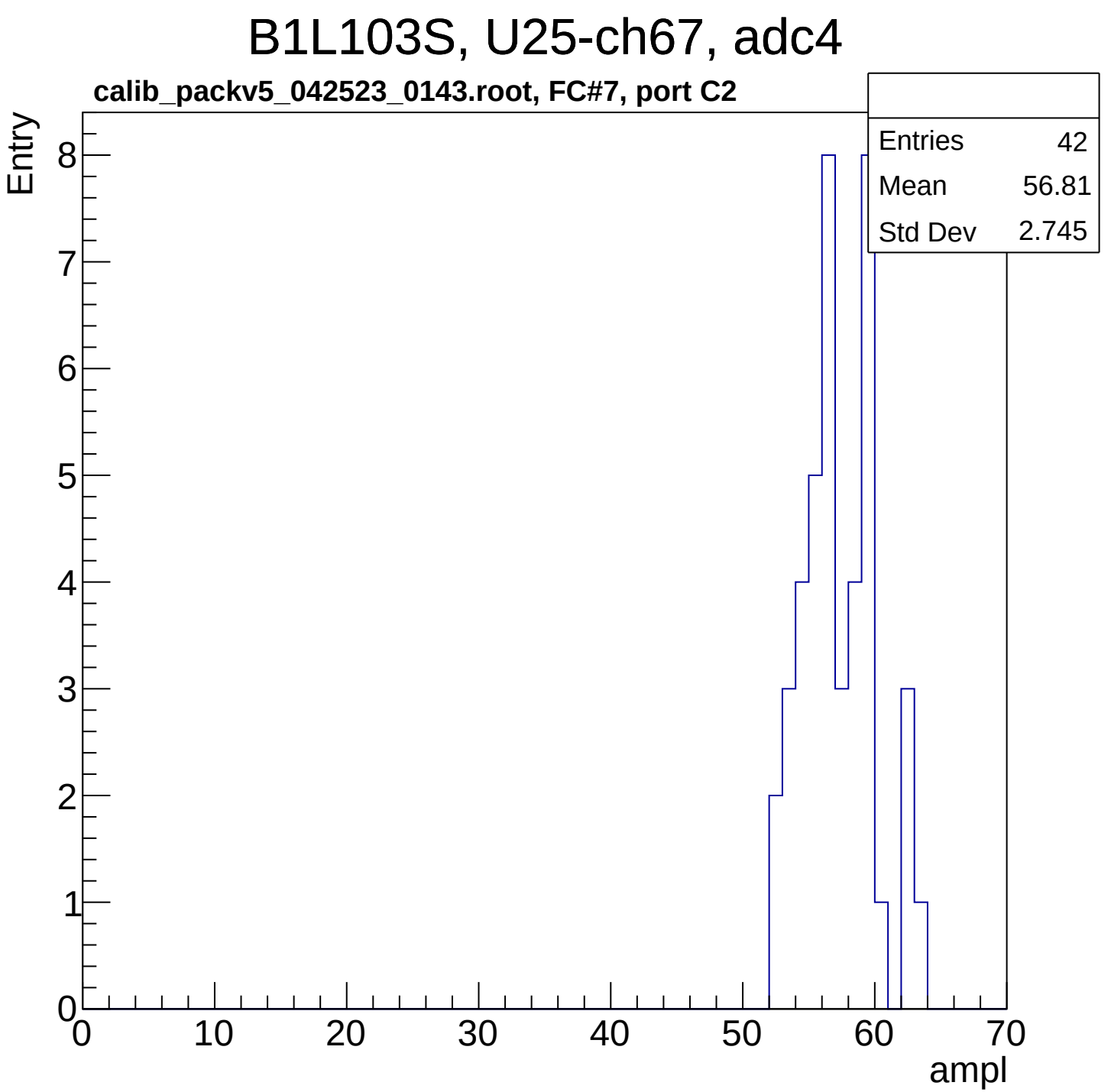
B1L103S, U25-ch67, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 56.81
Std Dev 2.745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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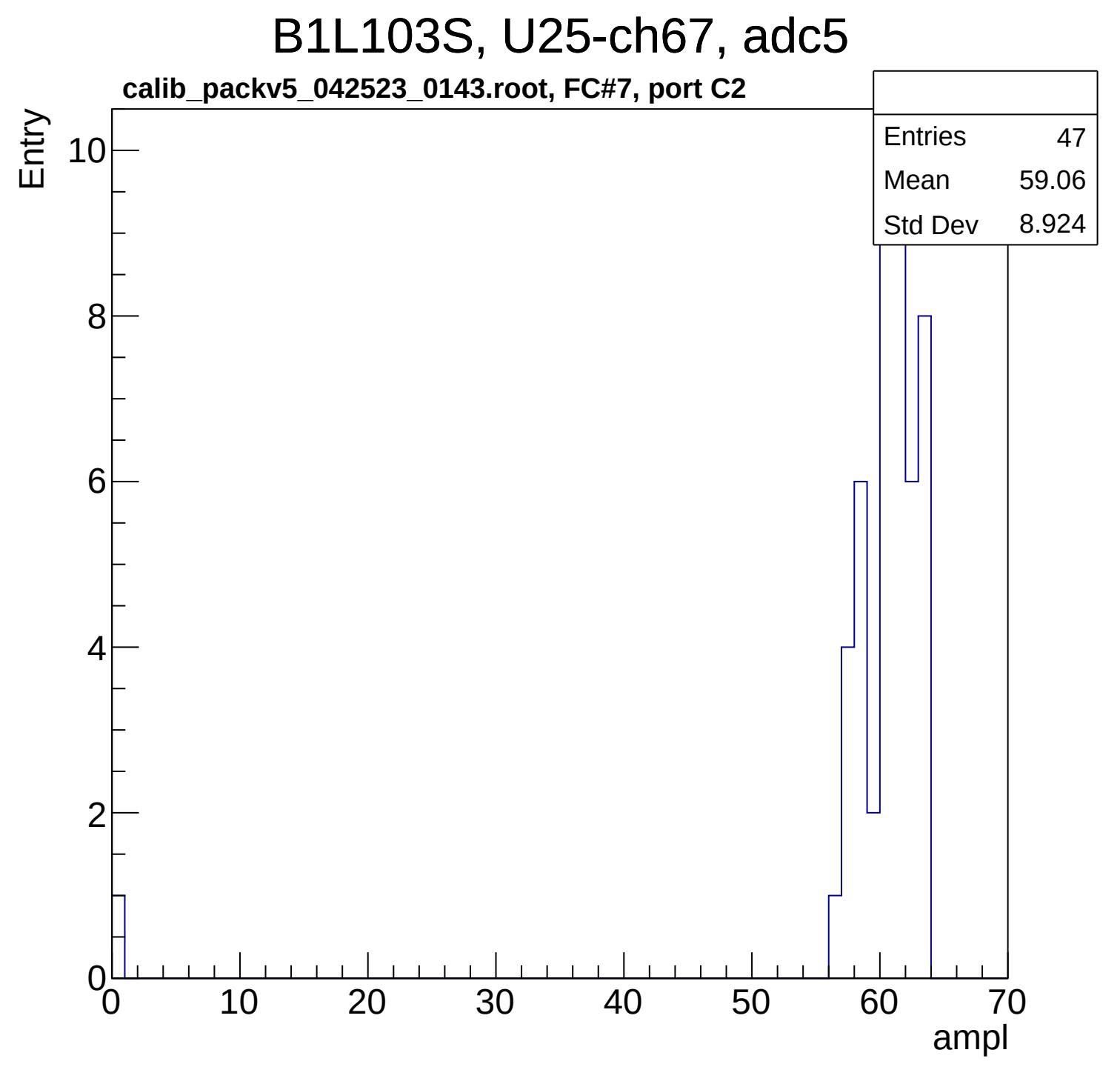
B1L103S, U25-ch67, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.06
Std Dev 8.924
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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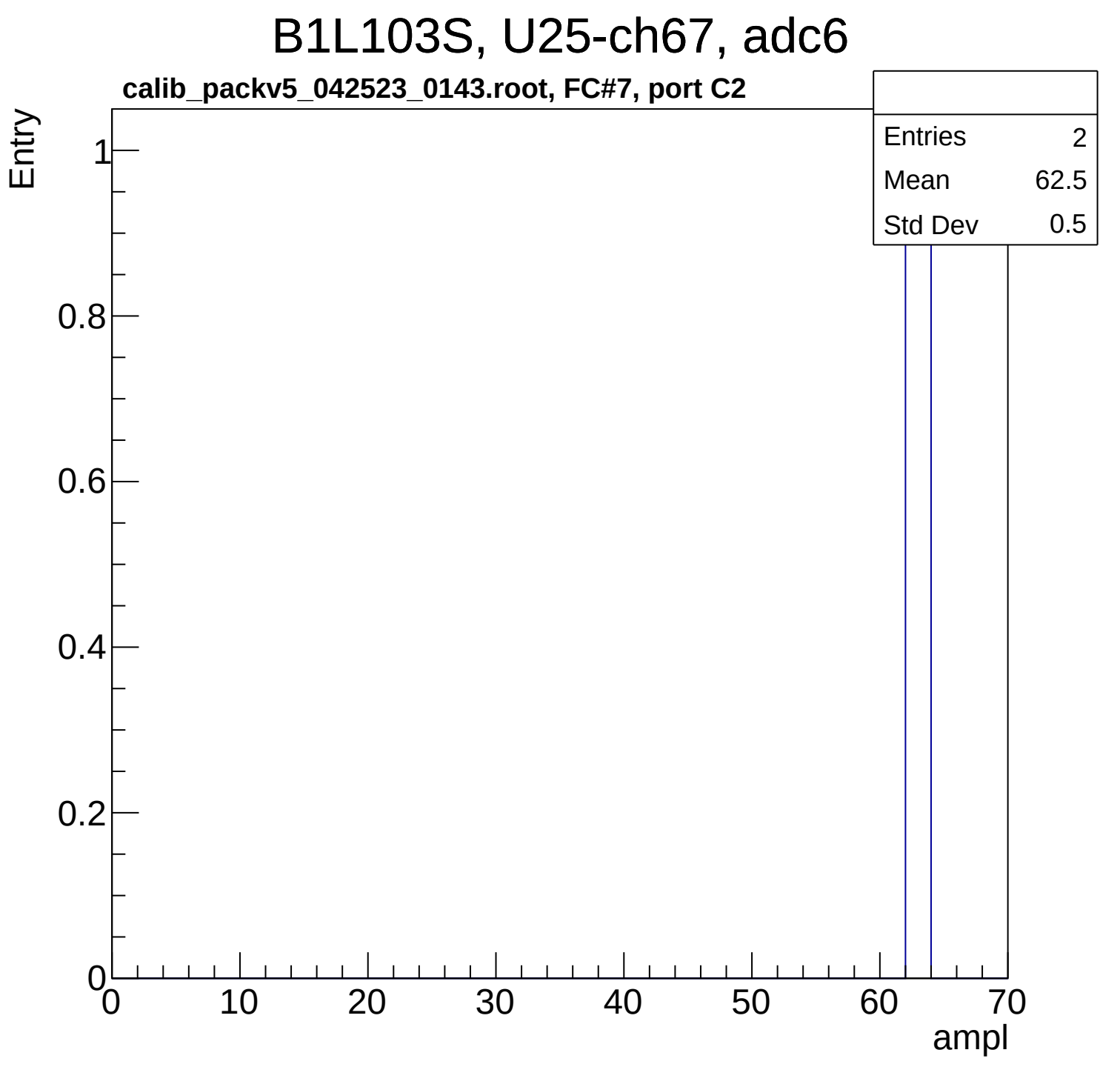
B1L103S, U25-ch67, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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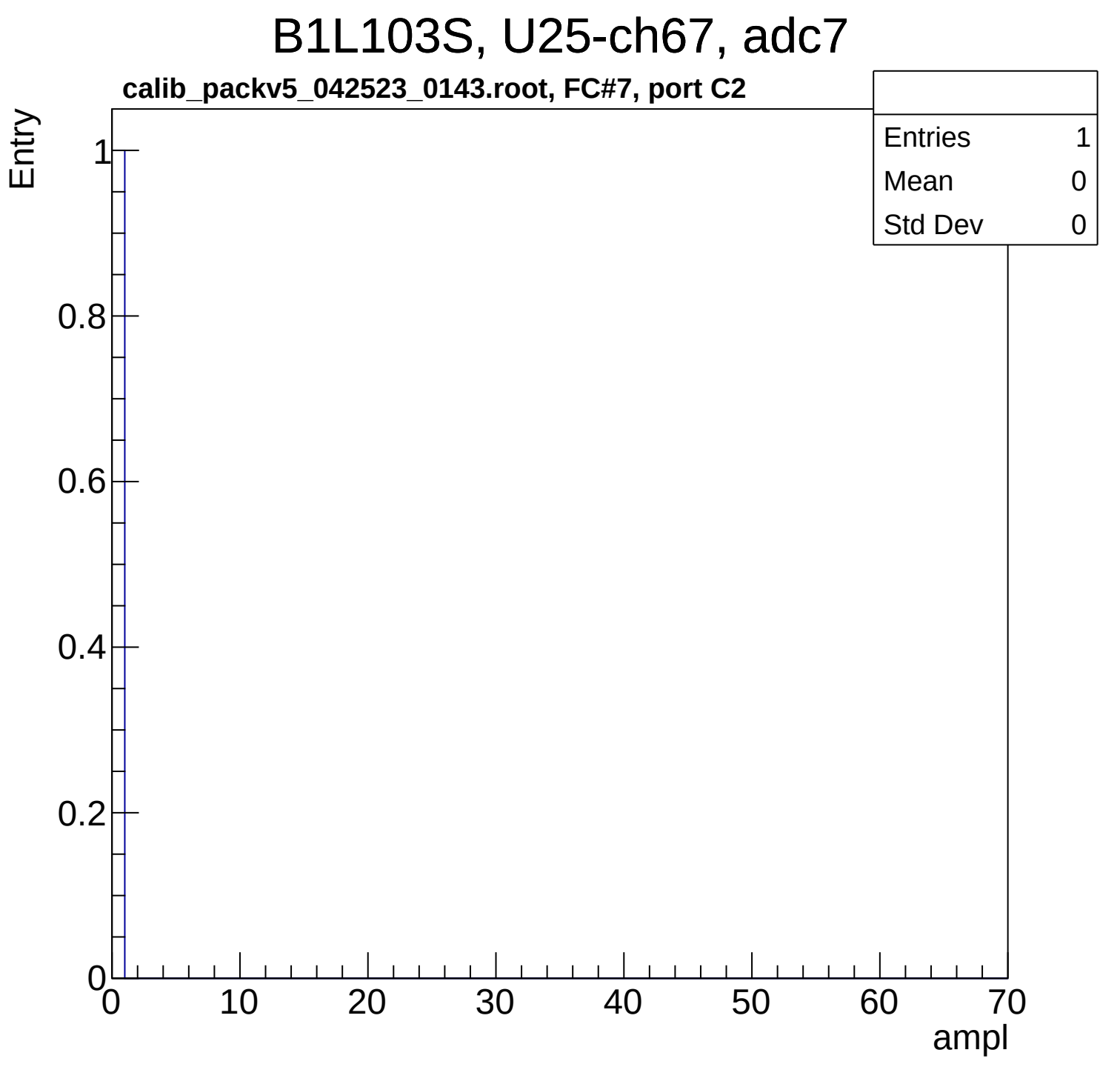
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch67, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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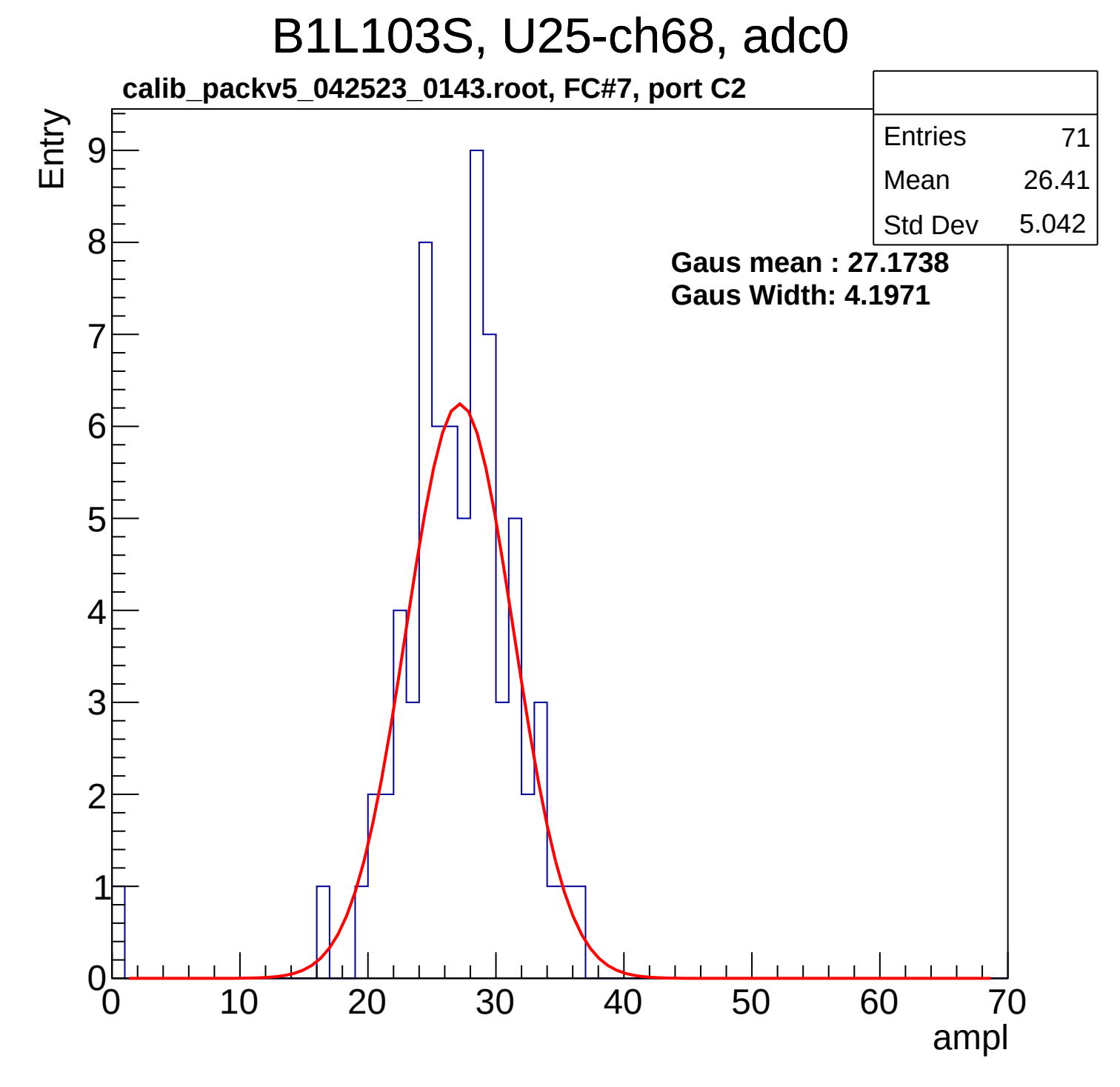
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch68, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 26.41
Std Dev 5.042
Gaus mean : 27.1738
Gaus Width: 4.1971
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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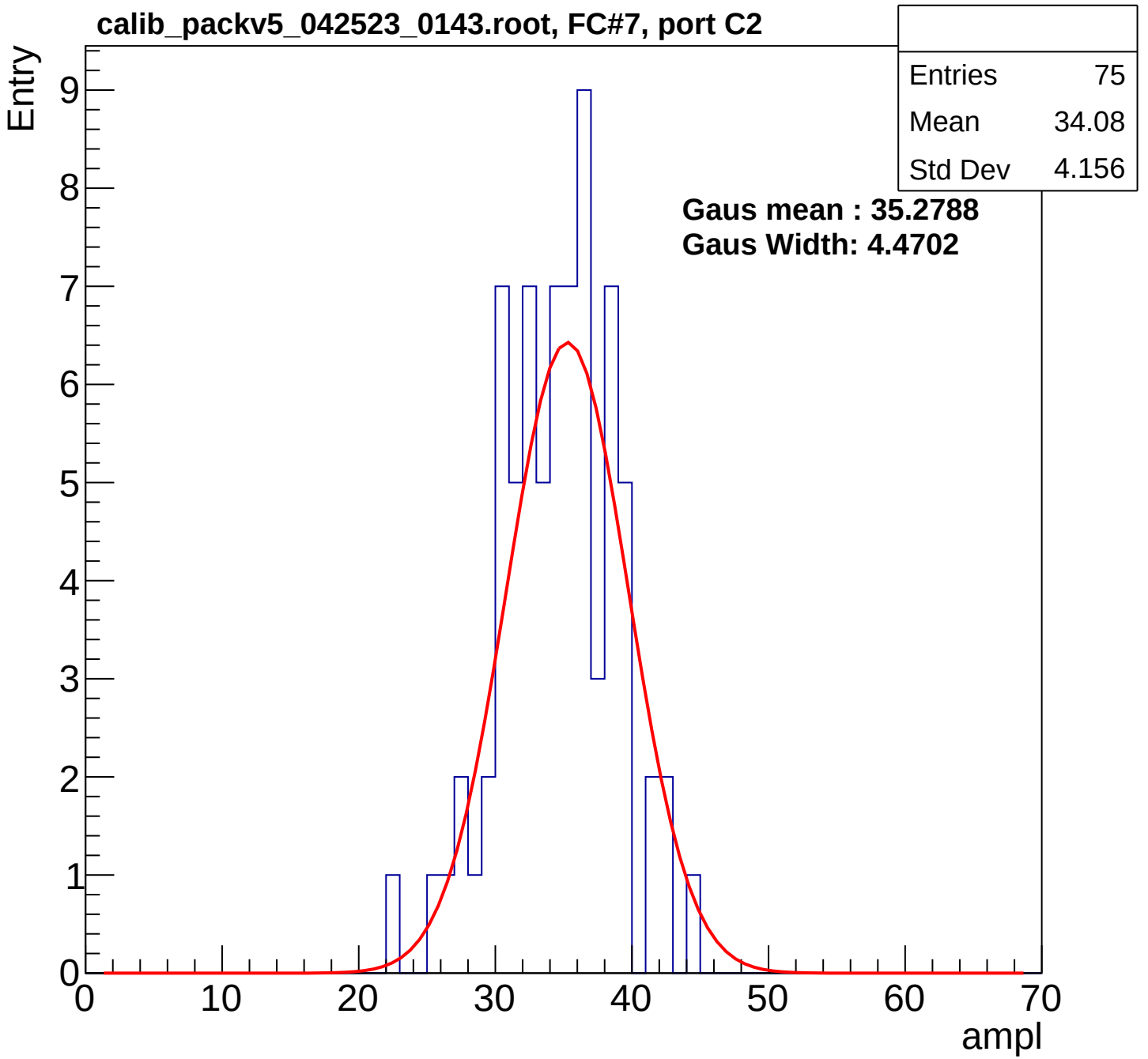
60
70


B1L103S, U25-ch68, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 34.08
Std Dev 4.156
Gaus mean : 35.2788
Gaus Width: 4.4702
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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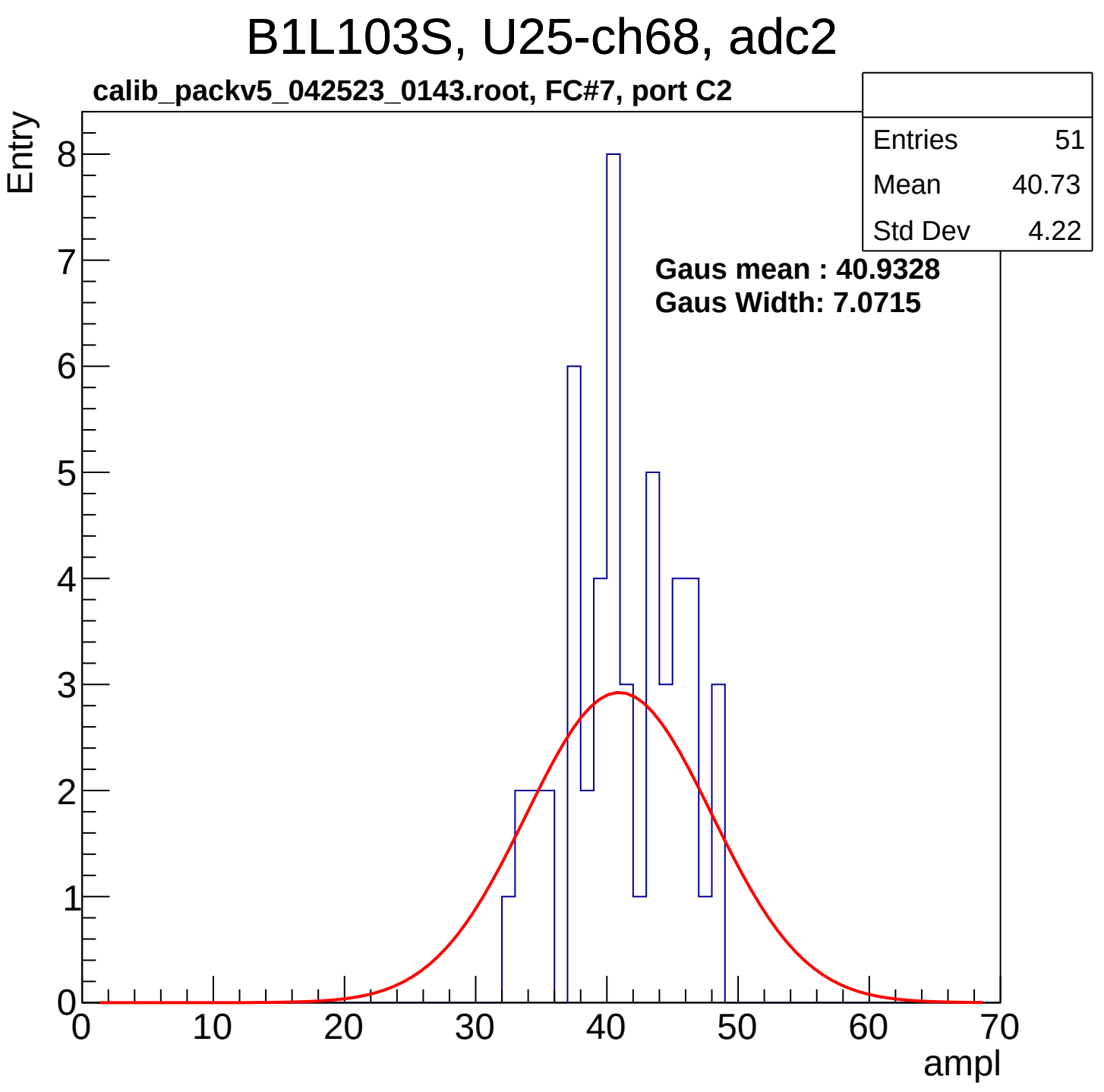
B1L103S, U25-ch68, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 40.73
Std Dev 4.22
Gaus mean : 40.9328
Gaus Width: 7.0715
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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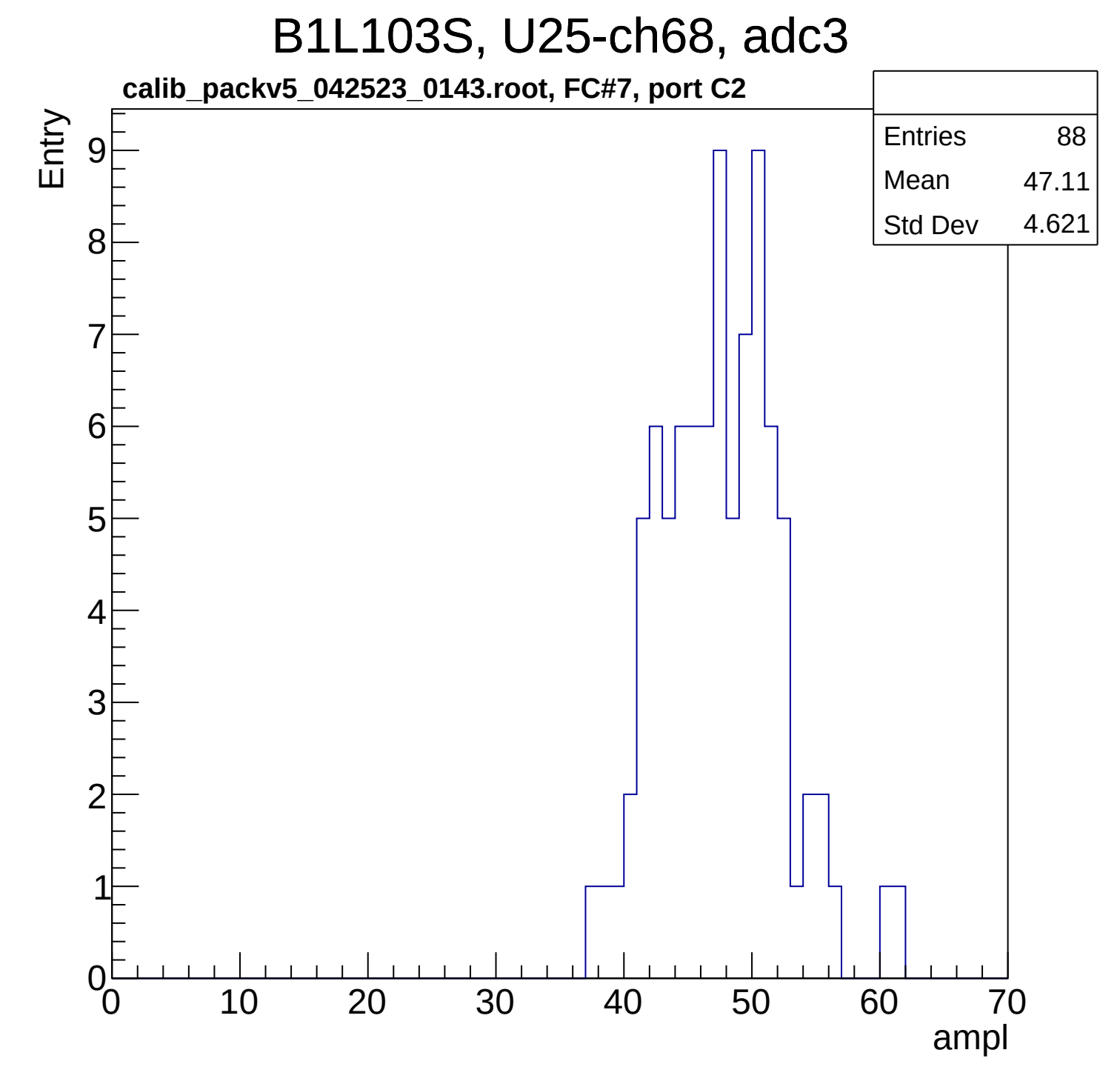
B1L103S, U25-ch68, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 47.11
Std Dev 4.621
Entry
ampl
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4
5
6
7
8
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0
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60
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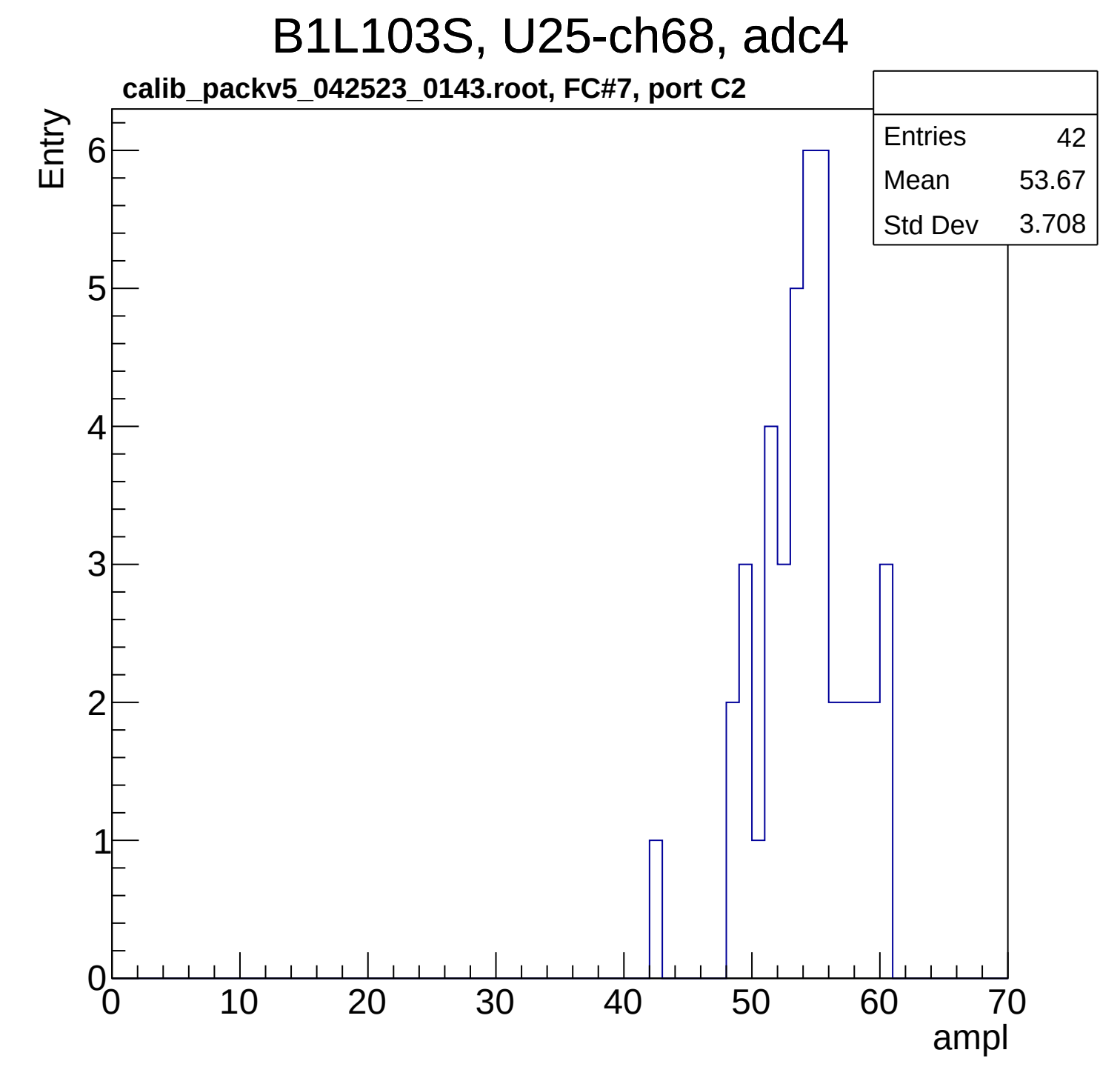
B1L103S, U25-ch68, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 53.67
Std Dev 3.708
Entry
ampl
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3
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0
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60
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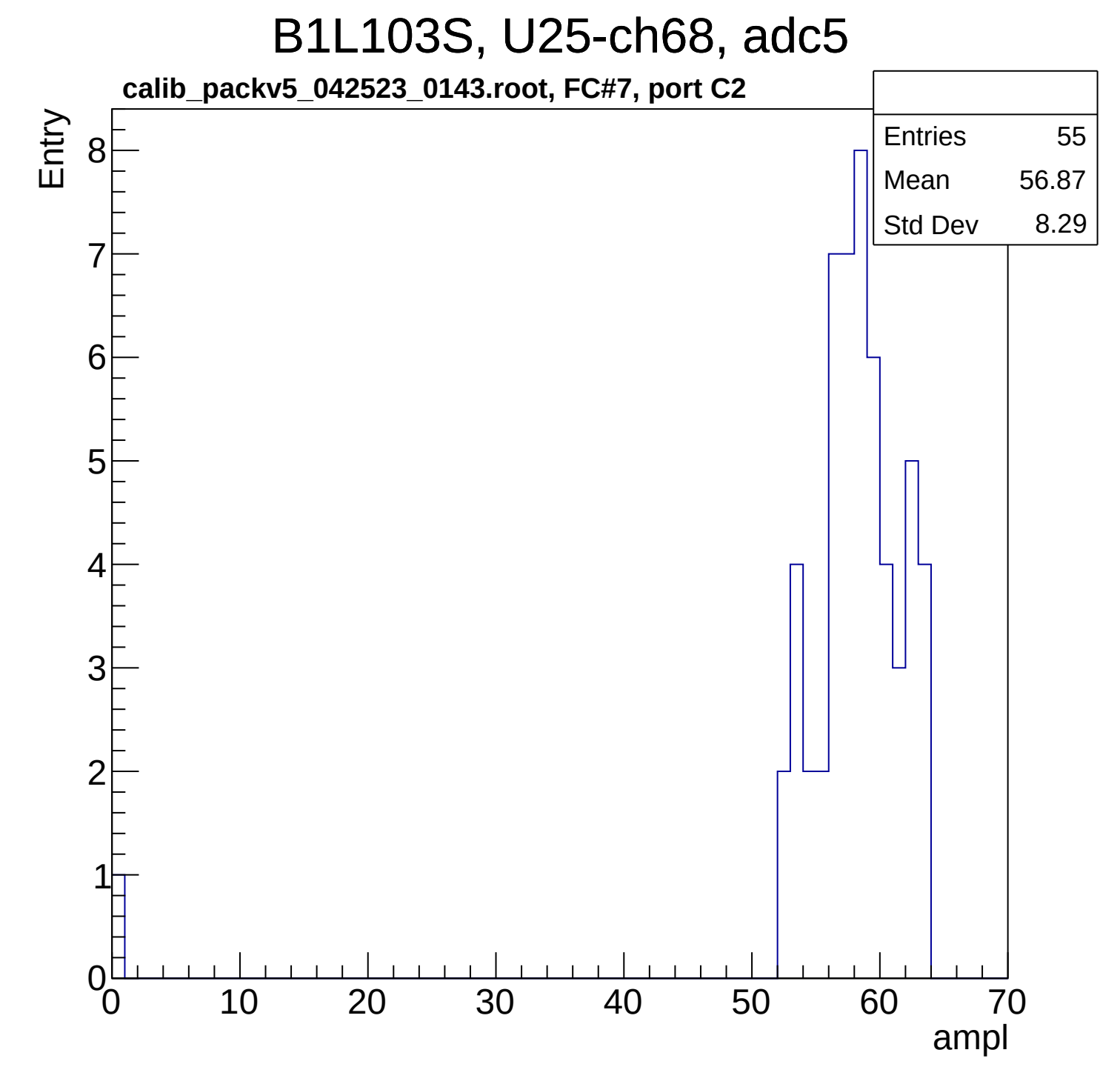
B1L103S, U25-ch68, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 56.87
Std Dev 8.29
Entry
ampl
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4
5
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0
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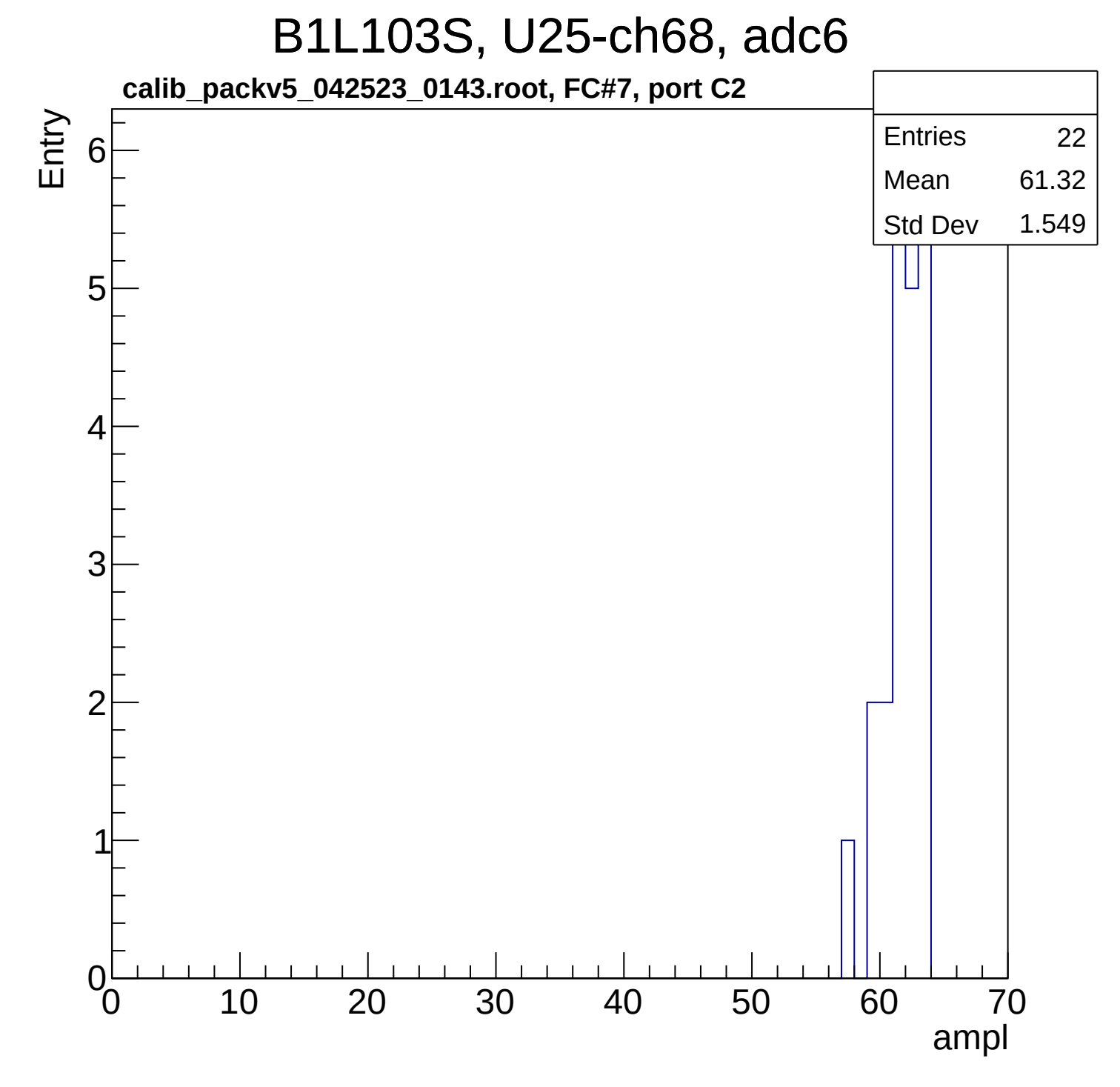
B1L103S, U25-ch68, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 61.32
Std Dev 1.549
Entry
ampl
0
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2
3
4
5
6
0
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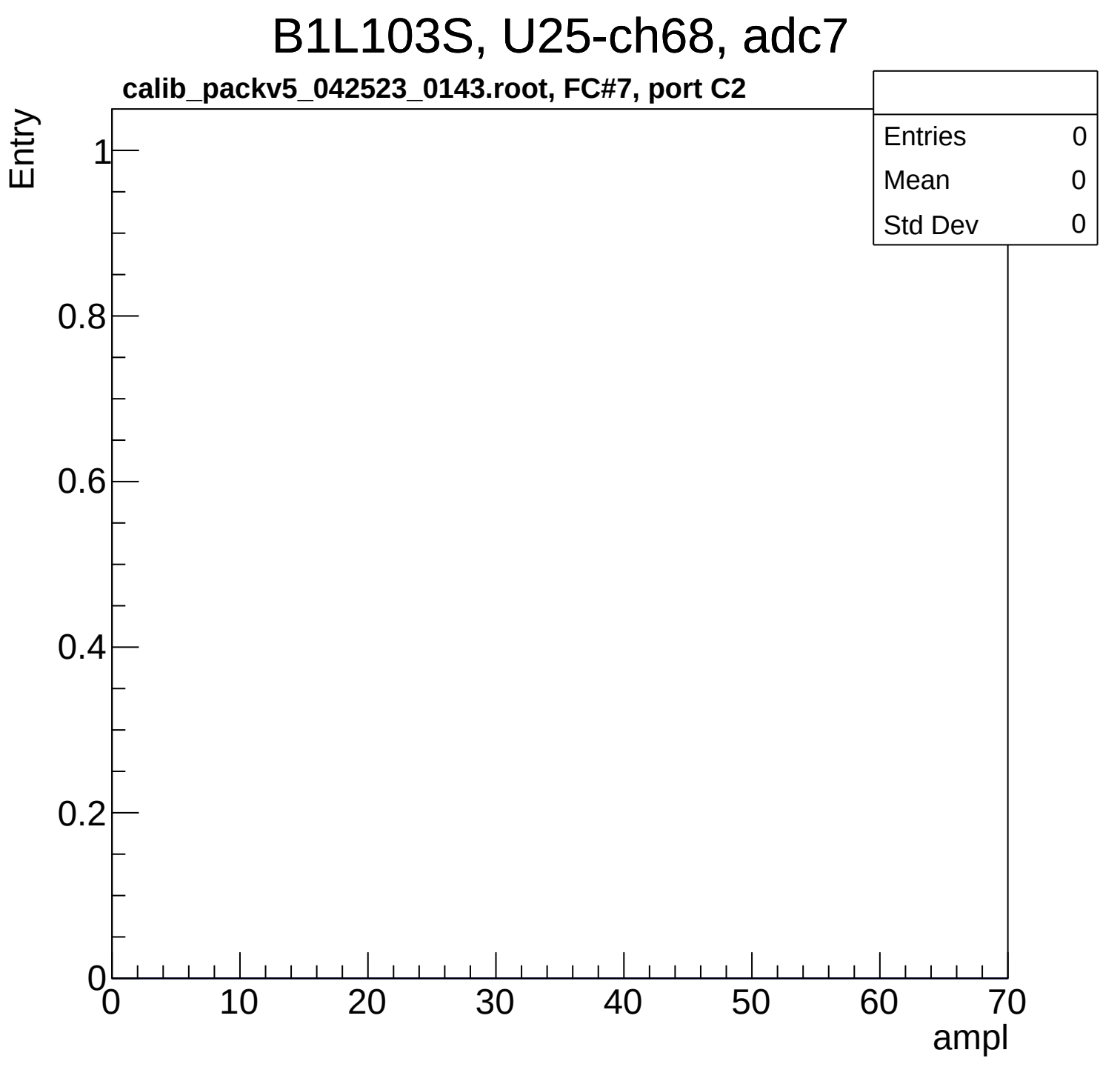
20
30
40
50
60
70


B1L103S, U25-ch68, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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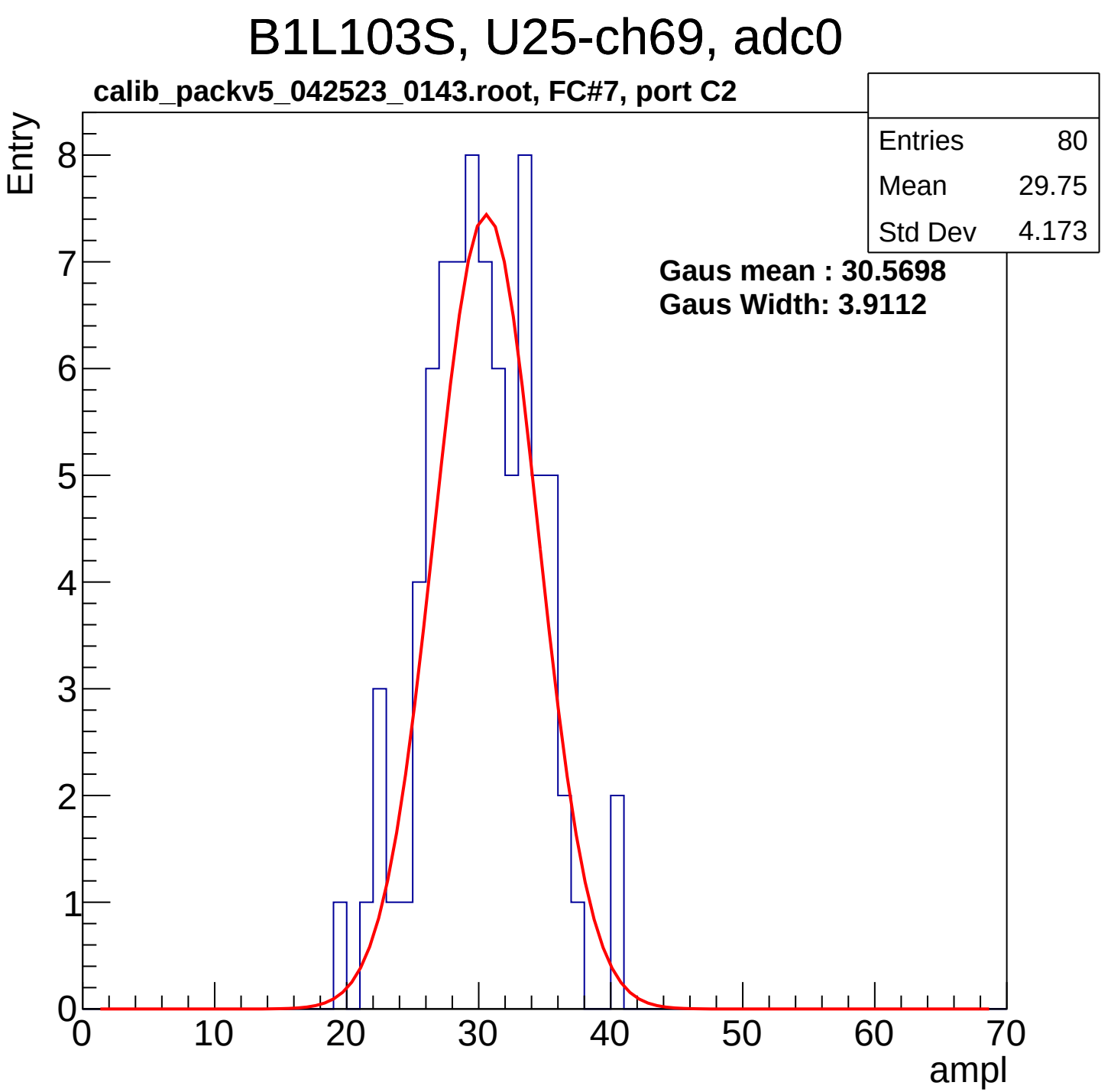
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch69, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 29.75
Std Dev 4.173
Gaus mean : 30.5698
Gaus Width: 3.9112
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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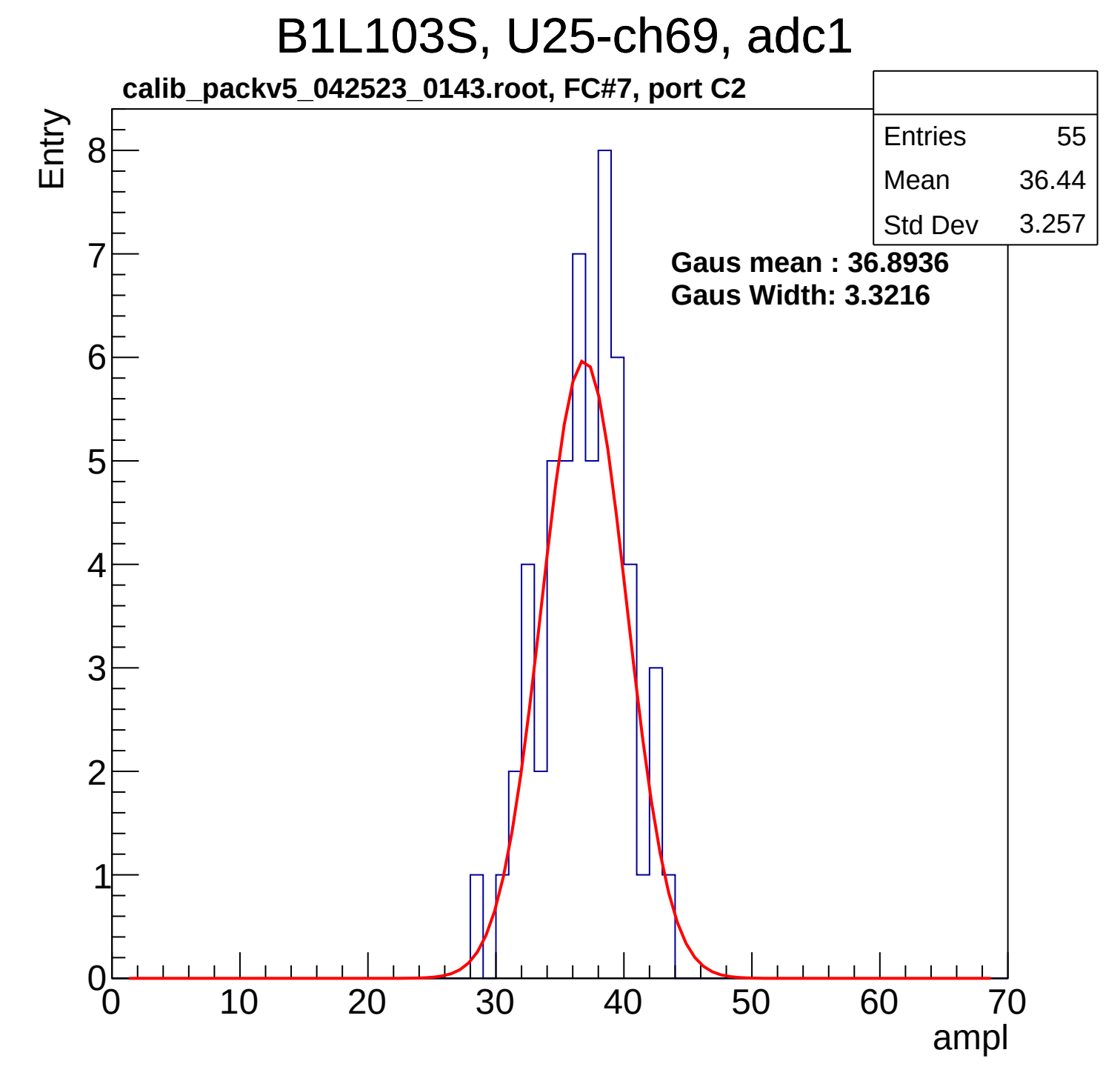
B1L103S, U25-ch69, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 36.44
Std Dev 3.257
Gaus mean : 36.8936
Gaus Width: 3.3216
Entry
ampl
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2
3
4
5
6
7
8
0
10
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50
60
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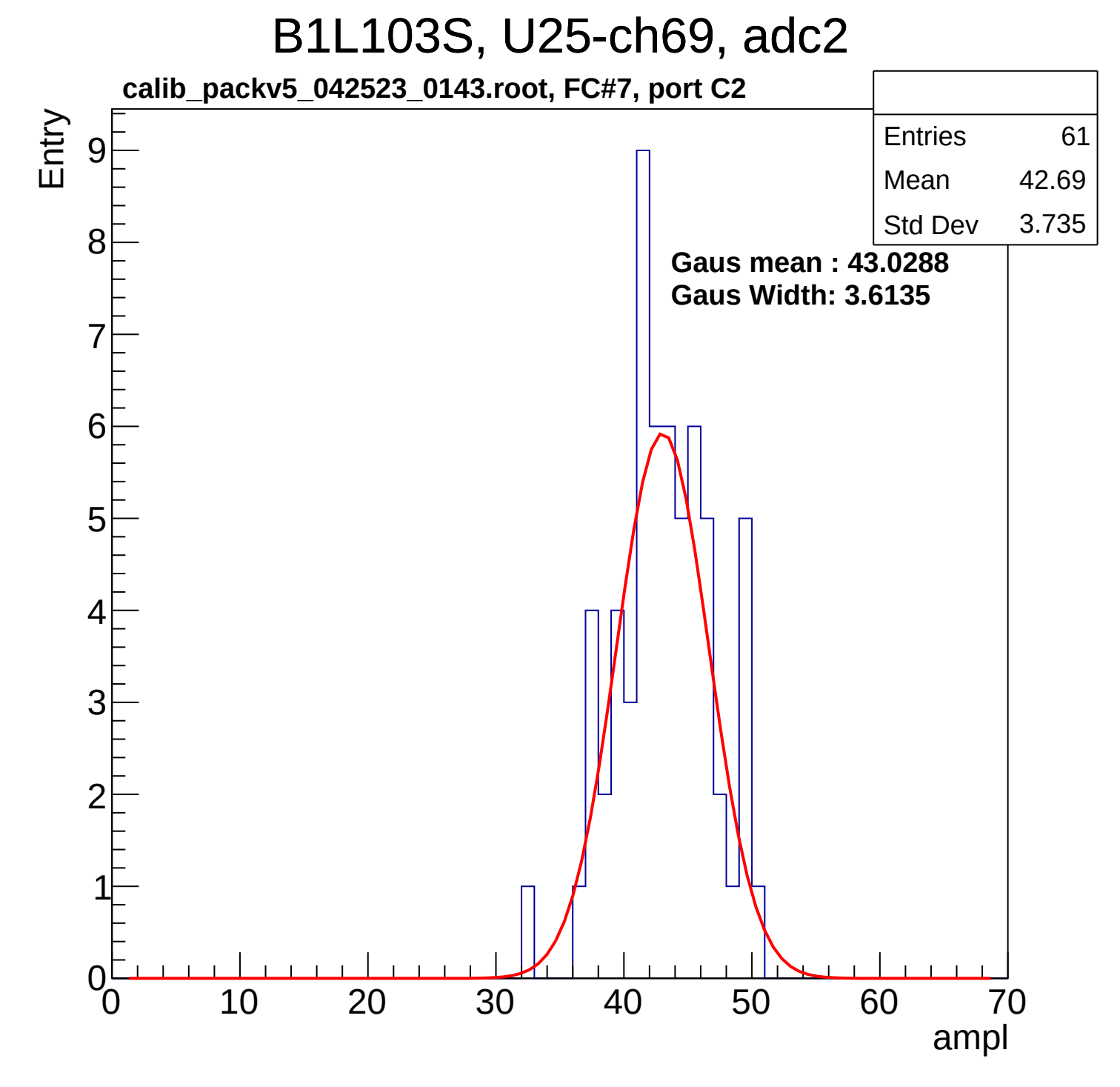
B1L103S, U25-ch69, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.69
Std Dev 3.735
Gaus mean : 43.0288
Gaus Width: 3.6135
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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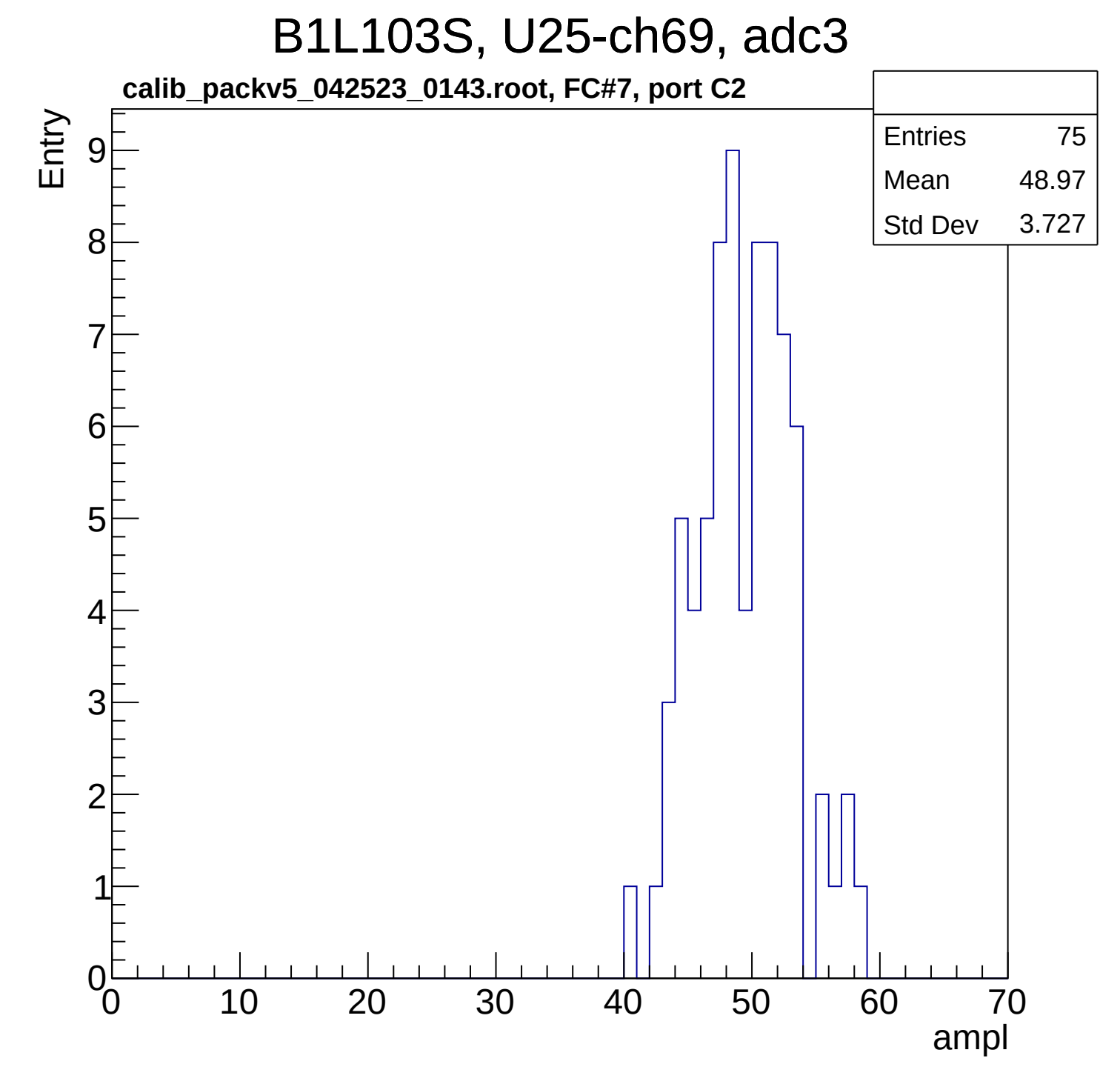
B1L103S, U25-ch69, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 48.97
Std Dev 3.727
Entry
ampl
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3
4
5
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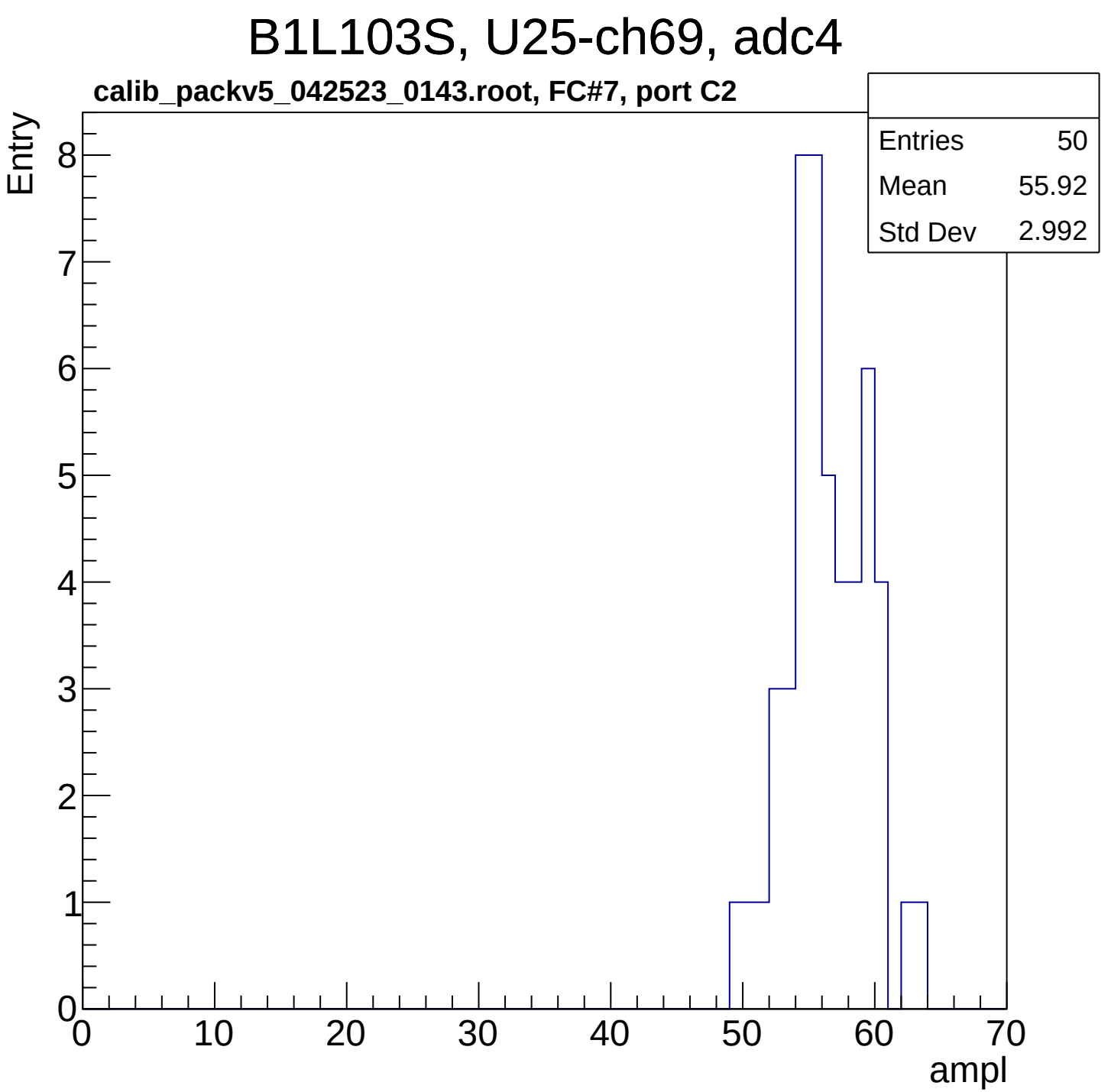
B1L103S, U25-ch69, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 55.92
Std Dev 2.992
Entry
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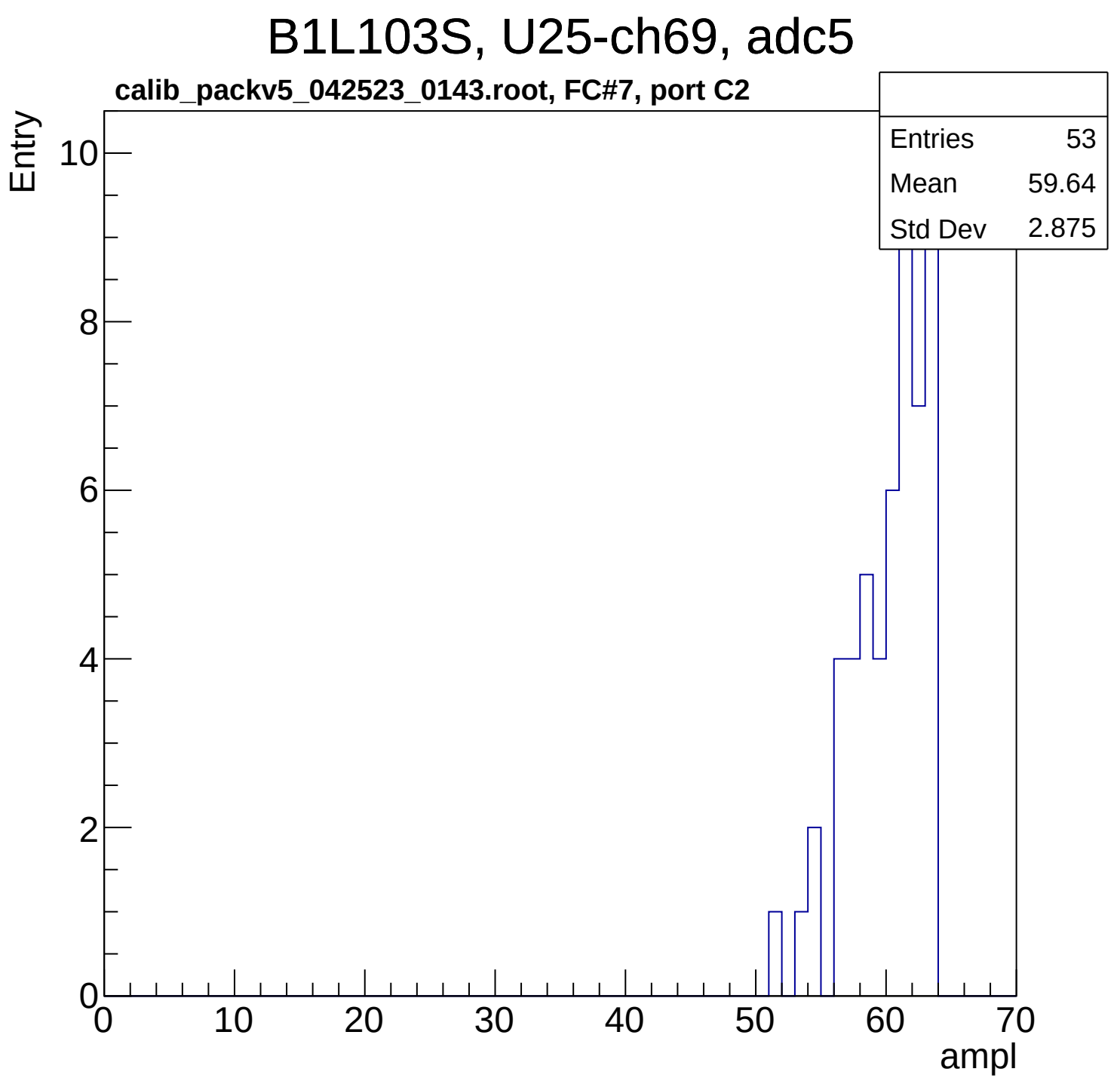
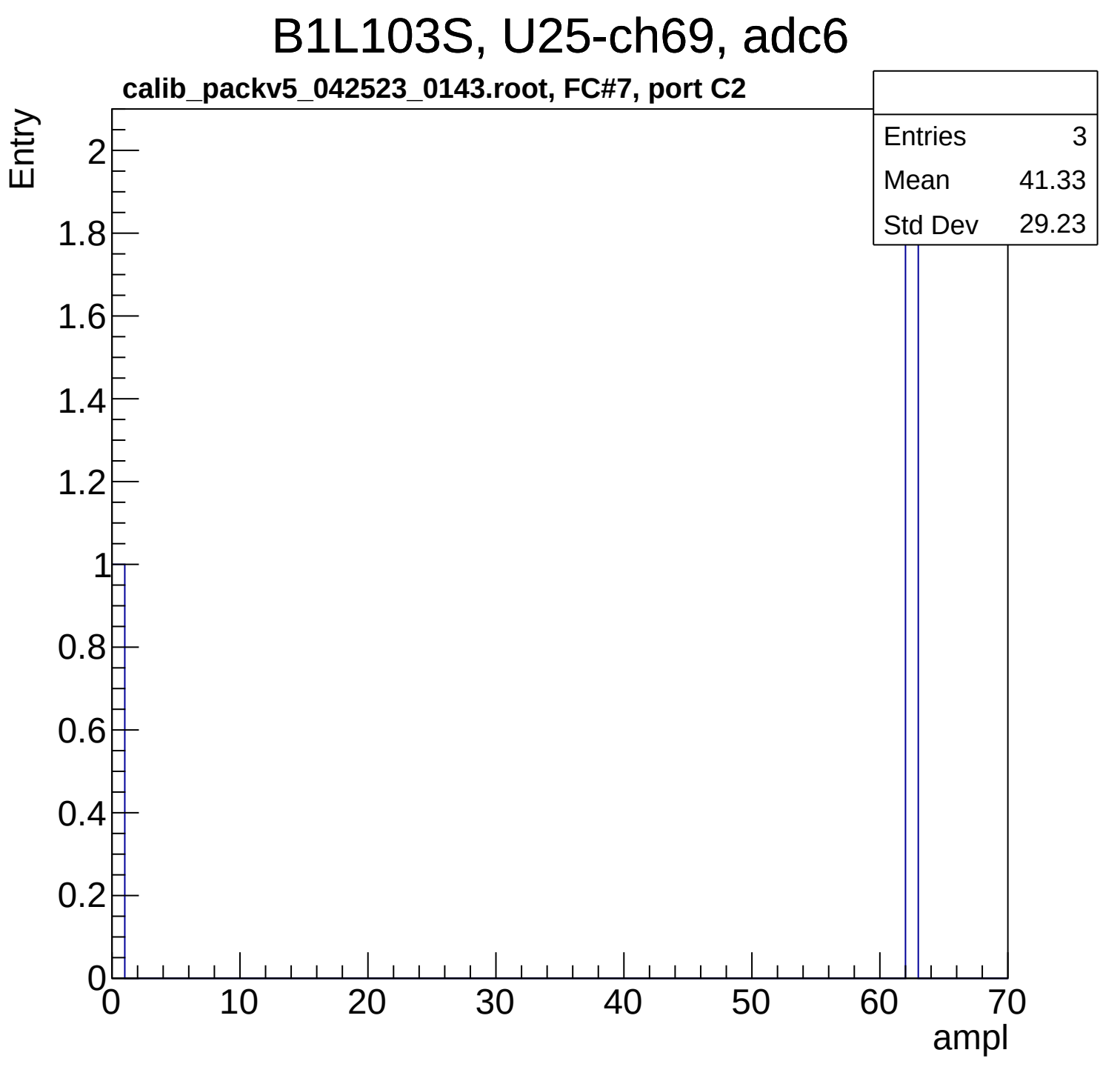
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch69, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 59.64
Std Dev 2.875
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch69, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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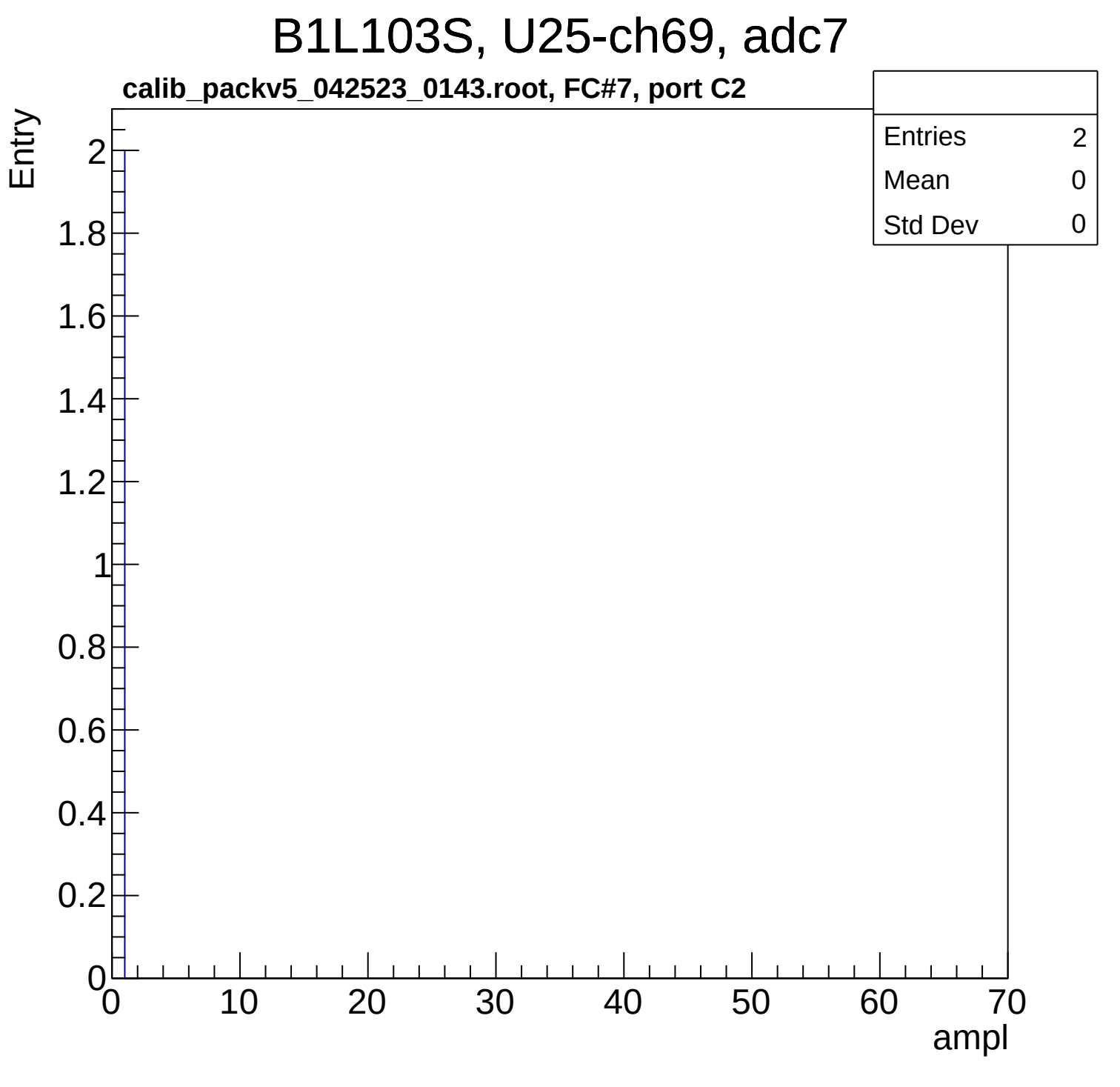
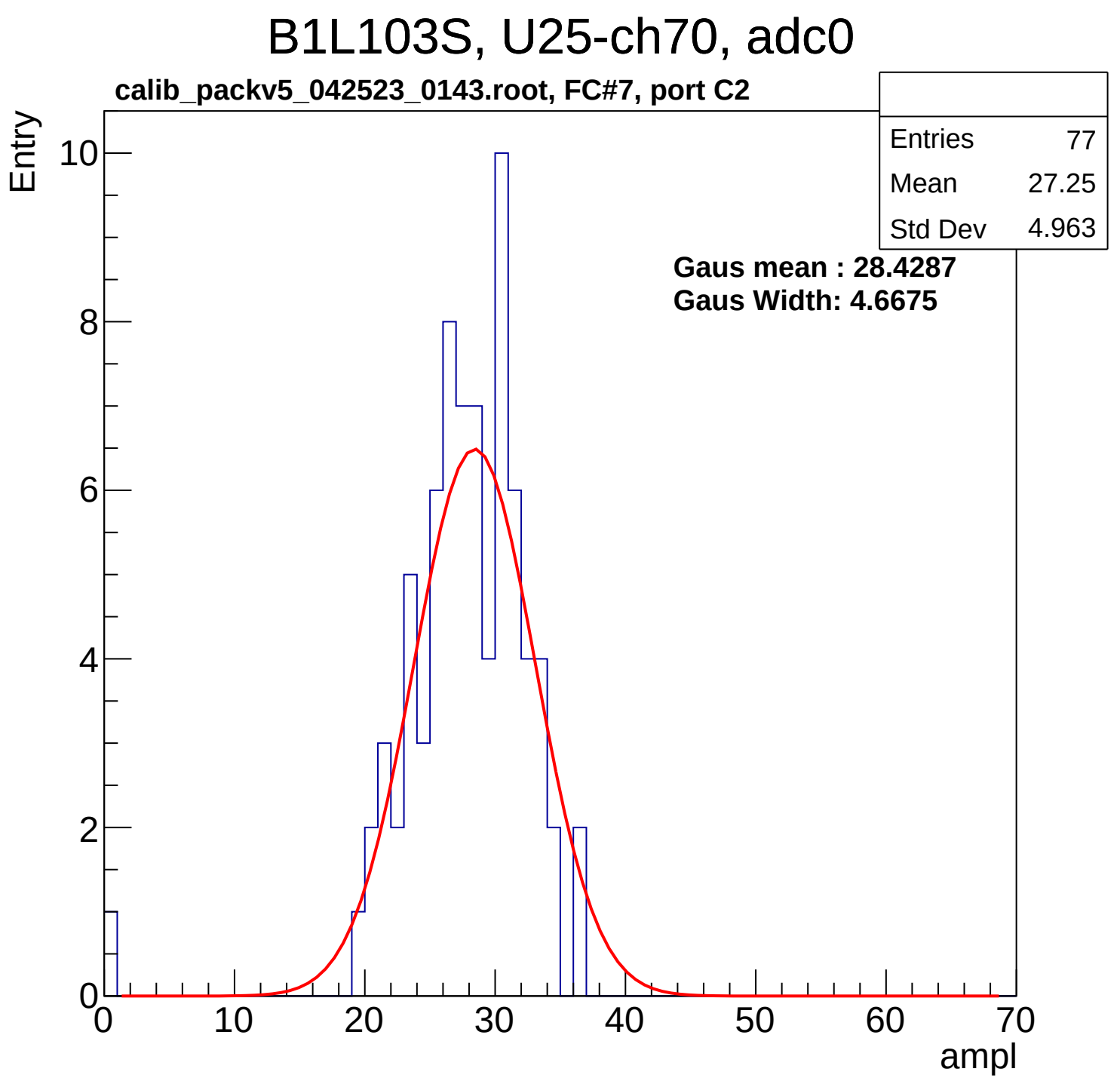
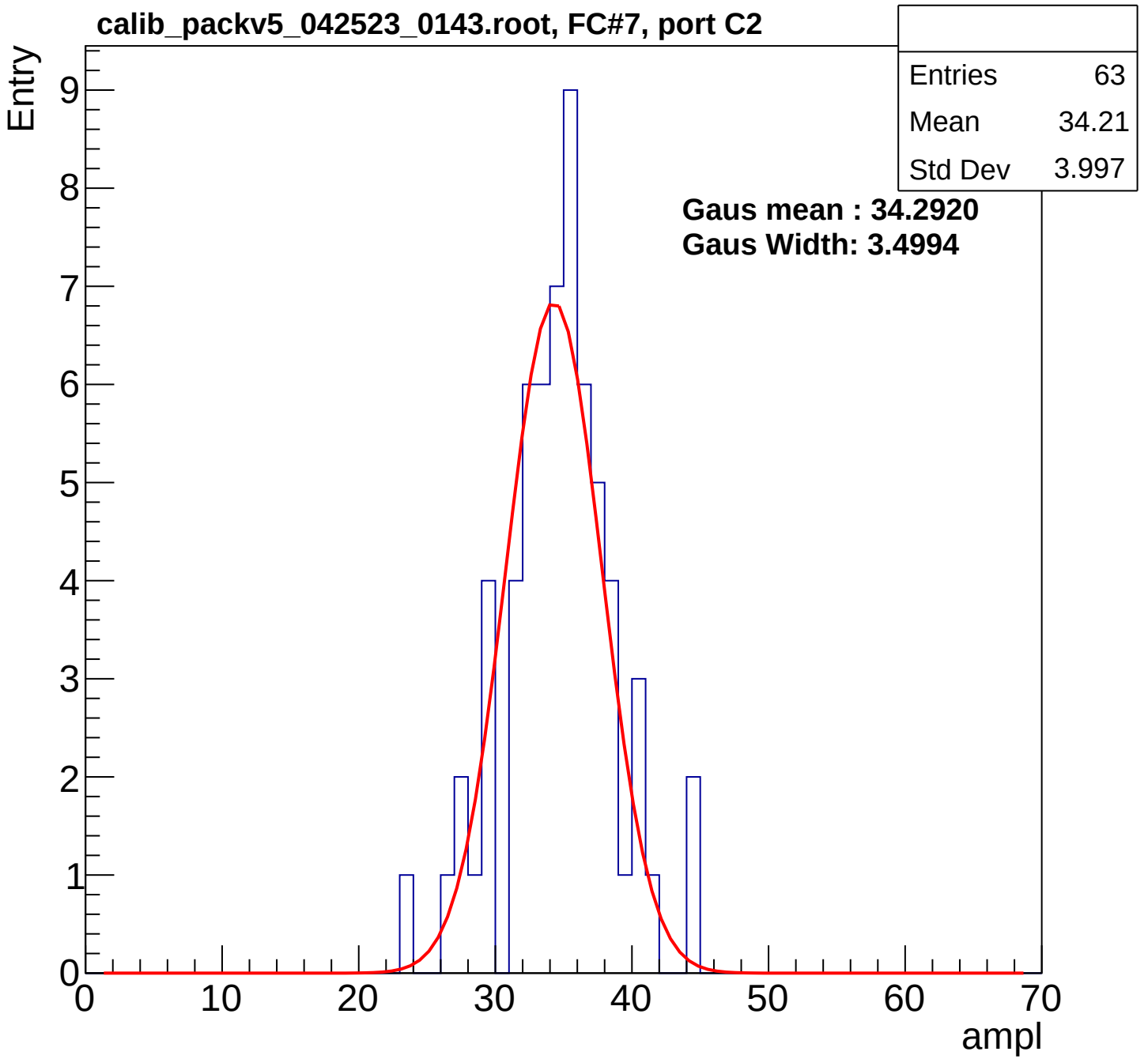

B1L103S, U25-ch69, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch70, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 27.25
Std Dev 4.963
Gaus mean : 28.4287
Gaus Width: 4.6675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch70, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 34.21
Std Dev 3.997
Gaus mean : 34.2920
Gaus Width: 3.4994
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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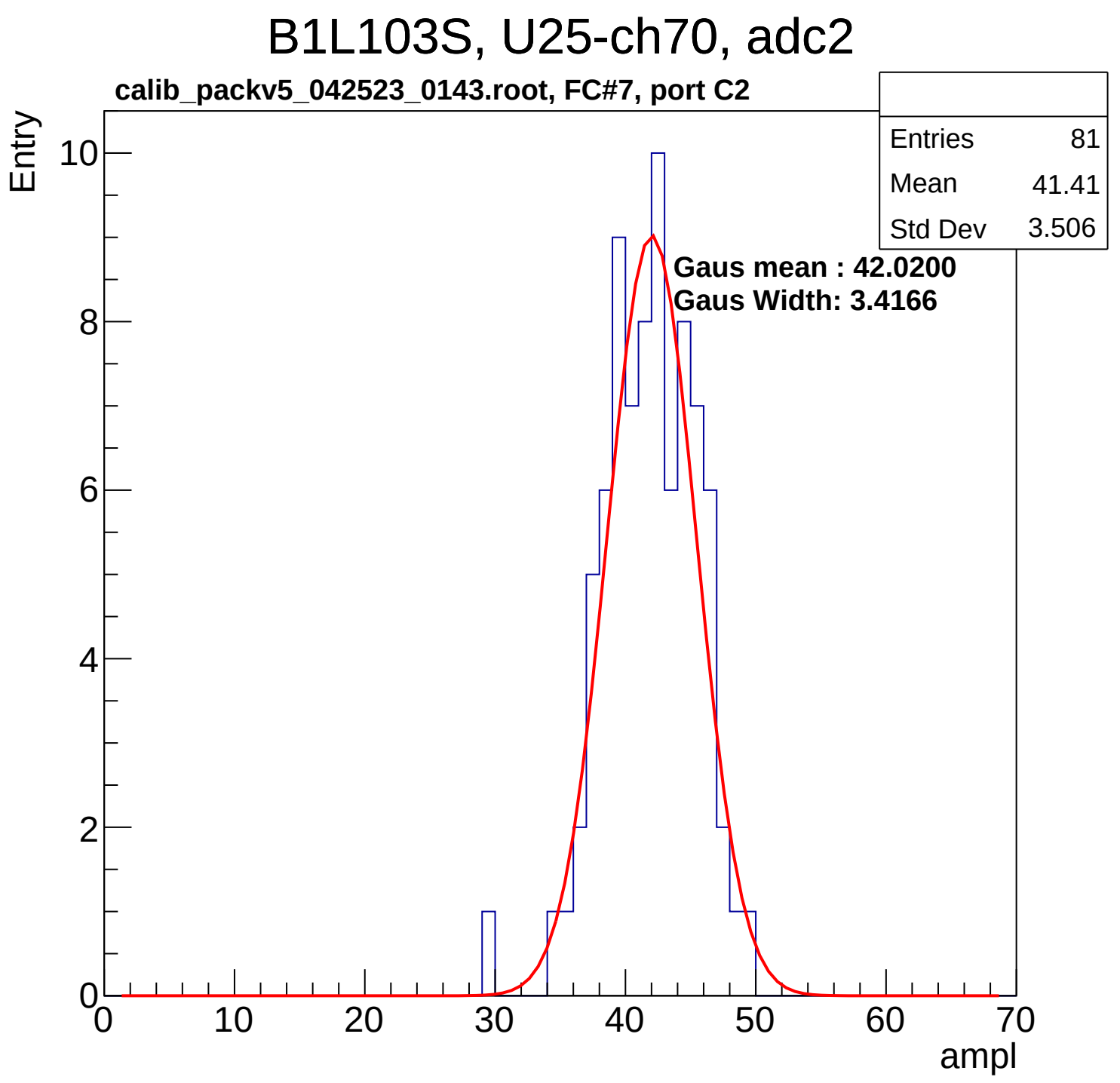
B1L103S, U25-ch70, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 41.41
Std Dev 3.506
Gaus mean : 42.0200
Gaus Width: 3.4166
Entry
ampl
0
2
4
6
8
10
0
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20
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60
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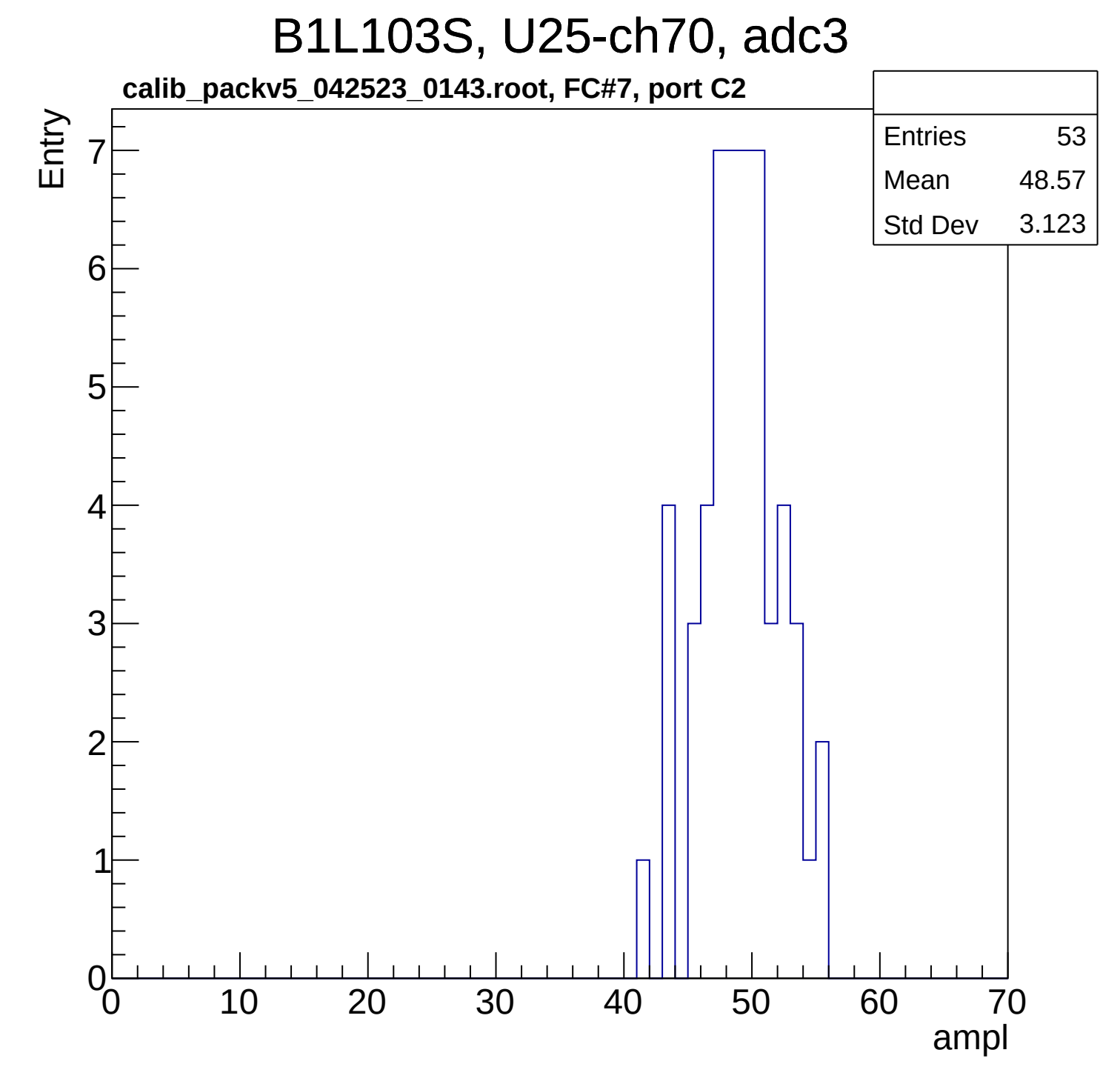
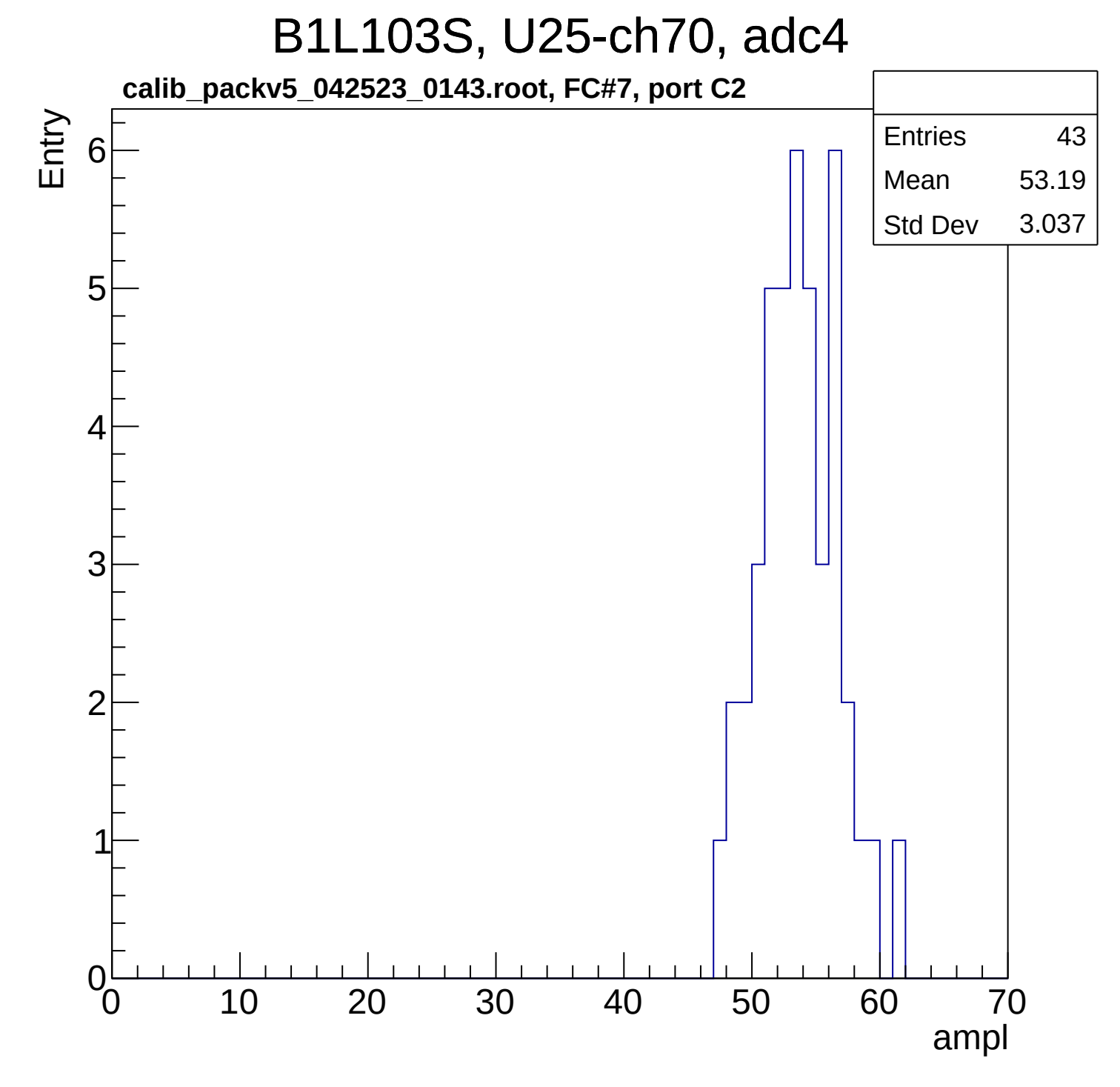
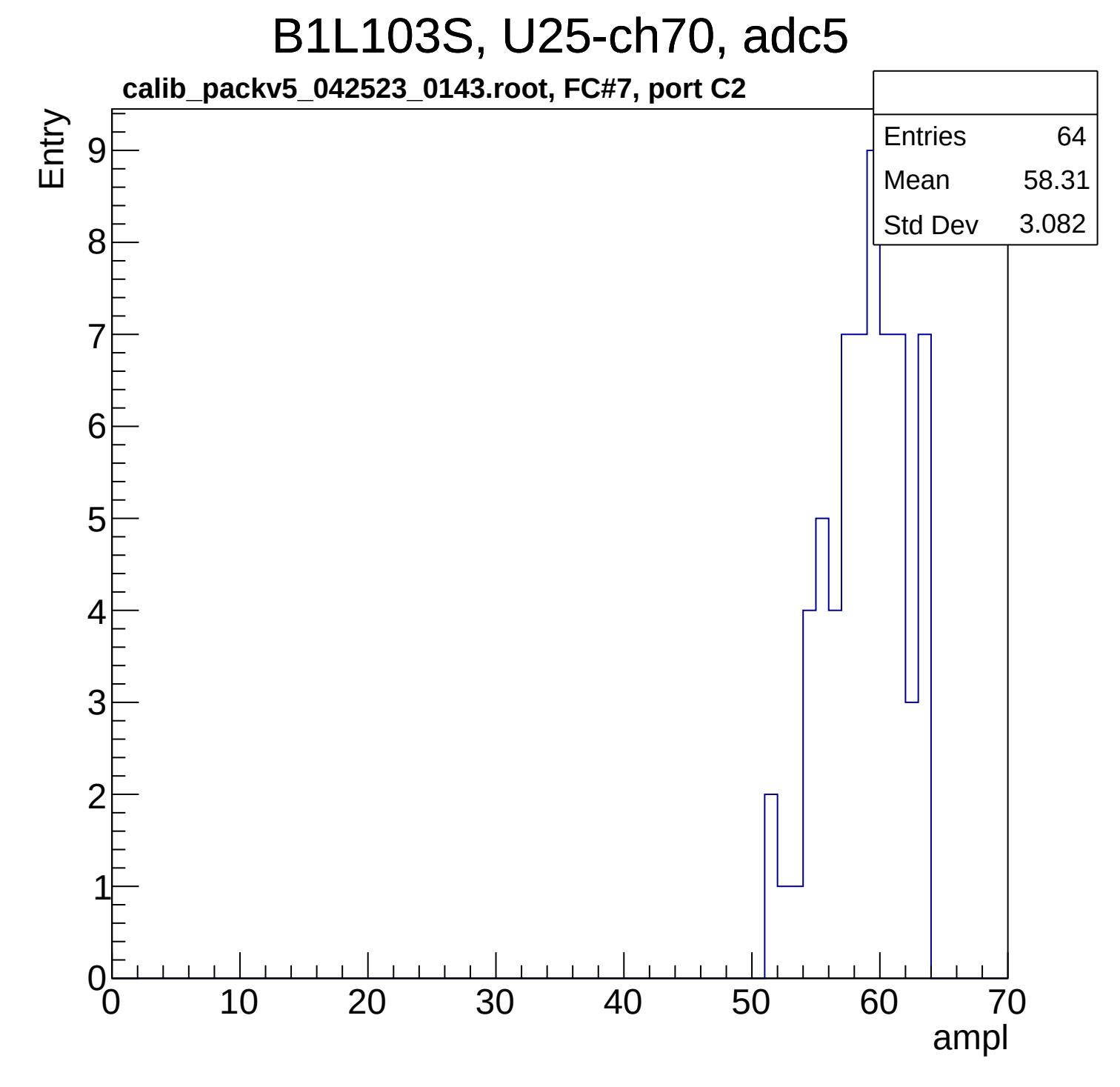

B1L103S, U25-ch70, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 48.57
Std Dev 3.123
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch70, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 53.19
Std Dev 3.037
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch70, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 58.31
Std Dev 3.082
Entry
ampl
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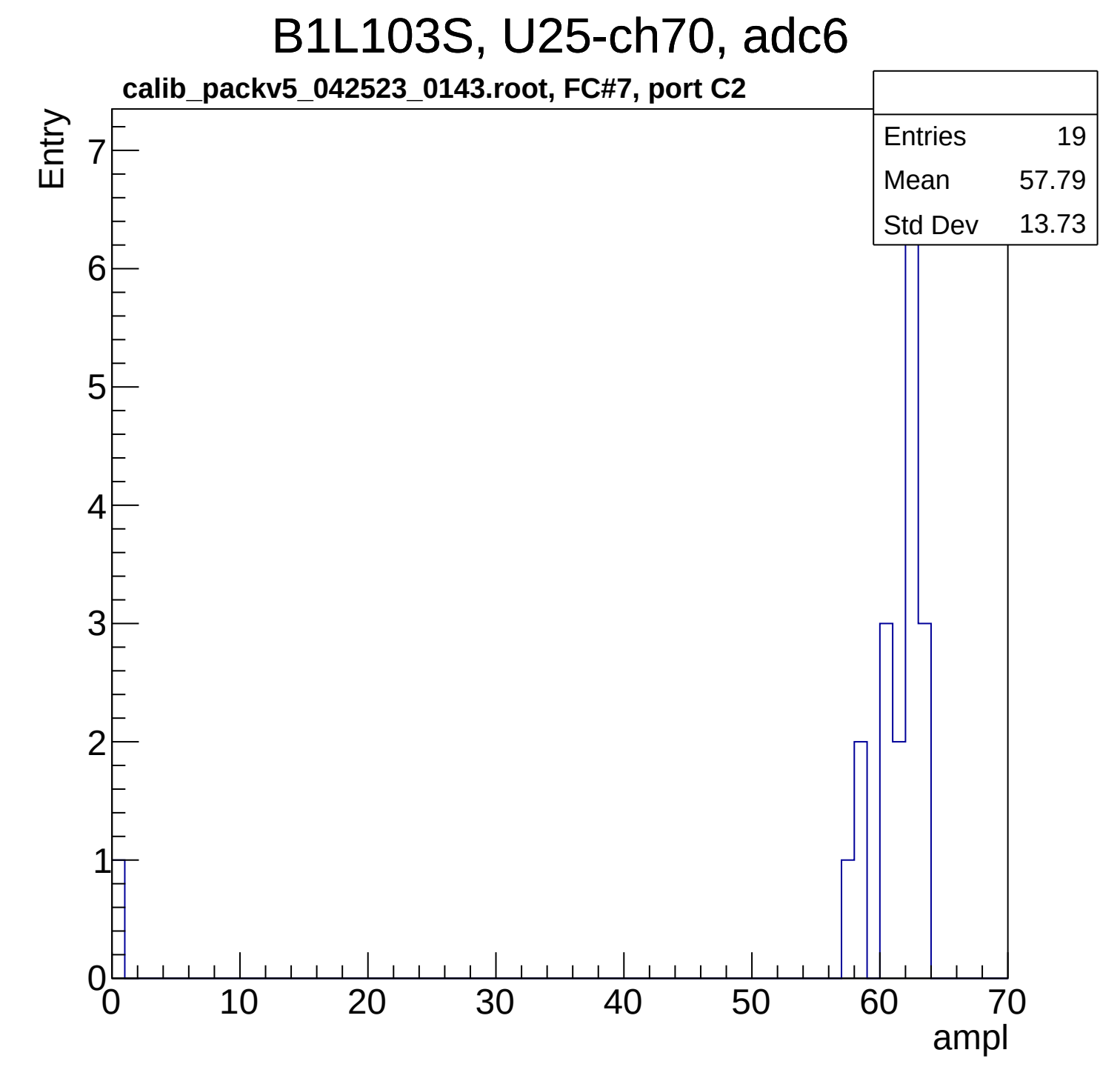
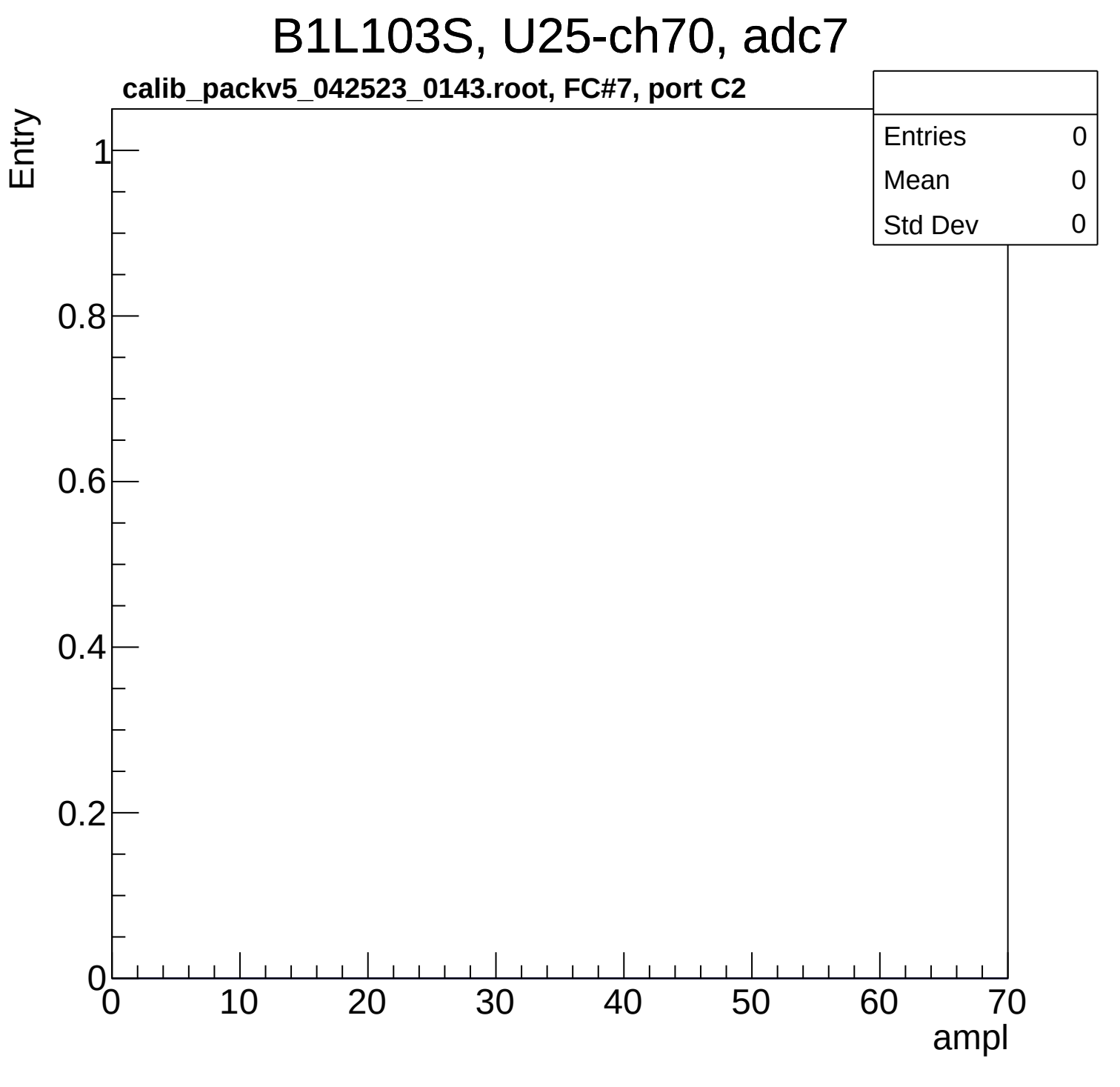
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch70, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 57.79
Std Dev 13.73
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch70, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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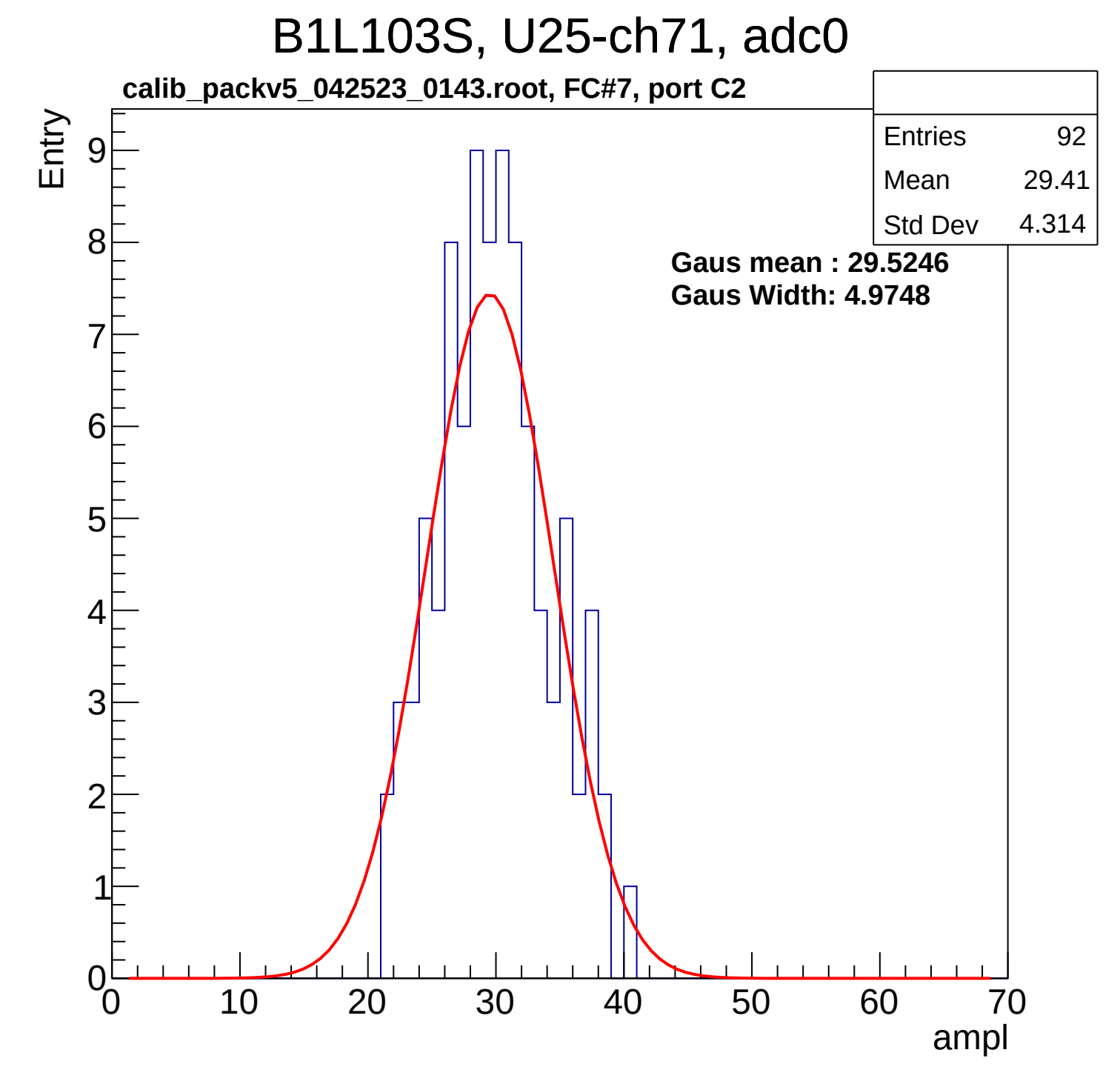
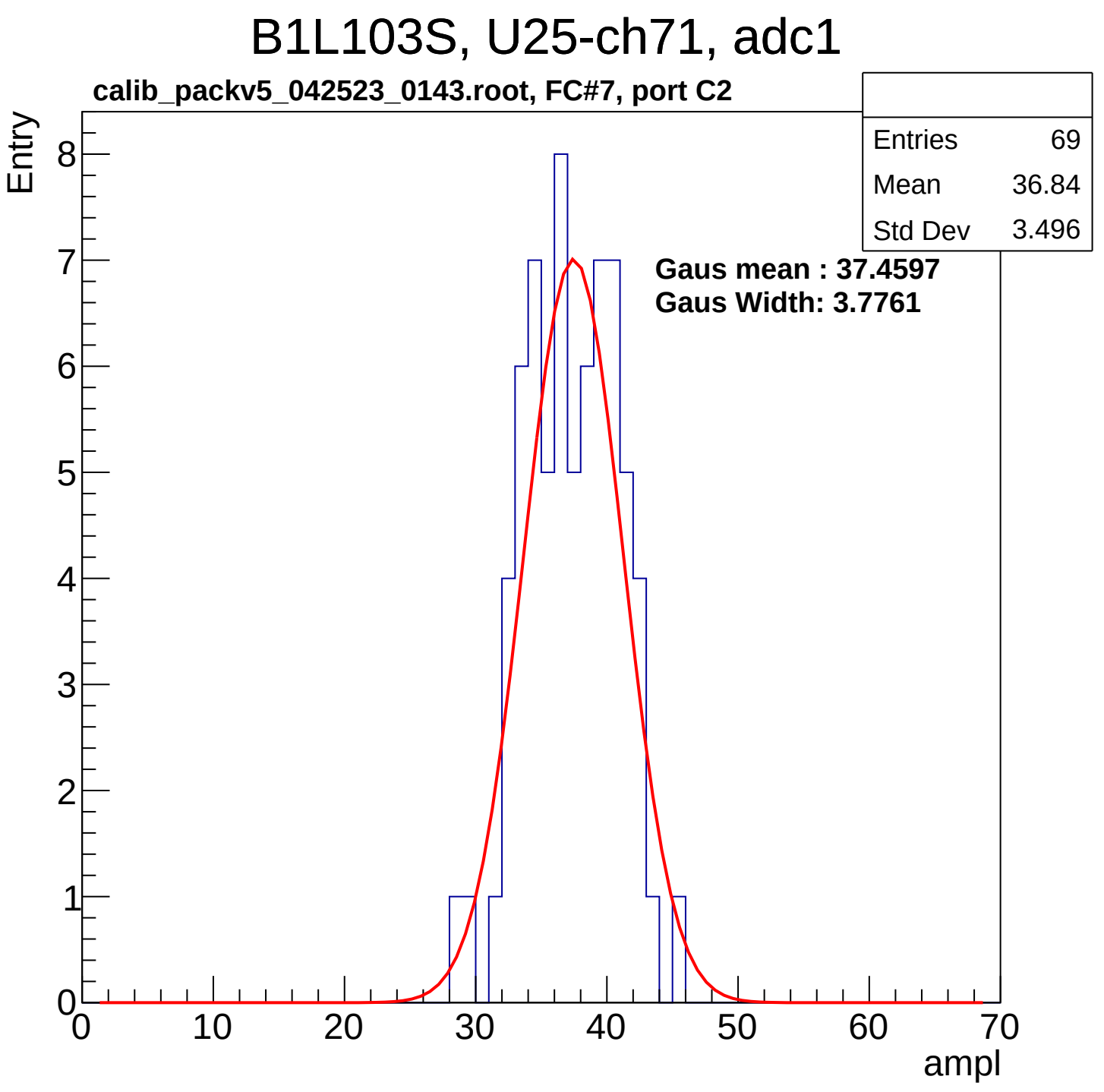
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch71, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 92
Mean 29.41
Std Dev 4.314
Gaus mean : 29.5246
Gaus Width: 4.9748
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch71, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.84
Std Dev 3.496
Gaus mean : 37.4597
Gaus Width: 3.7761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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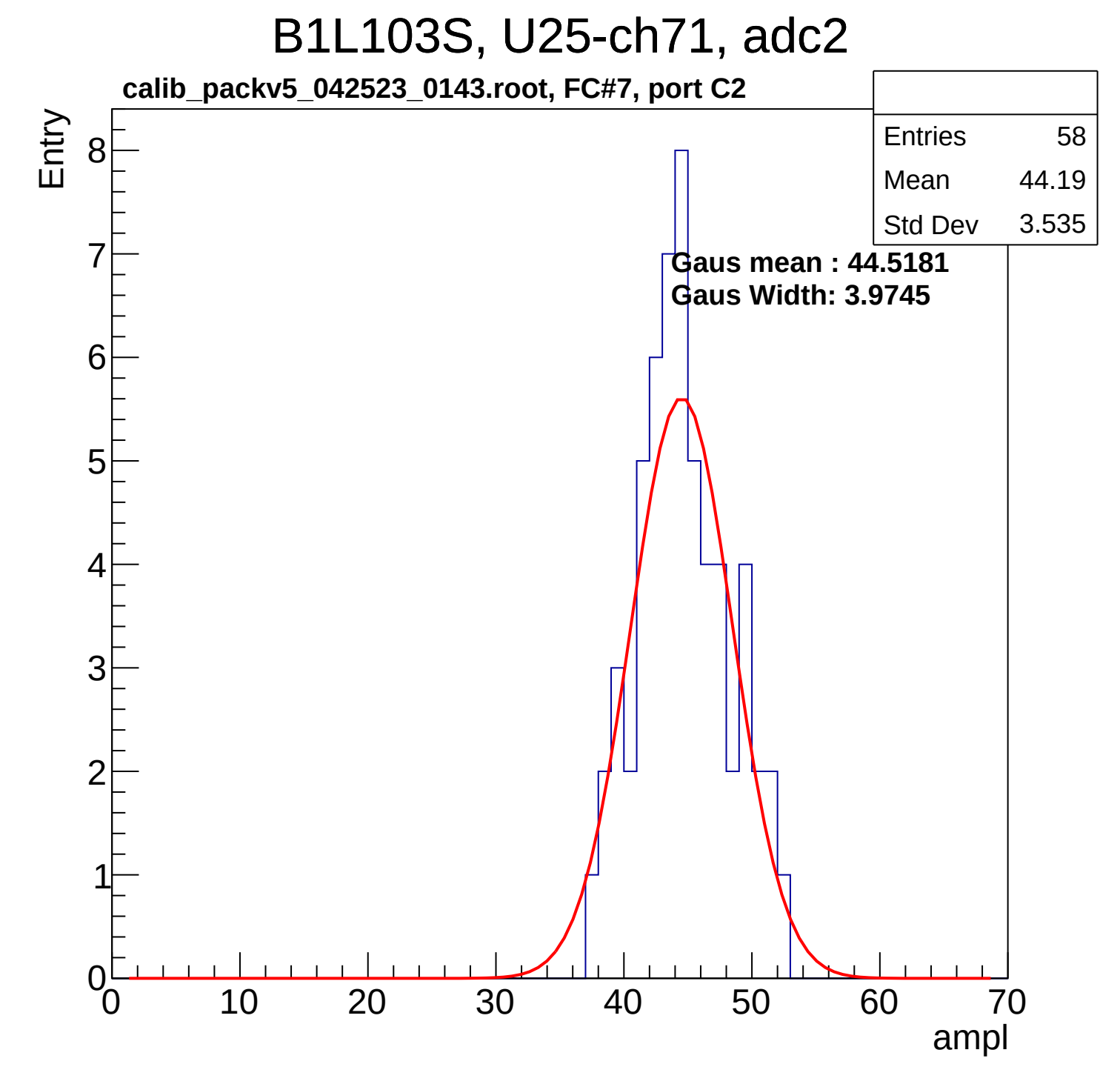
B1L103S, U25-ch71, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 44.19
Std Dev 3.535
Gaus mean : 44.5181
Gaus Width: 3.9745
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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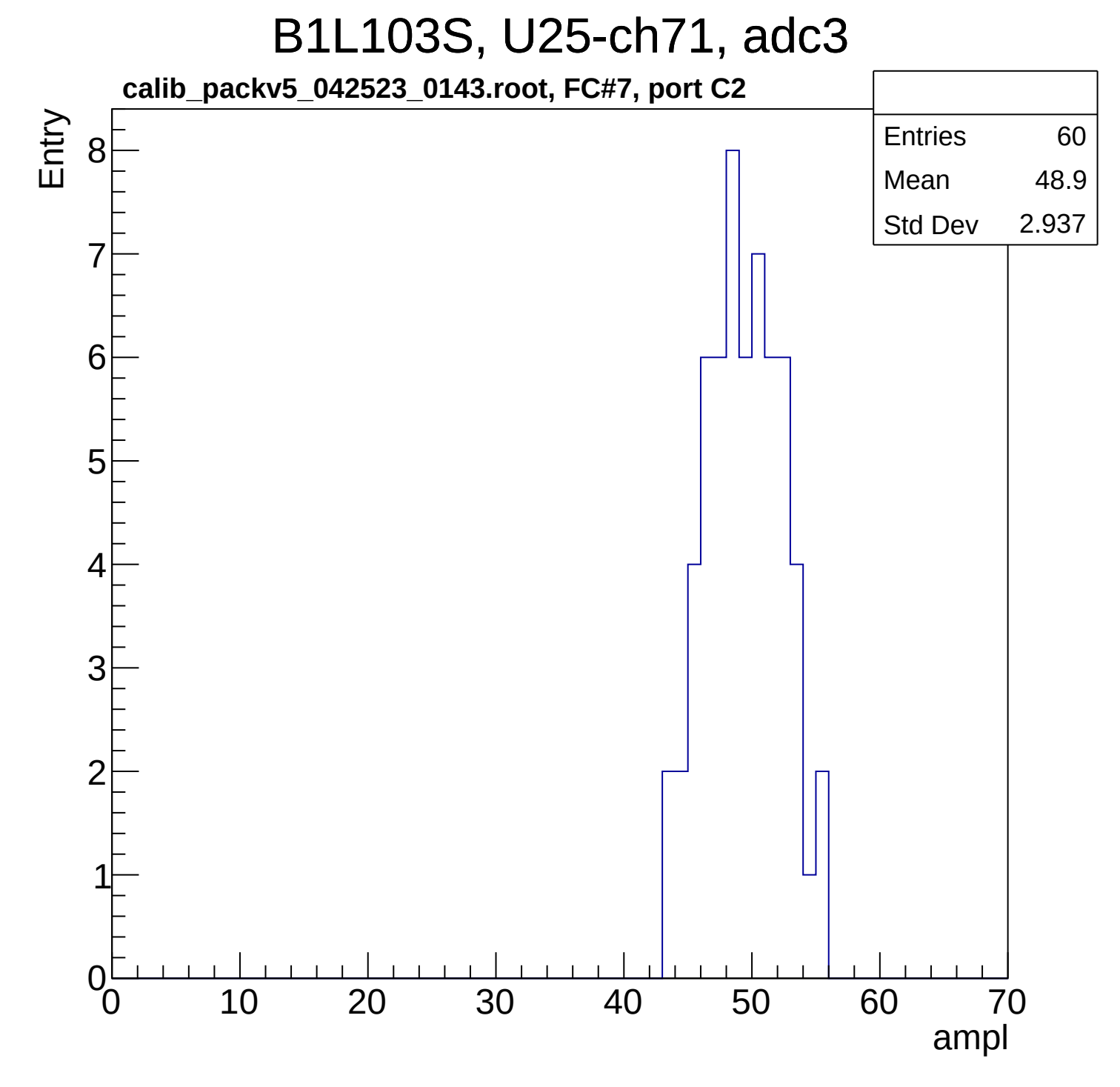
B1L103S, U25-ch71, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.9
Std Dev 2.937
Entry
ampl
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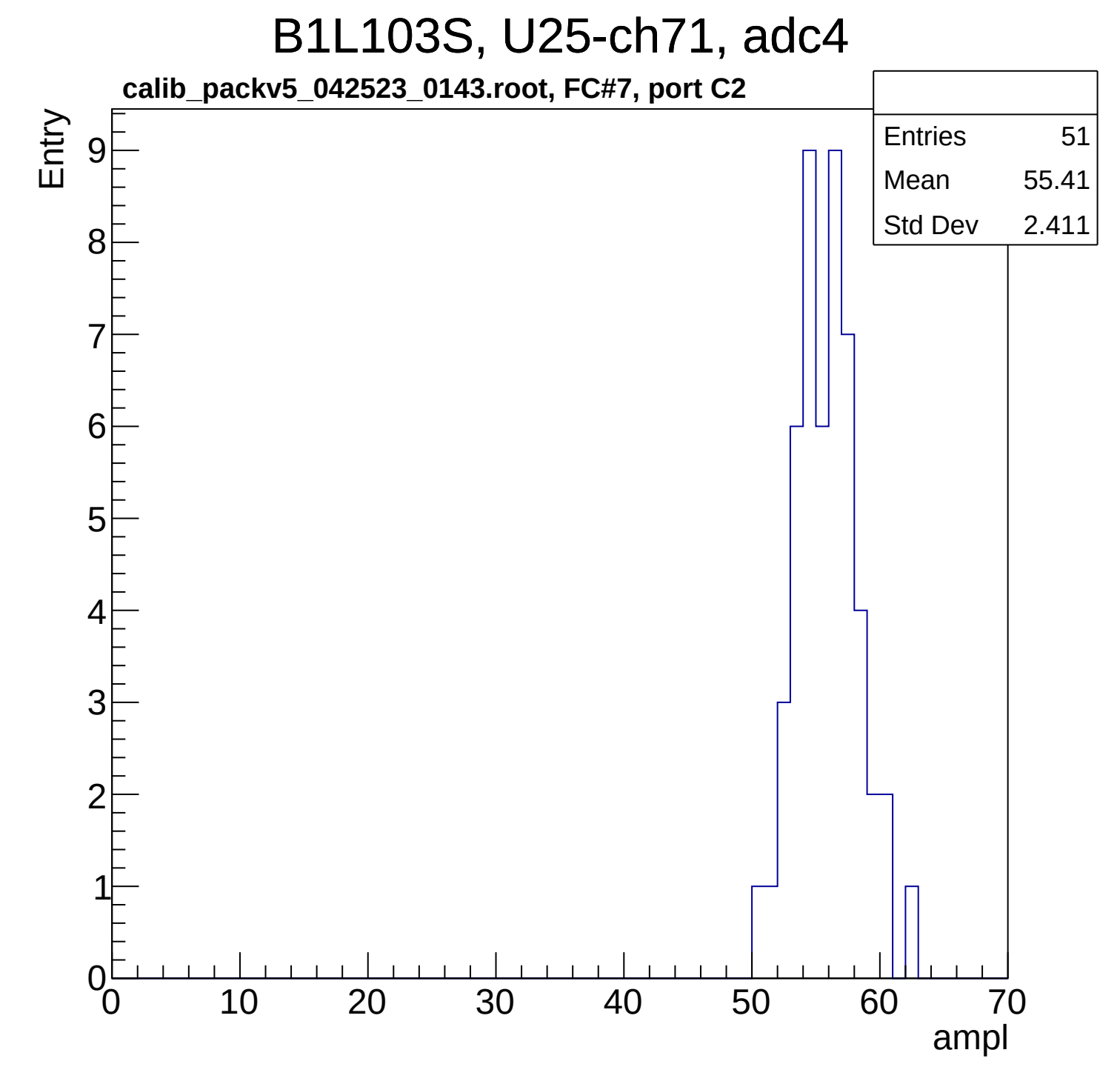
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch71, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.41
Std Dev 2.411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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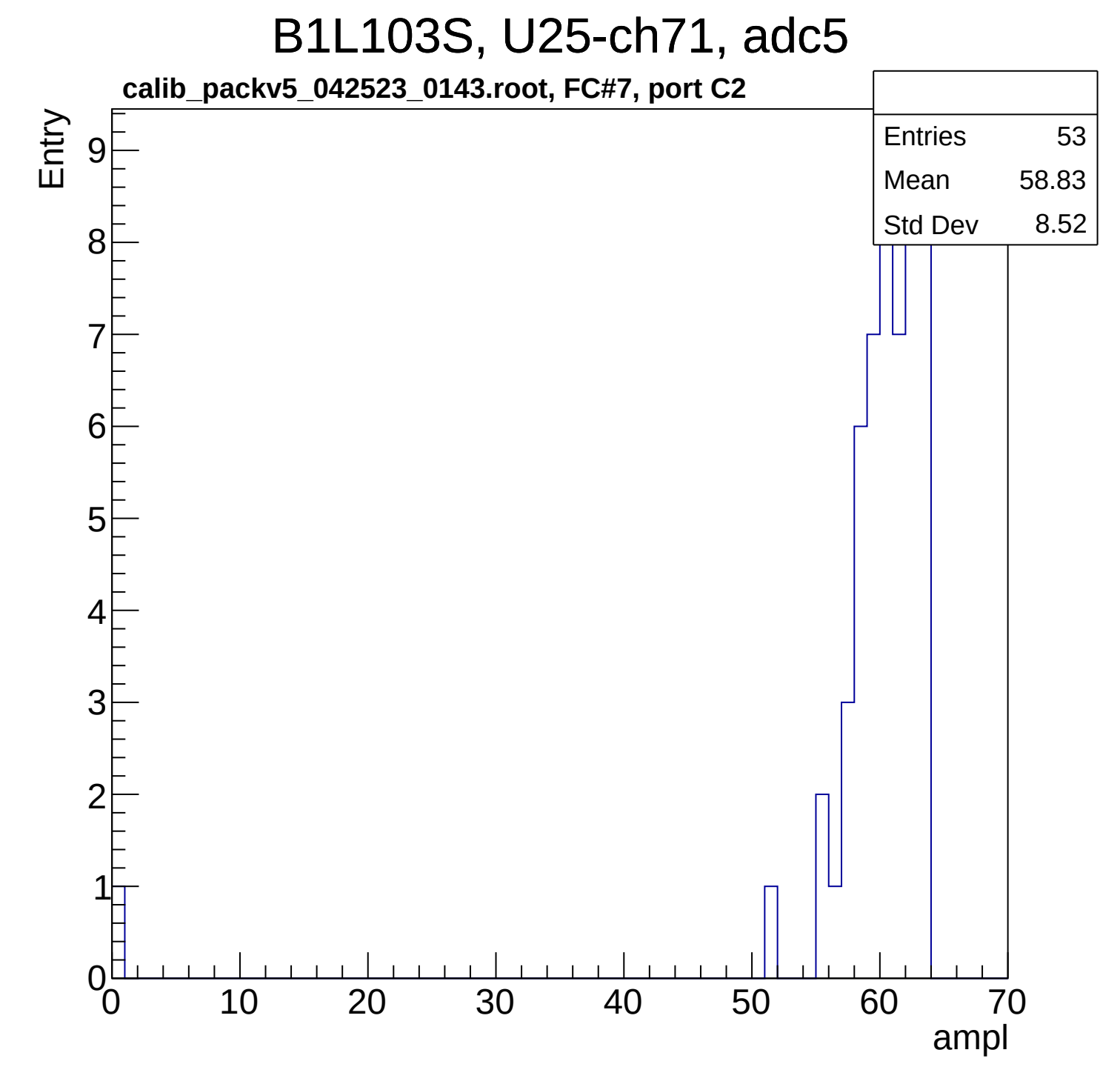
B1L103S, U25-ch71, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 58.83
Std Dev 8.52
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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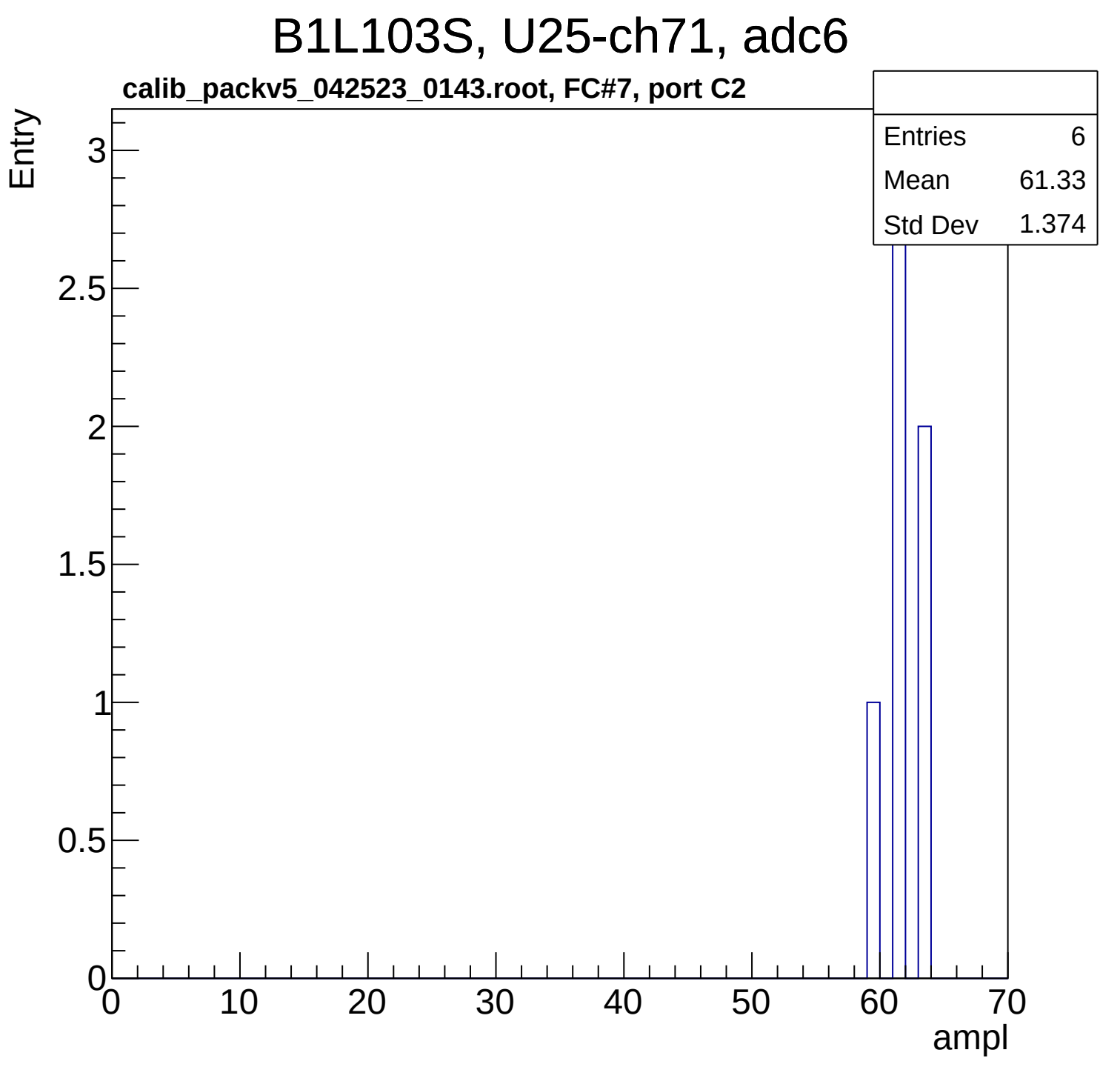
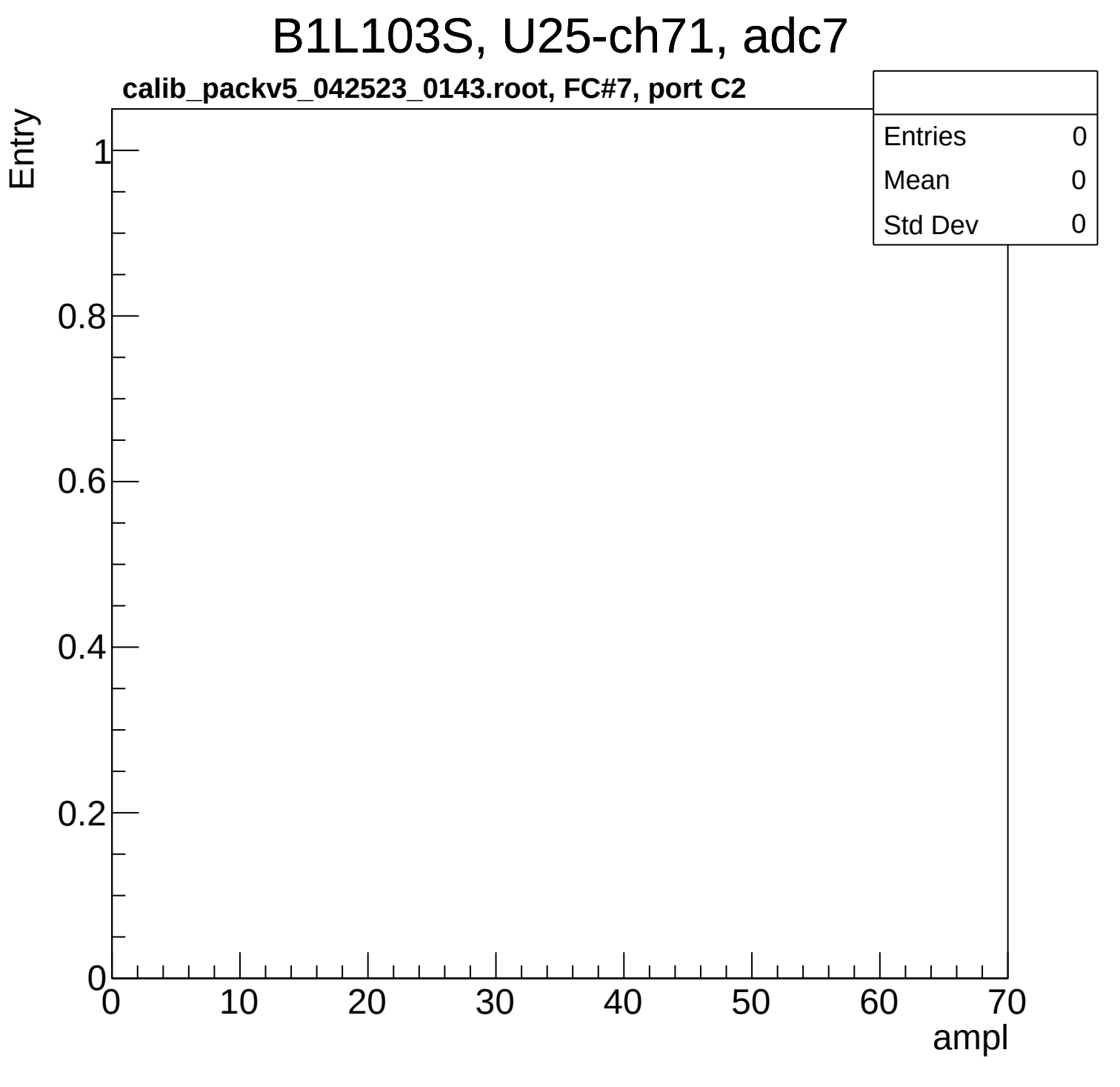
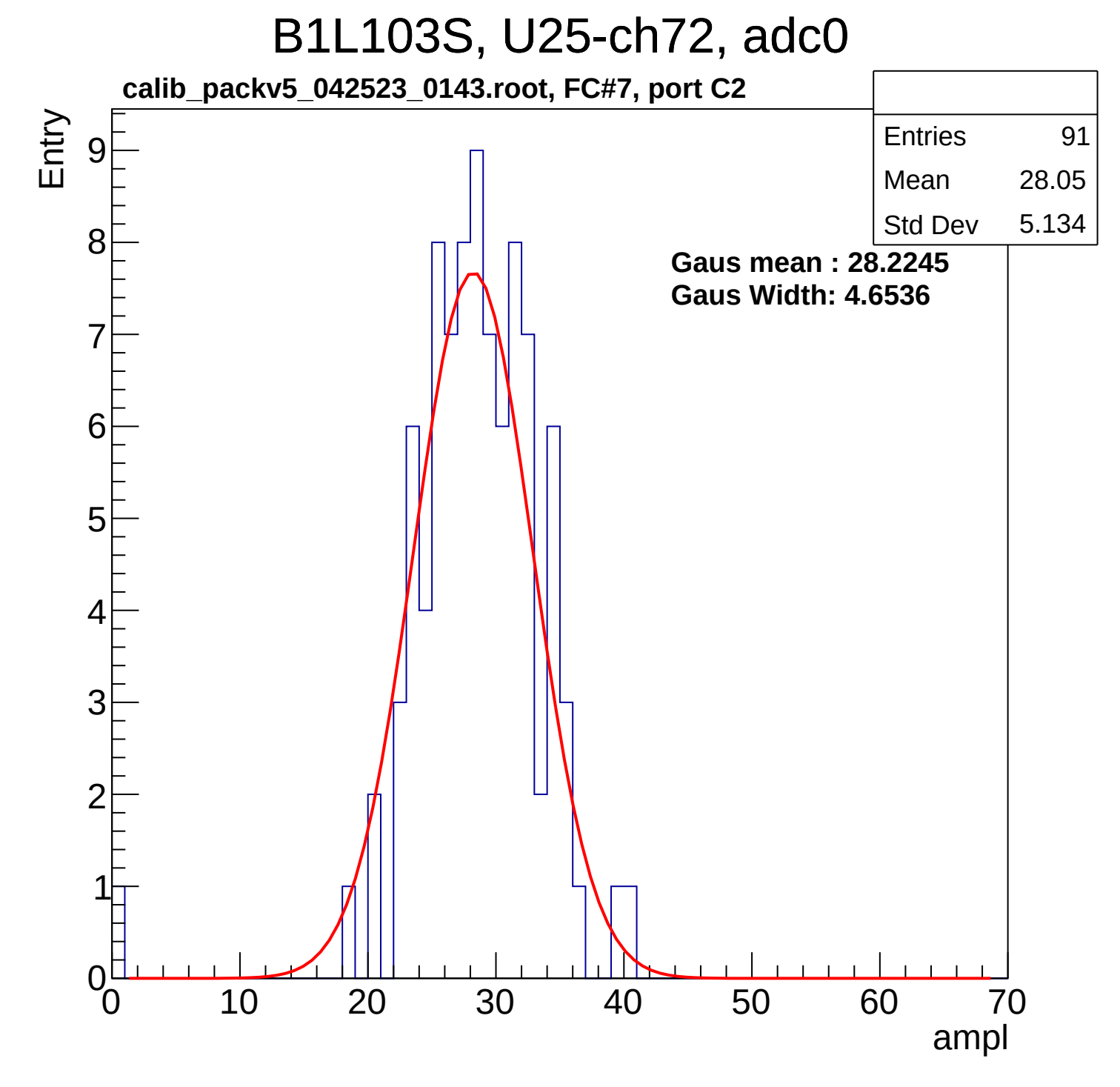
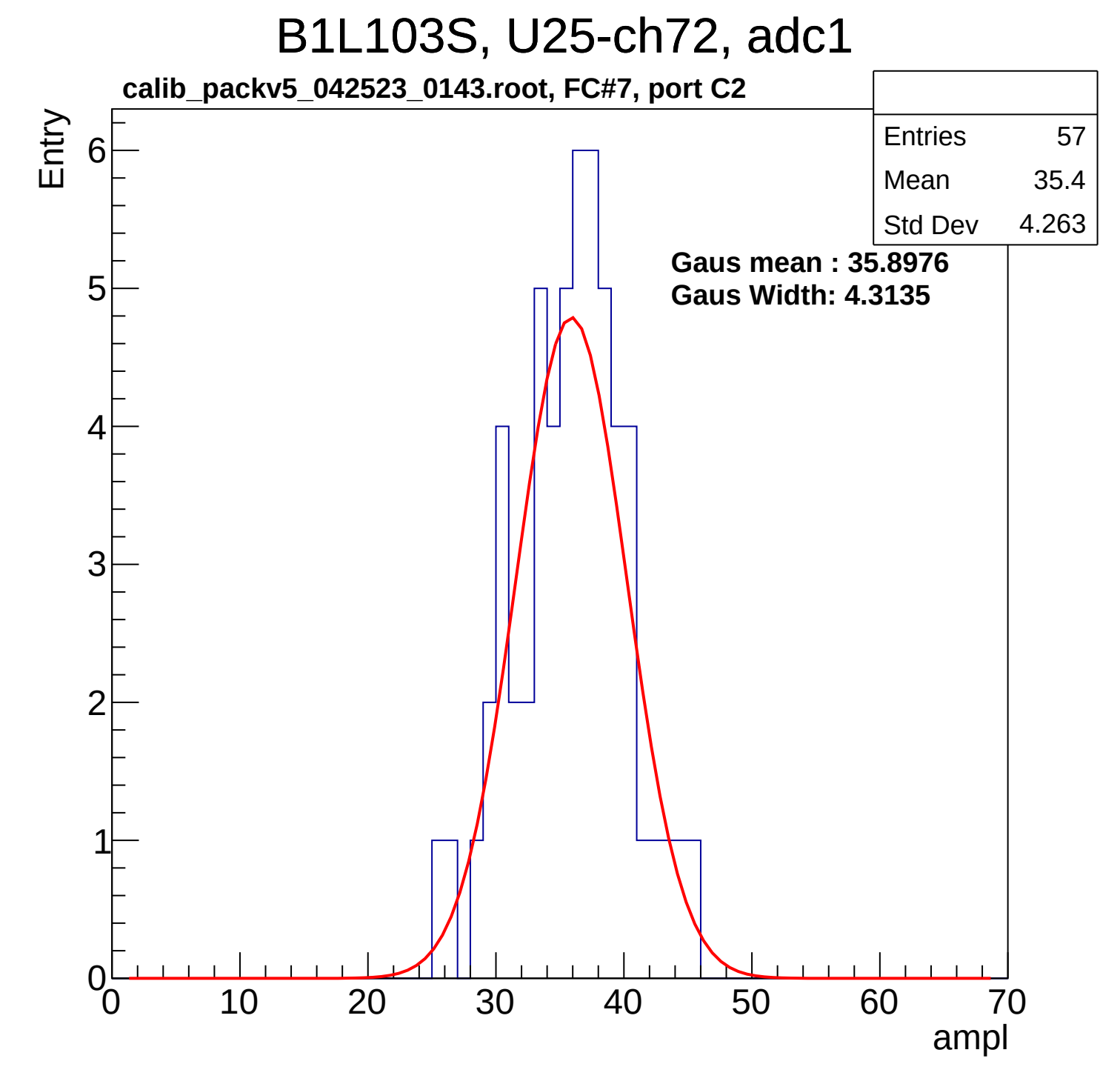
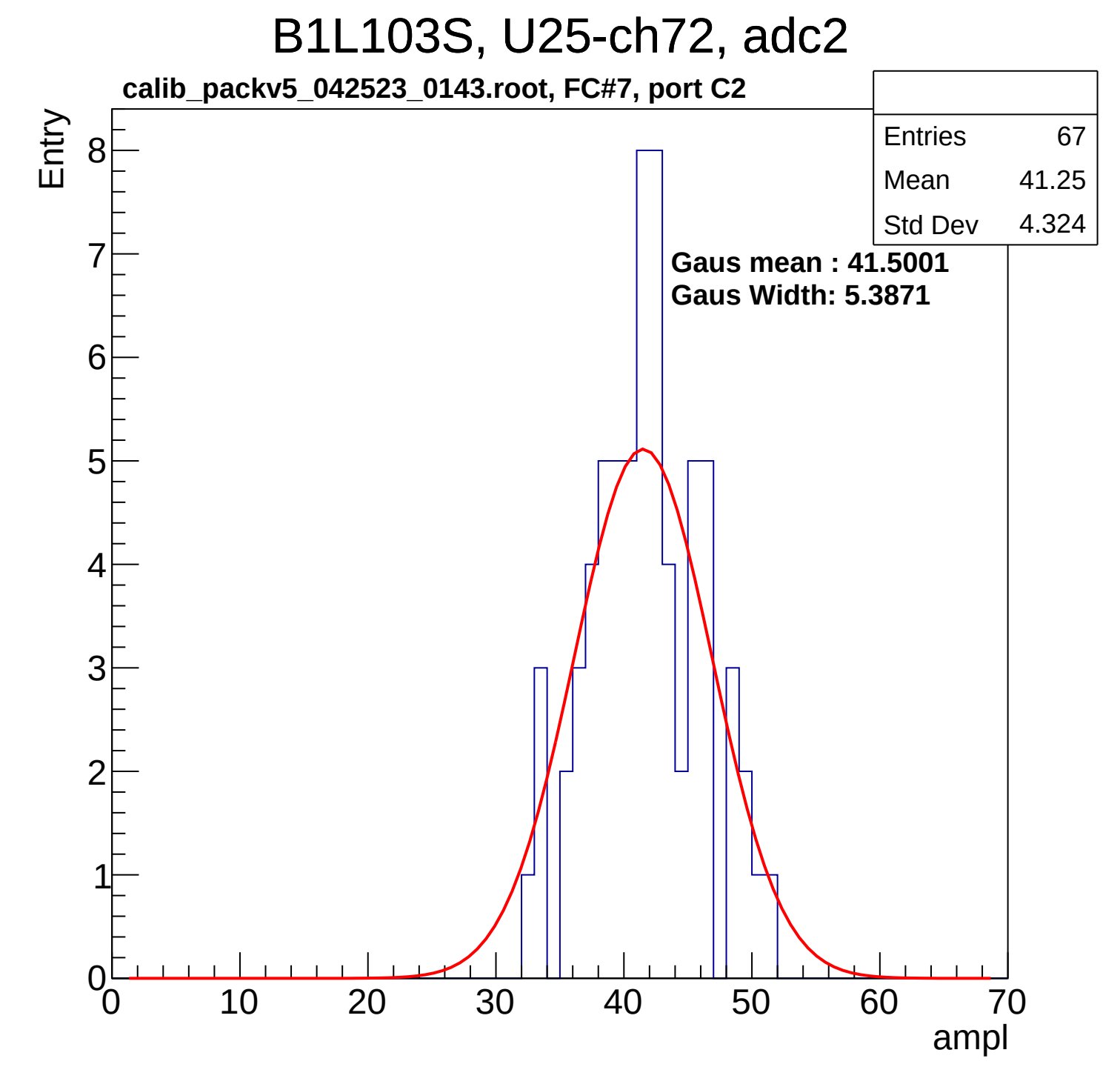
60
70


B1L103S, U25-ch71, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.33
Std Dev 1.374
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch71, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch72, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 91
Mean 28.05
Std Dev 5.134
Gaus mean : 28.2245
Gaus Width: 4.6536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch72, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 35.4
Std Dev 4.263
Gaus mean : 35.8976
Gaus Width: 4.3135
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch72, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 41.25
Std Dev 4.324
Gaus mean : 41.5001
Gaus Width: 5.3871
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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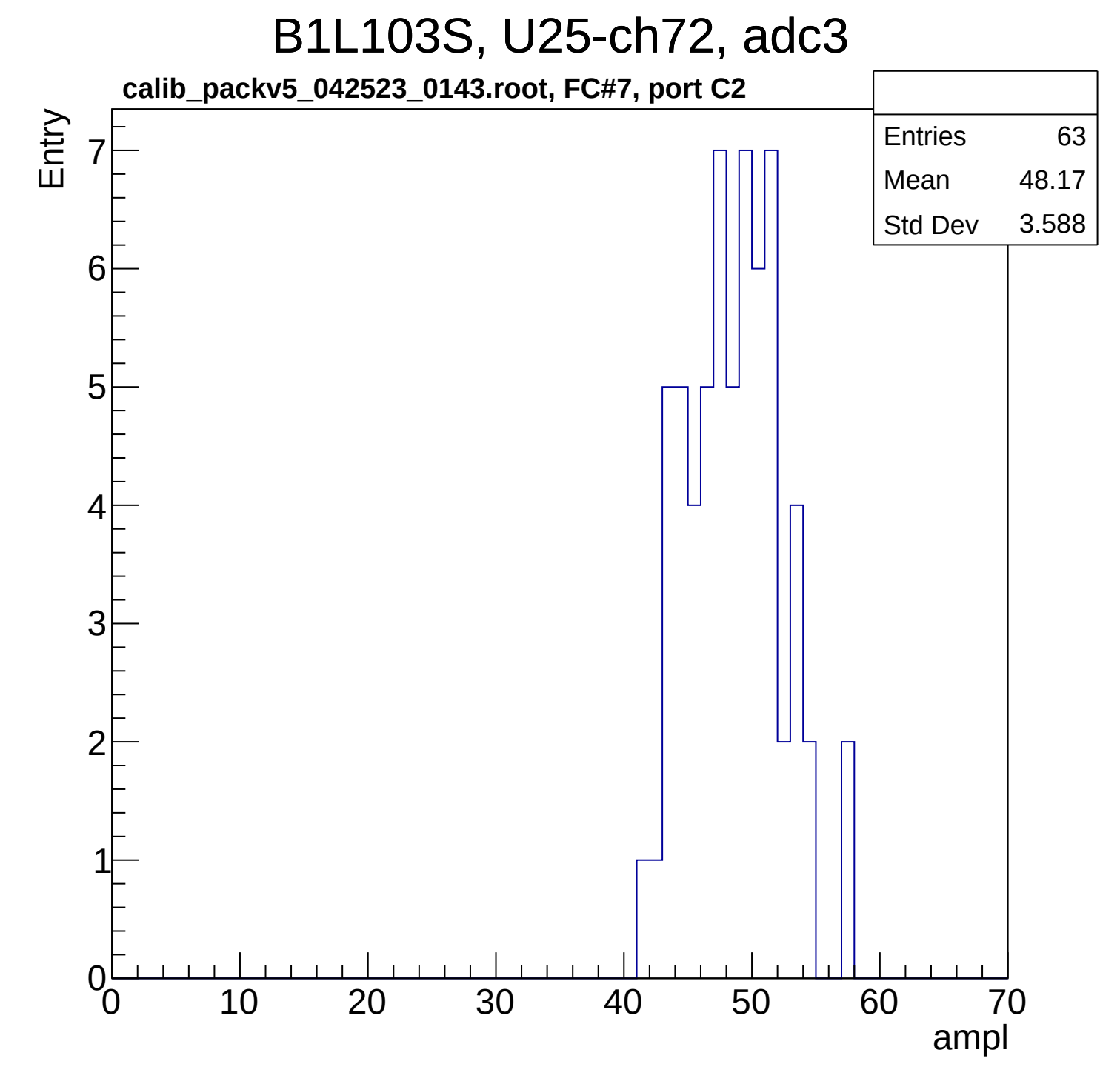
40
50
60
70


B1L103S, U25-ch72, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.17
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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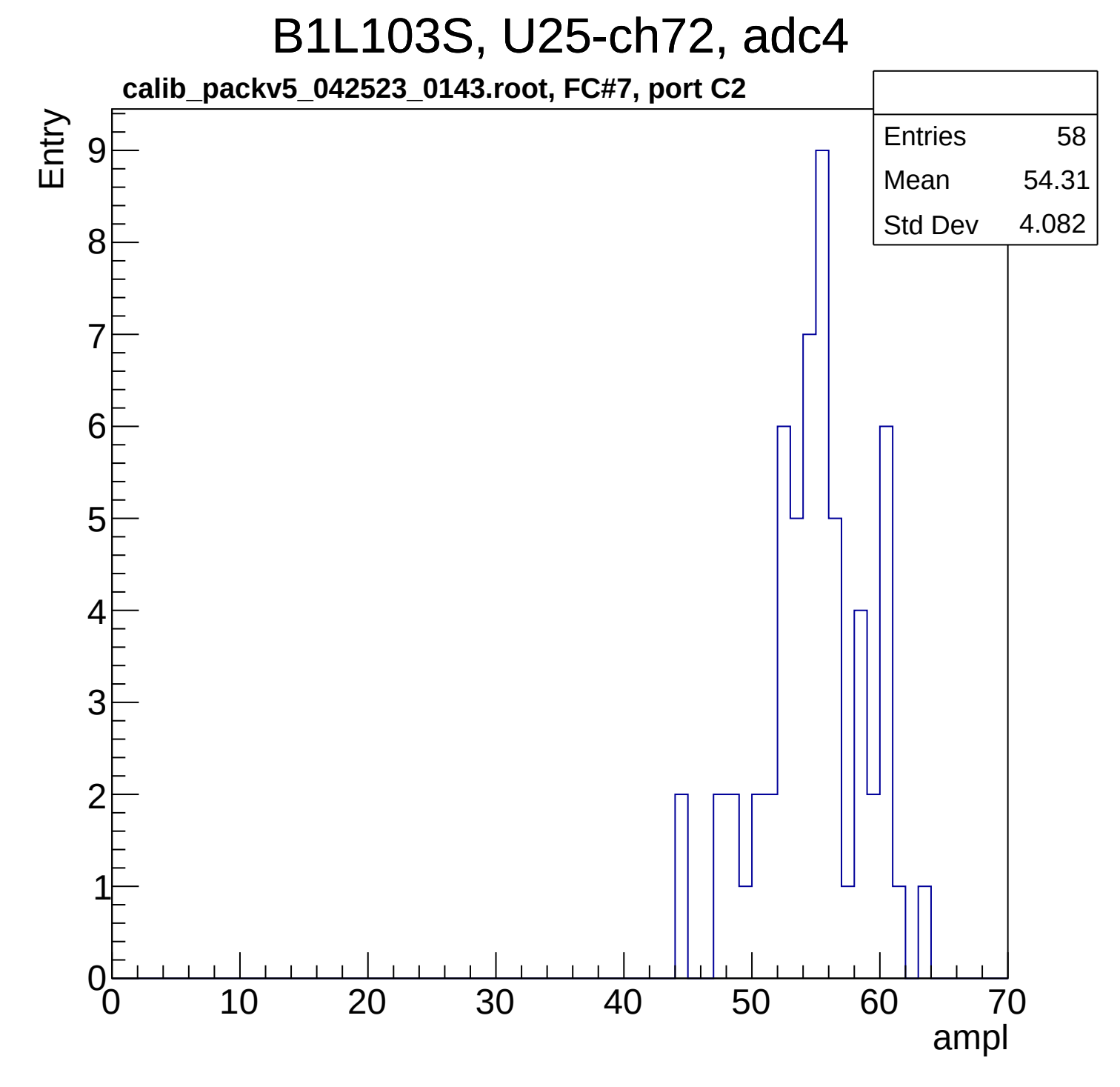
B1L103S, U25-ch72, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 54.31
Std Dev 4.082
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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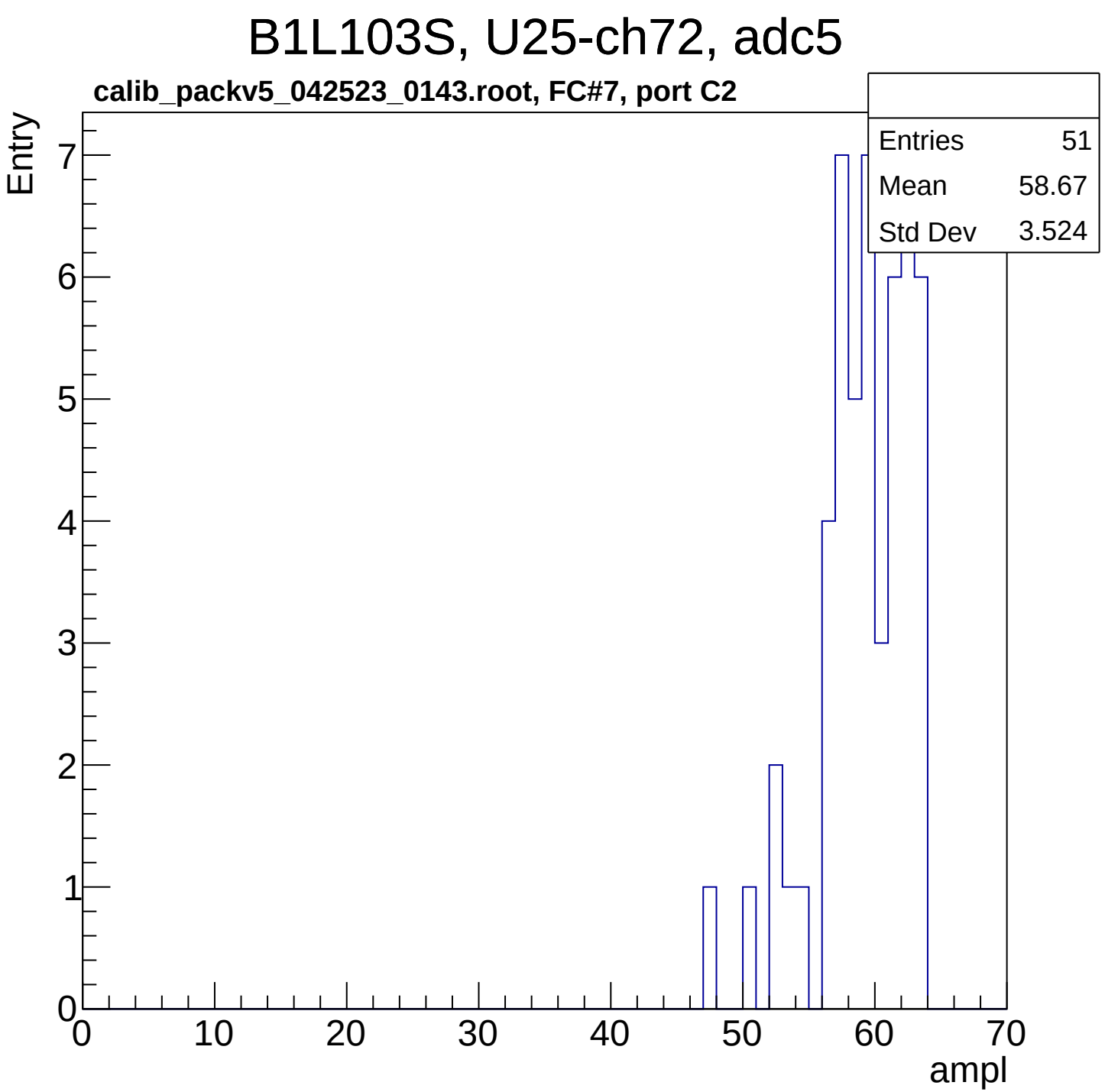
B1L103S, U25-ch72, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 58.67
Std Dev 3.524
Entry
ampl
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2
3
4
5
6
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0
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60
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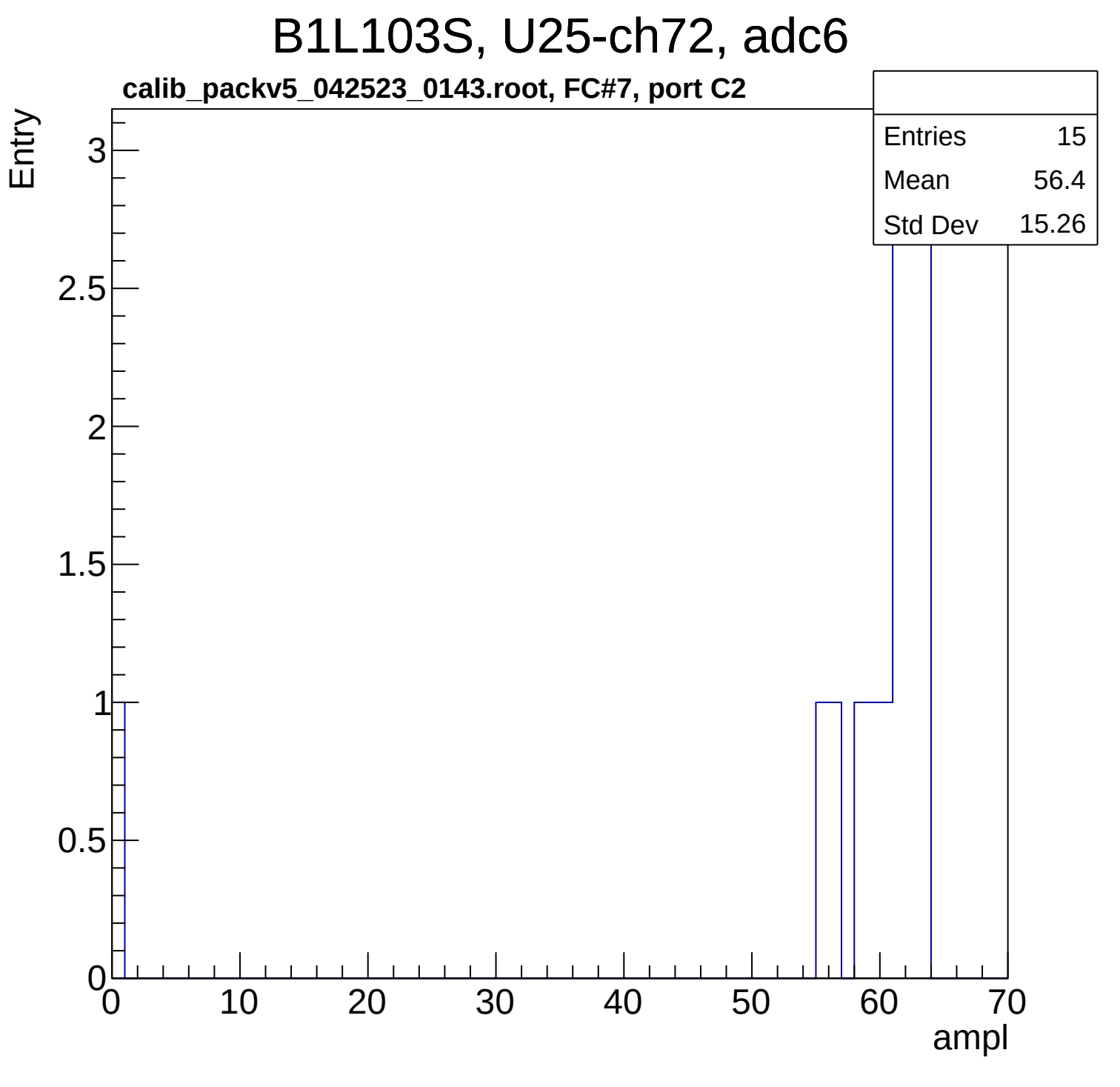
B1L103S, U25-ch72, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 15
Mean 56.4
Std Dev 15.26
Entry
ampl
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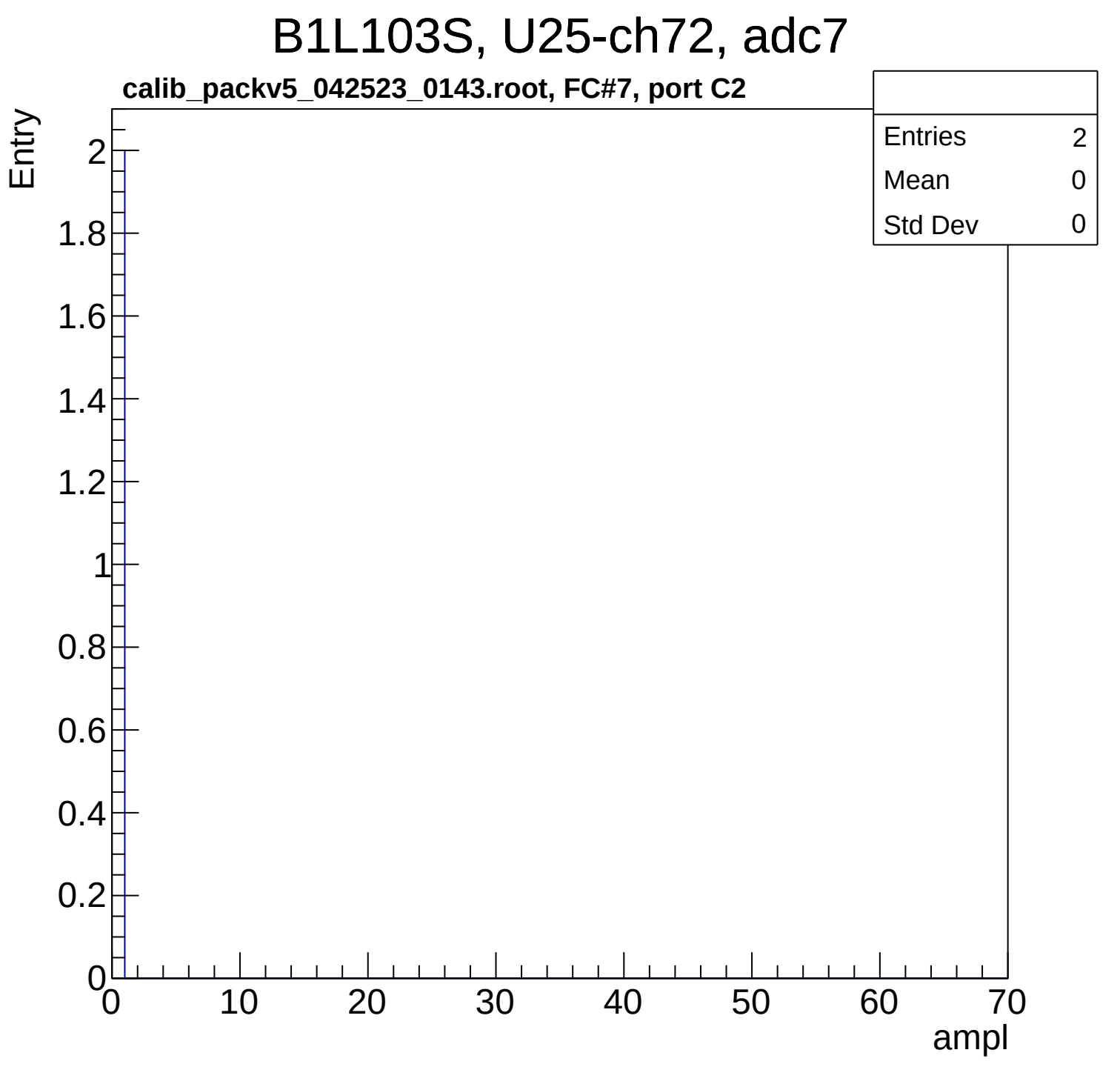
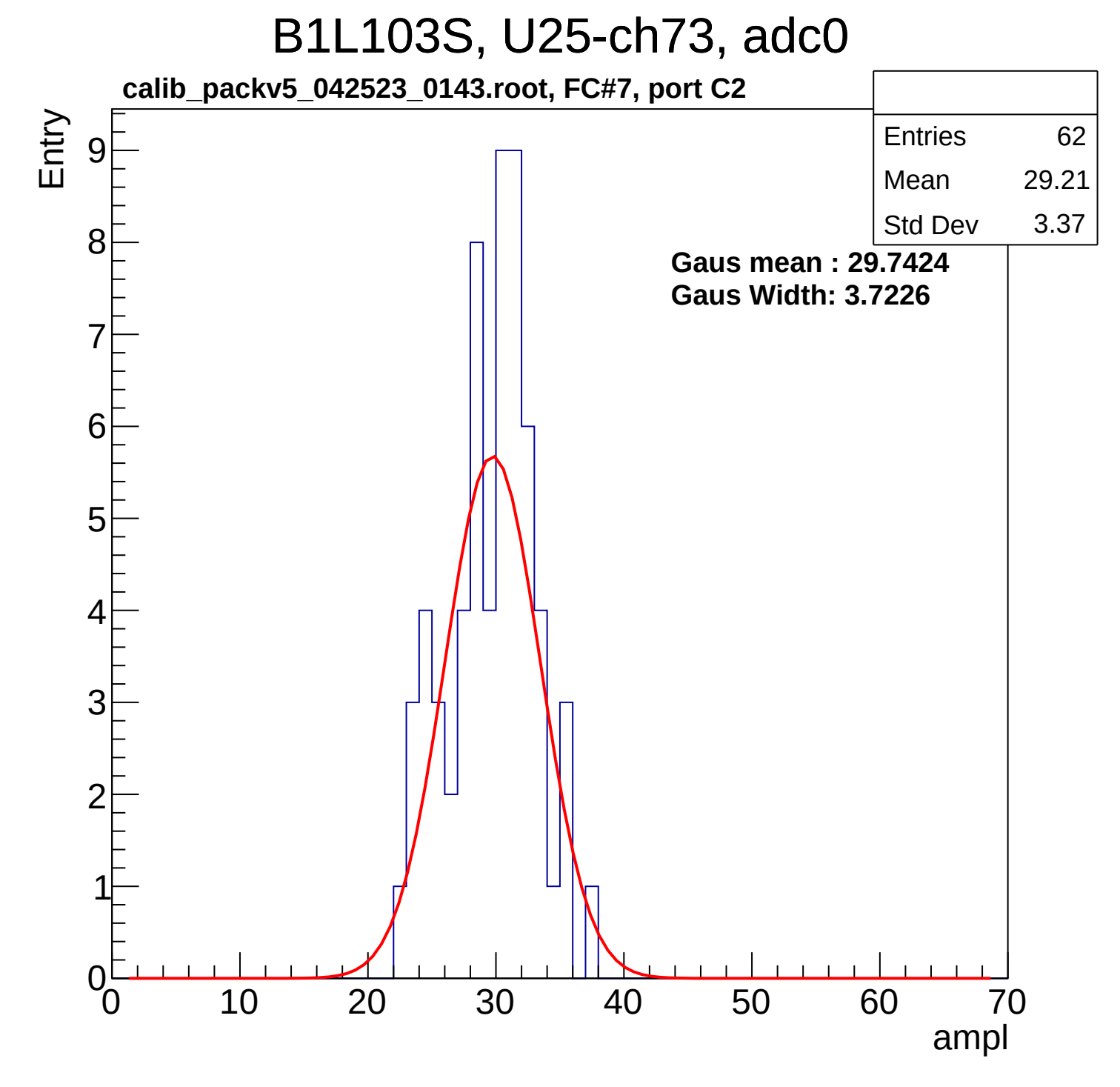
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch72, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch73, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 29.21
Std Dev 3.37
Gaus mean : 29.7424
Gaus Width: 3.7226
Entry
ampl
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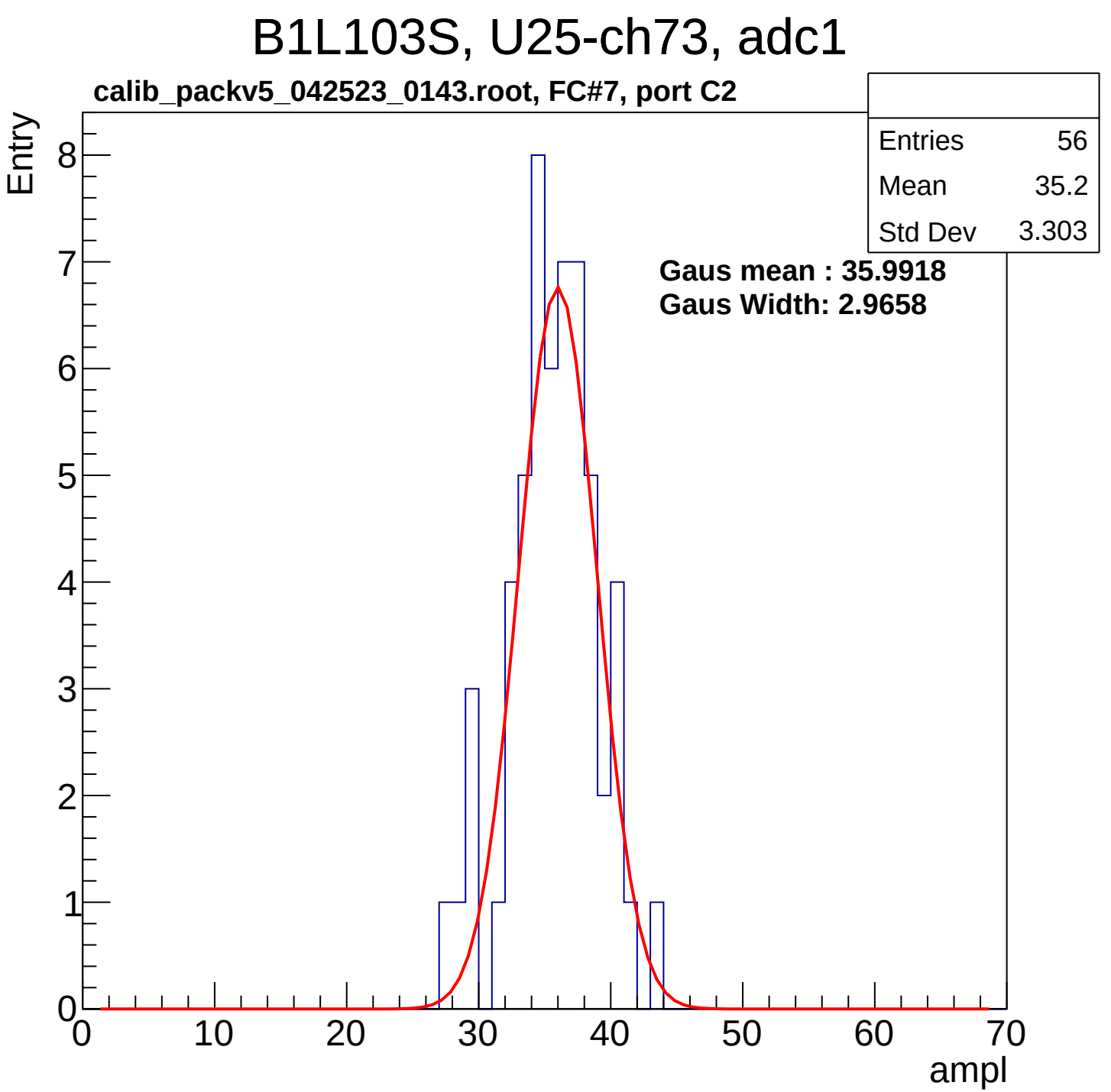
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch73, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 35.2
Std Dev 3.303
Gaus mean : 35.9918
Gaus Width: 2.9658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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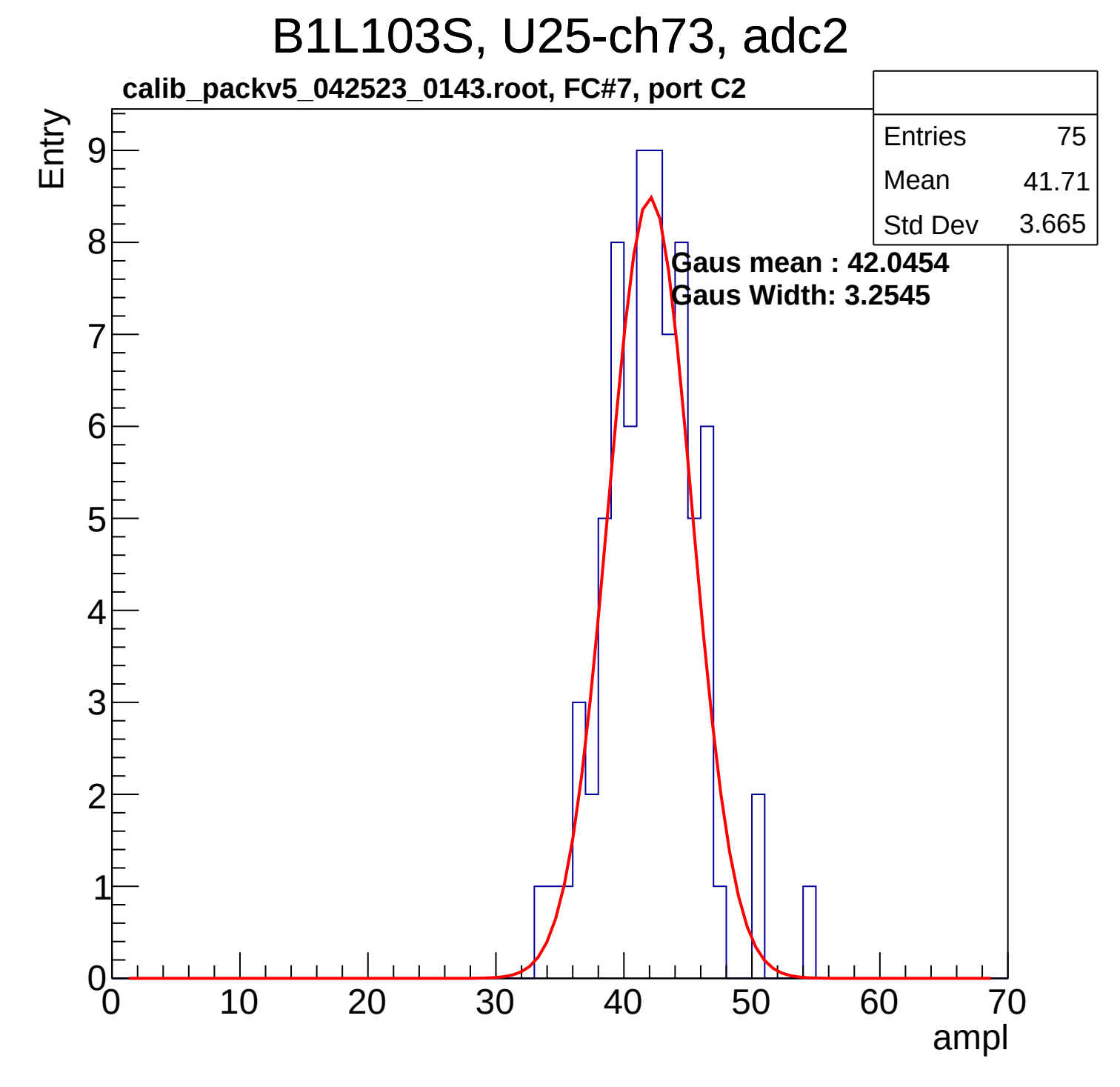
40
50
60
70


B1L103S, U25-ch73, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 41.71
Std Dev 3.665
Gaus mean : 42.0454
Gaus Width: 3.2545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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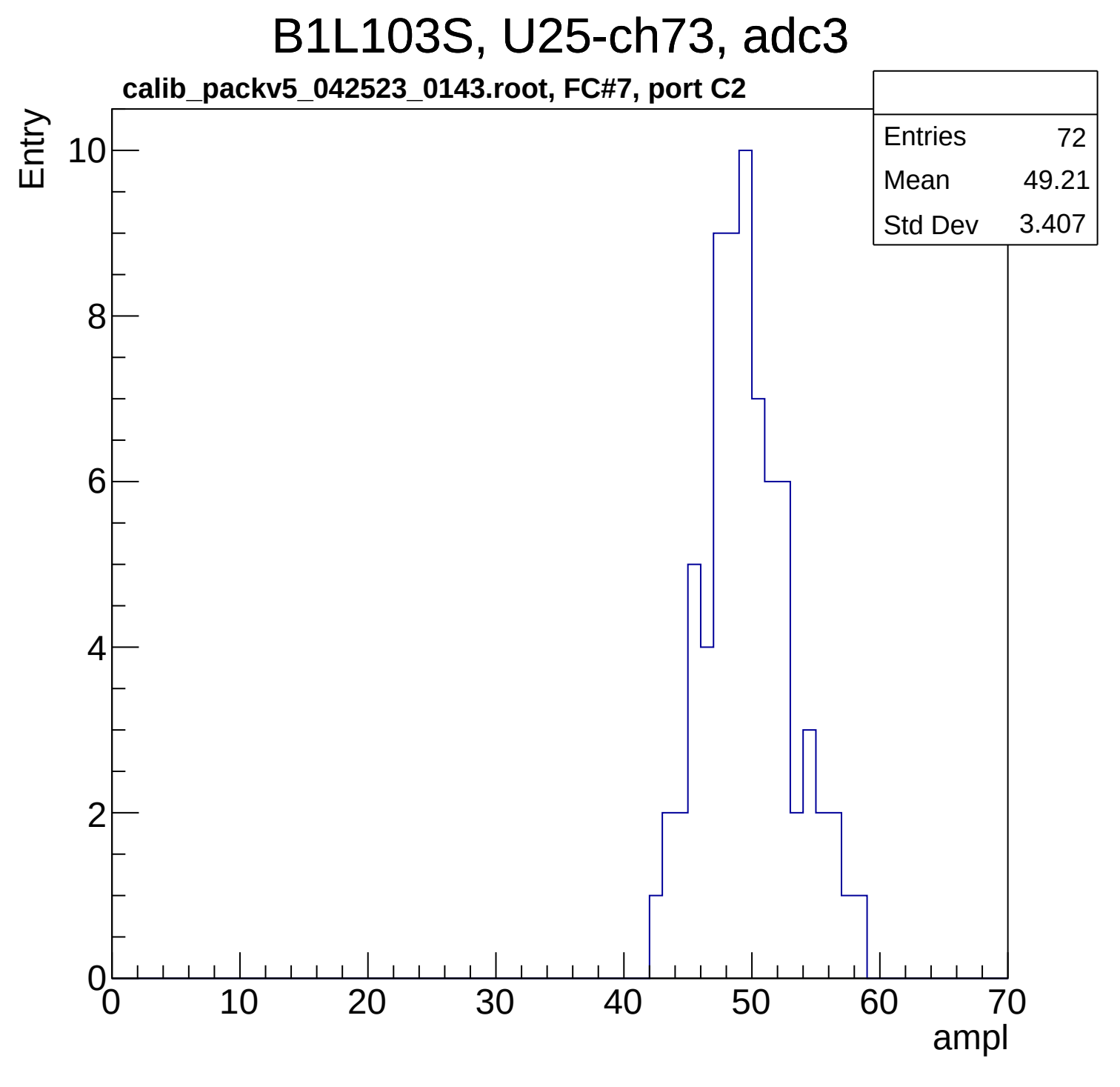
B1L103S, U25-ch73, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 49.21
Std Dev 3.407
Entry
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4
6
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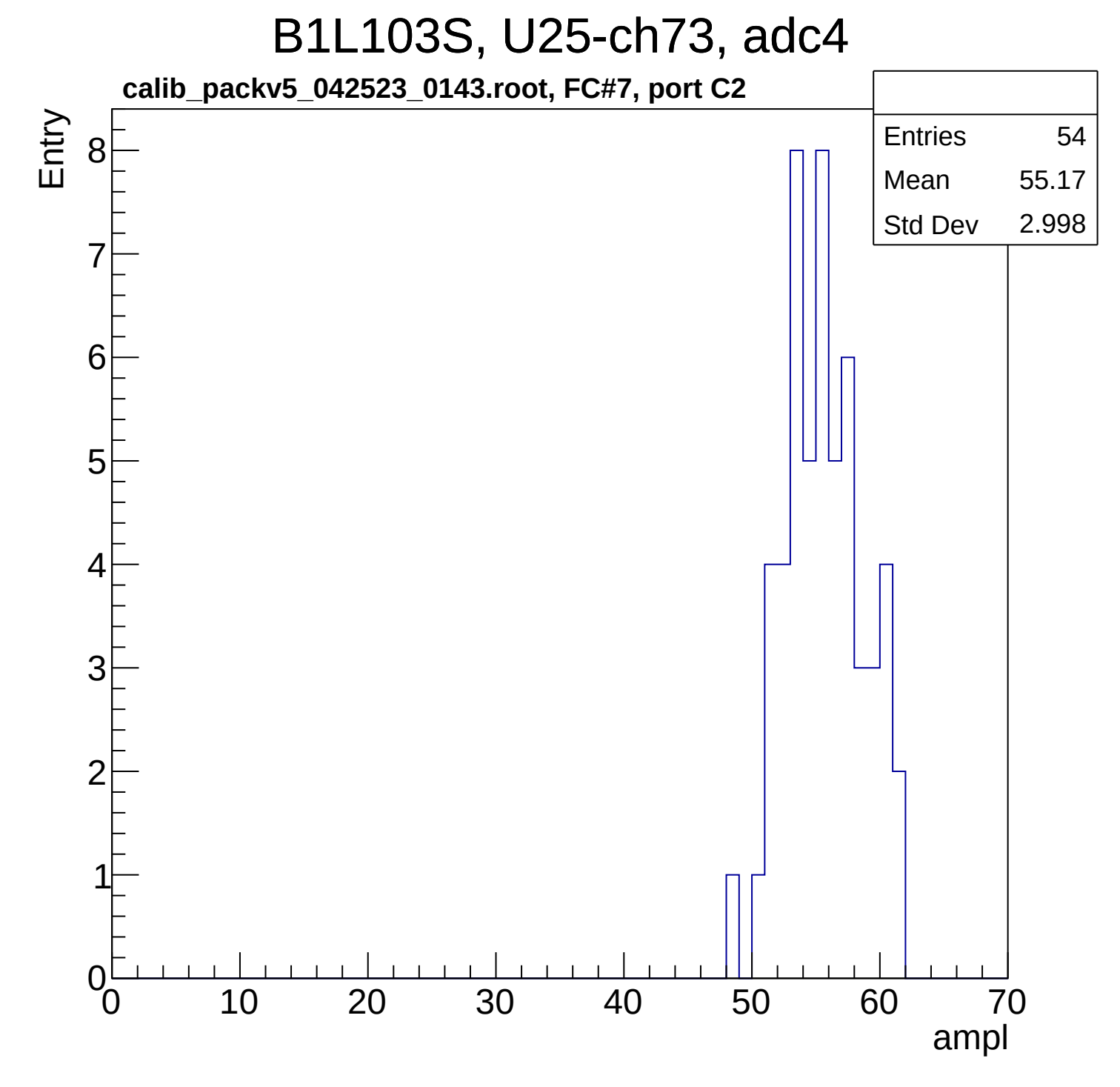
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch73, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.17
Std Dev 2.998
Entry
ampl
0
1
2
3
4
5
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7
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0
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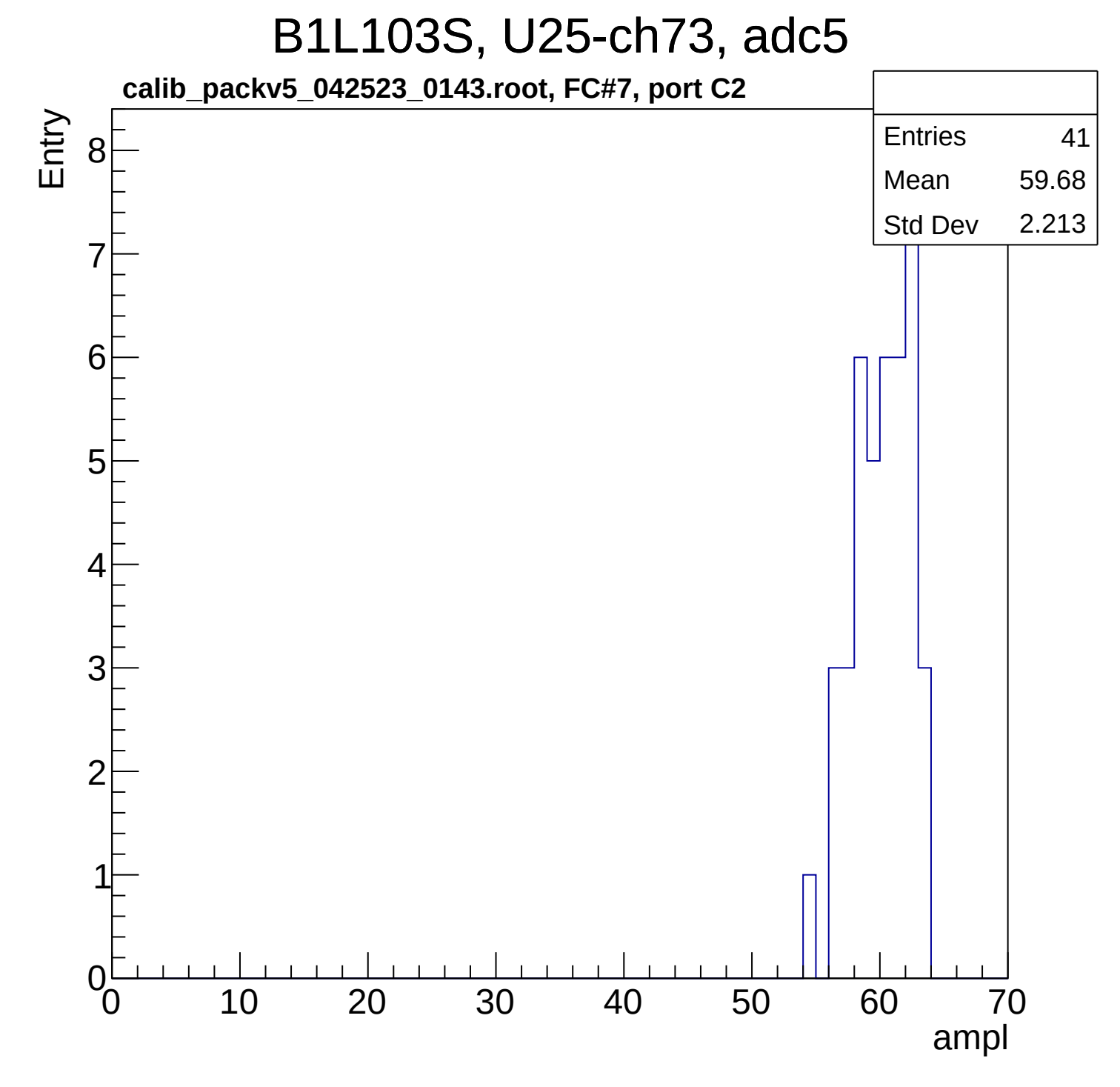
60
70


B1L103S, U25-ch73, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.68
Std Dev 2.213
Entry
ampl
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1
2
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4
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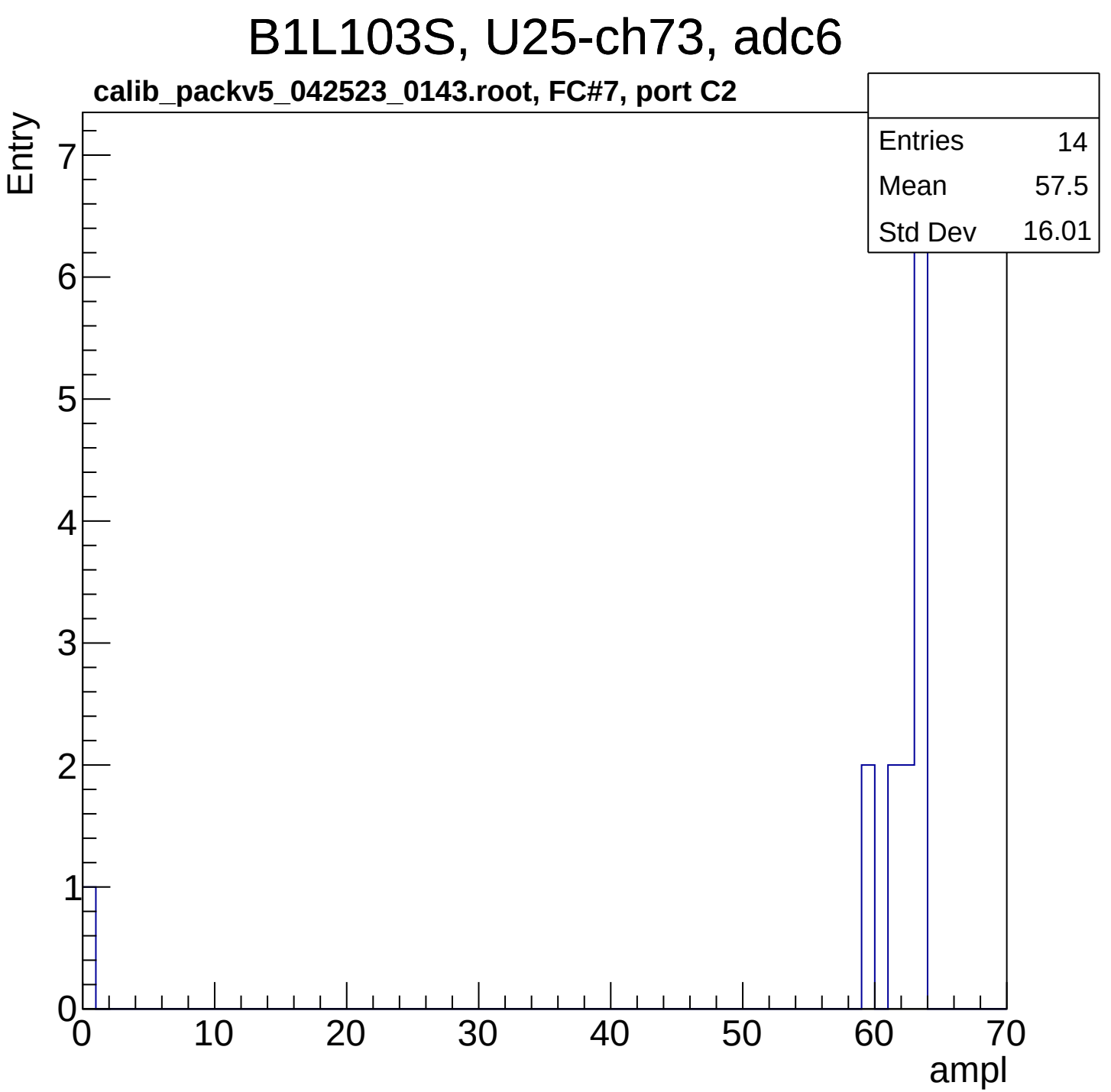
B1L103S, U25-ch73, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 57.5
Std Dev 16.01
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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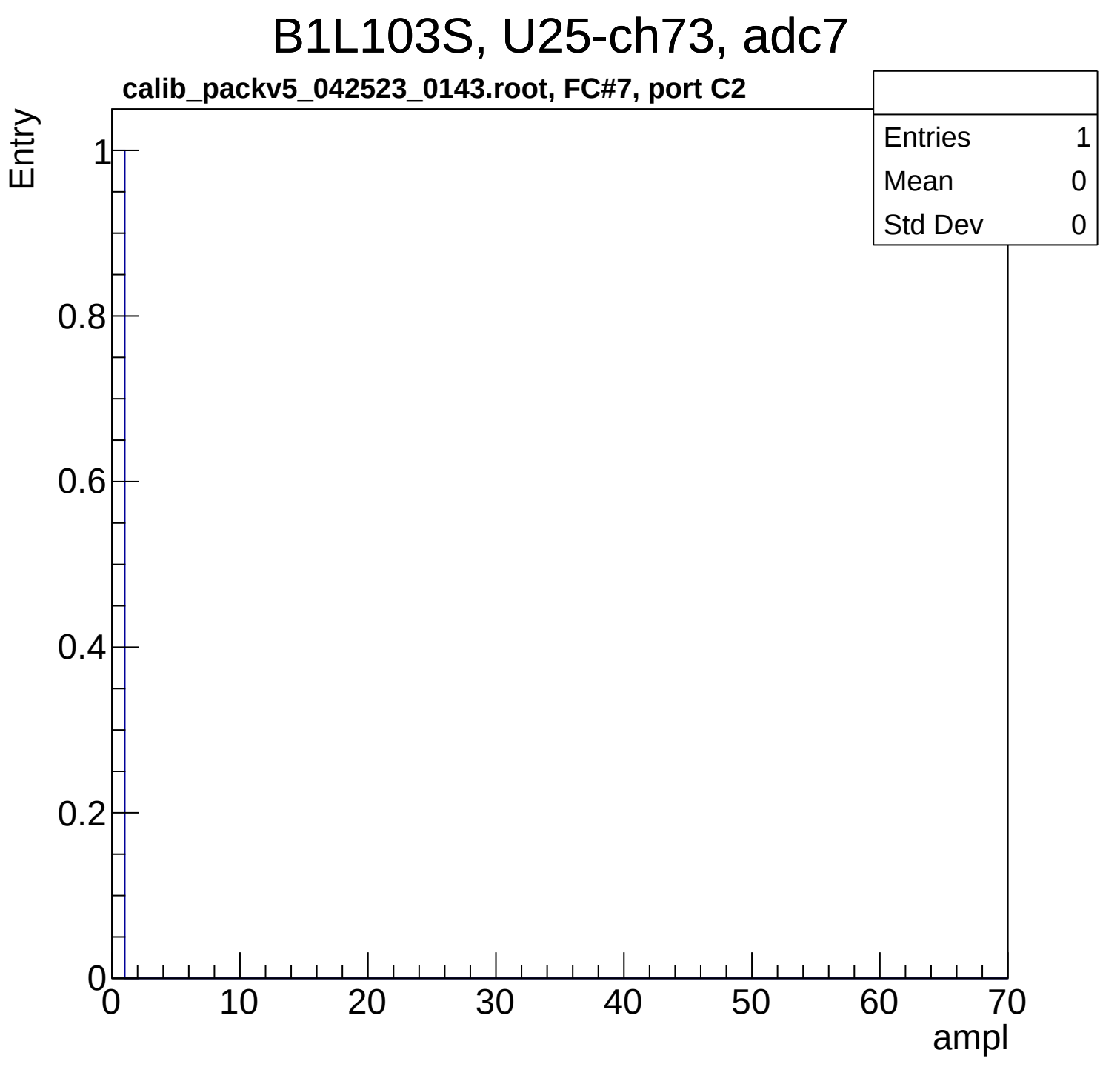
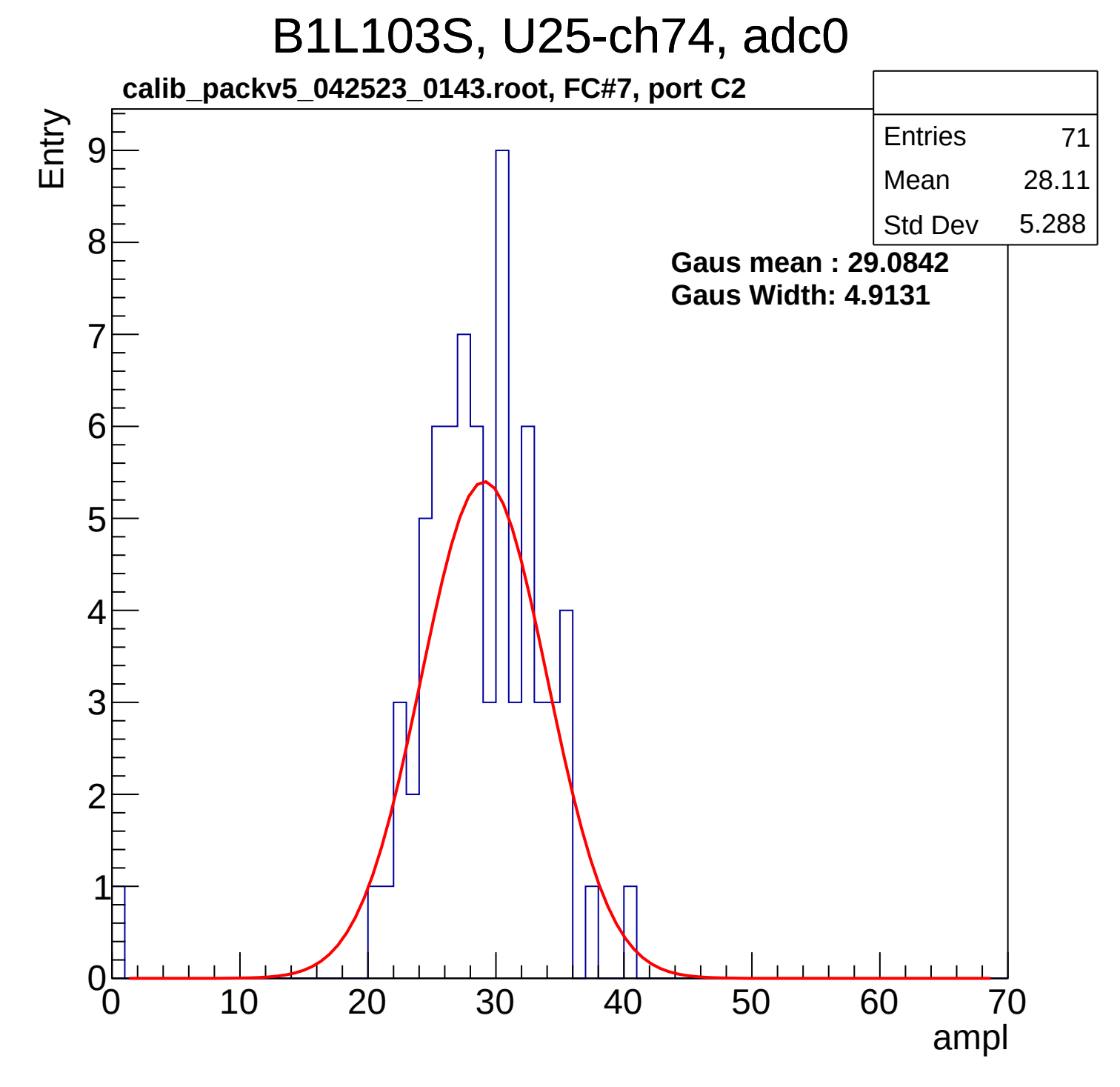

B1L103S, U25-ch73, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch74, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 28.11
Std Dev 5.288
Gaus mean : 29.0842
Gaus Width: 4.9131
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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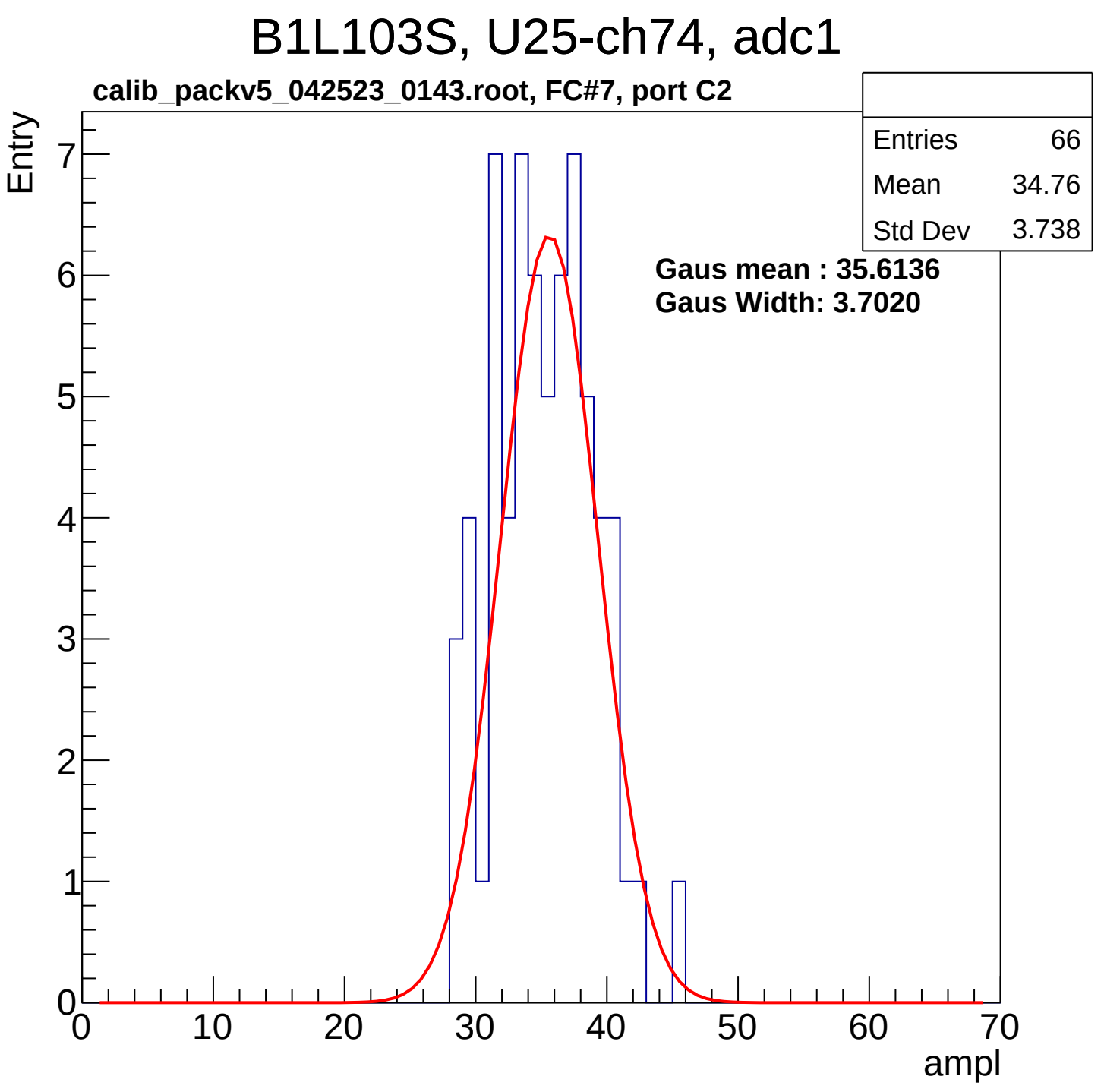
B1L103S, U25-ch74, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 34.76
Std Dev 3.738
Gaus mean : 35.6136
Gaus Width: 3.7020
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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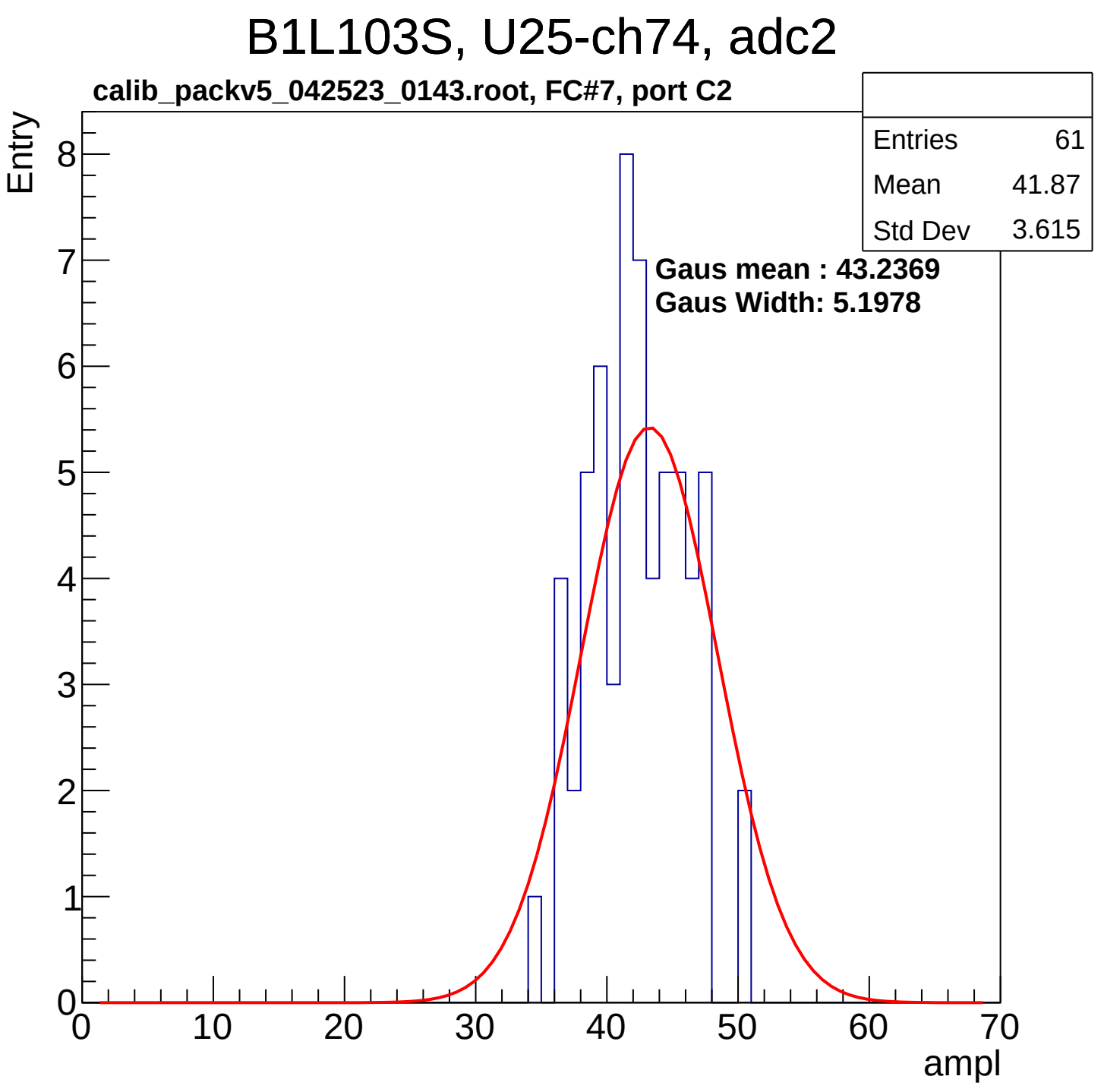
B1L103S, U25-ch74, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.87
Std Dev 3.615
Gaus mean : 43.2369
Gaus Width: 5.1978
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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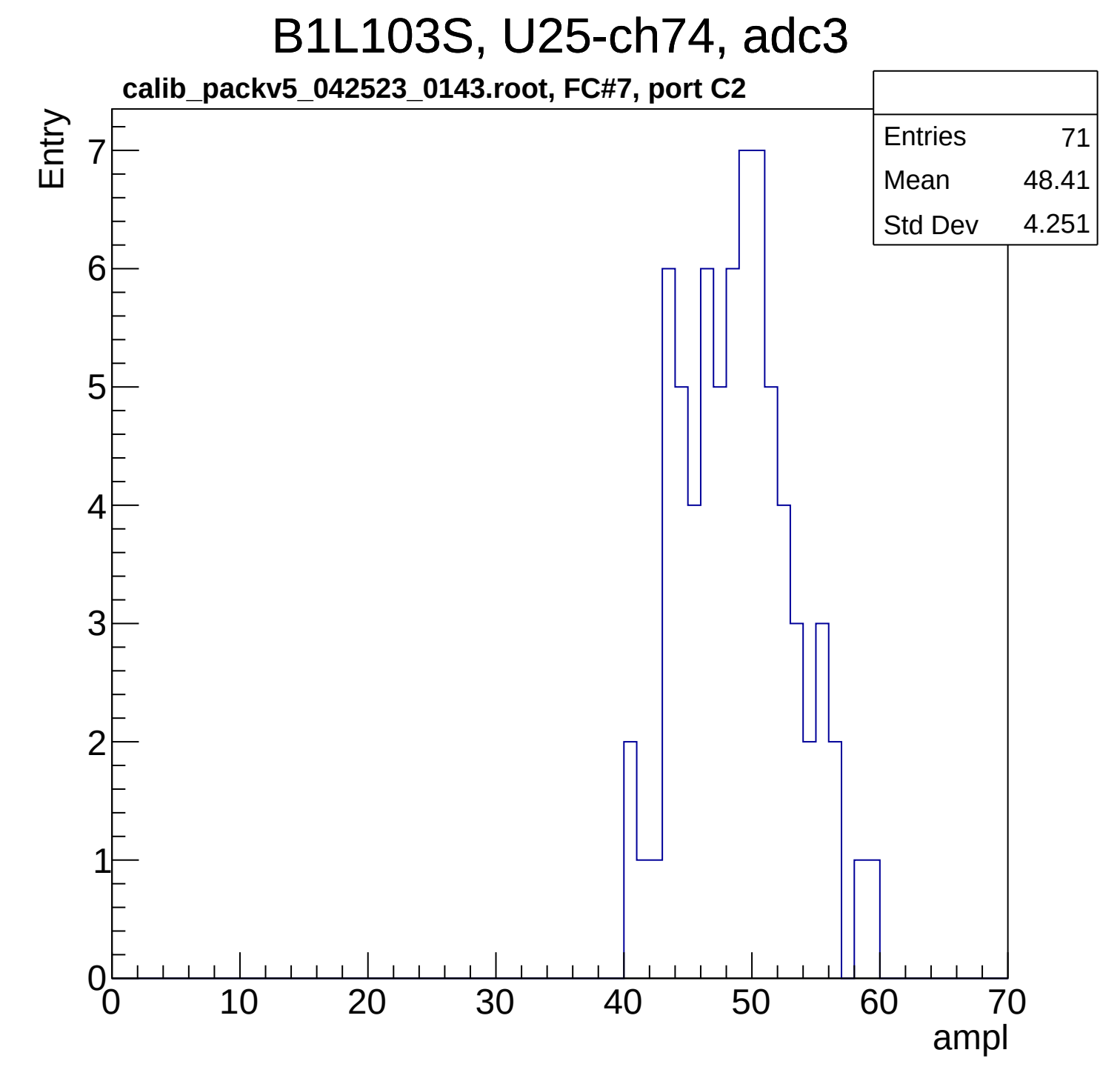
B1L103S, U25-ch74, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 48.41
Std Dev 4.251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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60
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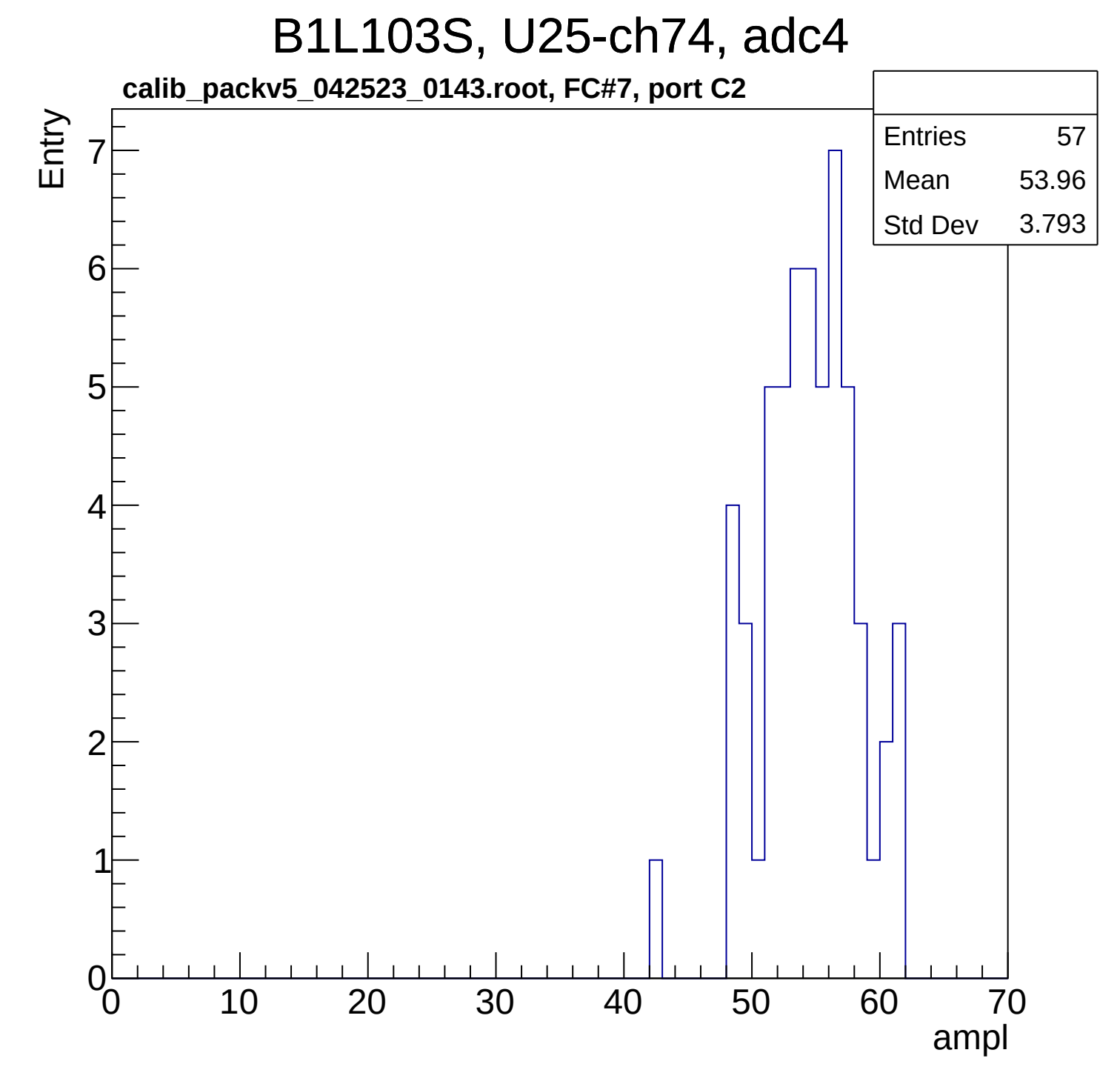
B1L103S, U25-ch74, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 53.96
Std Dev 3.793
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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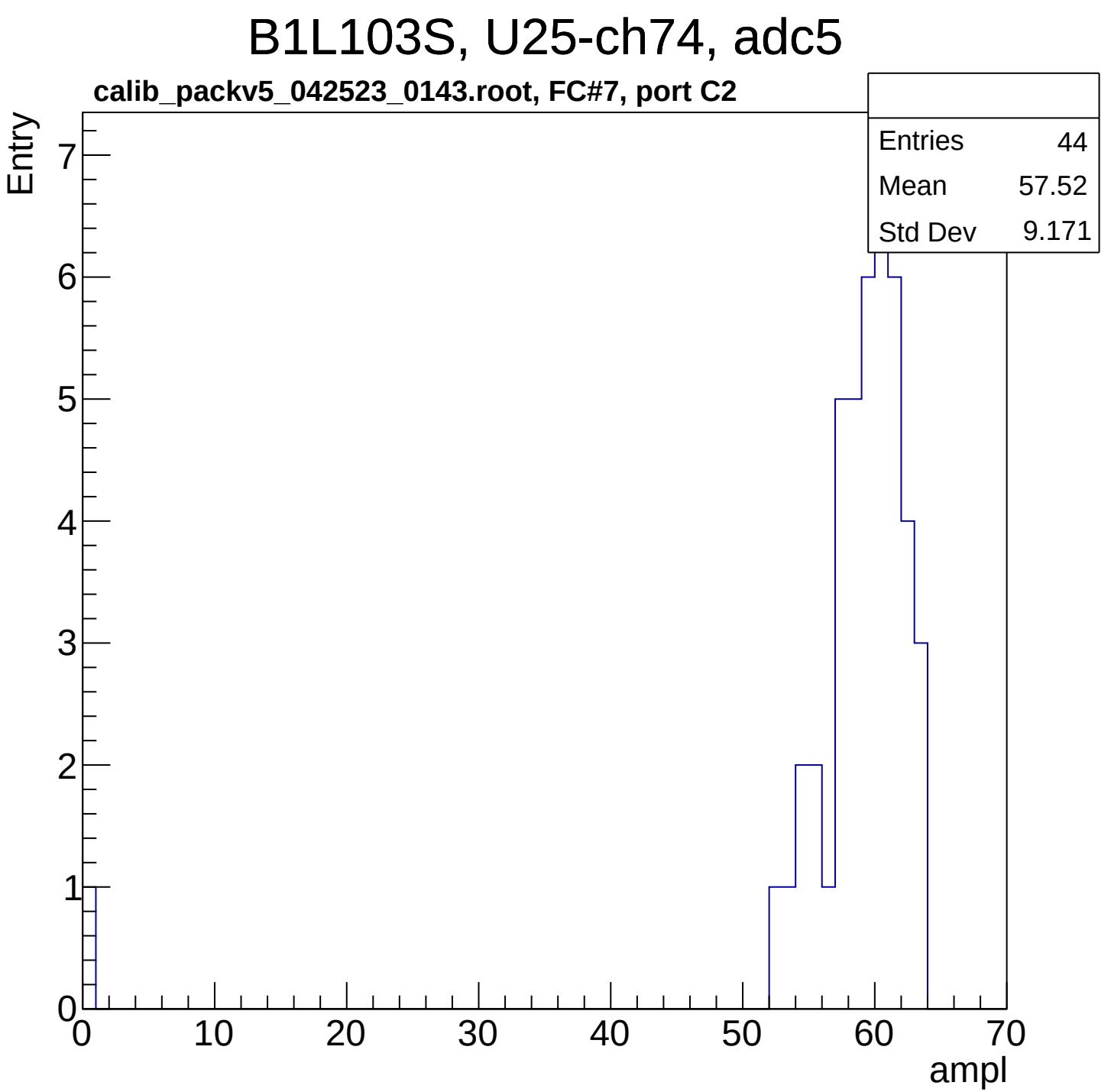
B1L103S, U25-ch74, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 57.52
Std Dev 9.171
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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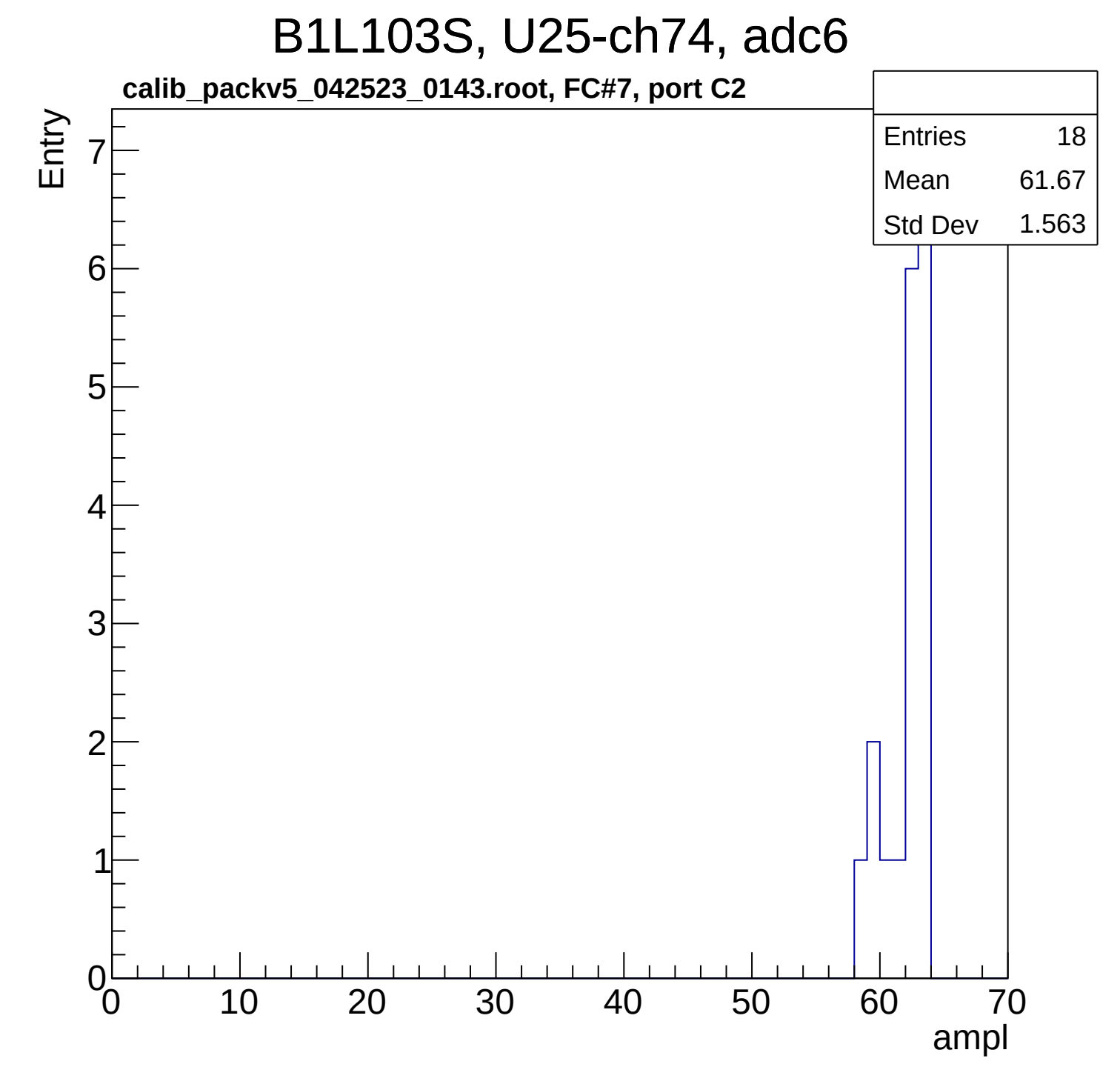
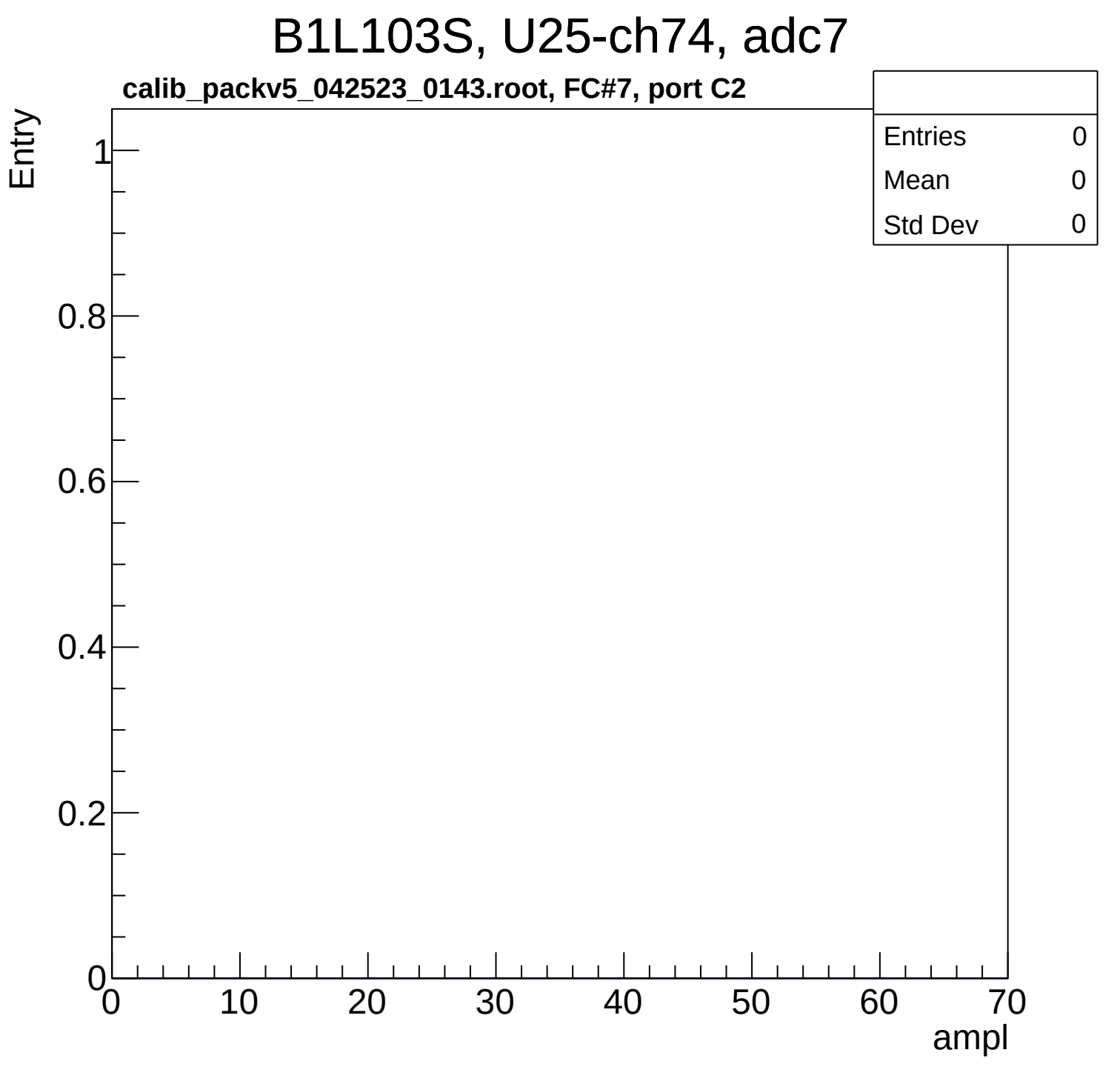

B1L103S, U25-ch74, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 61.67
Std Dev 1.563
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch74, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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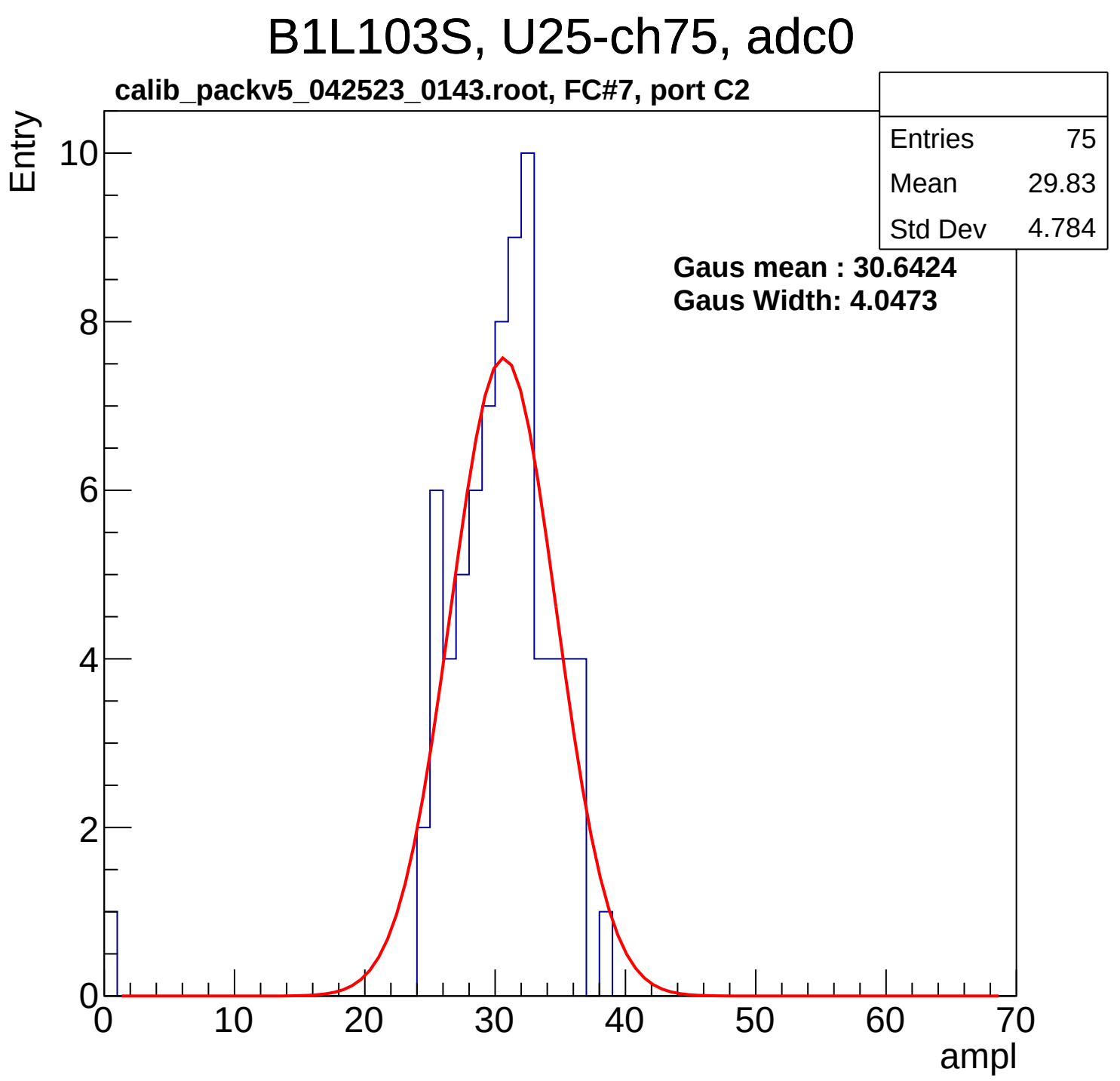
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch75, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 29.83
Std Dev 4.784
Gaus mean : 30.6424
Gaus Width: 4.0473
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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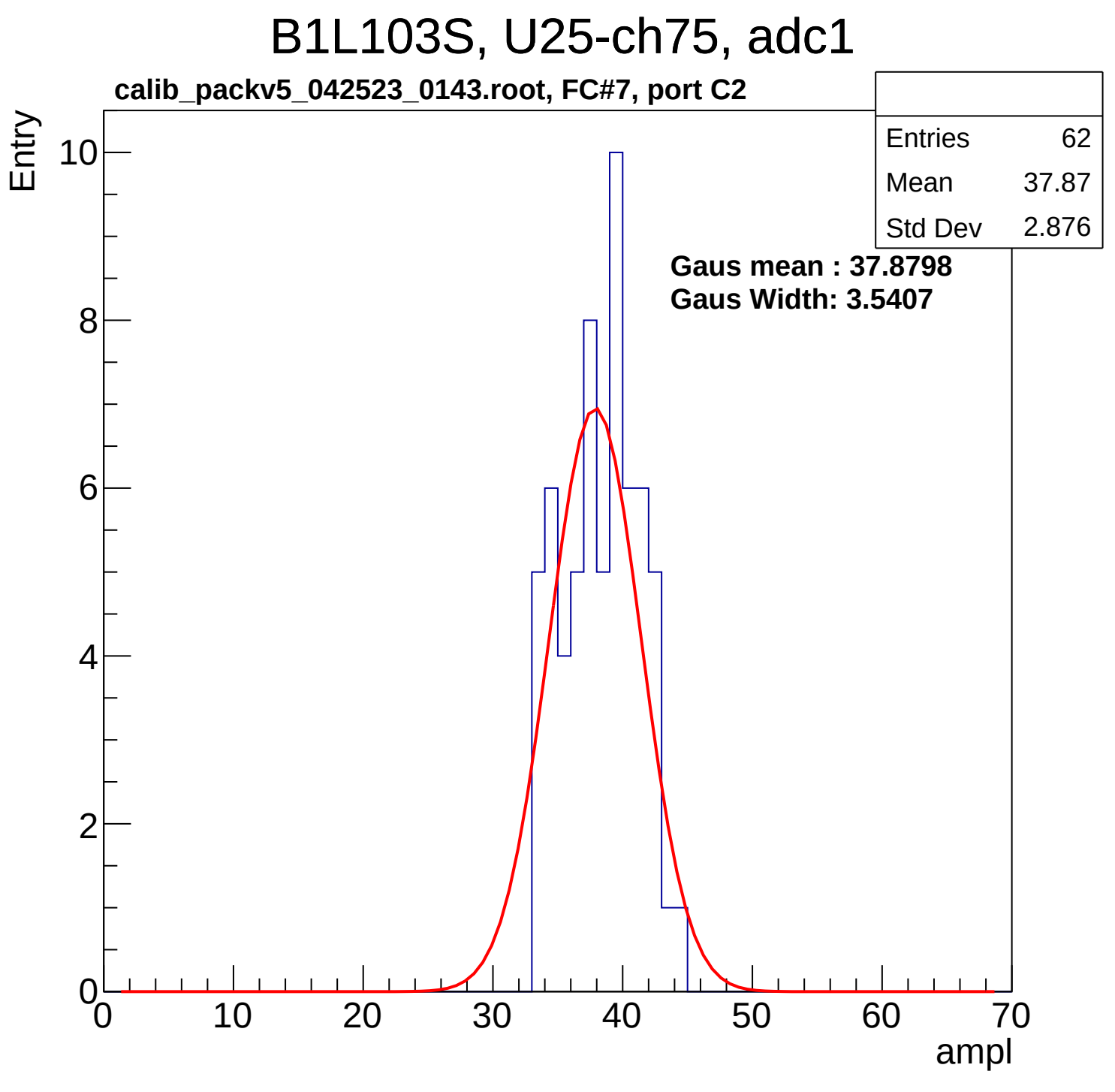
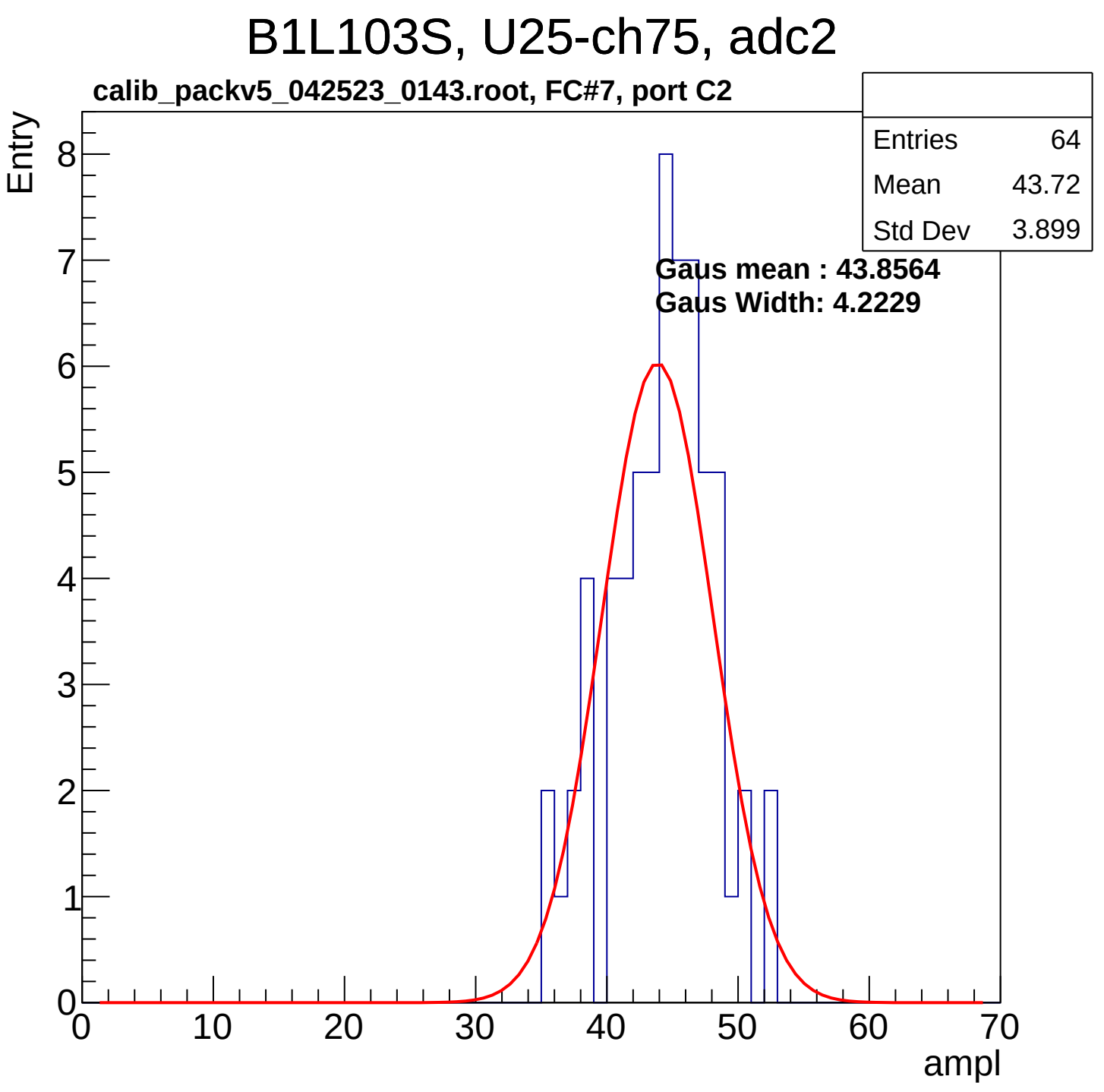

B1L103S, U25-ch75, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 37.87
Std Dev 2.876
Gaus mean : 37.8798
Gaus Width: 3.5407
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch75, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 43.72
Std Dev 3.899
Gaus mean : 43.8564
Gaus Width: 4.2229
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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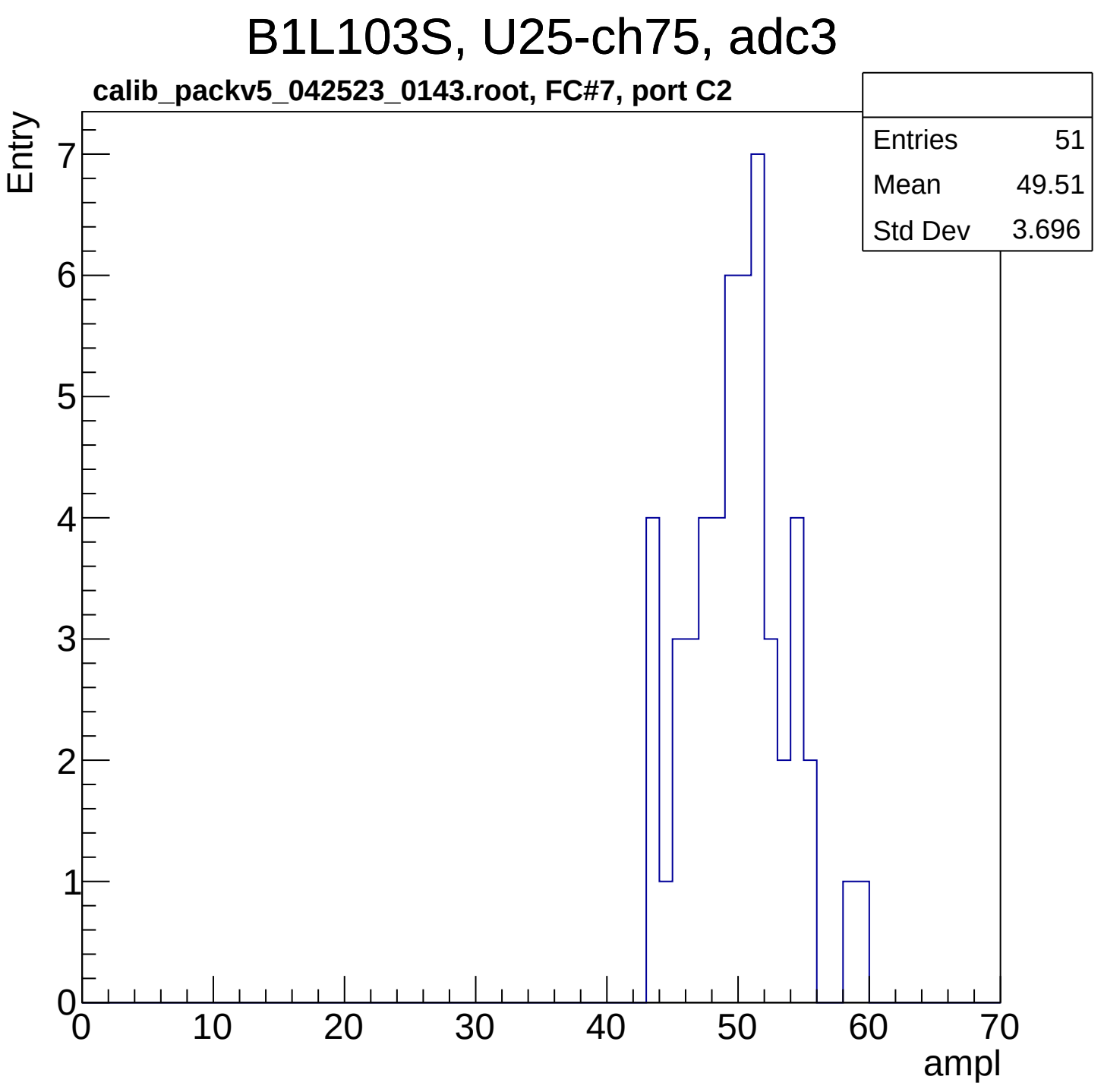
B1L103S, U25-ch75, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 49.51
Std Dev 3.696
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
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60
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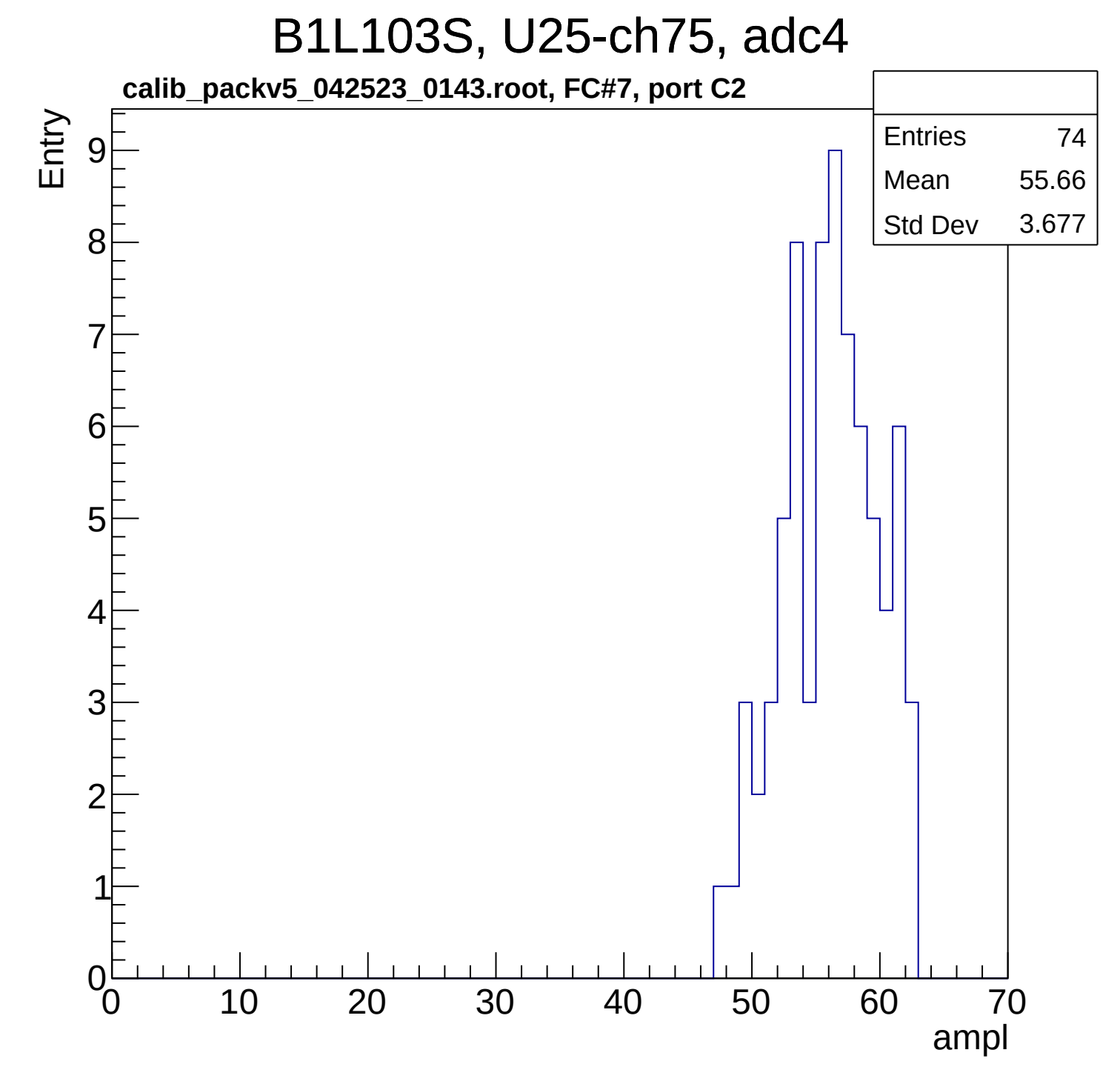
B1L103S, U25-ch75, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 55.66
Std Dev 3.677
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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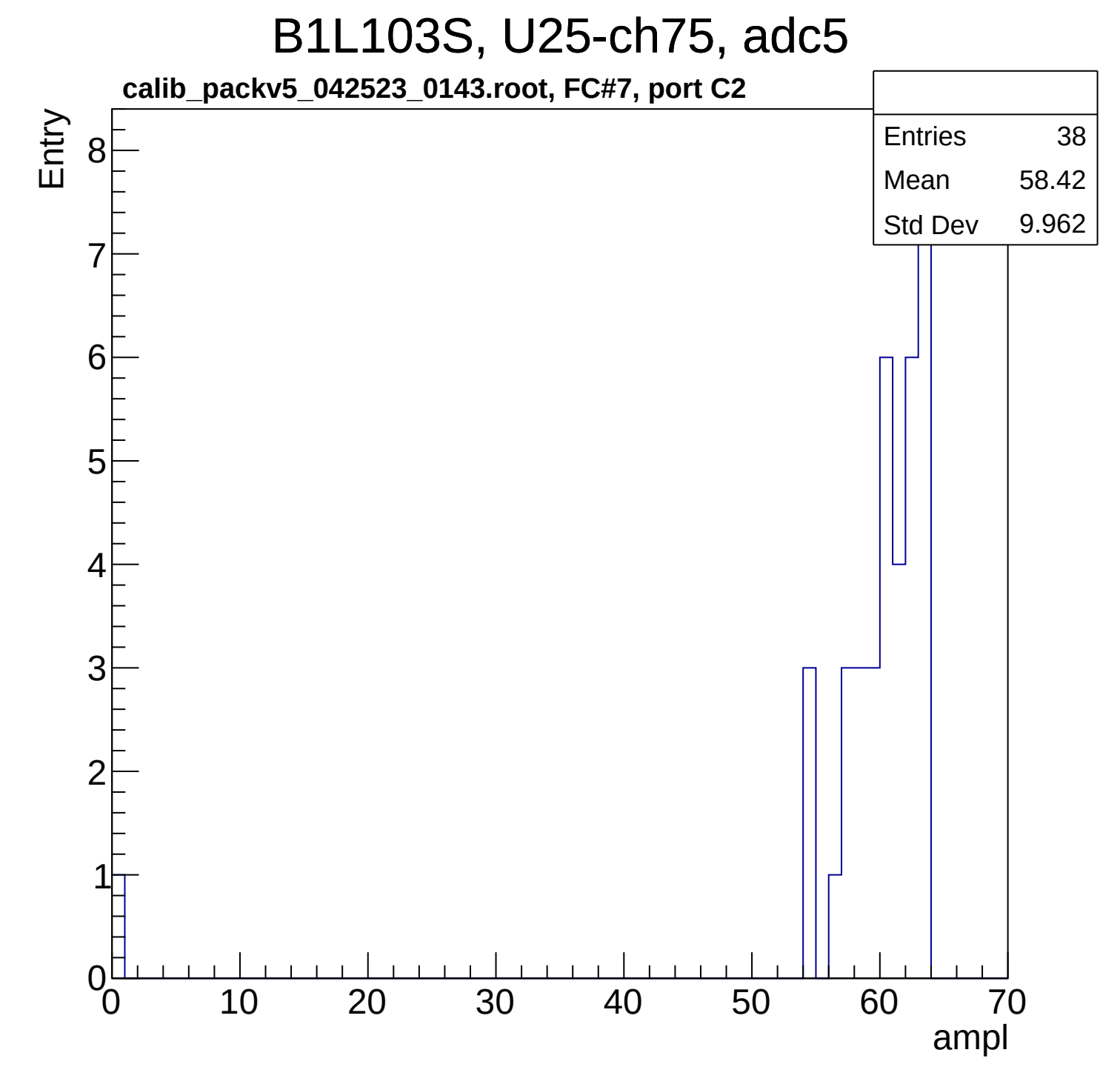
B1L103S, U25-ch75, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.42
Std Dev 9.962
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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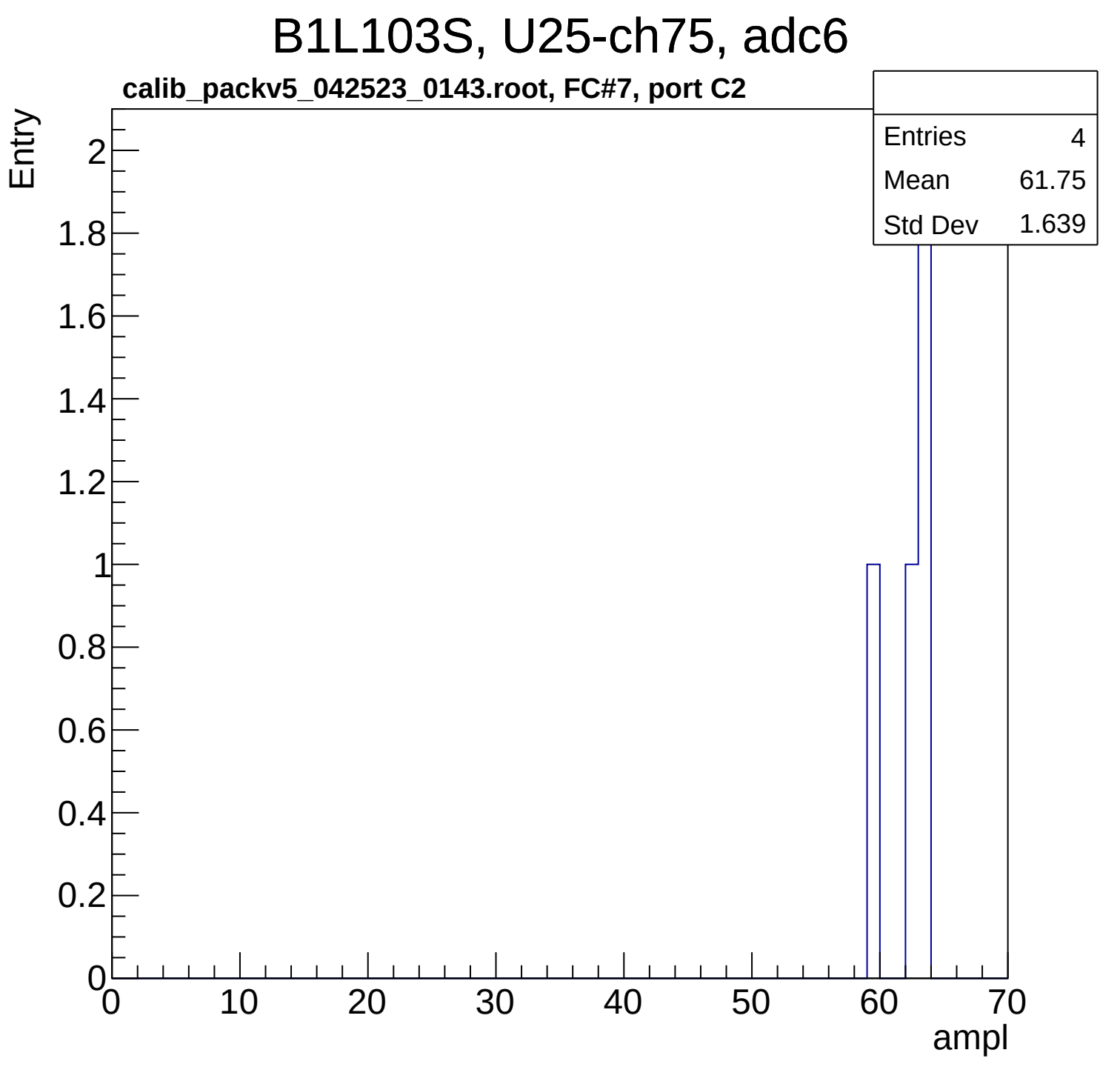
B1L103S, U25-ch75, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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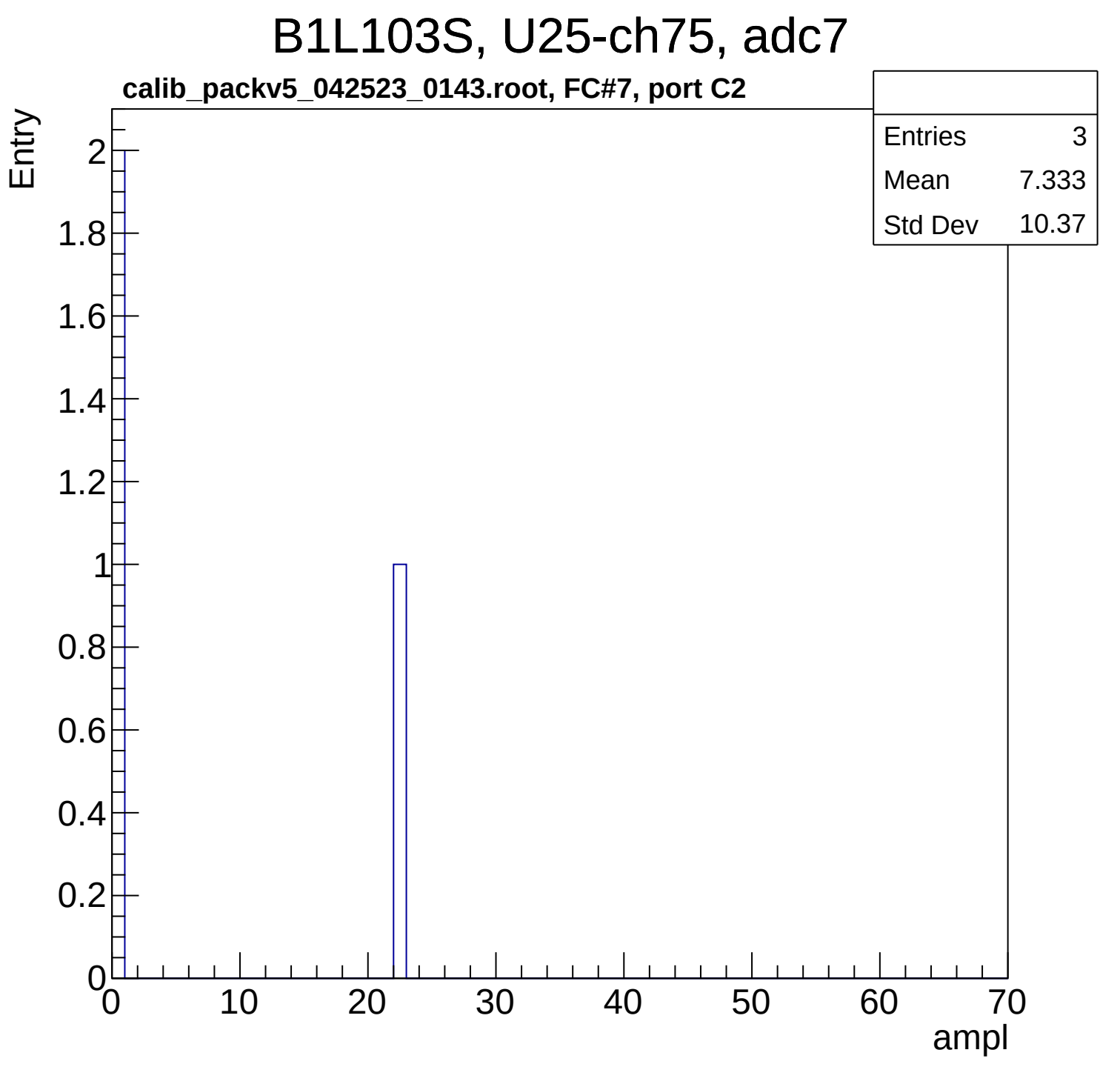
B1L103S, U25-ch75, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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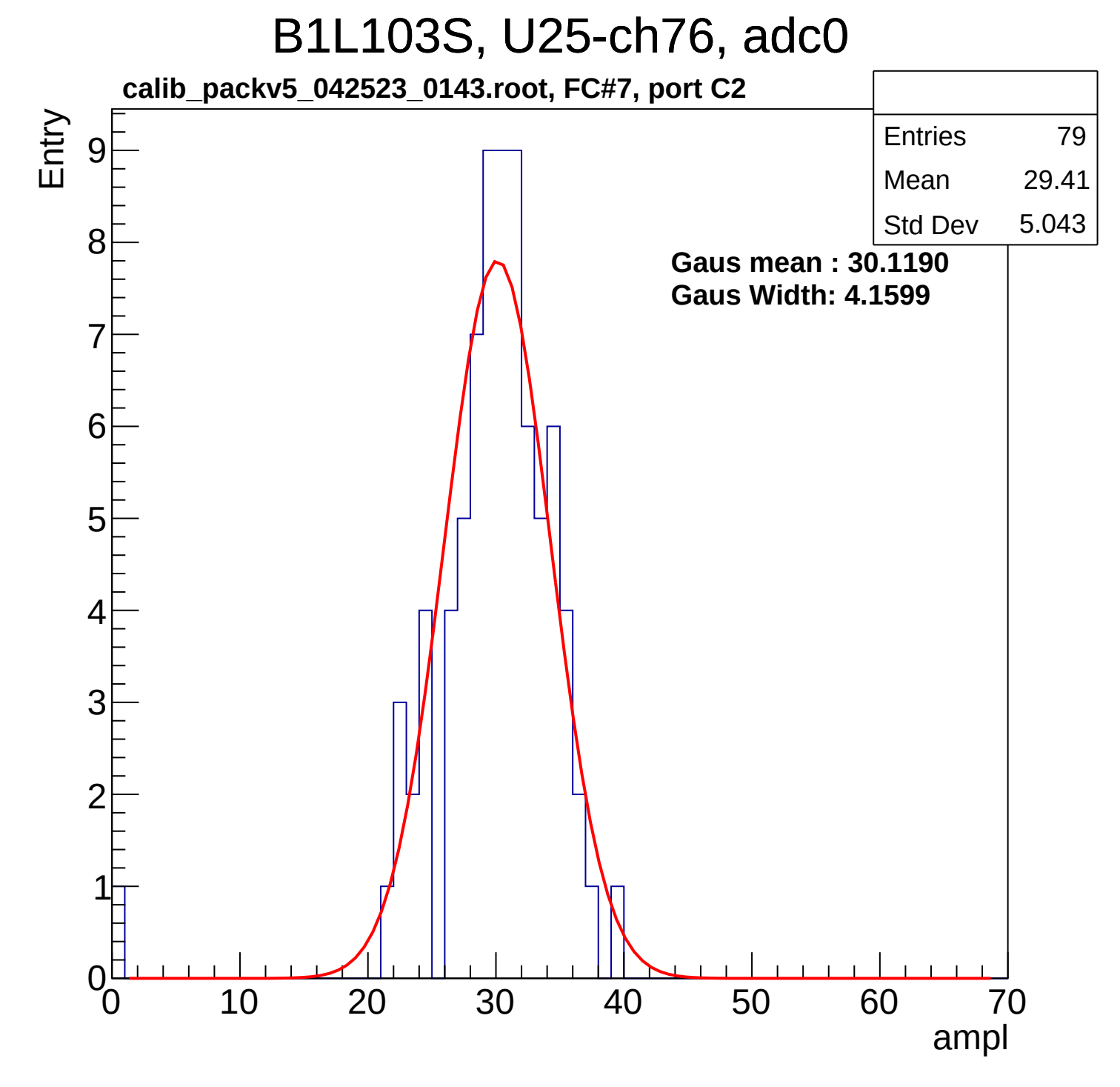
B1L103S, U25-ch76, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.41
Std Dev 5.043
Gaus mean : 30.1190
Gaus Width: 4.1599
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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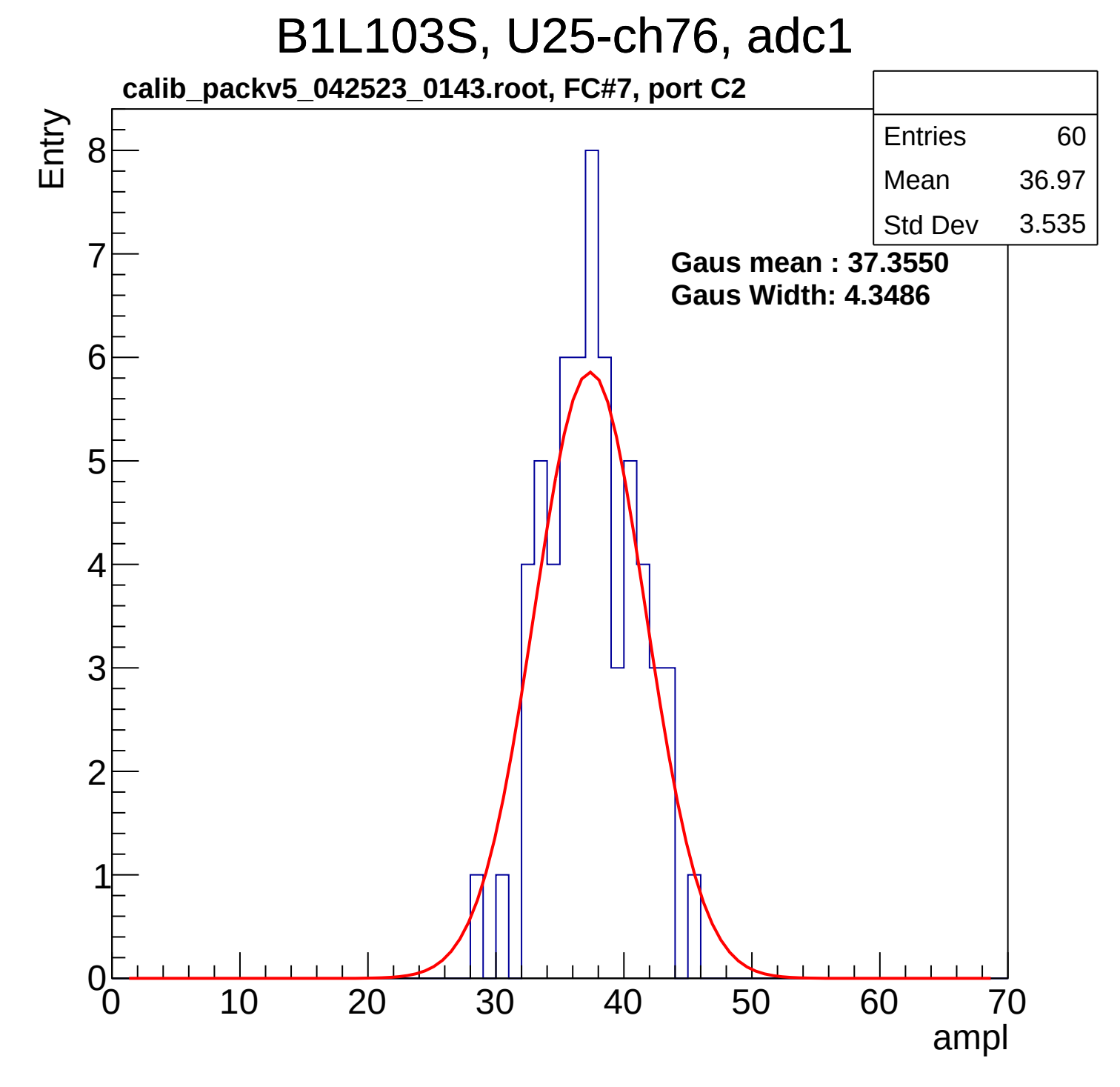
B1L103S, U25-ch76, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 36.97
Std Dev 3.535
Gaus mean : 37.3550
Gaus Width: 4.3486
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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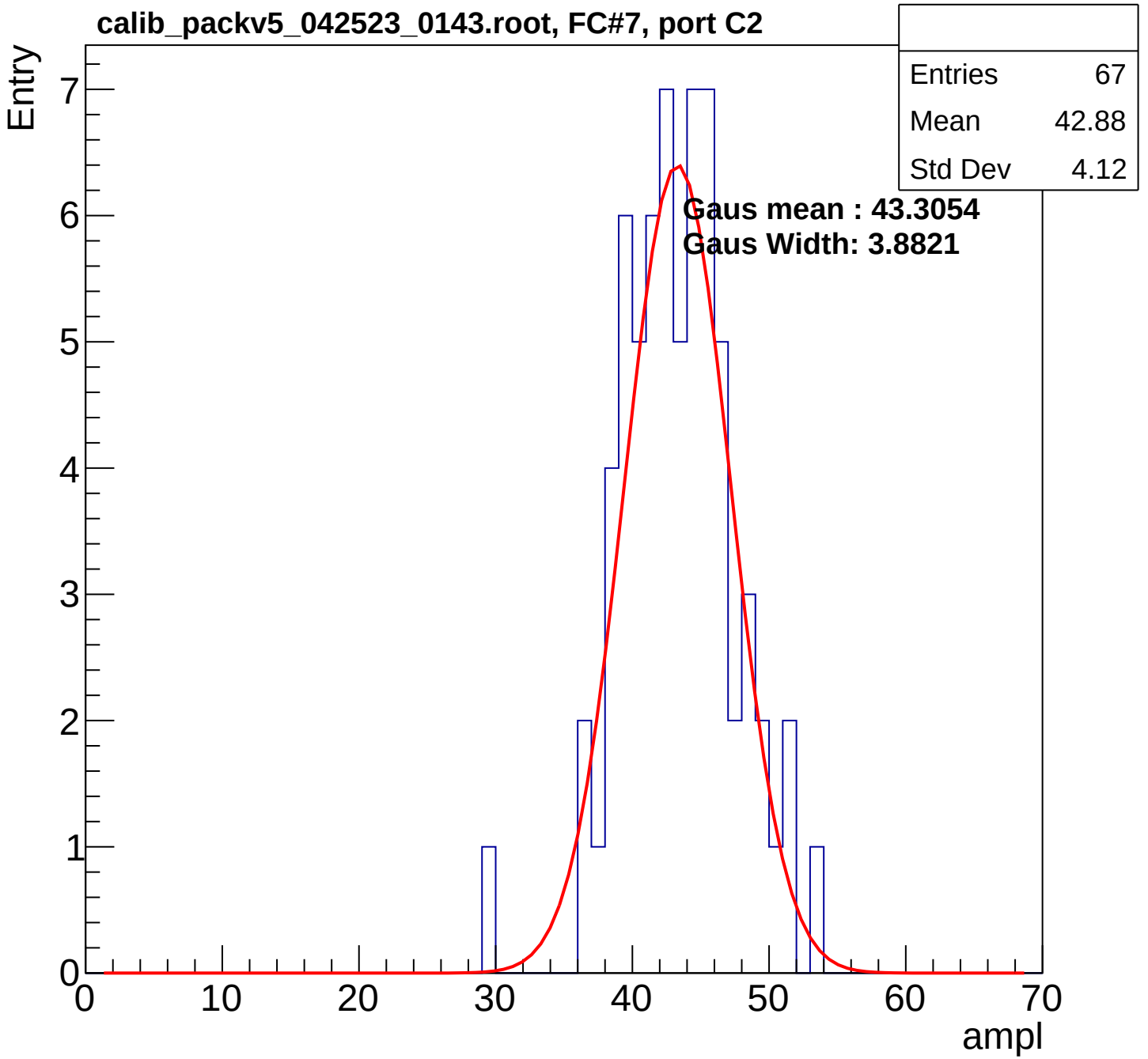
B1L103S, U25-ch76, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 42.88
Std Dev 4.12
Gaus mean : 43.3054
Gaus Width: 3.8821
Entry
ampl
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1
2
3
4
5
6
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0
10
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40
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60
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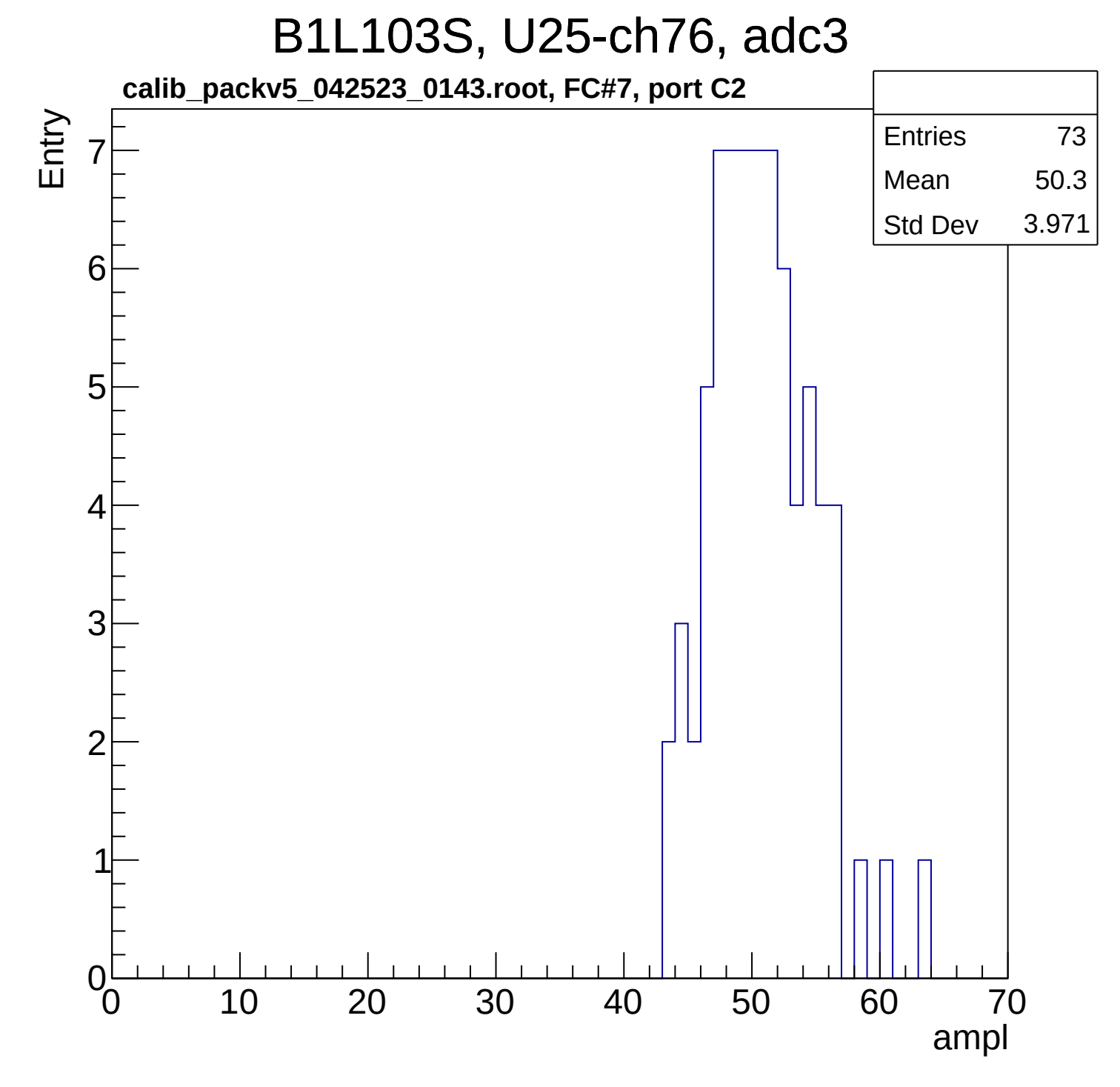
B1L103S, U25-ch76, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 50.3
Std Dev 3.971
Entry
ampl
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0
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20
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40
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60
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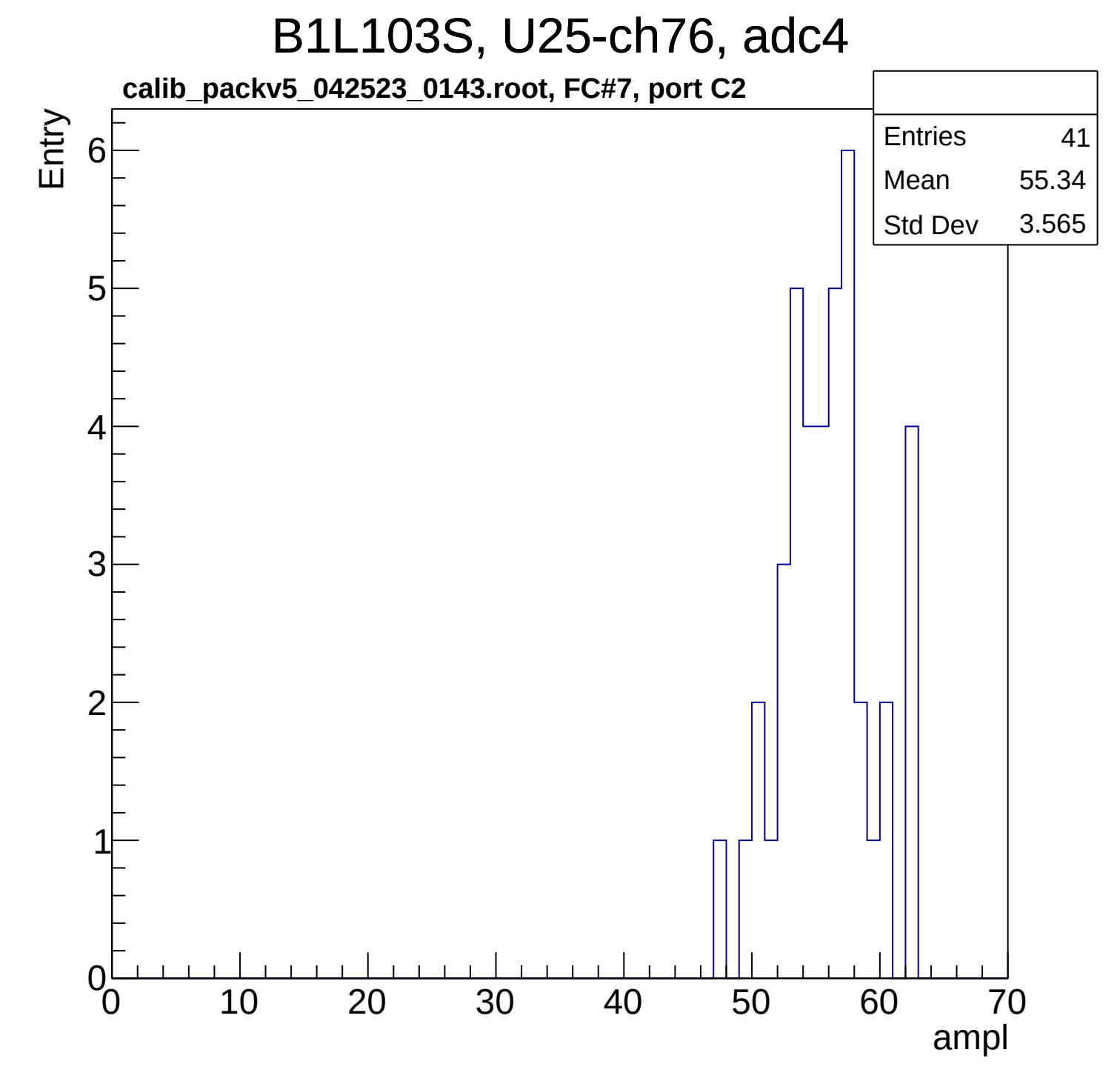
B1L103S, U25-ch76, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 55.34
Std Dev 3.565
Entry
ampl
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0
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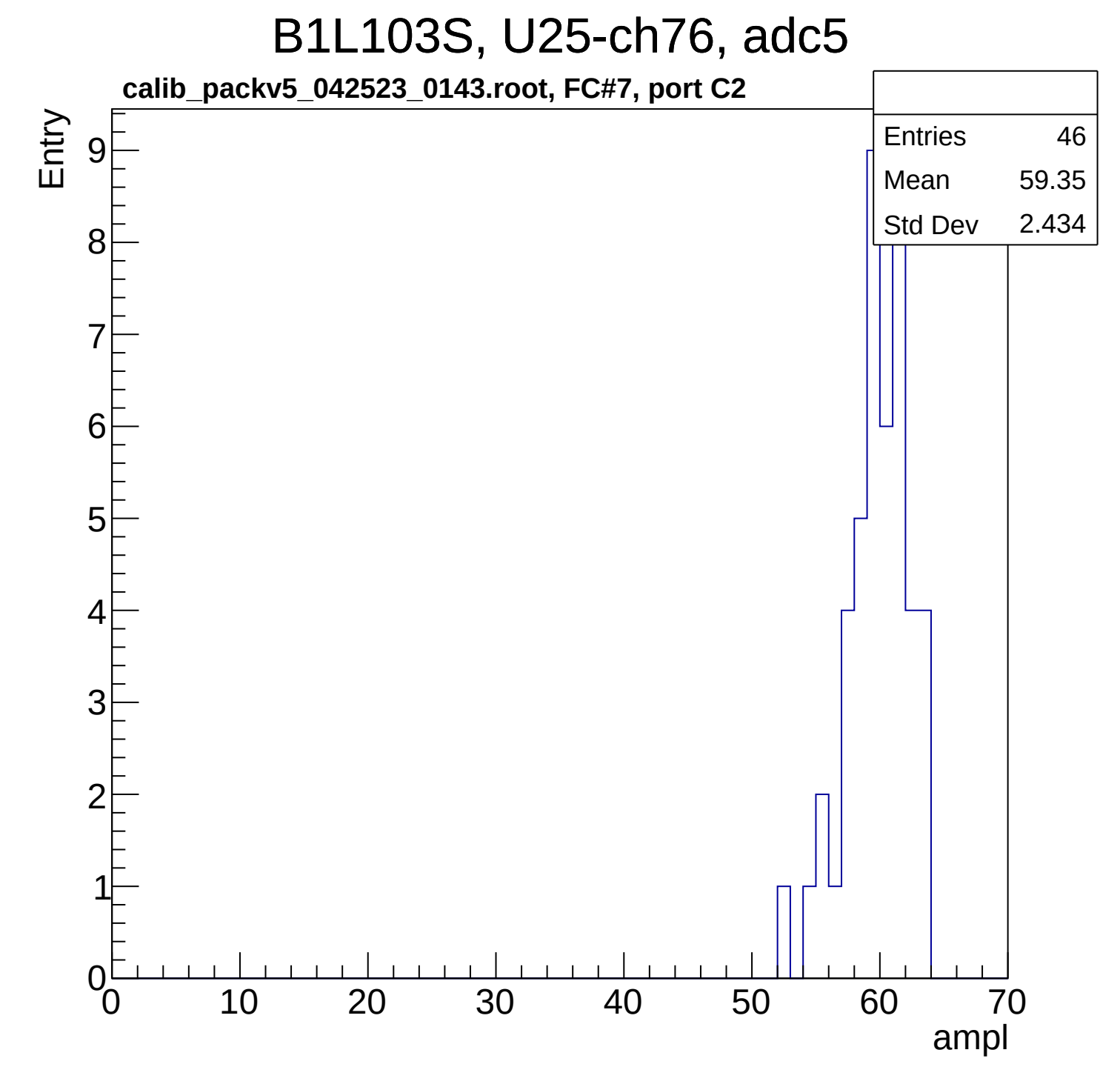
B1L103S, U25-ch76, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 59.35
Std Dev 2.434
Entry
ampl
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3
4
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0
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20
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60
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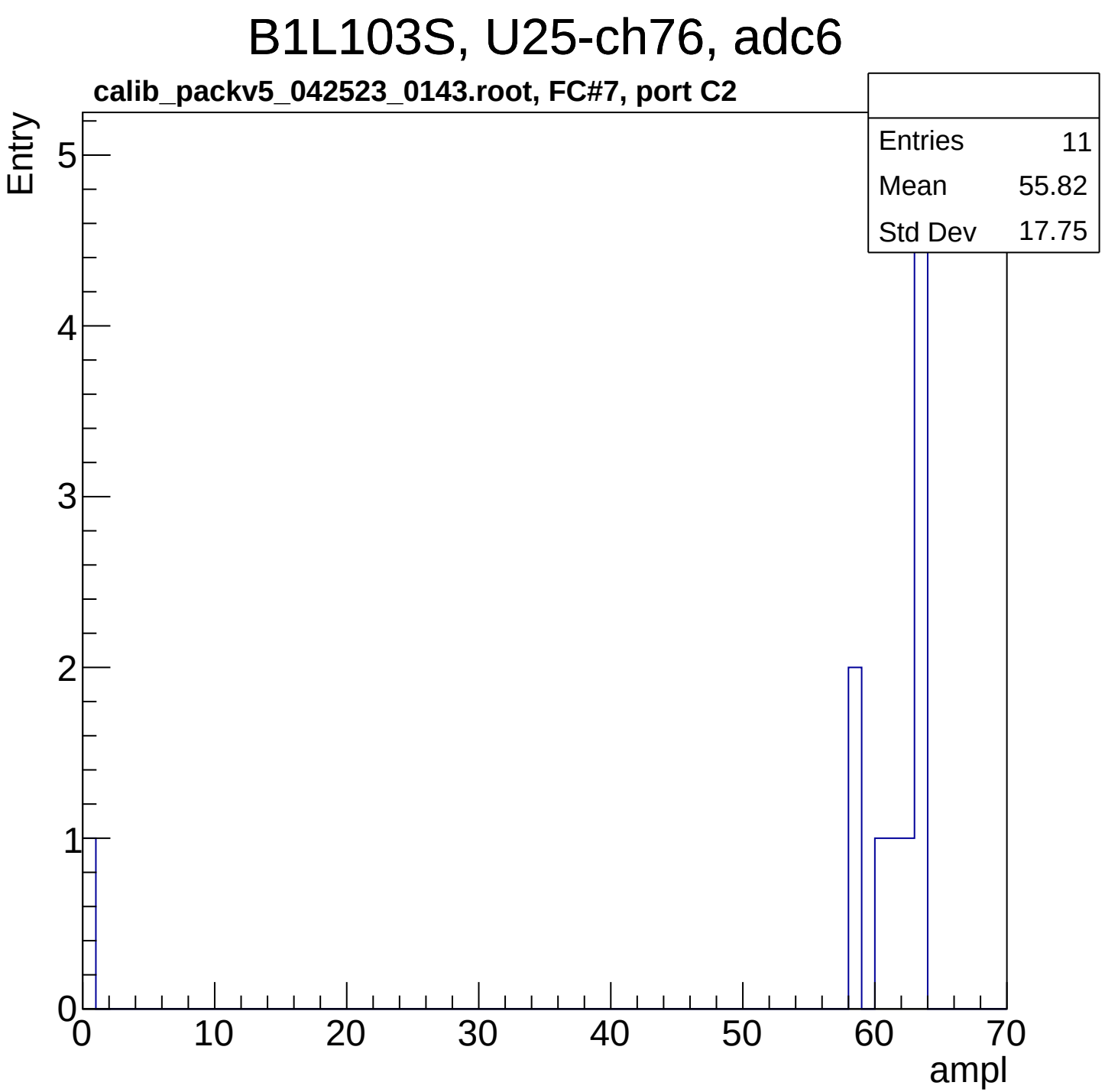
B1L103S, U25-ch76, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 55.82
Std Dev 17.75
Entry
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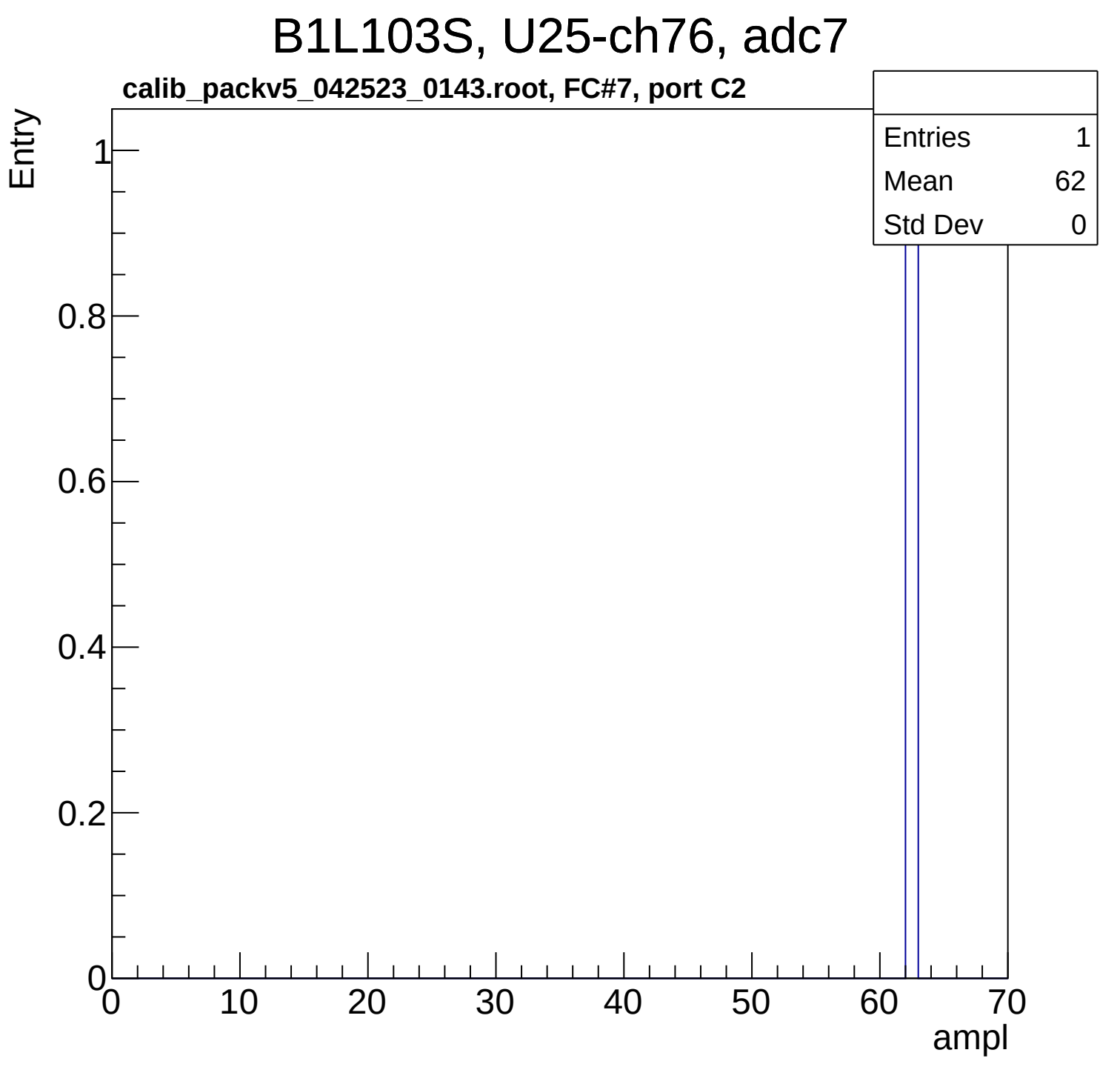
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch76, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
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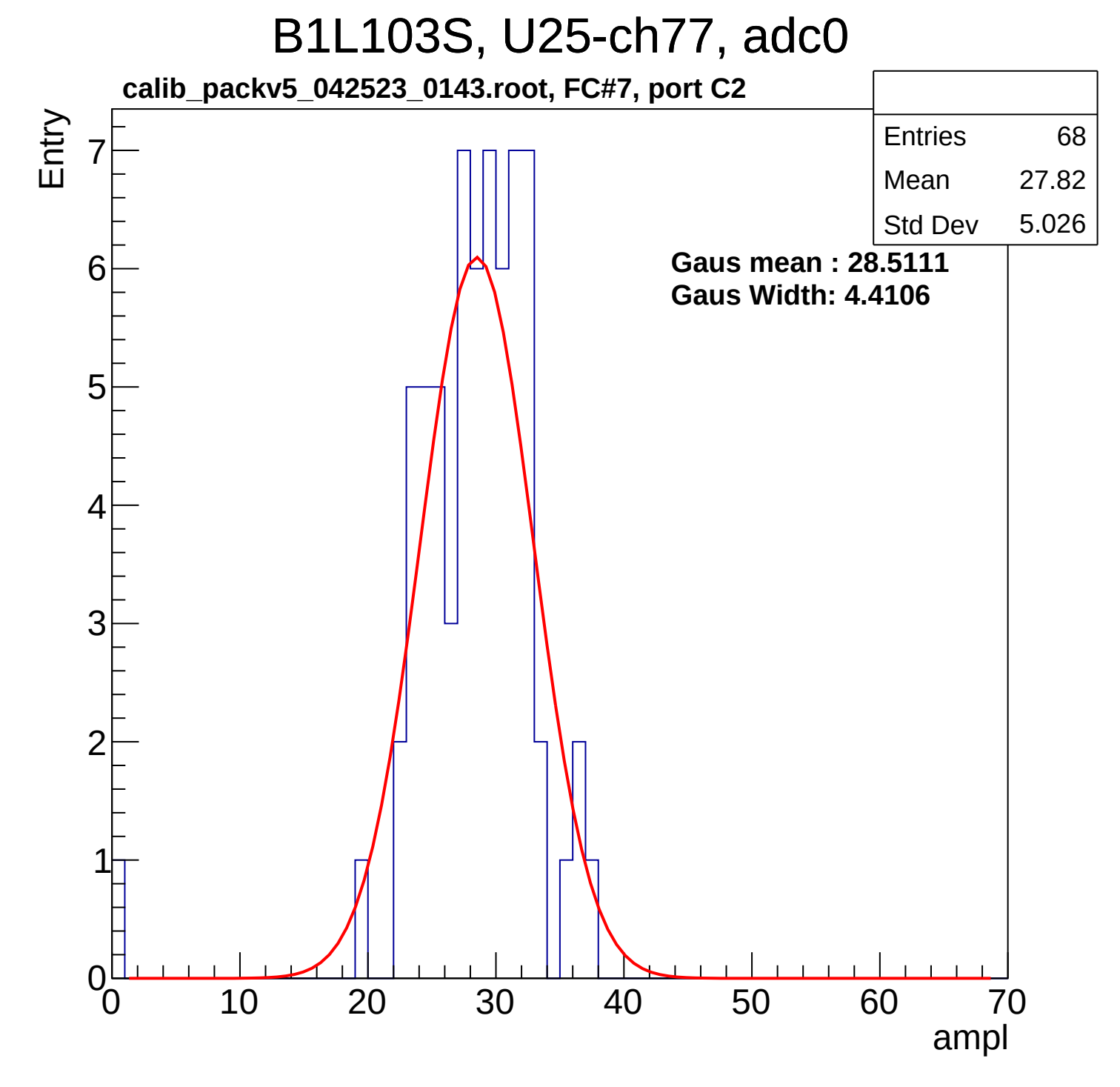
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch77, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 27.82
Std Dev 5.026
Gaus mean : 28.5111
Gaus Width: 4.4106
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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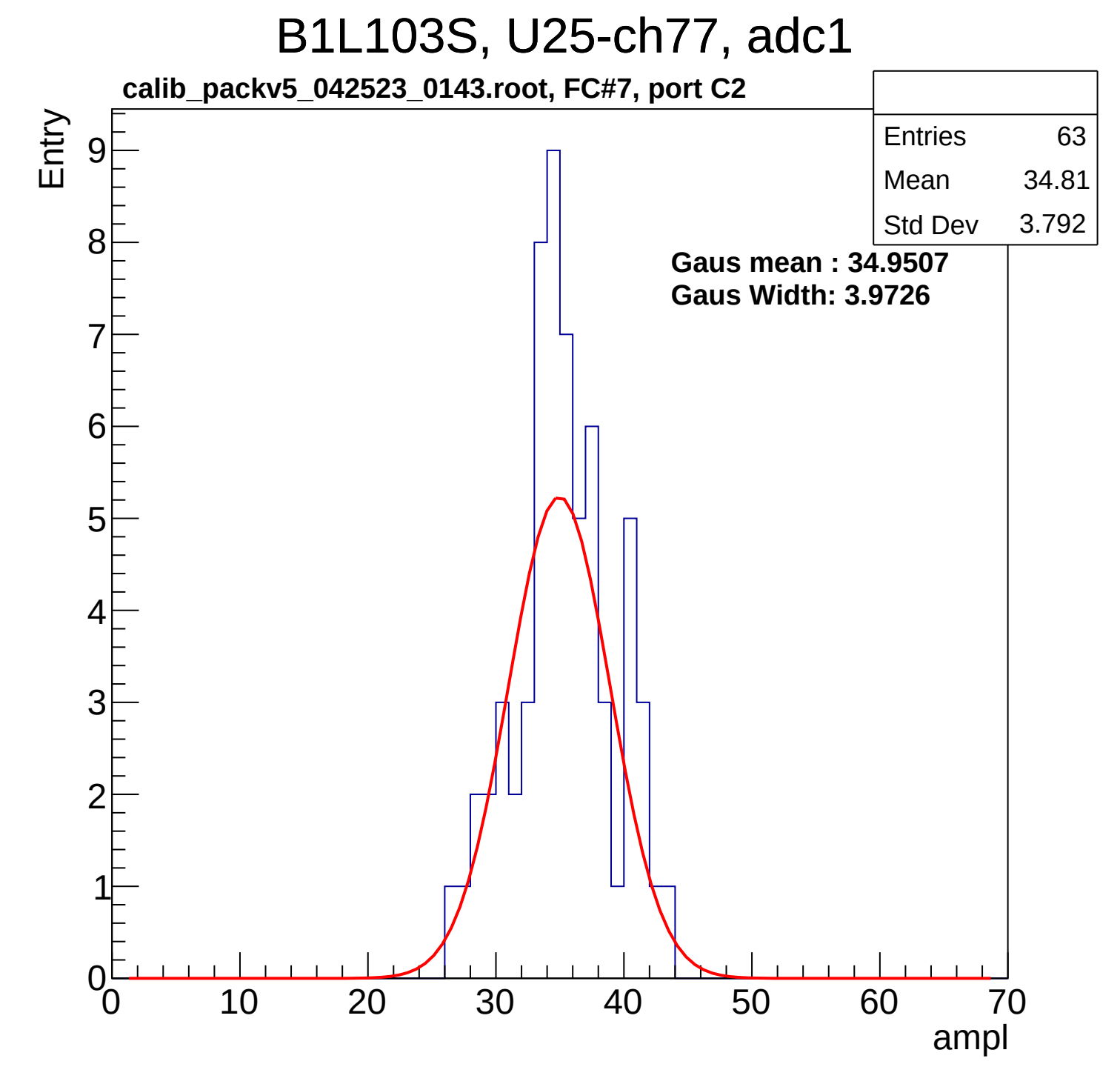
B1L103S, U25-ch77, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 34.81
Std Dev 3.792
Gaus mean : 34.9507
Gaus Width: 3.9726
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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60
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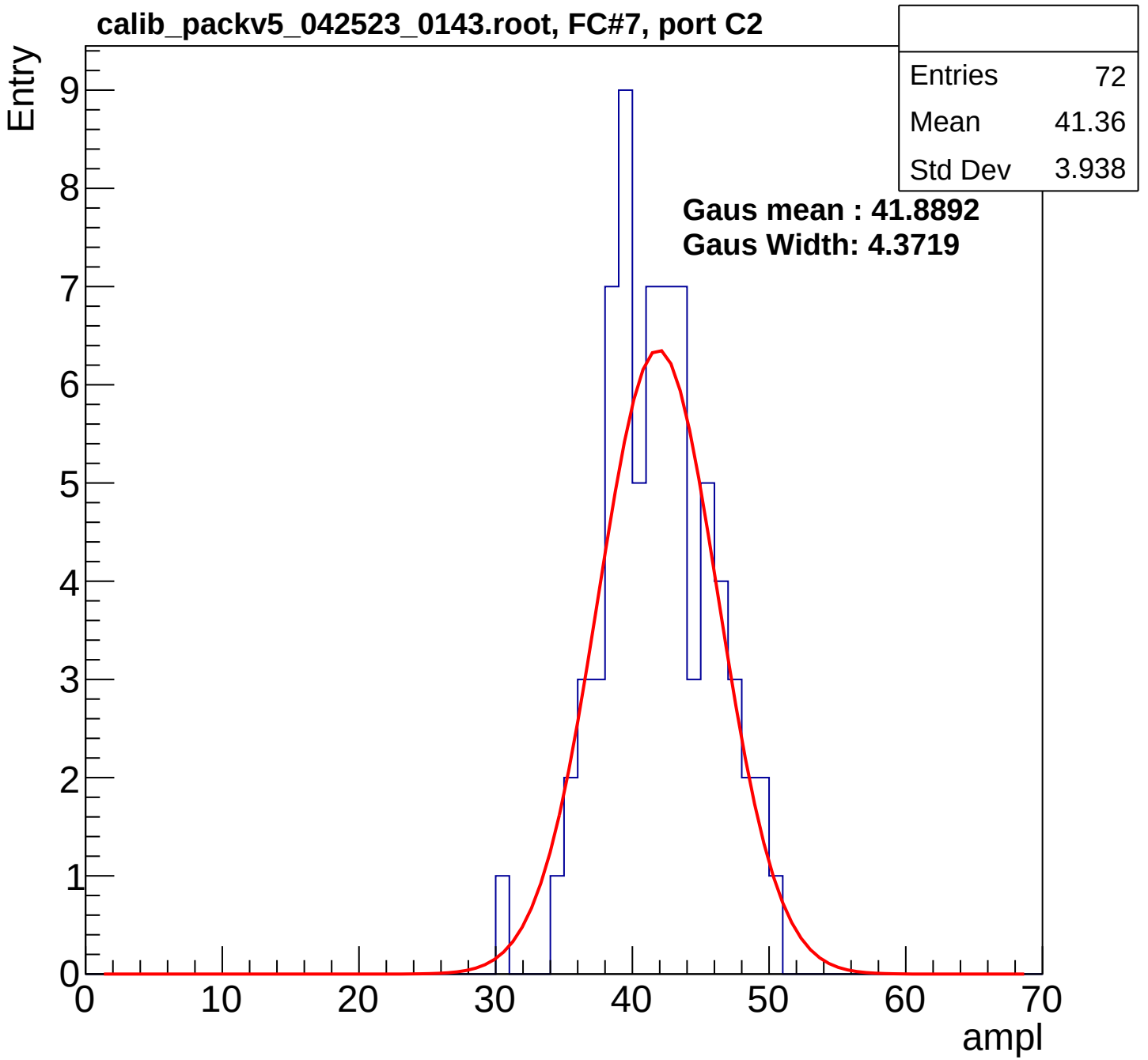
B1L103S, U25-ch77, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 41.36
Std Dev 3.938
Gaus mean : 41.8892
Gaus Width: 4.3719
Entry
ampl
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3
4
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0
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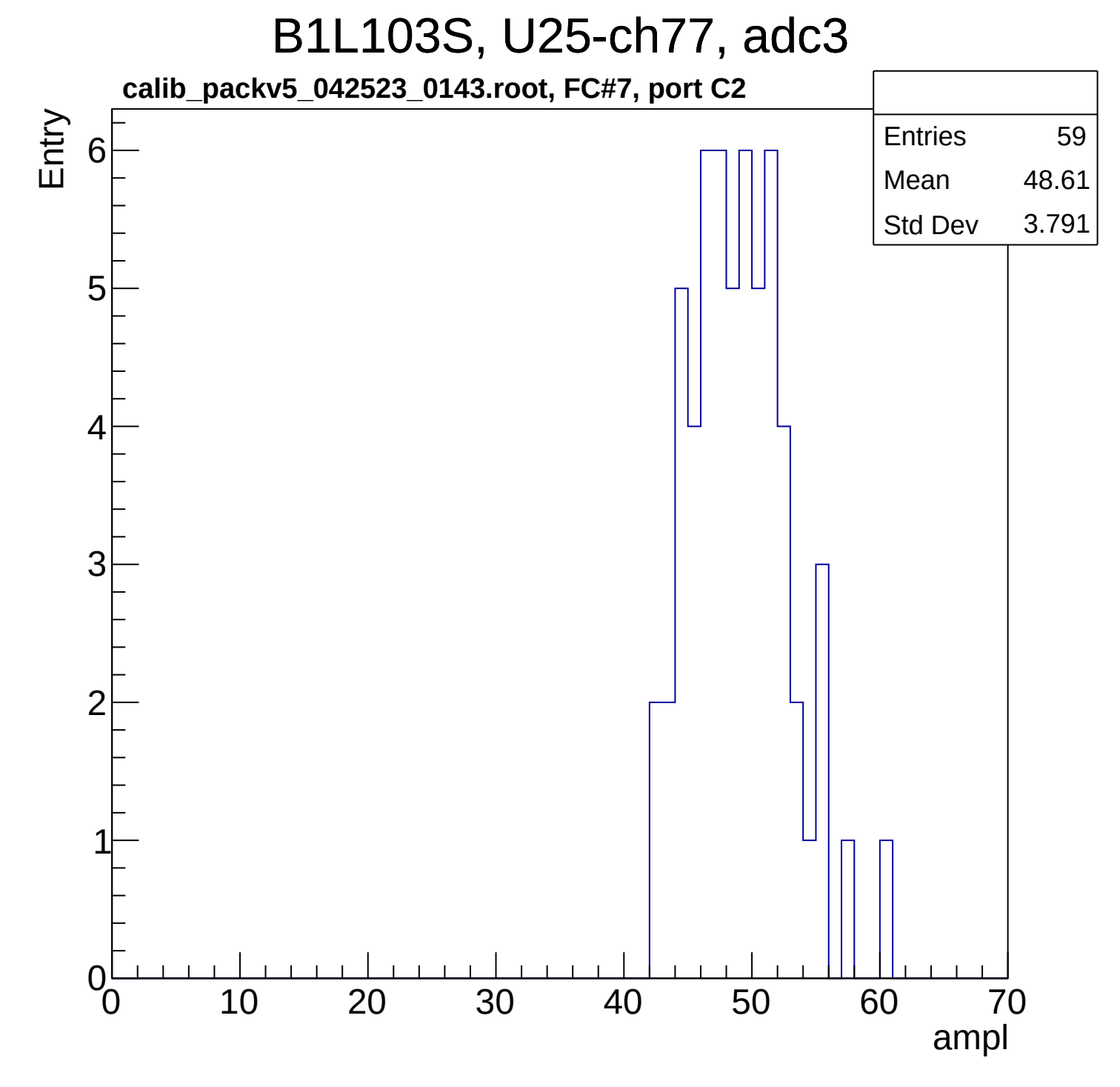
B1L103S, U25-ch77, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 48.61
Std Dev 3.791
Entry
0
1
2
3
4
5
6
0
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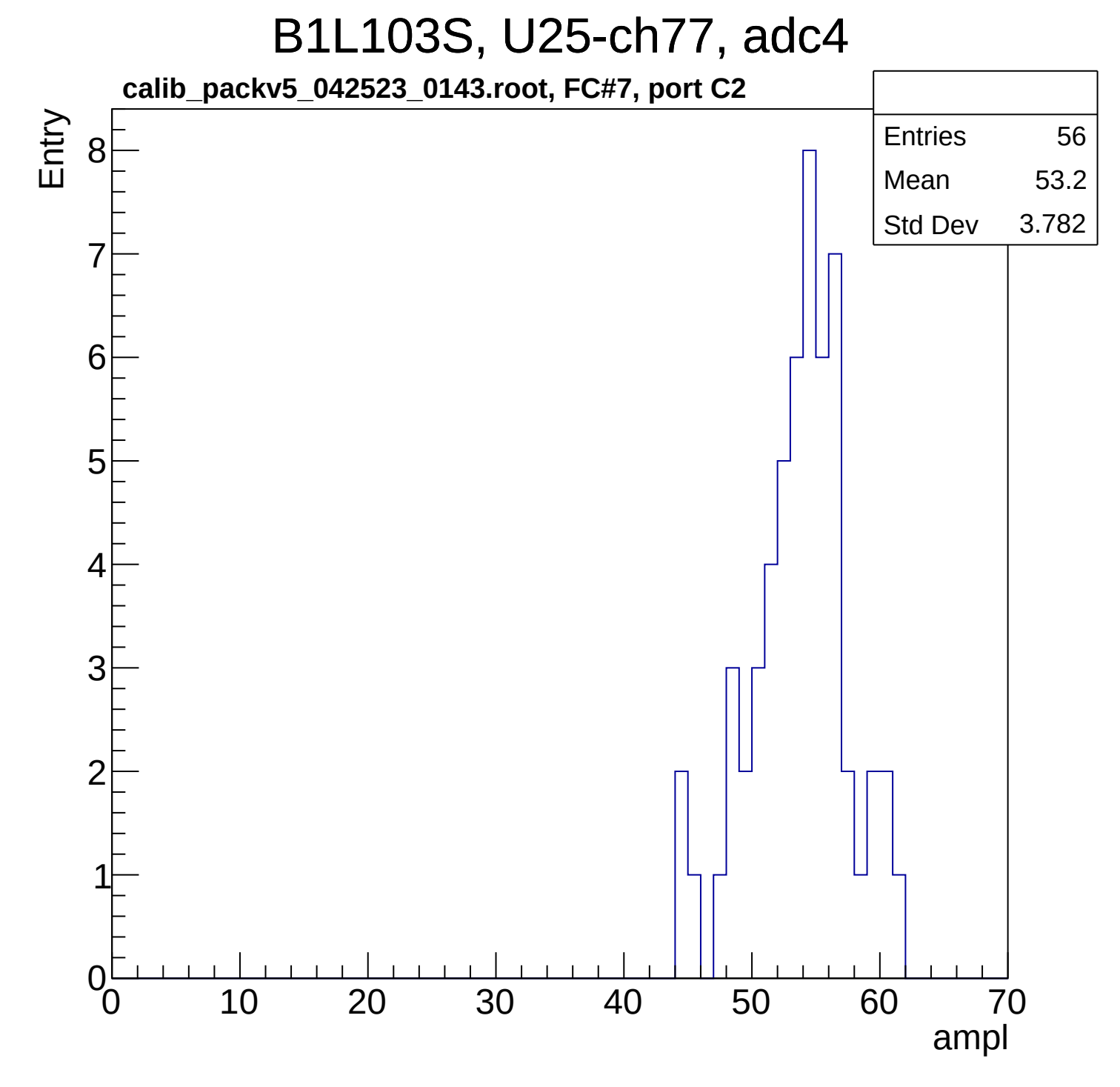
50
60
70
ampl


B1L103S, U25-ch77, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 53.2
Std Dev 3.782
Entry
ampl
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1
2
3
4
5
6
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0
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40
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60
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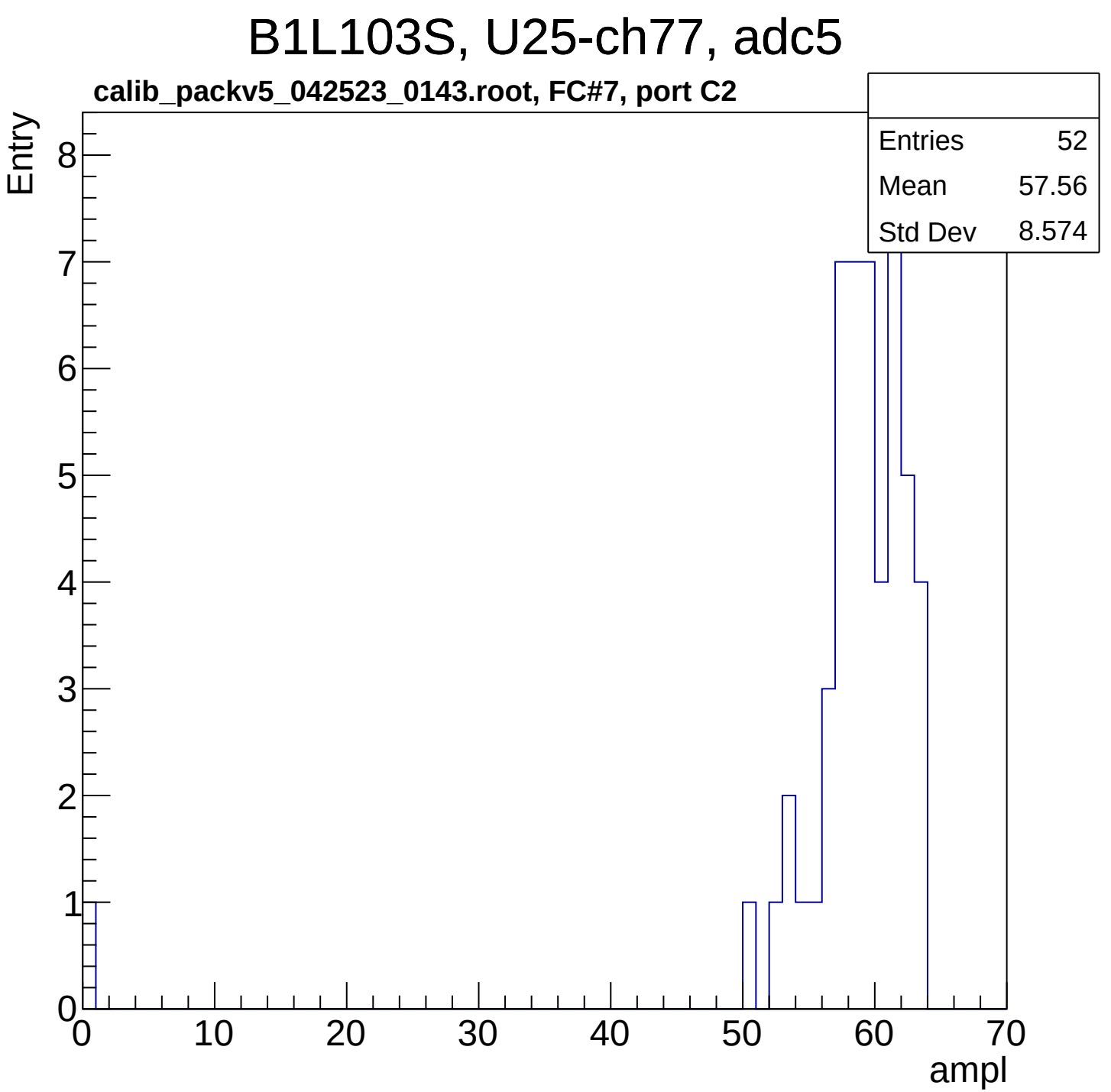
B1L103S, U25-ch77, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.56
Std Dev 8.574
Entry
ampl
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0
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40
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60
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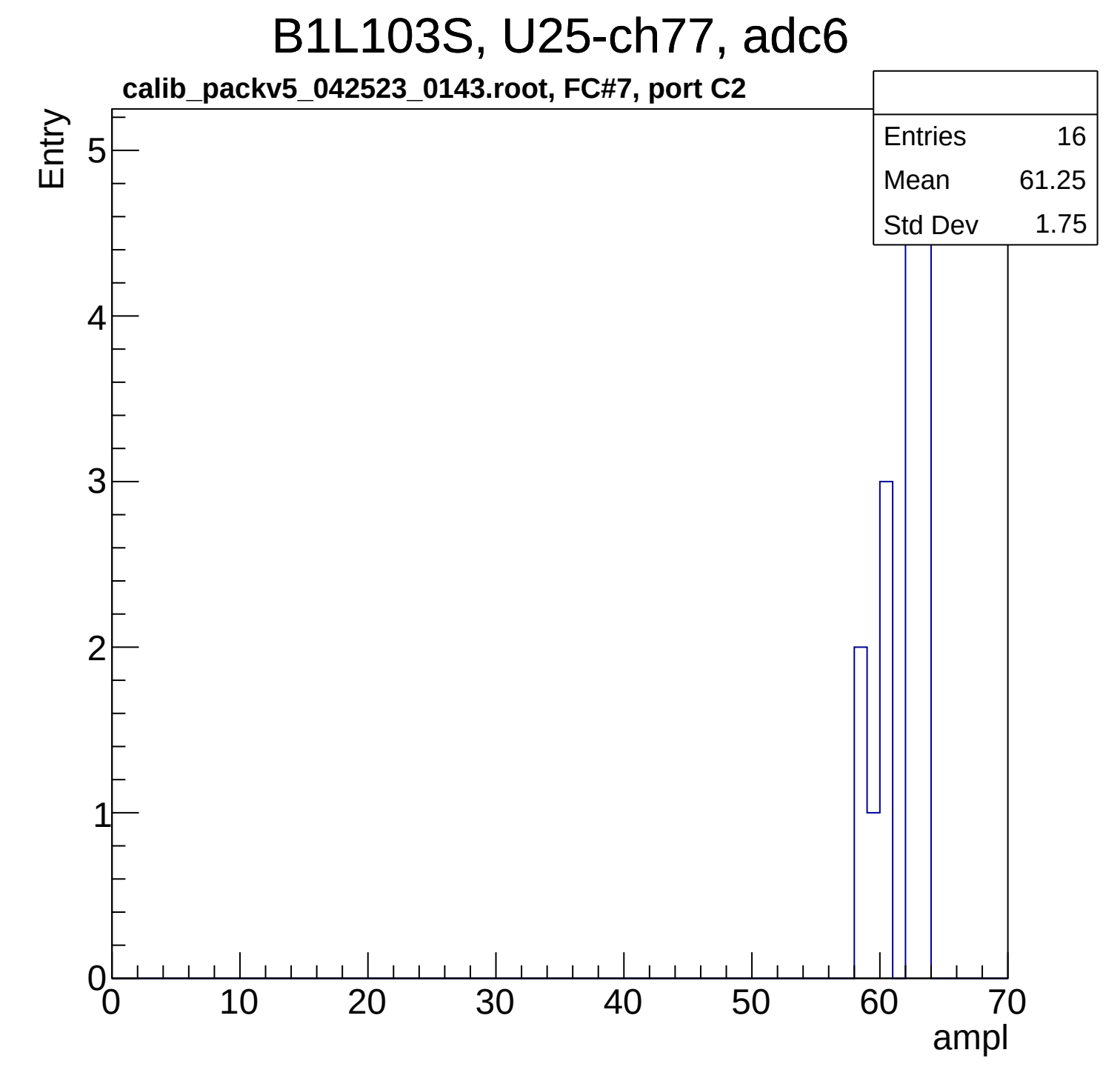
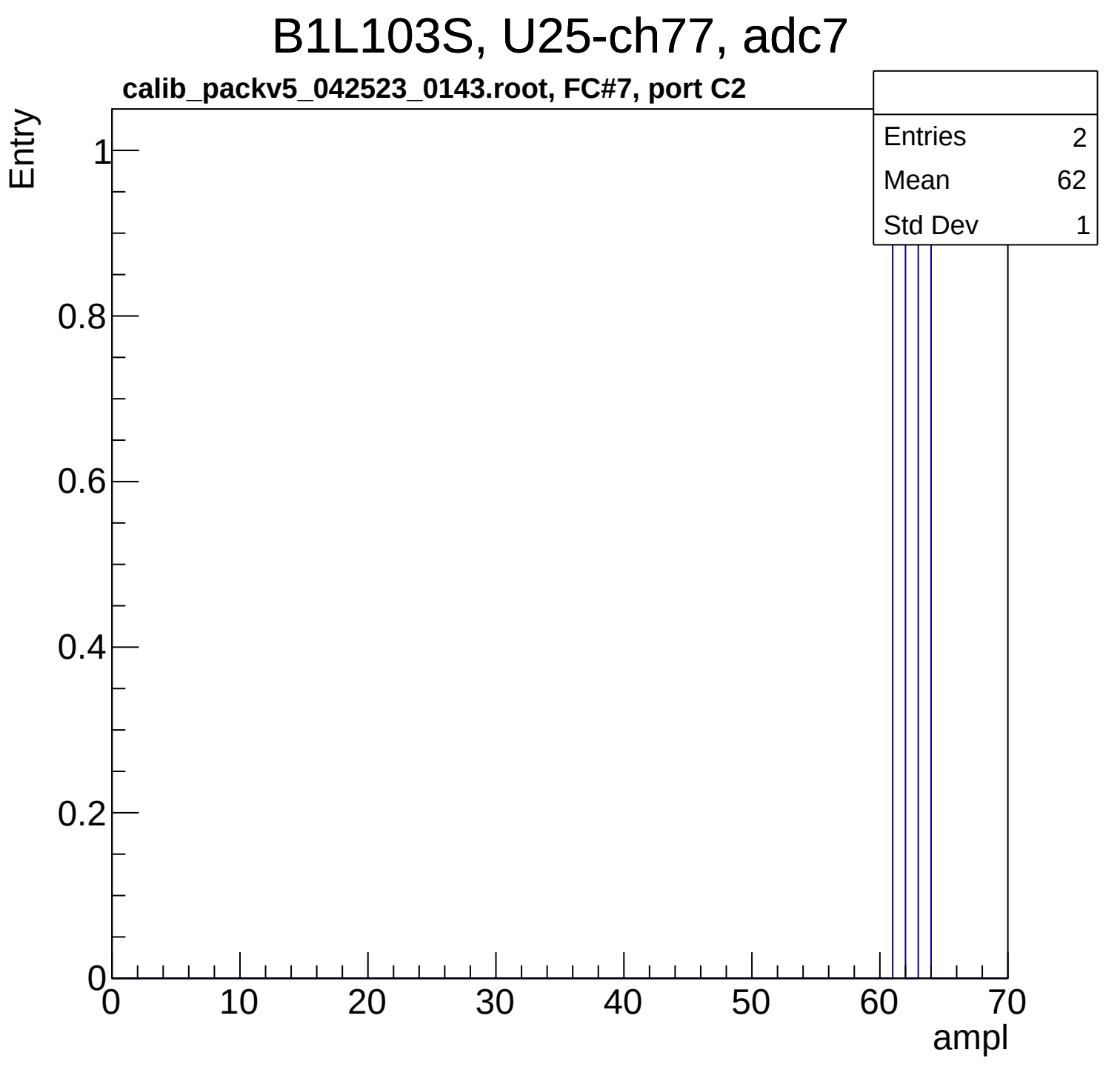
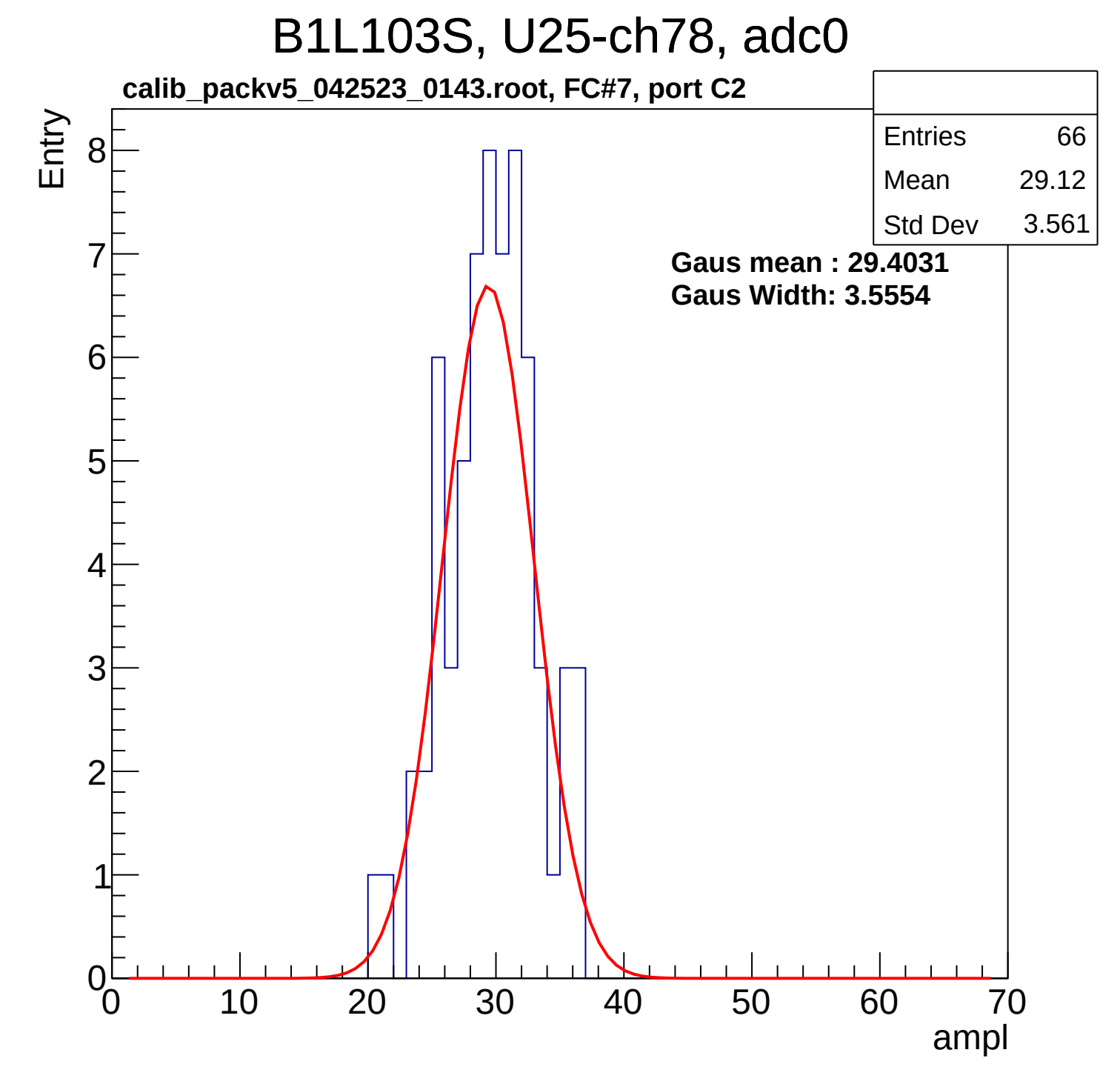
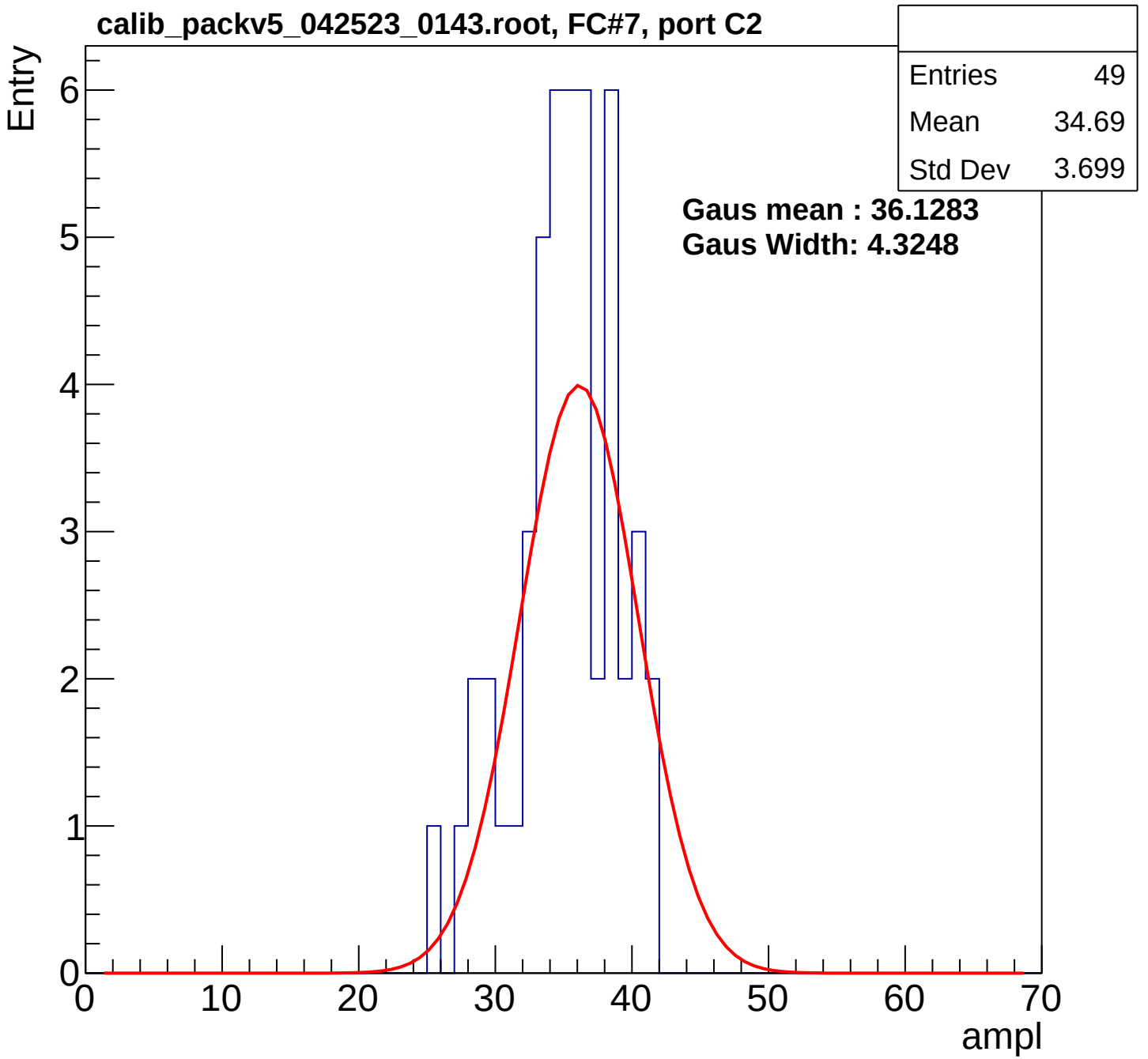

B1L103S, U25-ch77, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 61.25
Std Dev 1.75
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch77, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U25-ch78, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.12
Std Dev 3.561
Gaus mean : 29.4031
Gaus Width: 3.5554
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch78, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 34.69
Std Dev 3.699
Gaus mean : 36.1283
Gaus Width: 4.3248
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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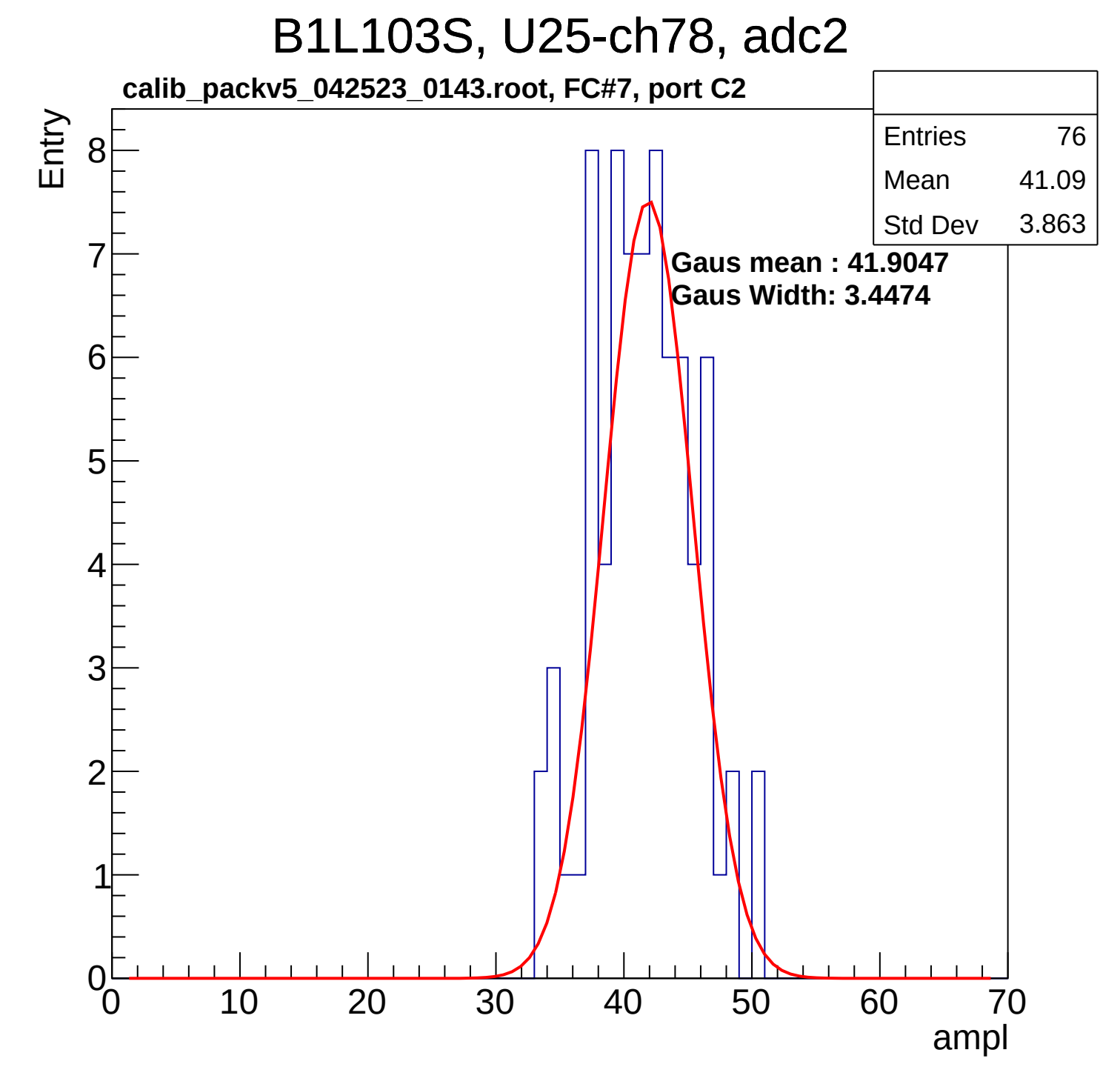
B1L103S, U25-ch78, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 41.09
Std Dev 3.863
Gaus mean : 41.9047
Gaus Width: 3.4474
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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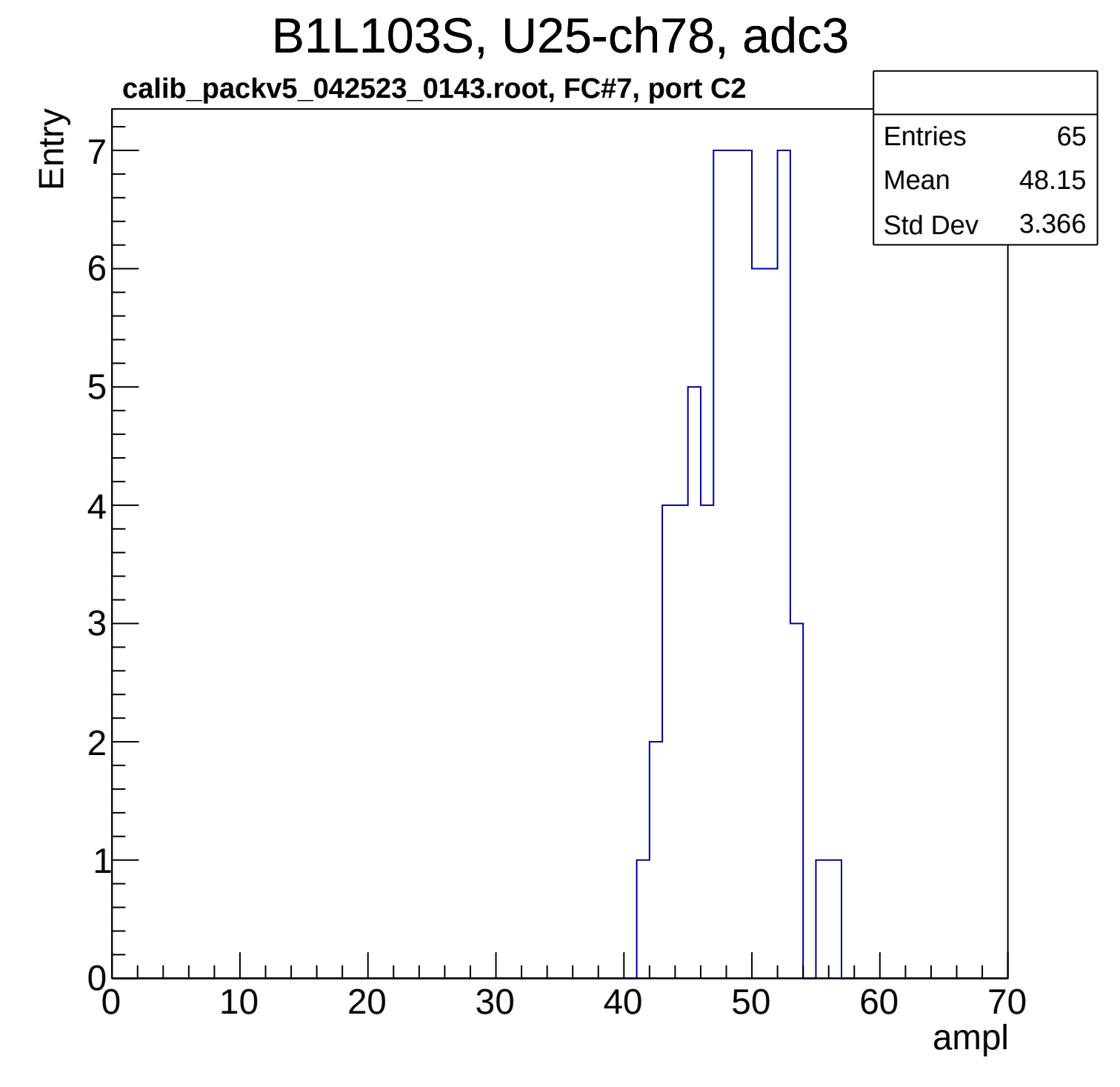
B1L103S, U25-ch78, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.15
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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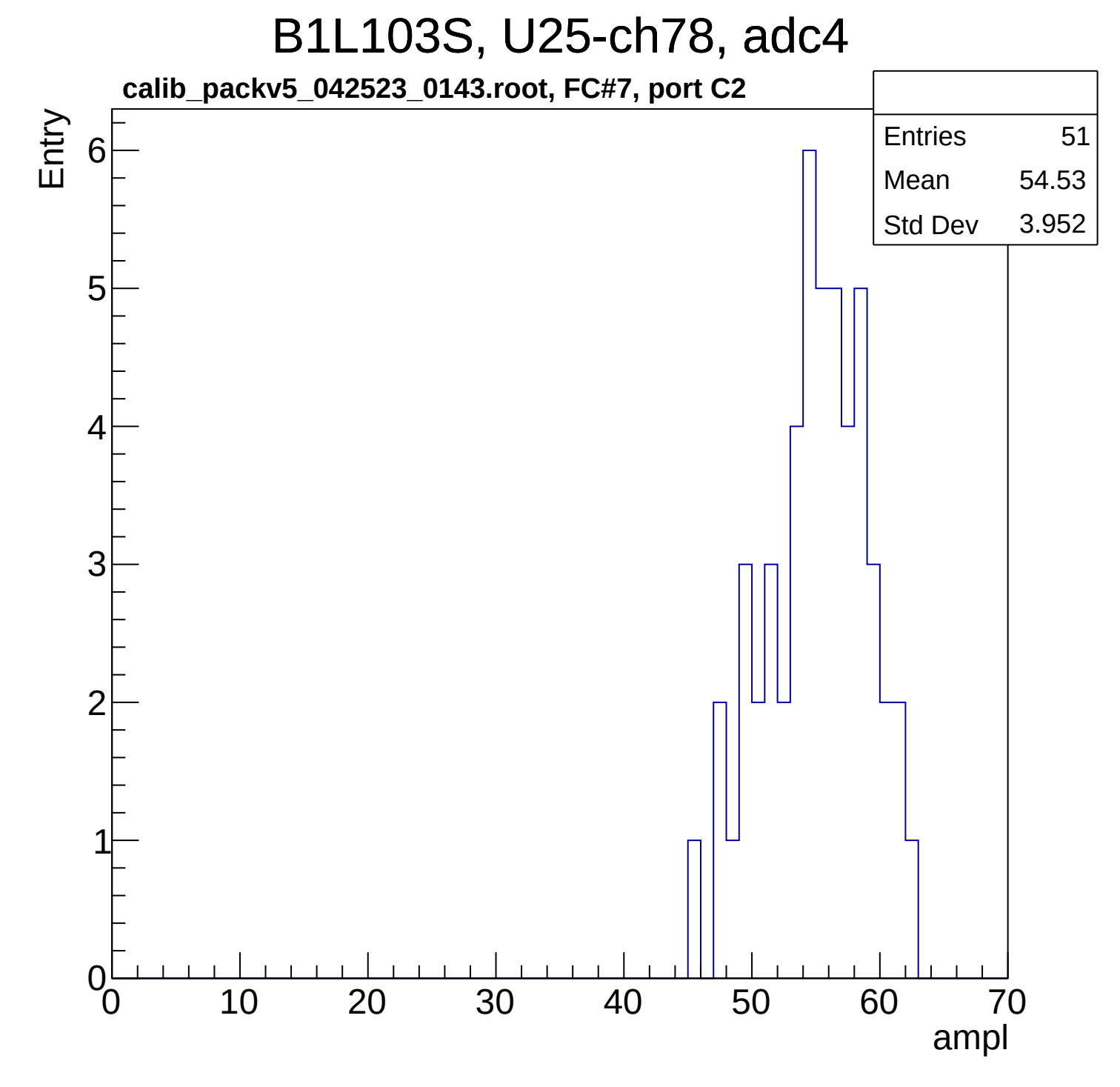
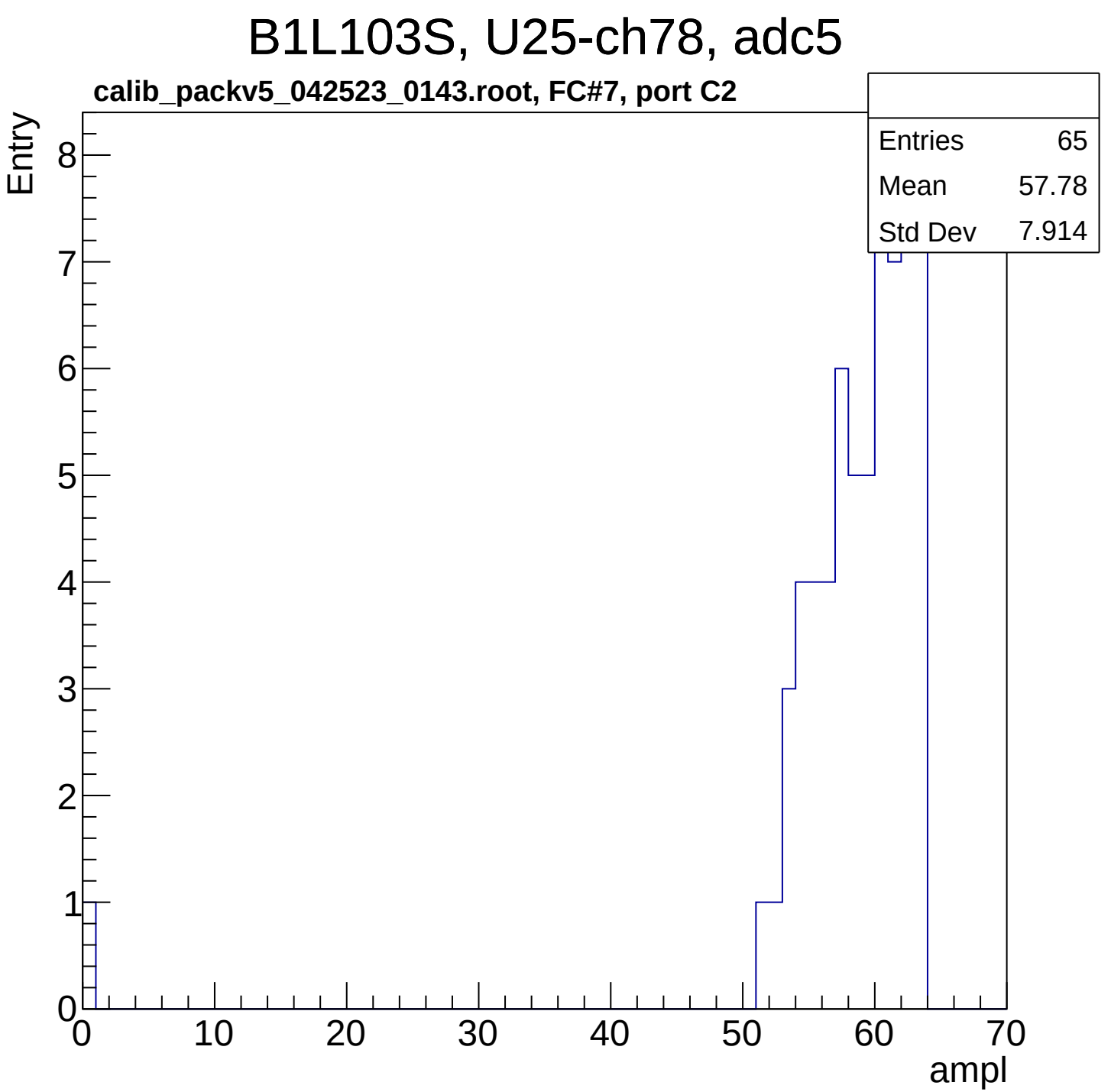
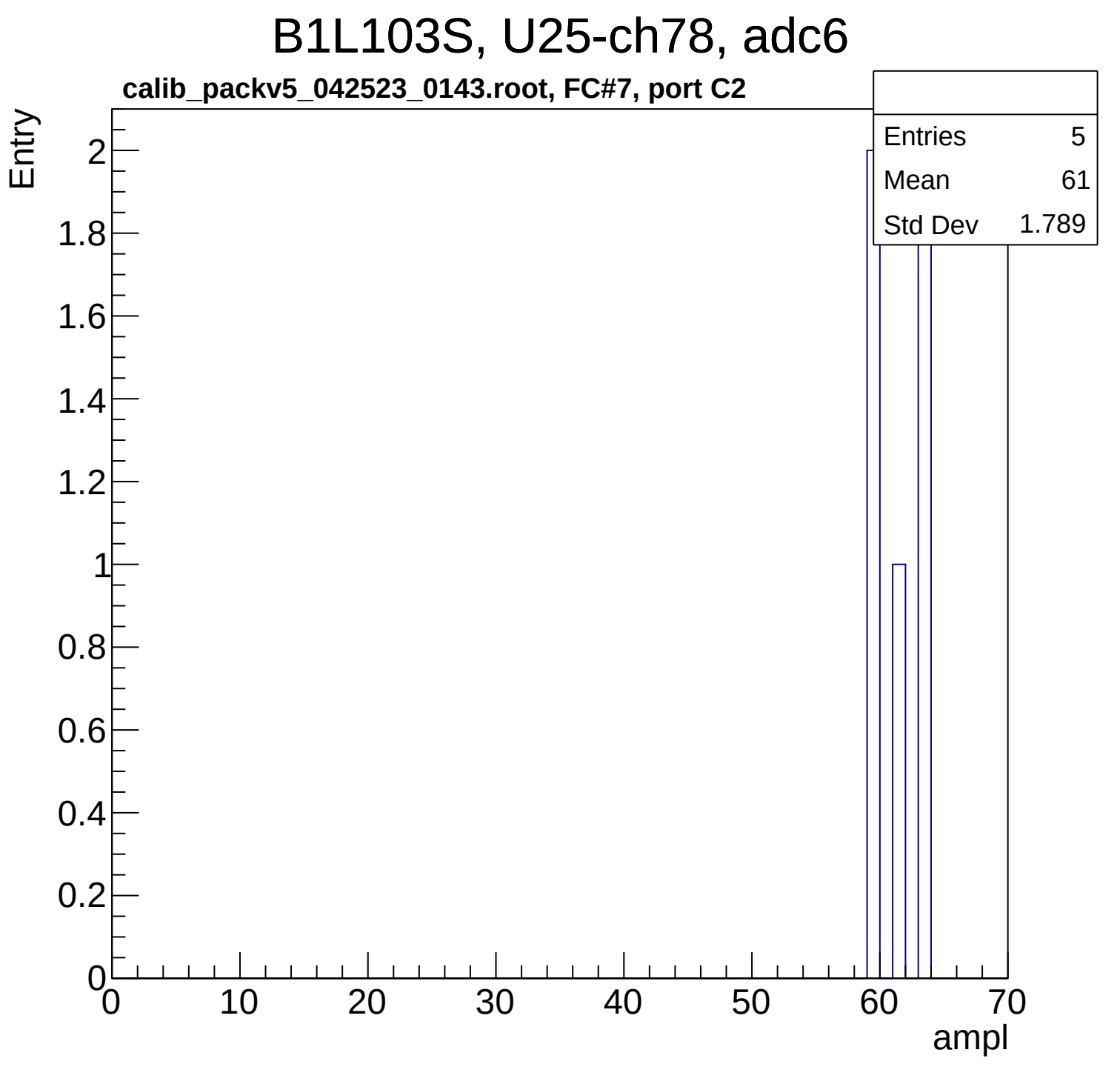
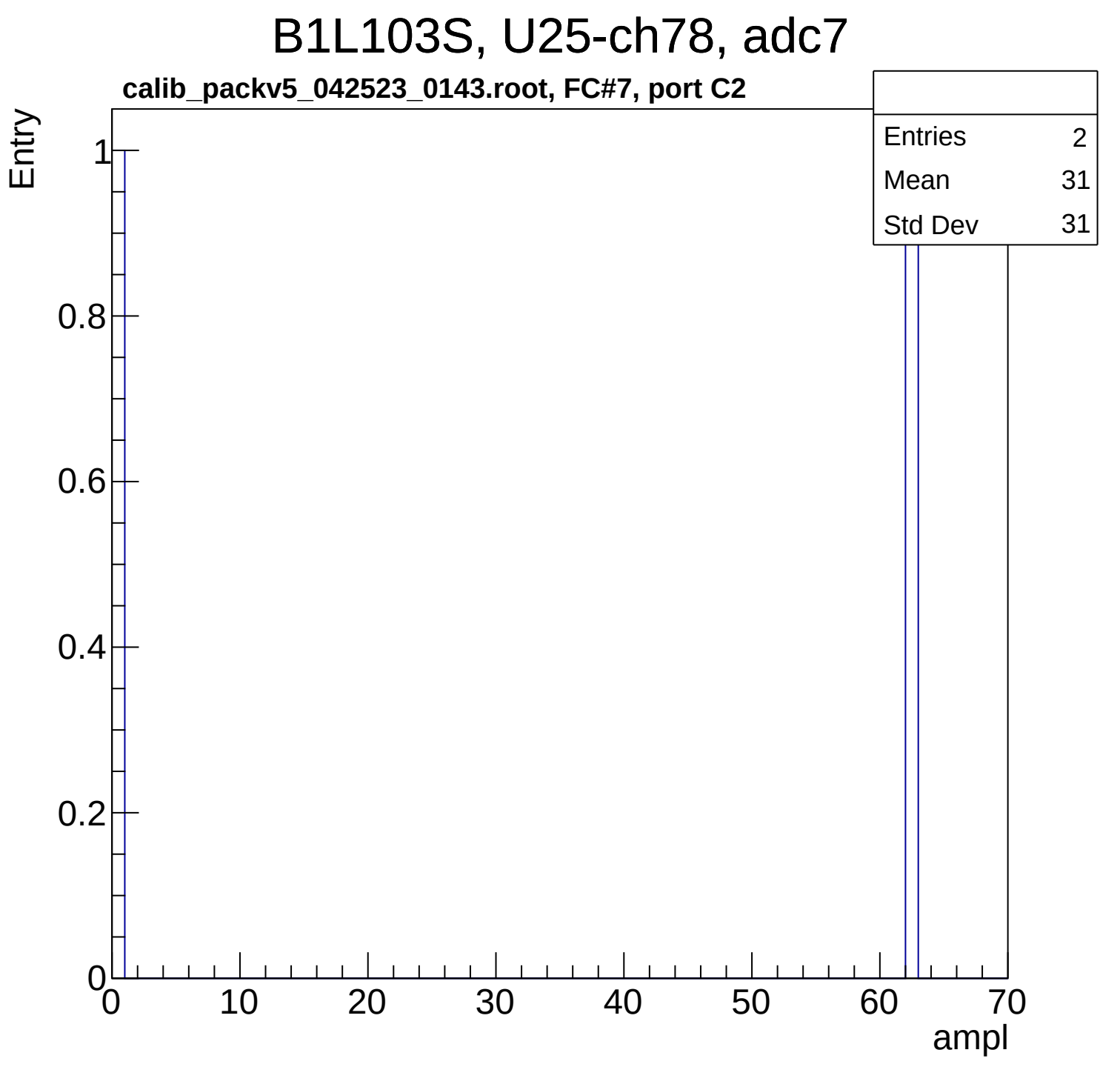
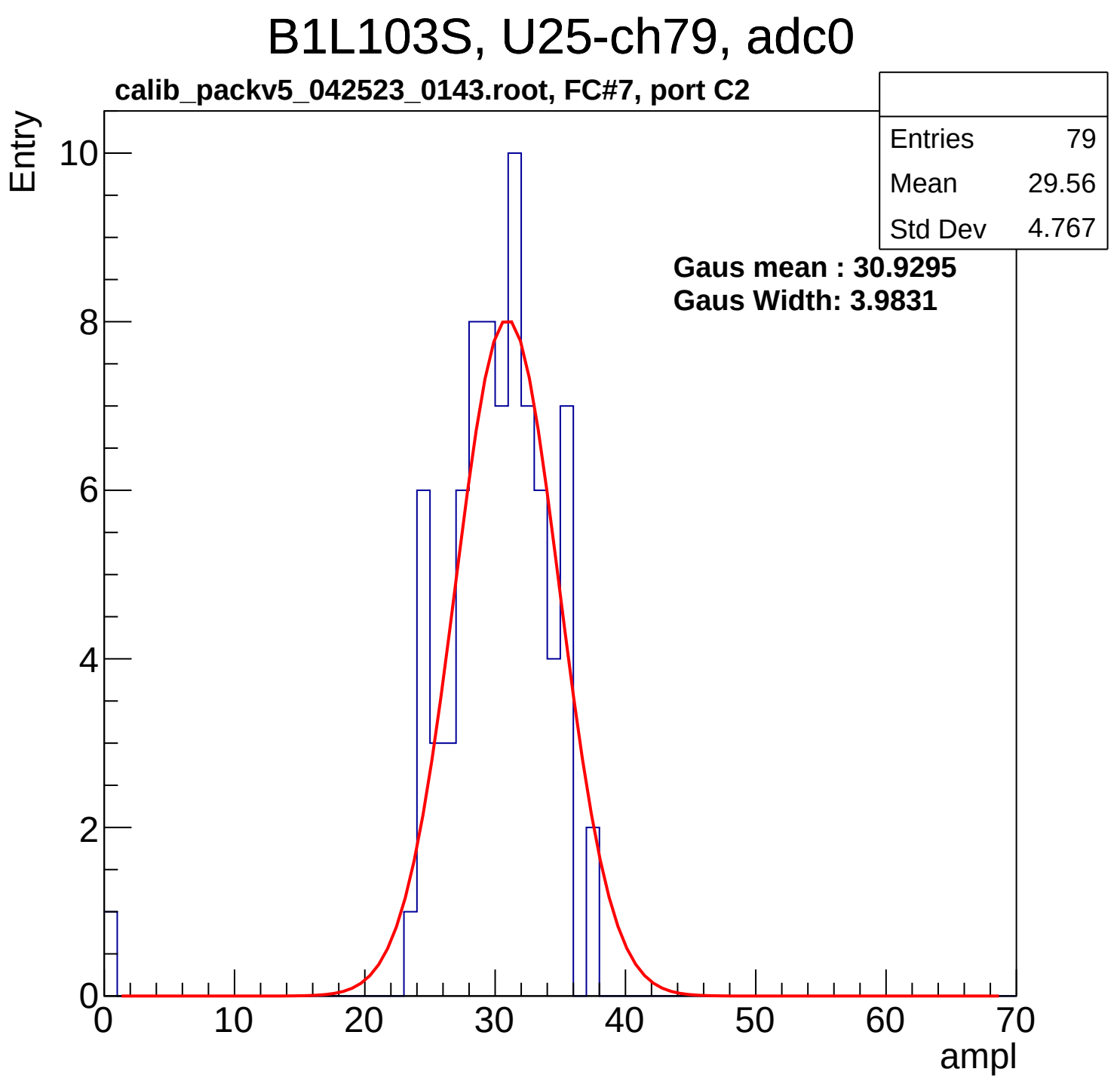
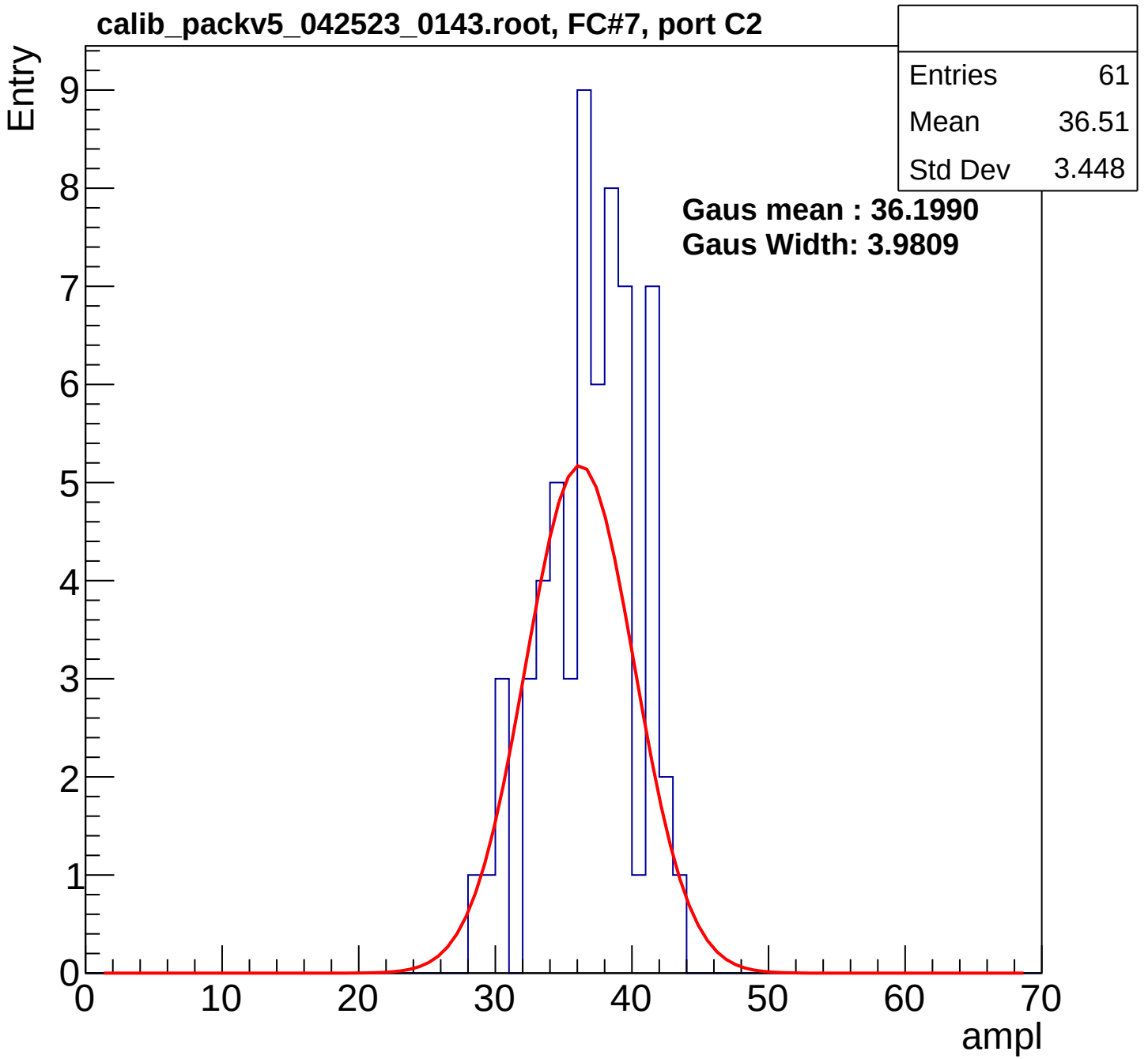

B1L103S, U25-ch78, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.53
Std Dev 3.952
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

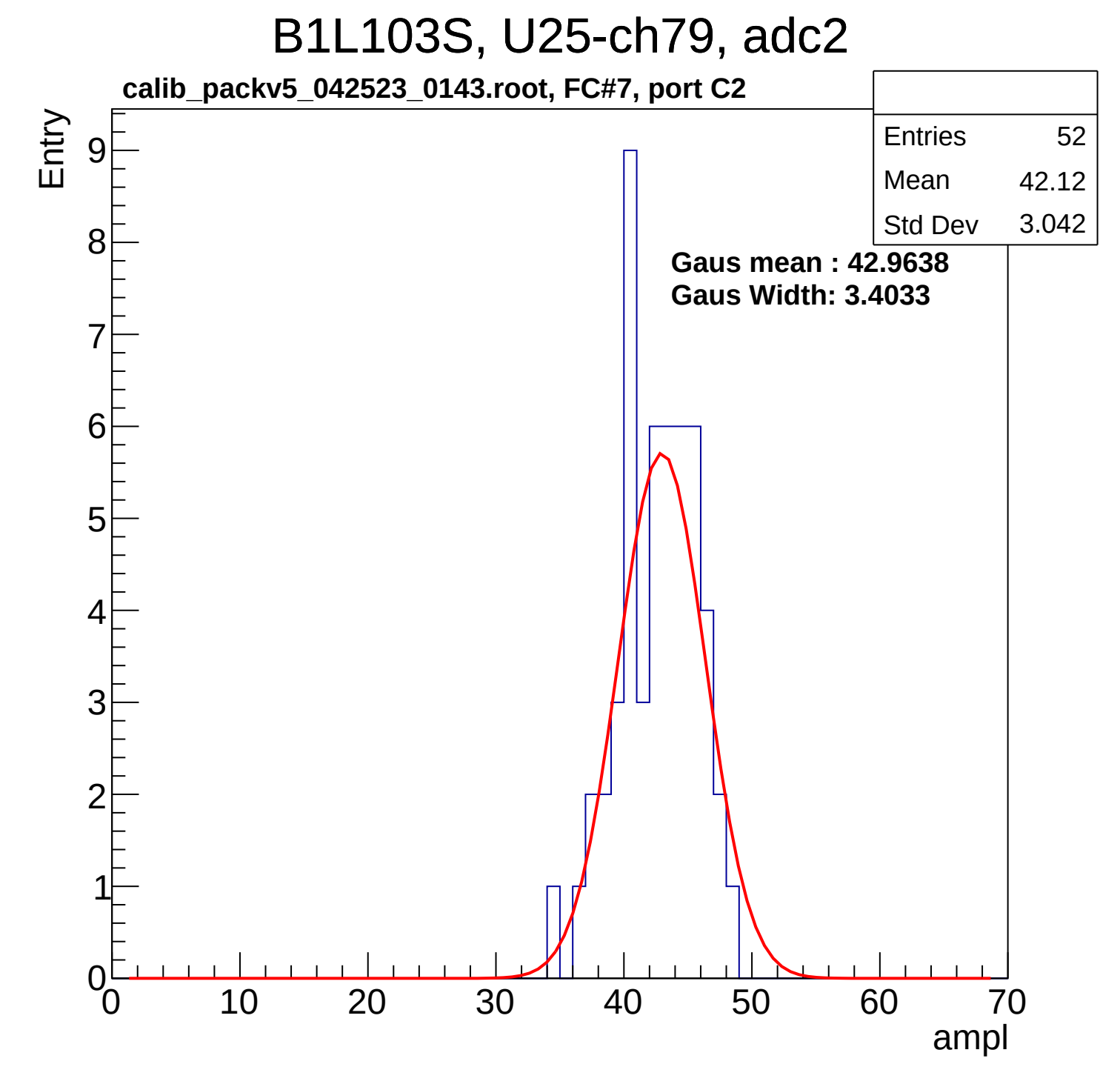

B1L103S, U25-ch78, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 57.78
Std Dev 7.914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

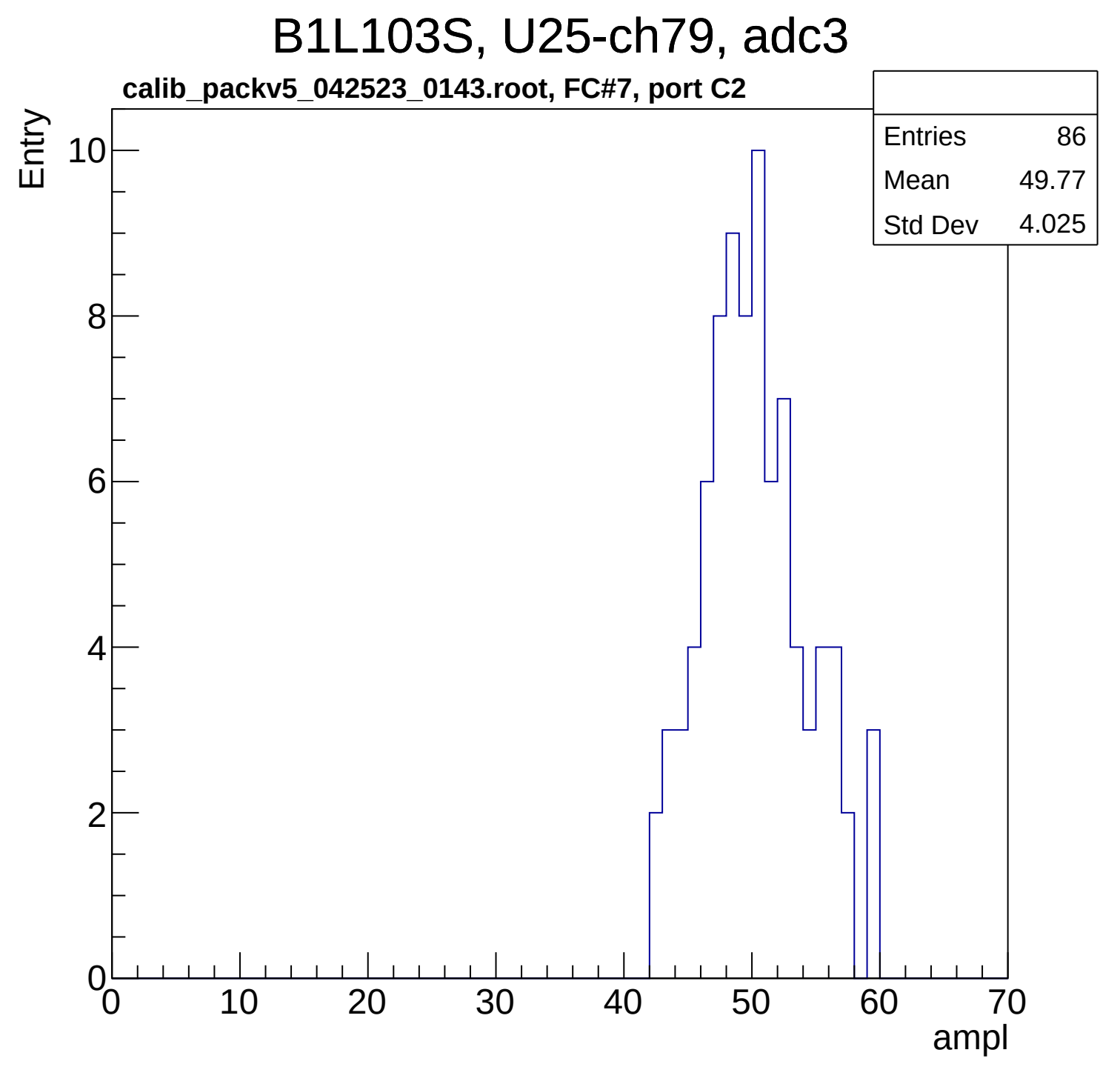

B1L103S, U25-ch78, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61
Std Dev 1.789
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

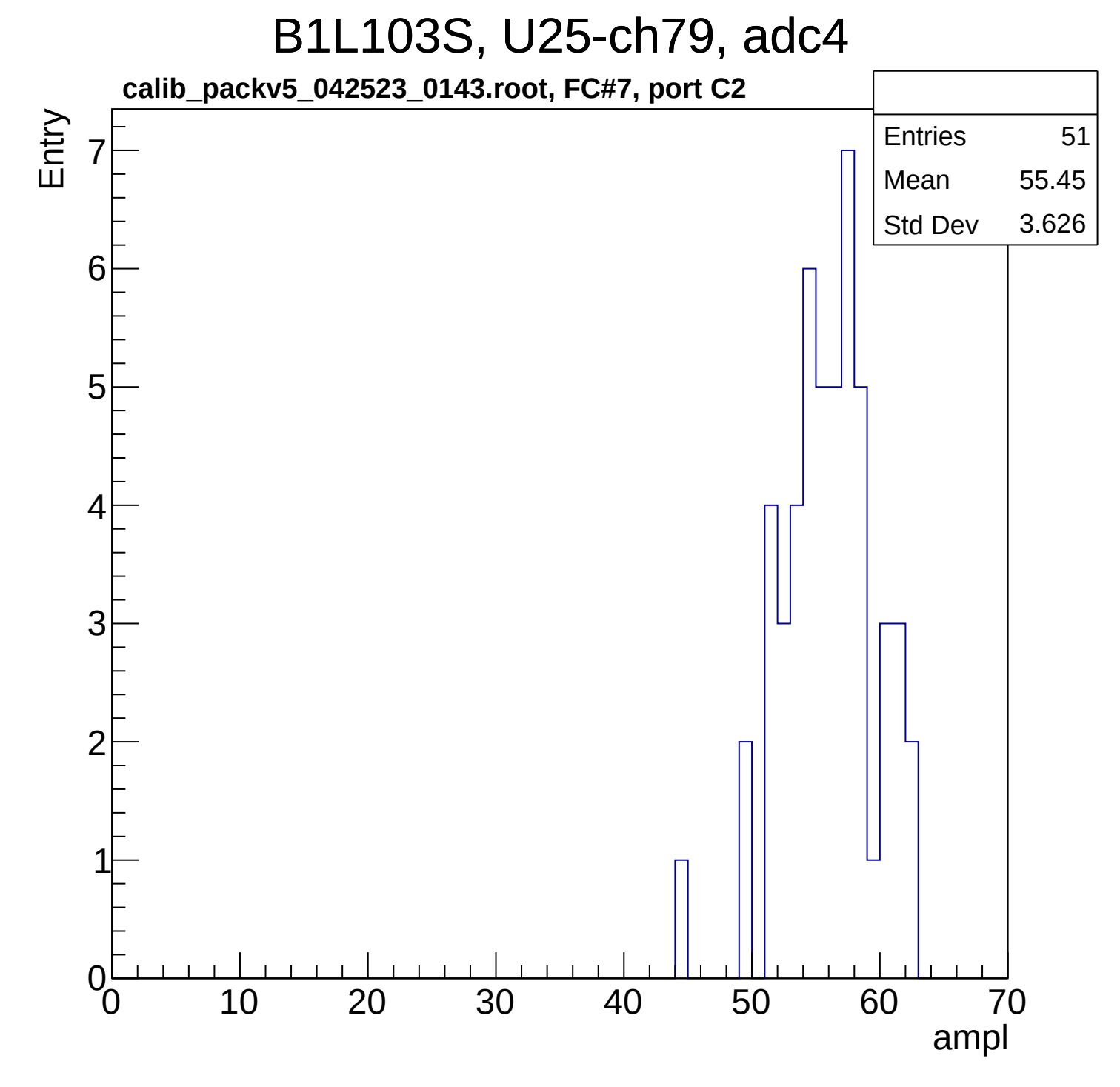

B1L103S, U25-ch78, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

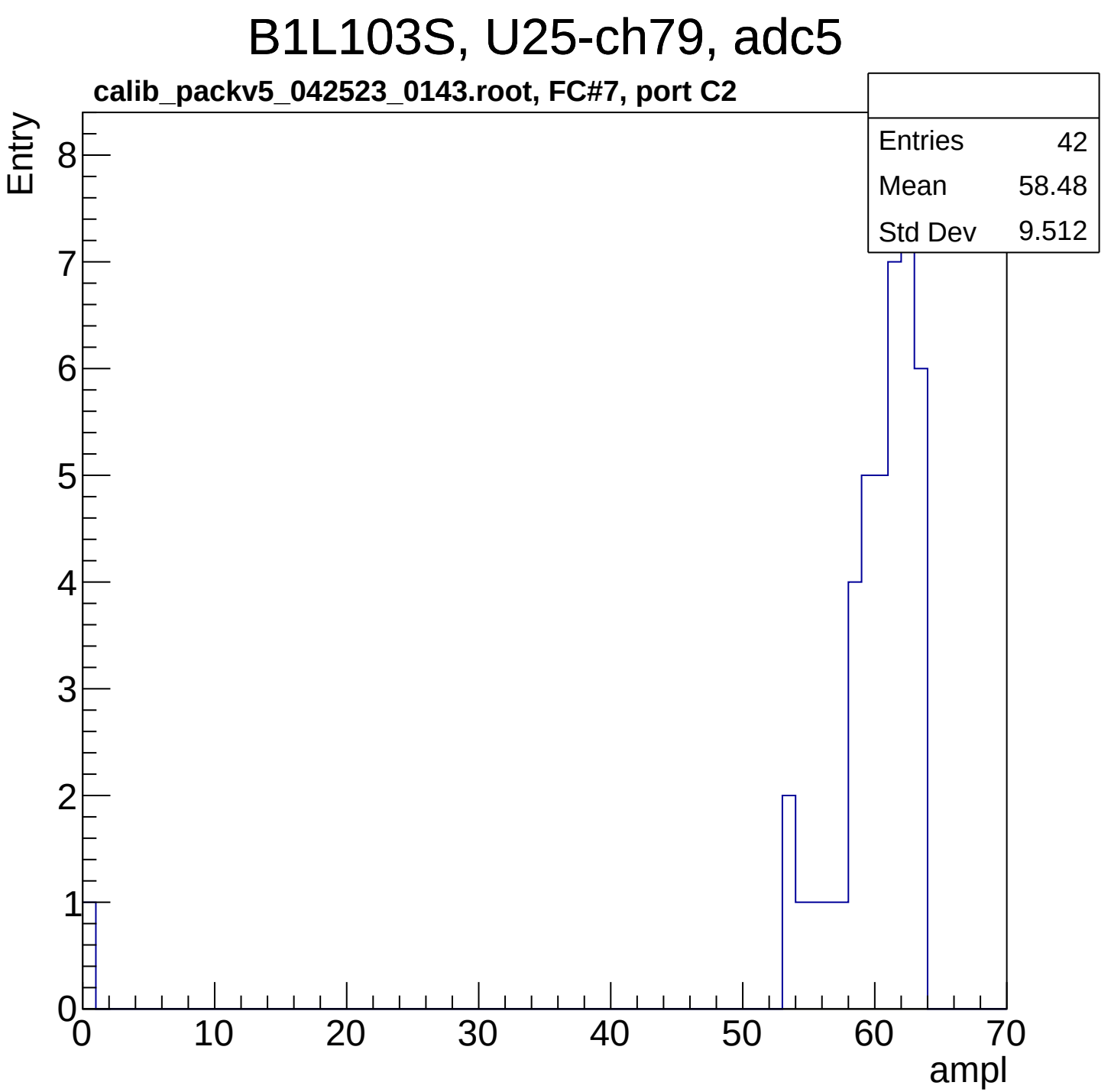

B1L103S, U25-ch79, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 29.56
Std Dev 4.767
Gaus mean : 30.9295
Gaus Width: 3.9831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch79, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 36.51
Std Dev 3.448
Gaus mean : 36.1990
Gaus Width: 3.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch79, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 42.12
Std Dev 3.042
Gaus mean : 42.9638
Gaus Width: 3.4033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch79, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 49.77
Std Dev 4.025
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch79, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 55.45
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch79, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 58.48
Std Dev 9.512
Entry
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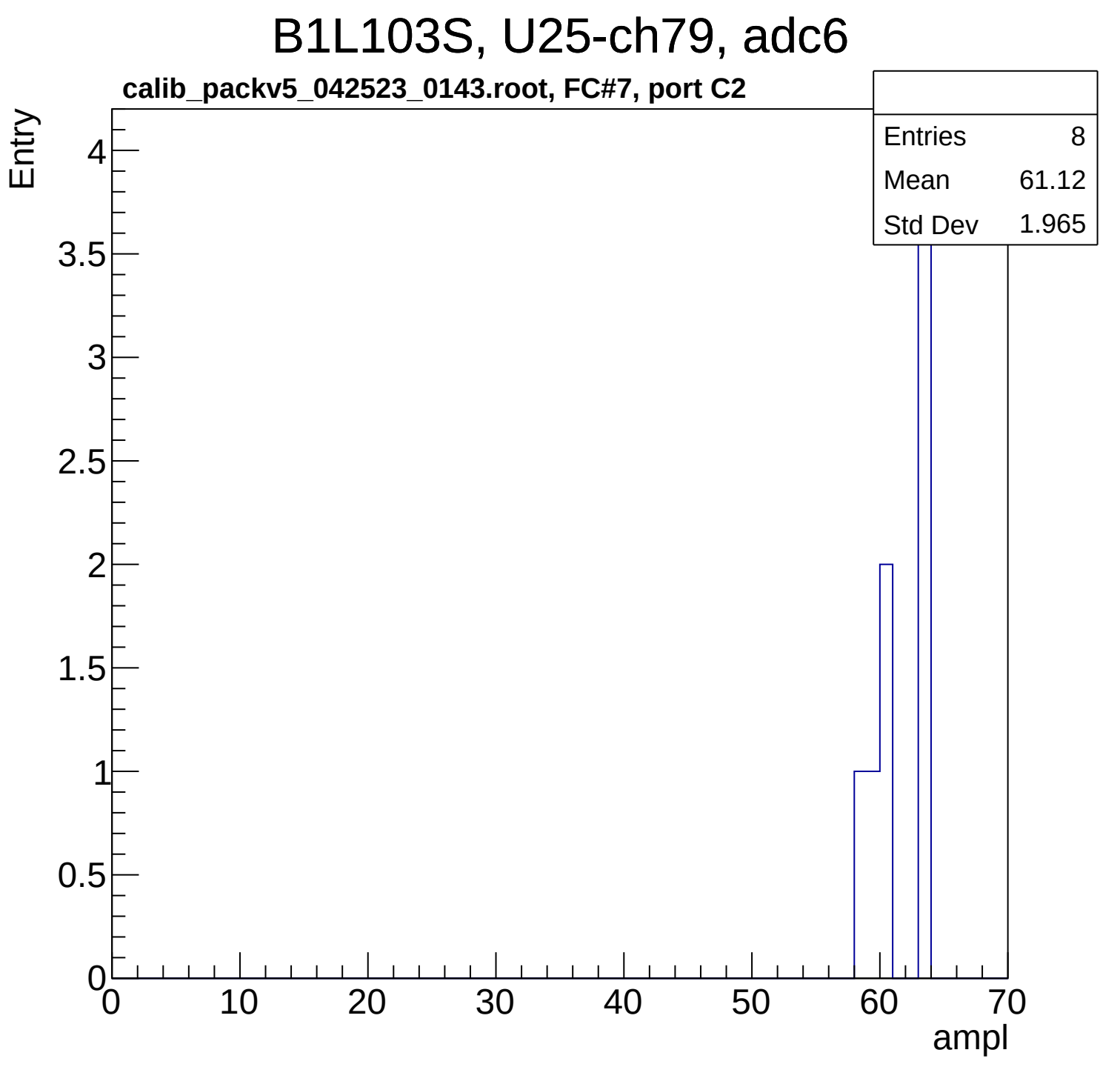
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch79, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 61.12
Std Dev 1.965
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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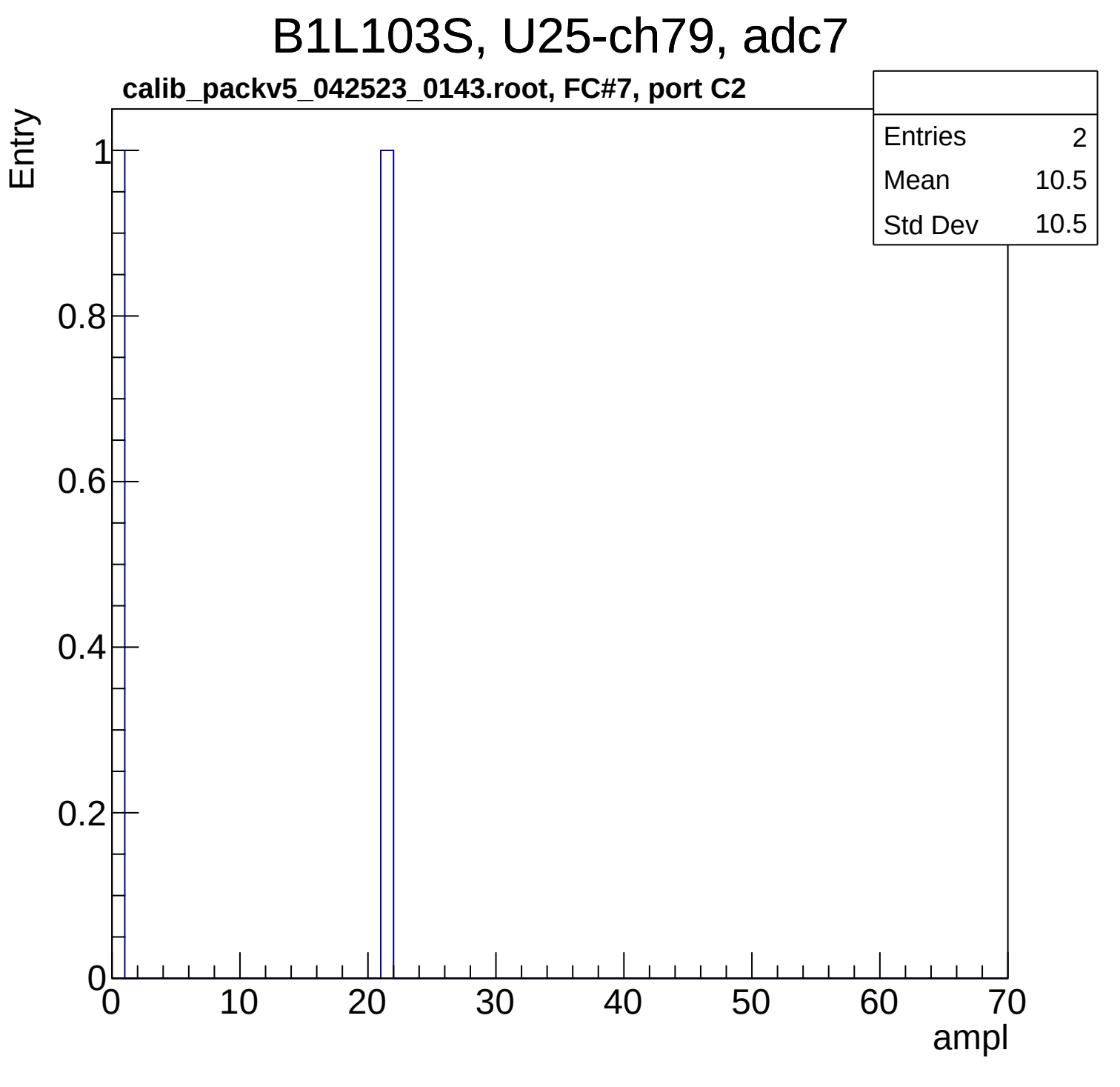
40
50
60
70


B1L103S, U25-ch79, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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0.6
0.4
0.2
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10
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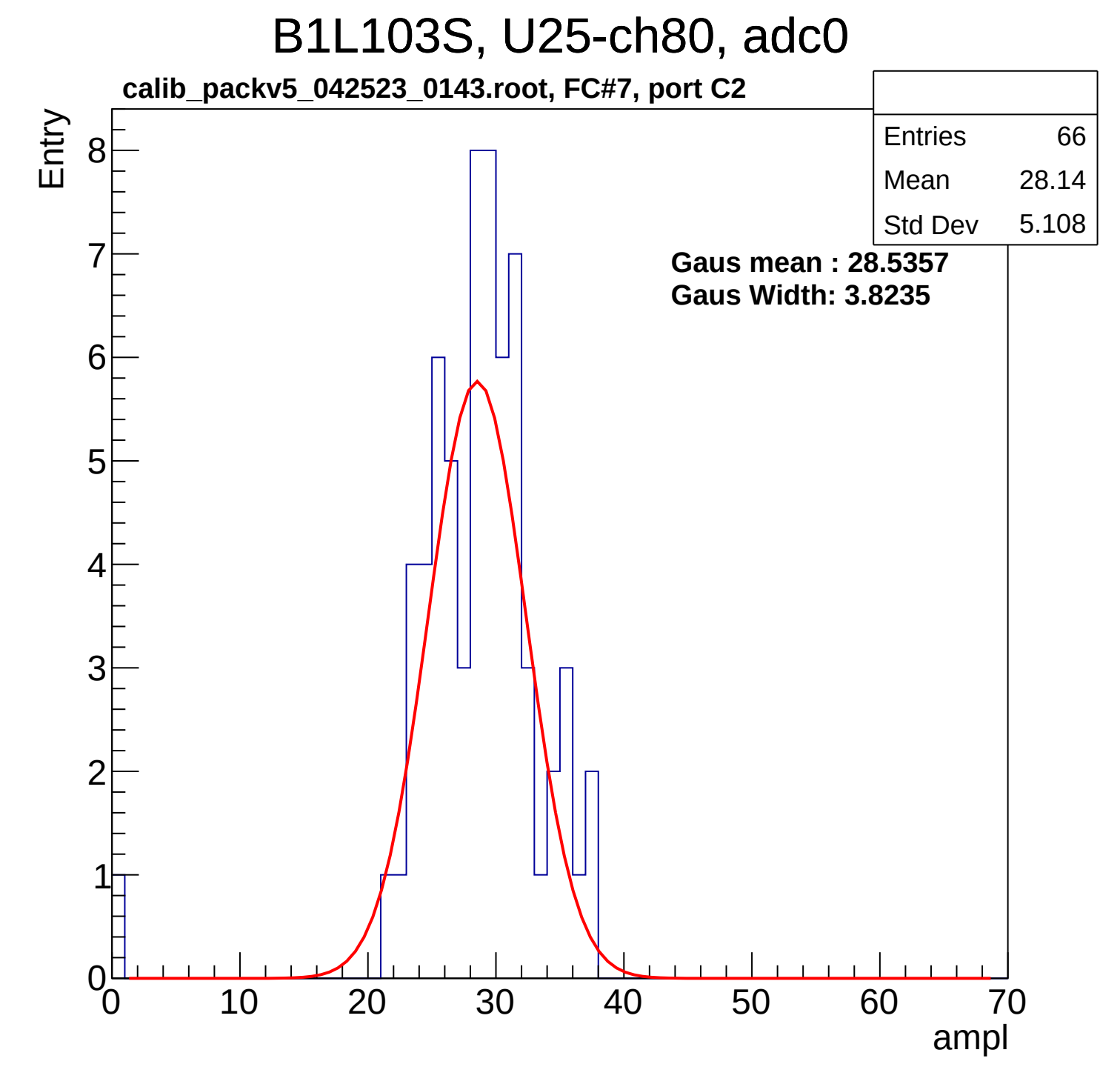
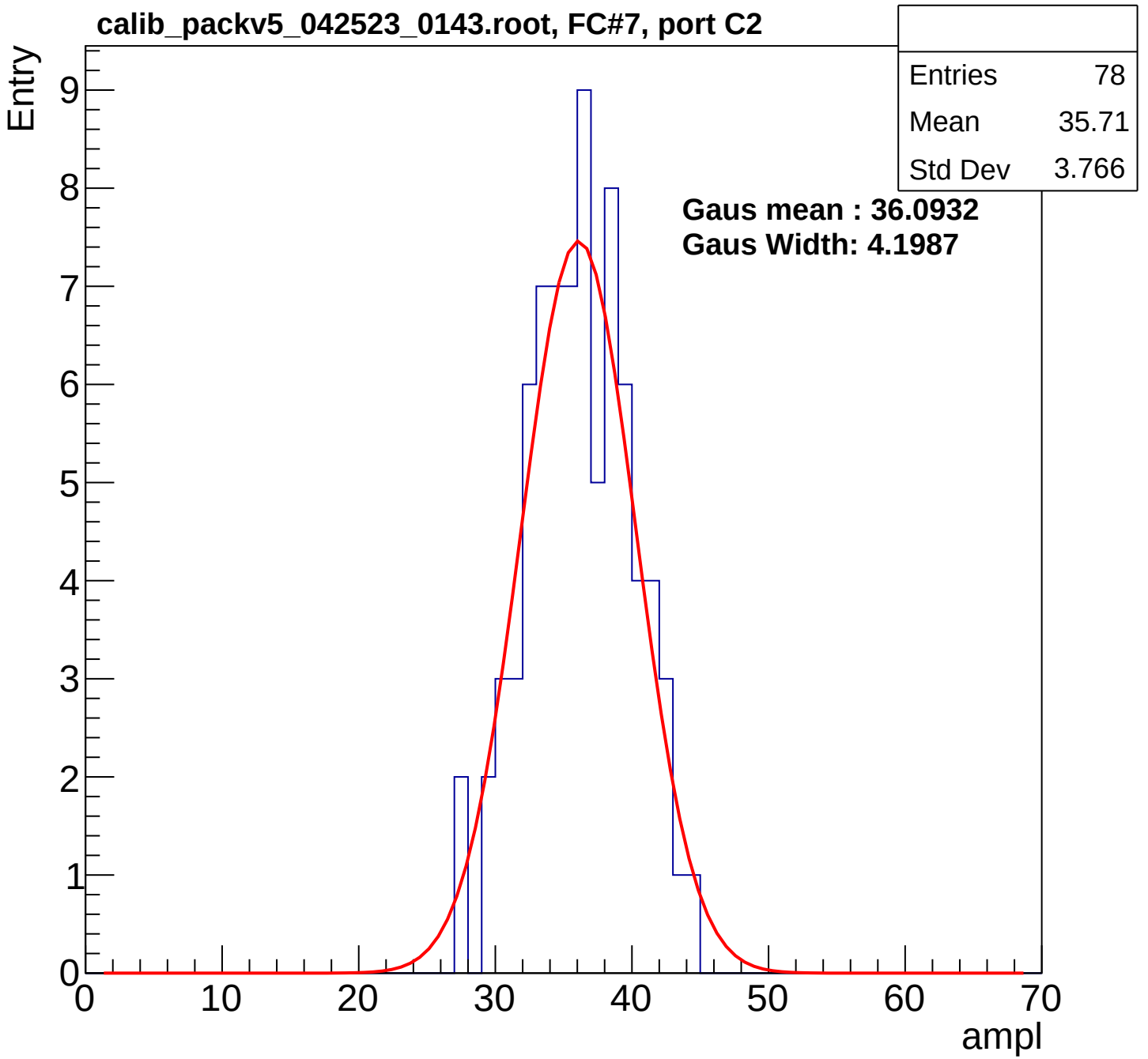
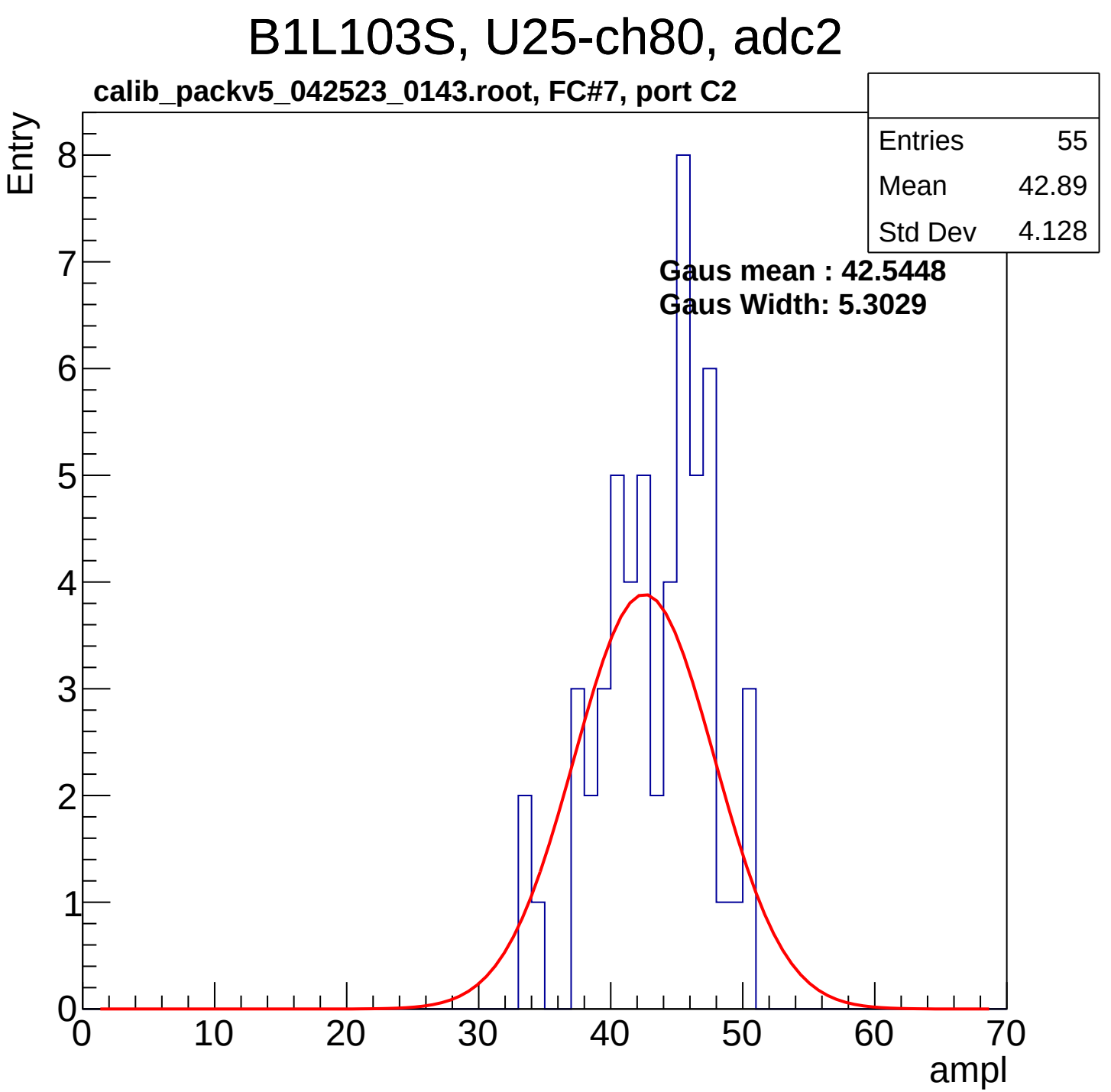
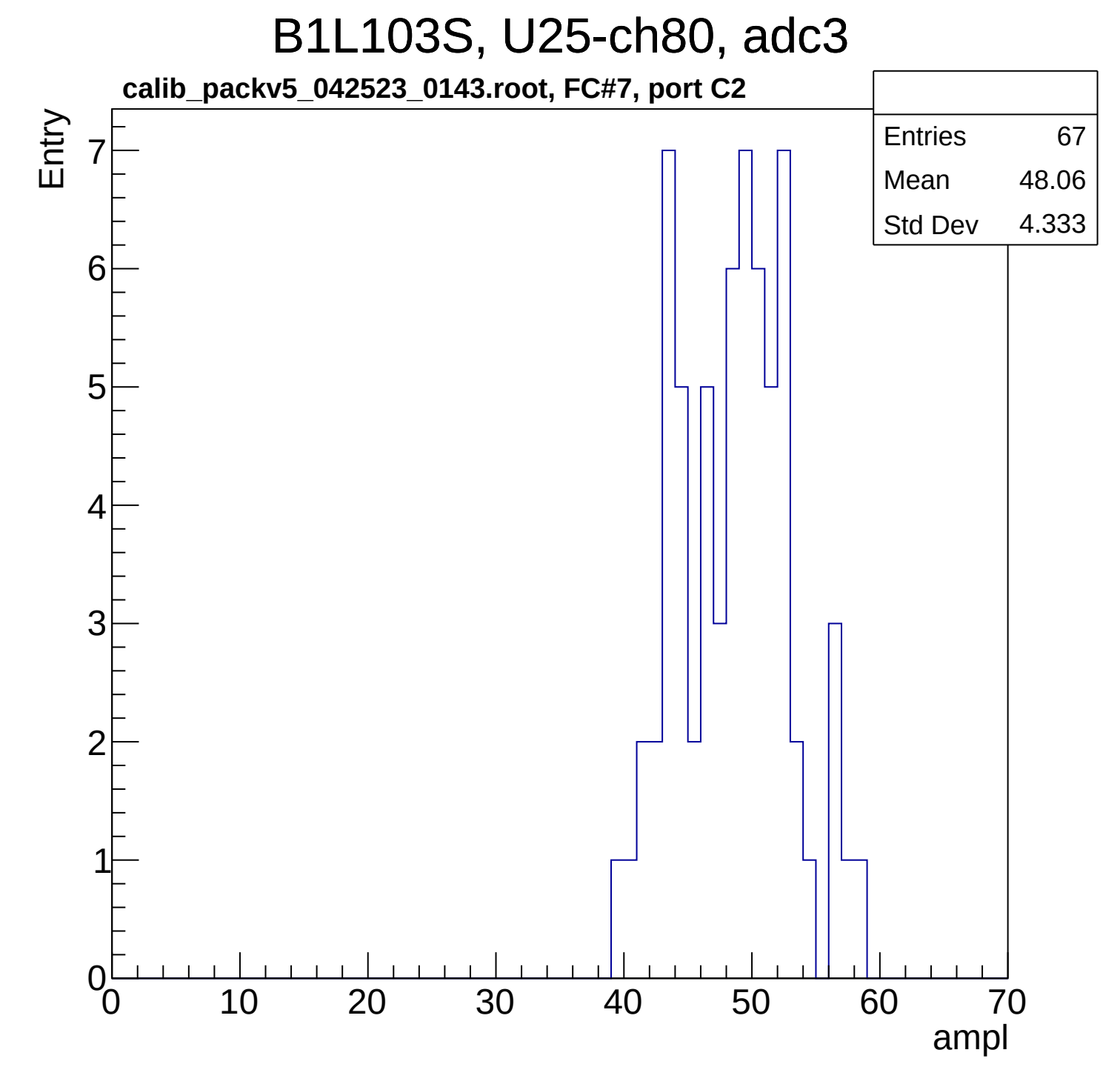
50
60
70
ampl


B1L103S, U25-ch80, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 28.14
Std Dev 5.108
Gaus mean : 28.5357
Gaus Width: 3.8235
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch80, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 35.71
Std Dev 3.766
Gaus mean : 36.0932
Gaus Width: 4.1987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch80, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 42.89
Std Dev 4.128
Gaus mean : 42.5448
Gaus Width: 5.3029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch80, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.06
Std Dev 4.333
Entry
ampl
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1
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3
4
5
6
7
0
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20
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40
50
60
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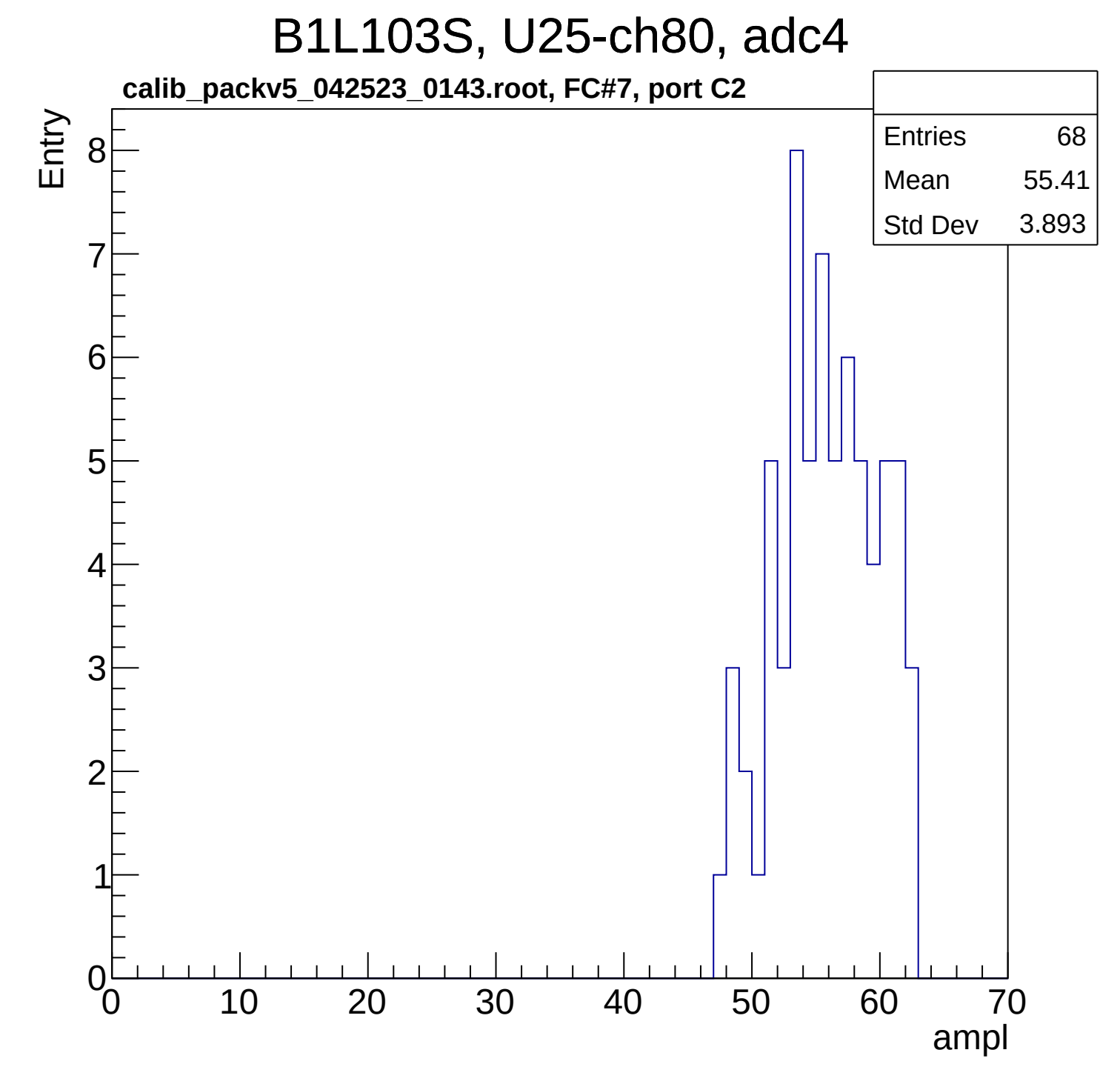
B1L103S, U25-ch80, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 55.41
Std Dev 3.893
Entry
ampl
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3
4
5
6
7
8
0
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40
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60
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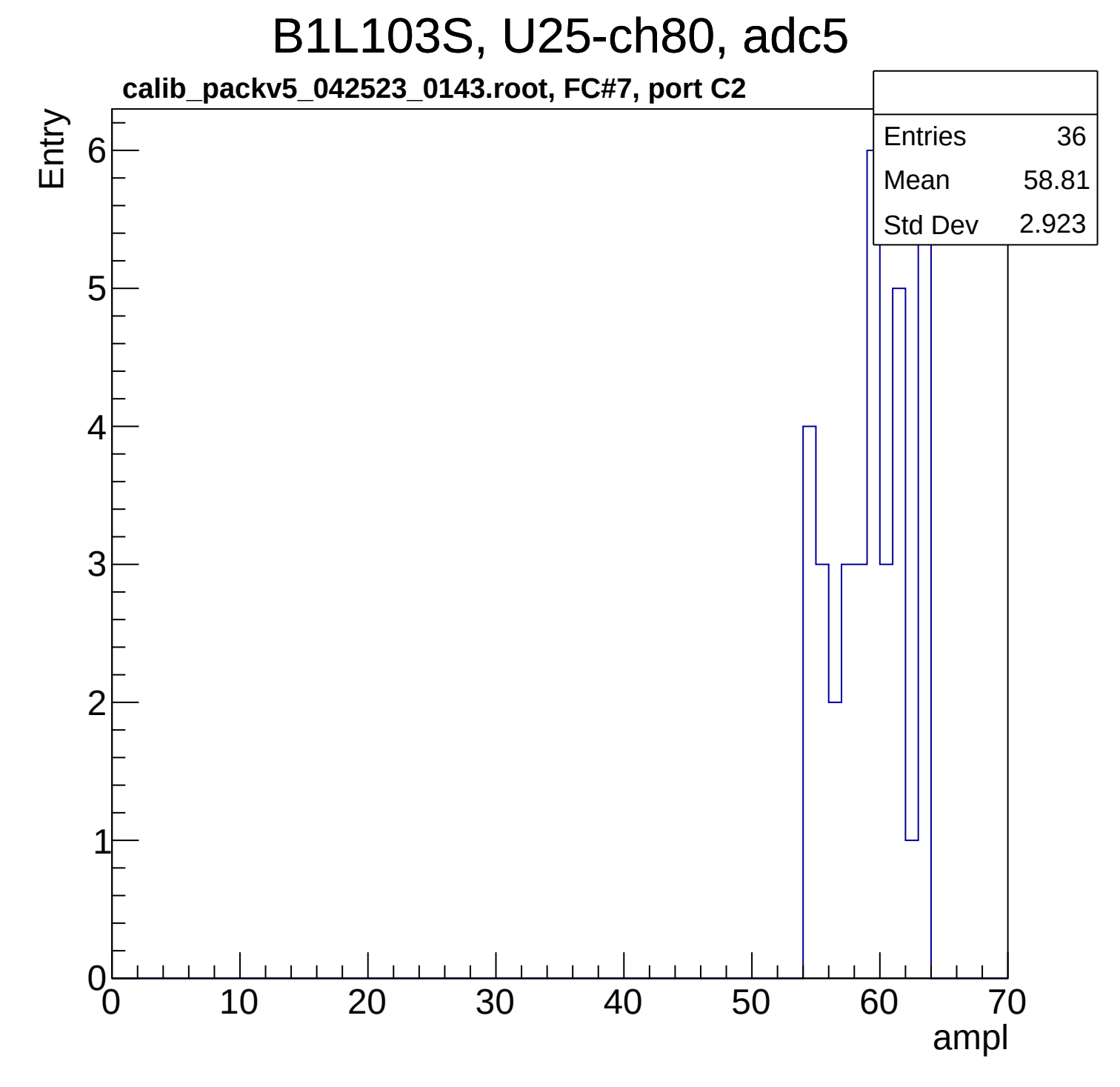
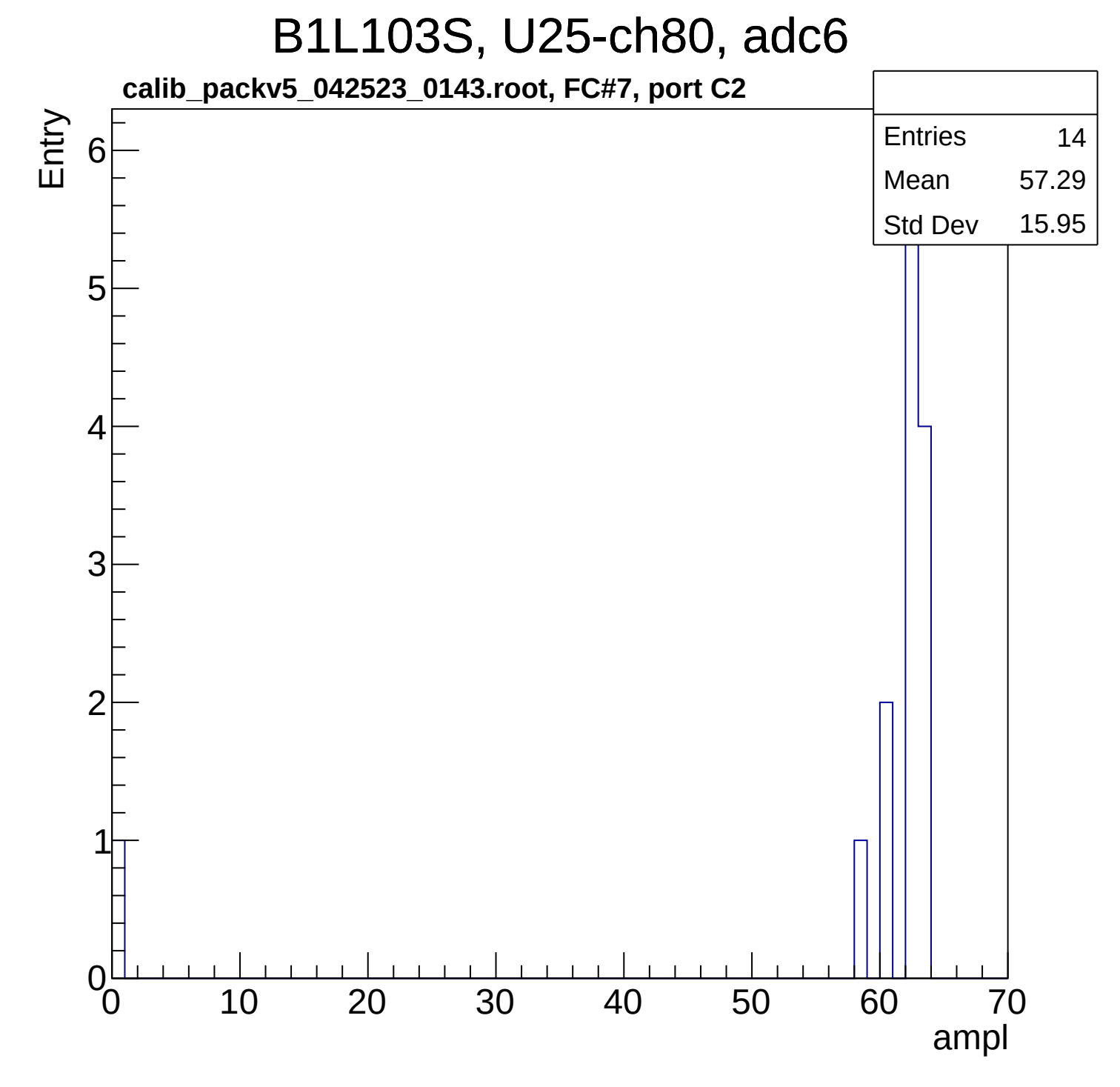
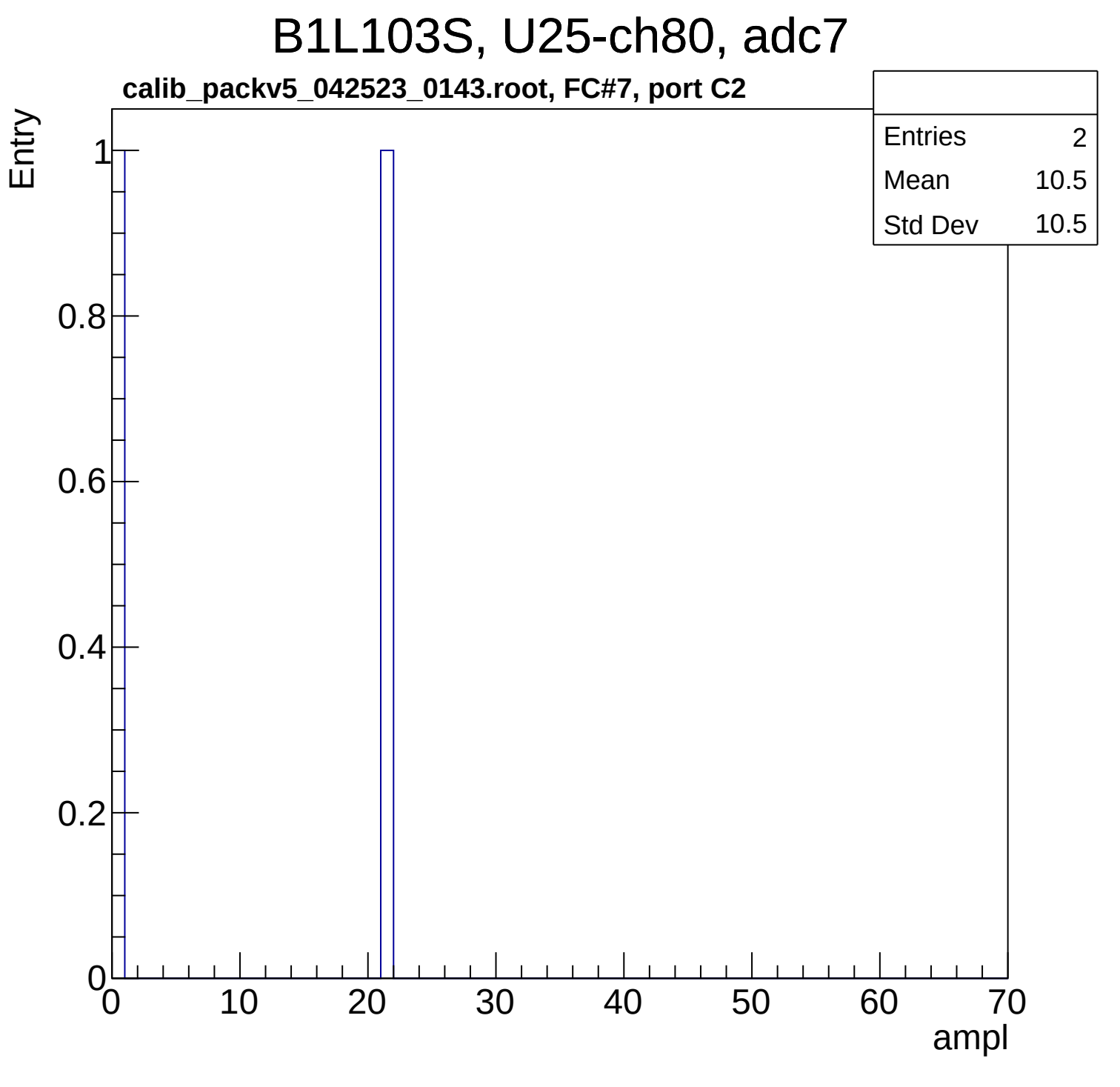
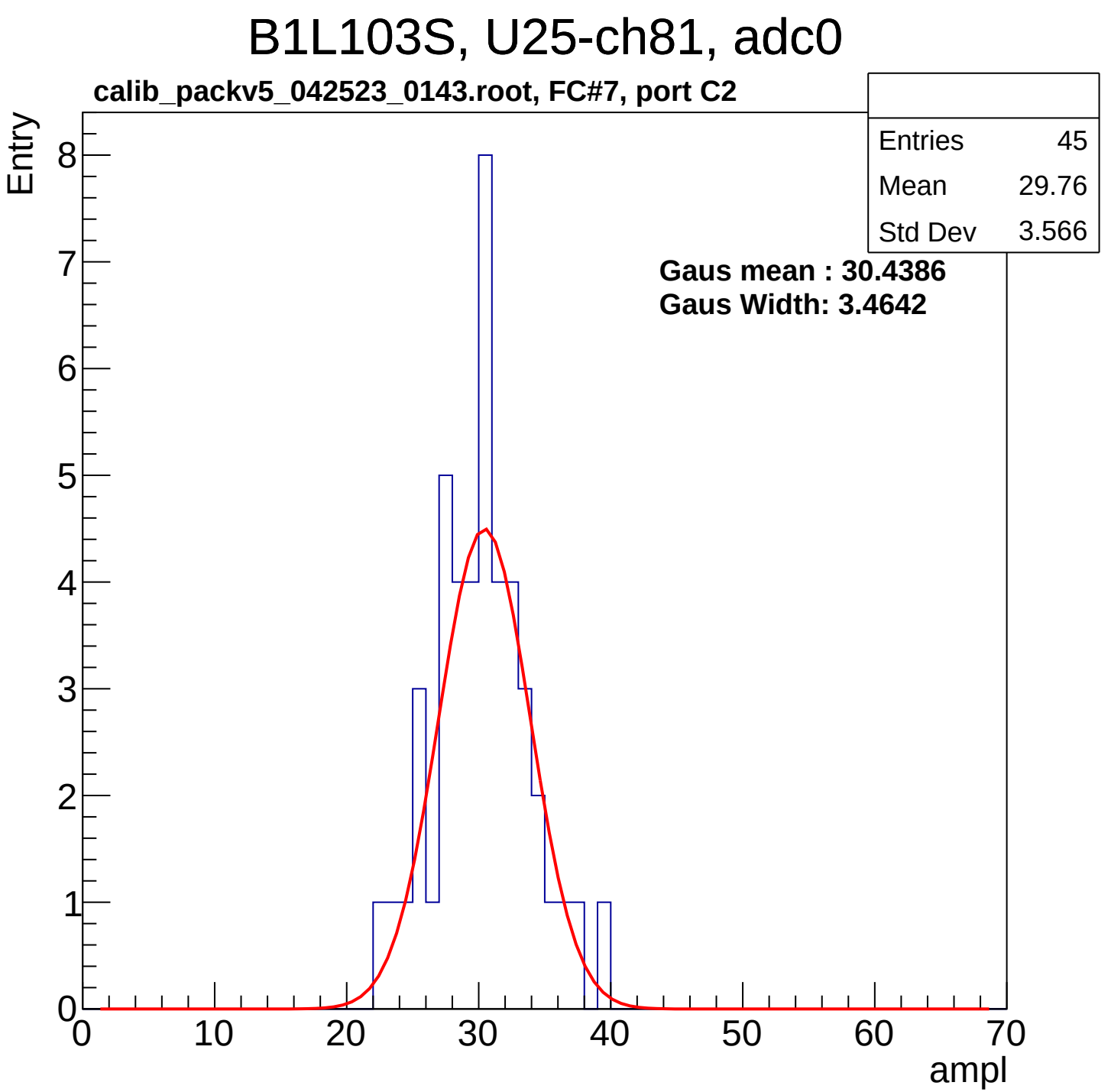

B1L103S, U25-ch80, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.81
Std Dev 2.923
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch80, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 57.29
Std Dev 15.95
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch80, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch81, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 29.76
Std Dev 3.566
Gaus mean : 30.4386
Gaus Width: 3.4642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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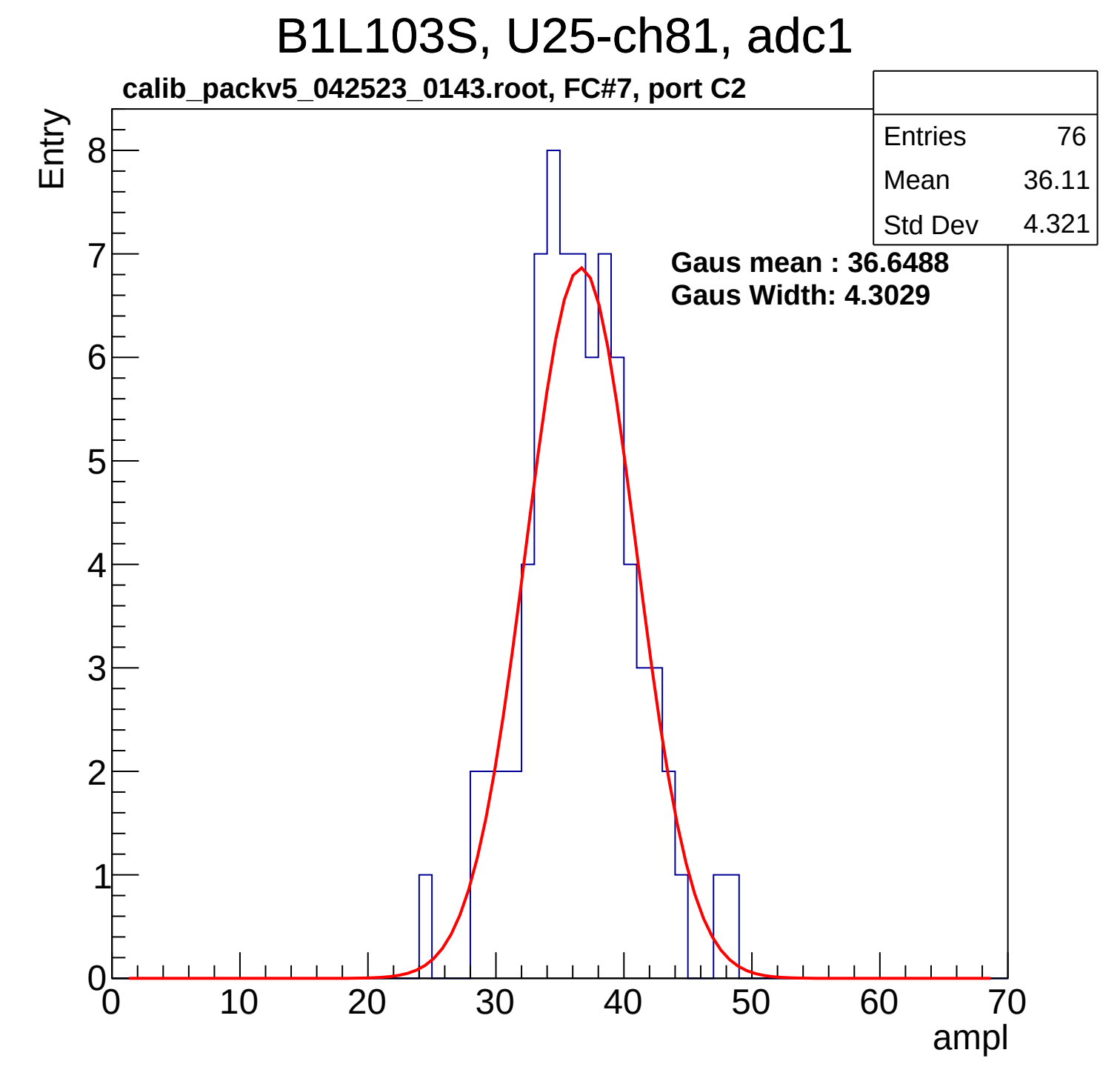
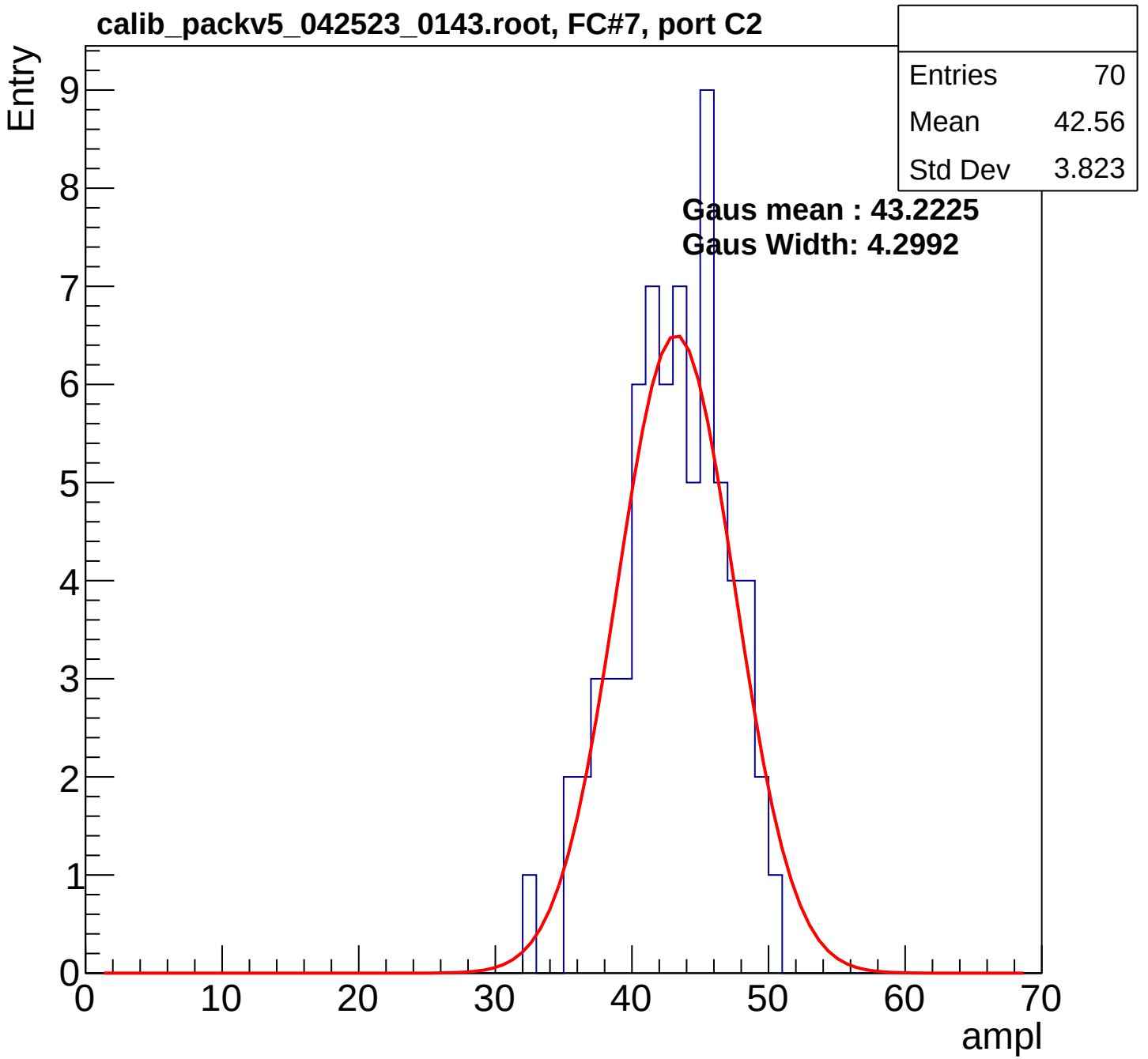

B1L103S, U25-ch81, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36.11
Std Dev 4.321
Gaus mean : 36.6488
Gaus Width: 4.3029
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch81, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 42.56
Std Dev 3.823
Gaus mean : 43.2225
Gaus Width: 4.2992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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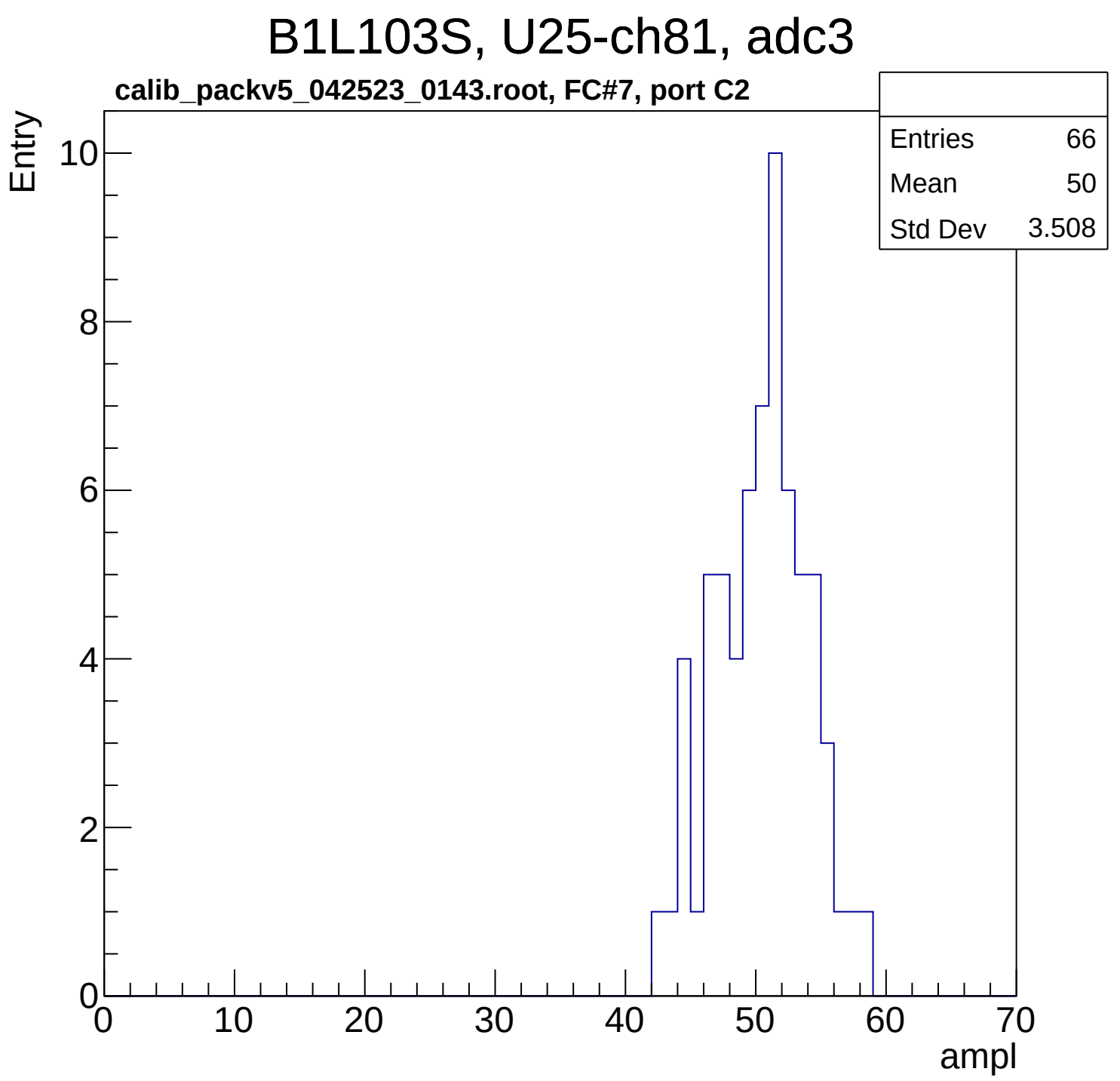
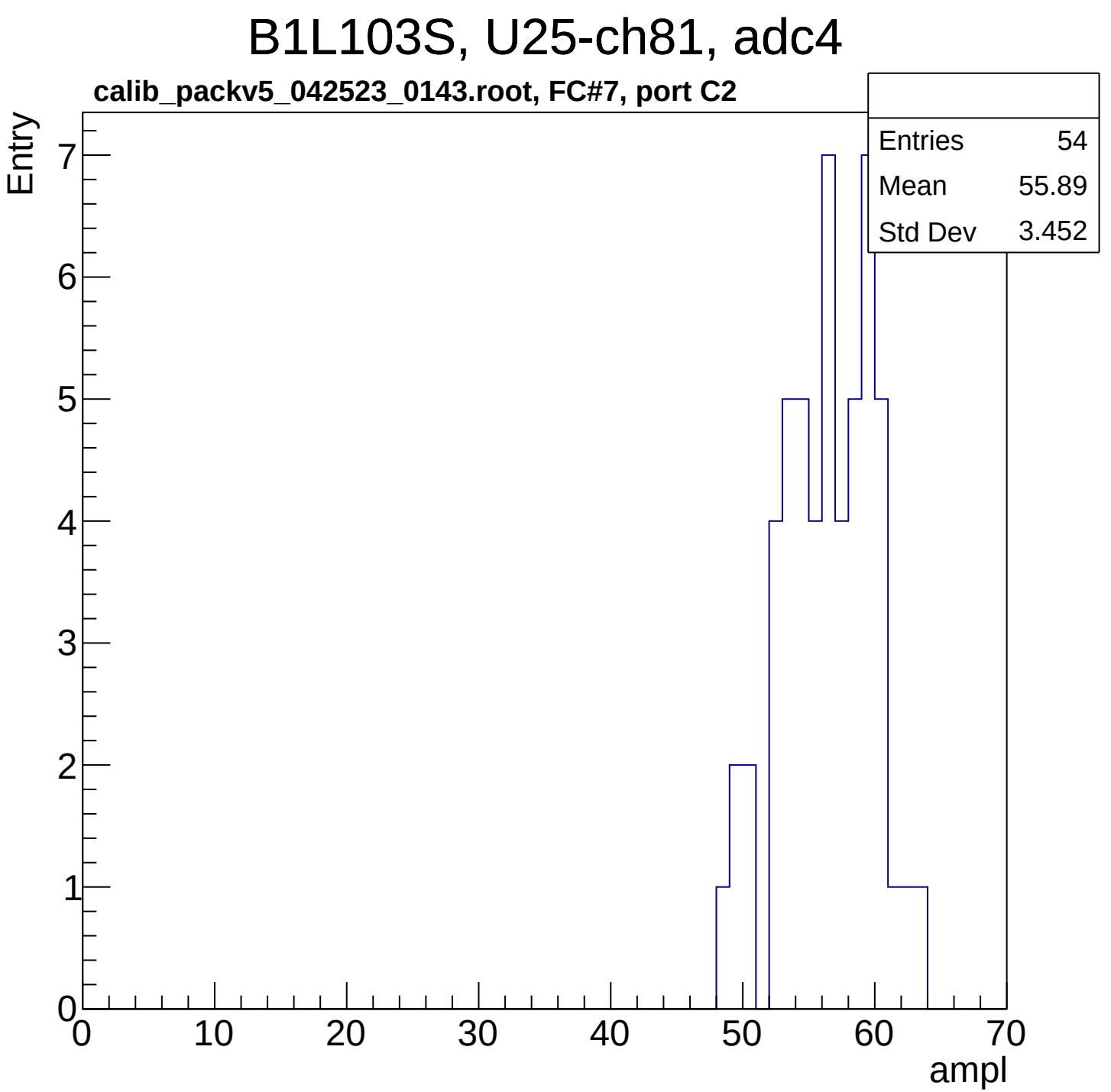
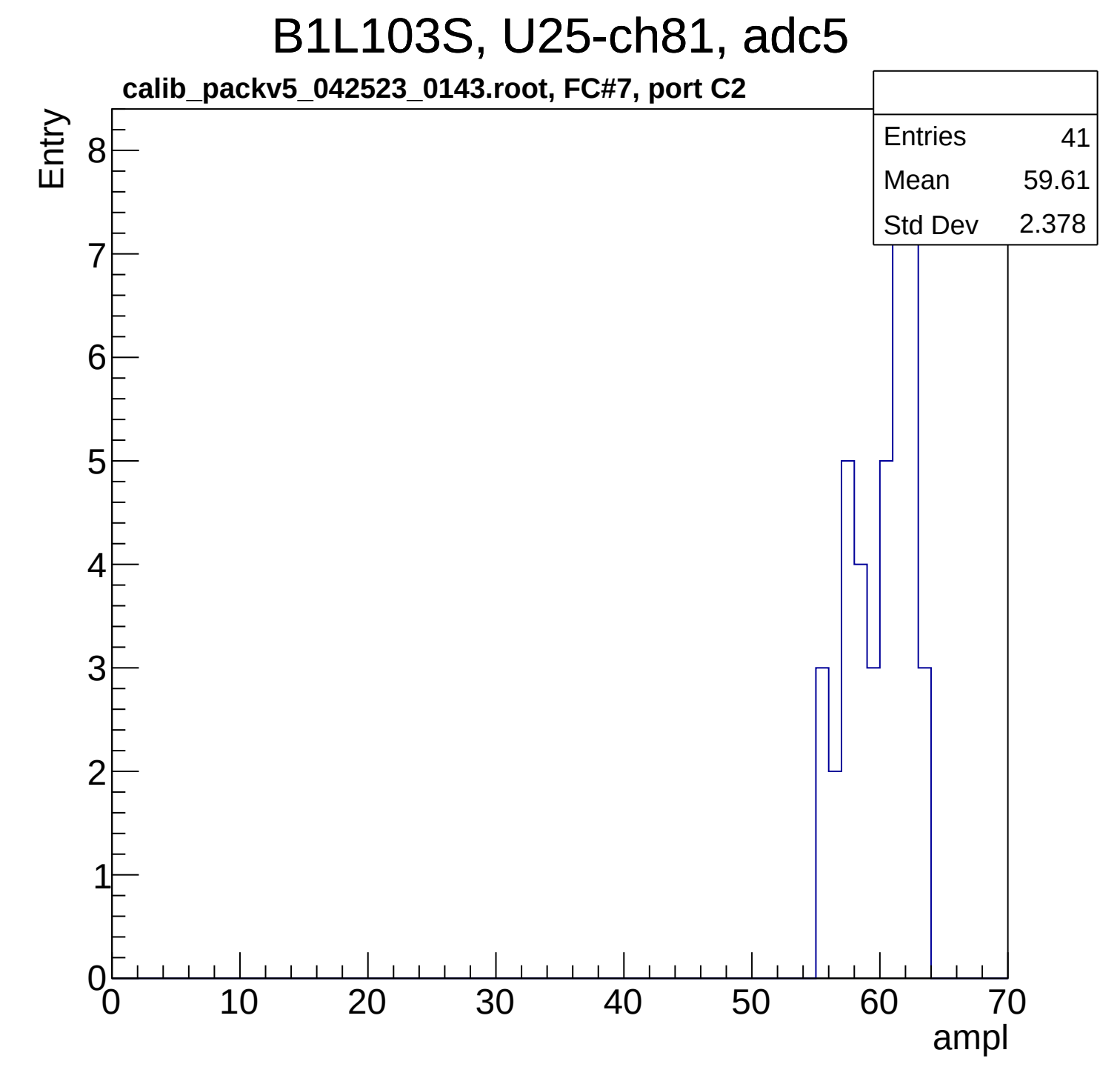

B1L103S, U25-ch81, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 50
Std Dev 3.508
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch81, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.89
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch81, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 59.61
Std Dev 2.378
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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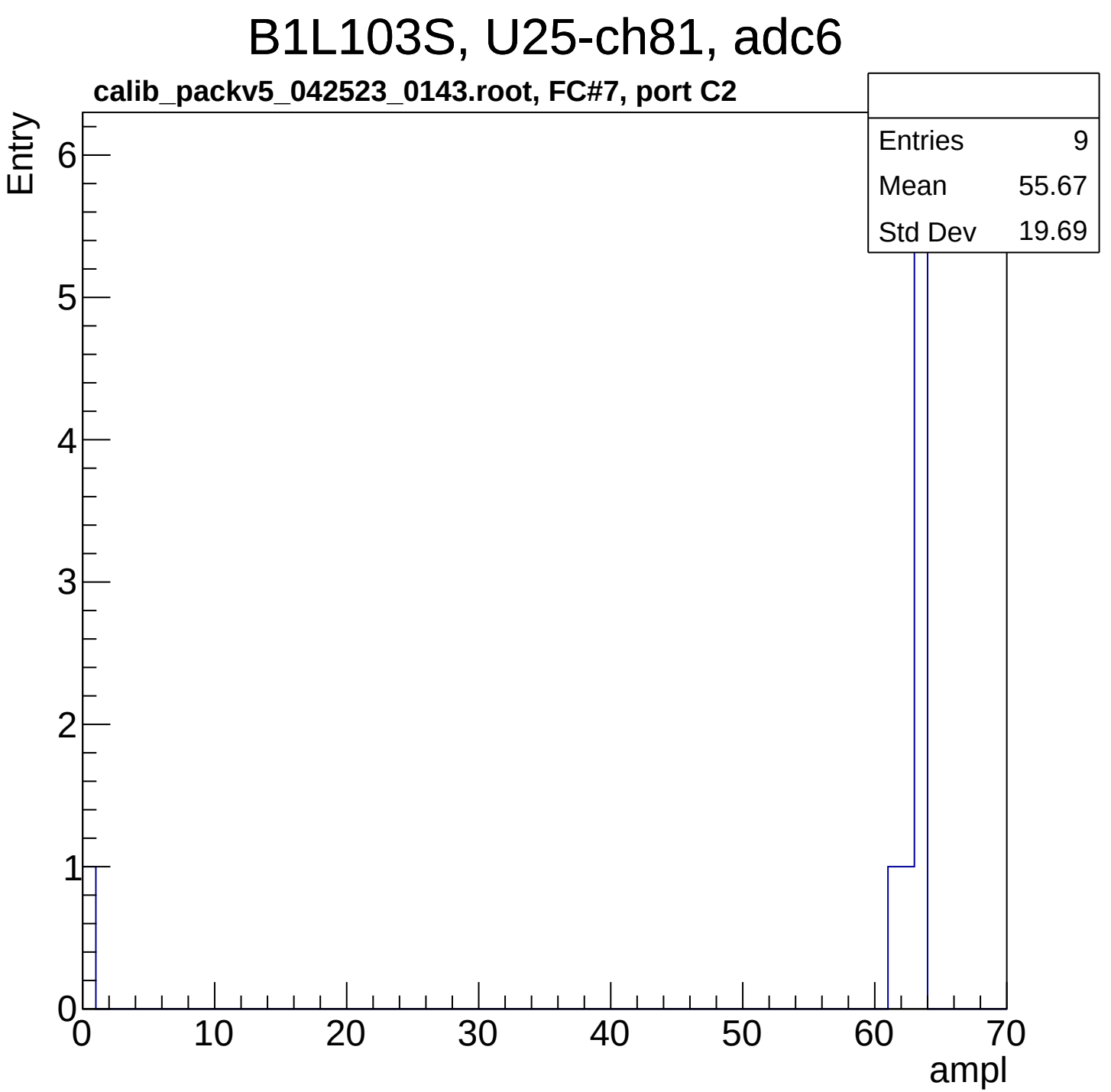
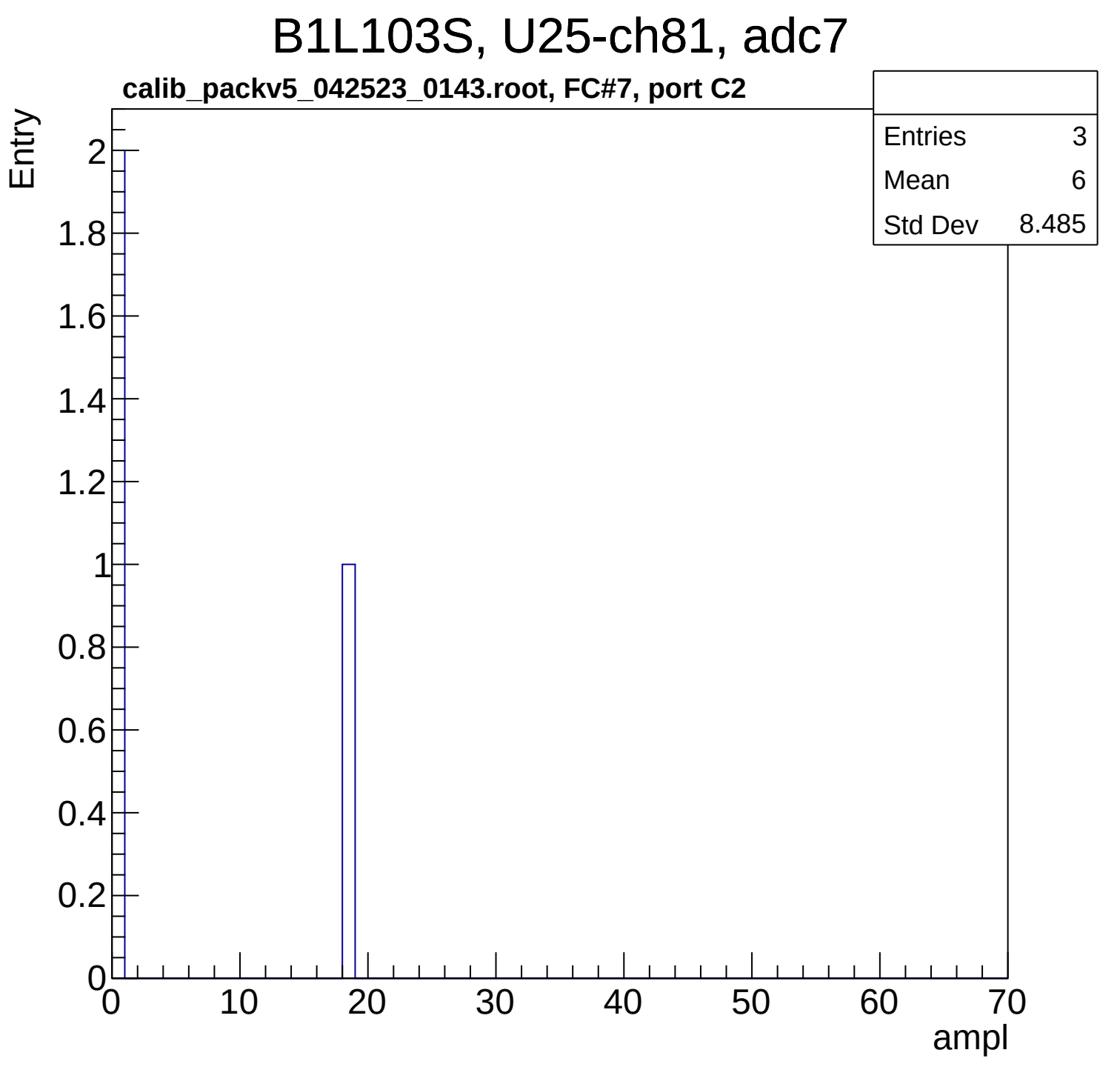
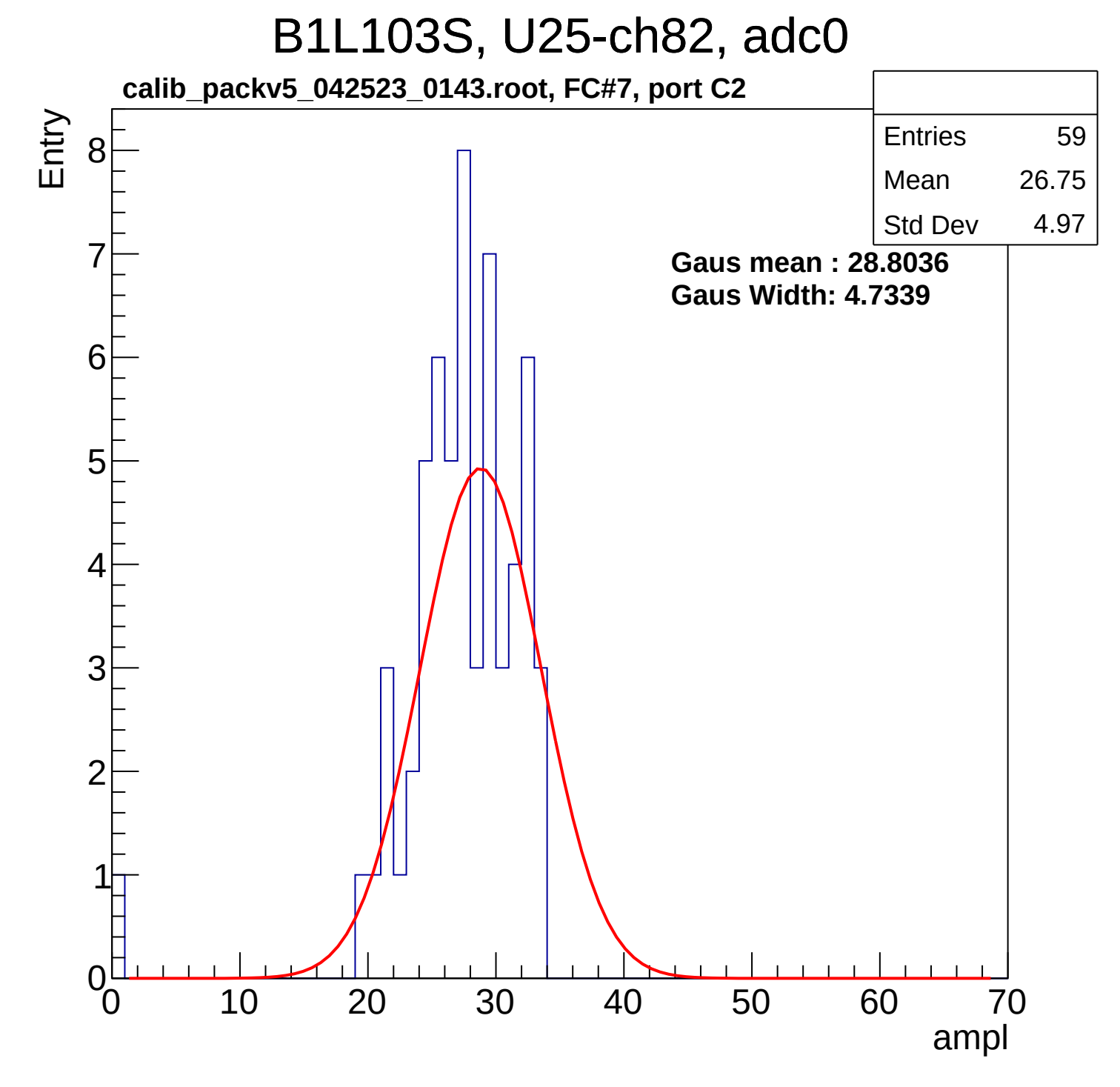
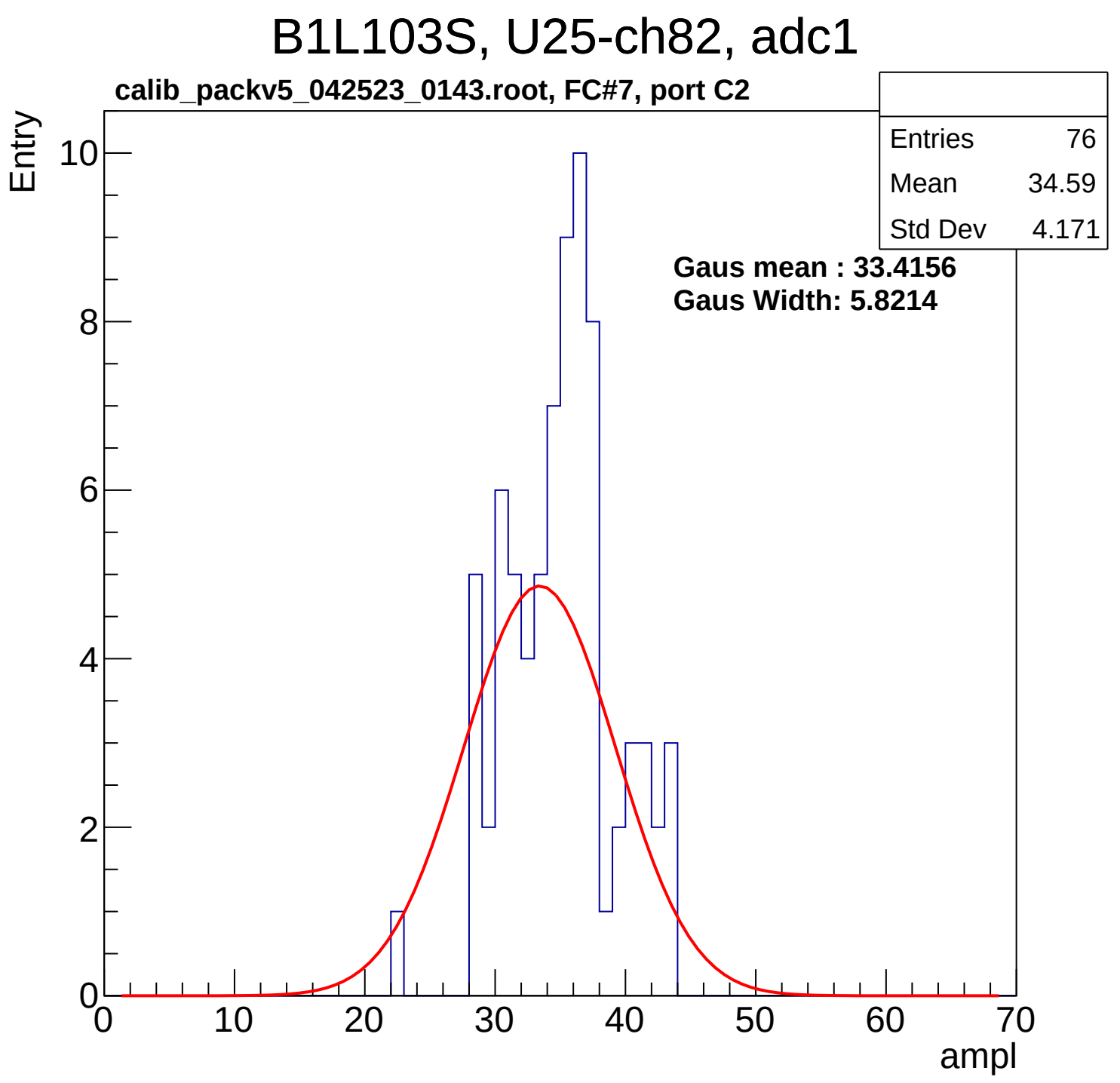
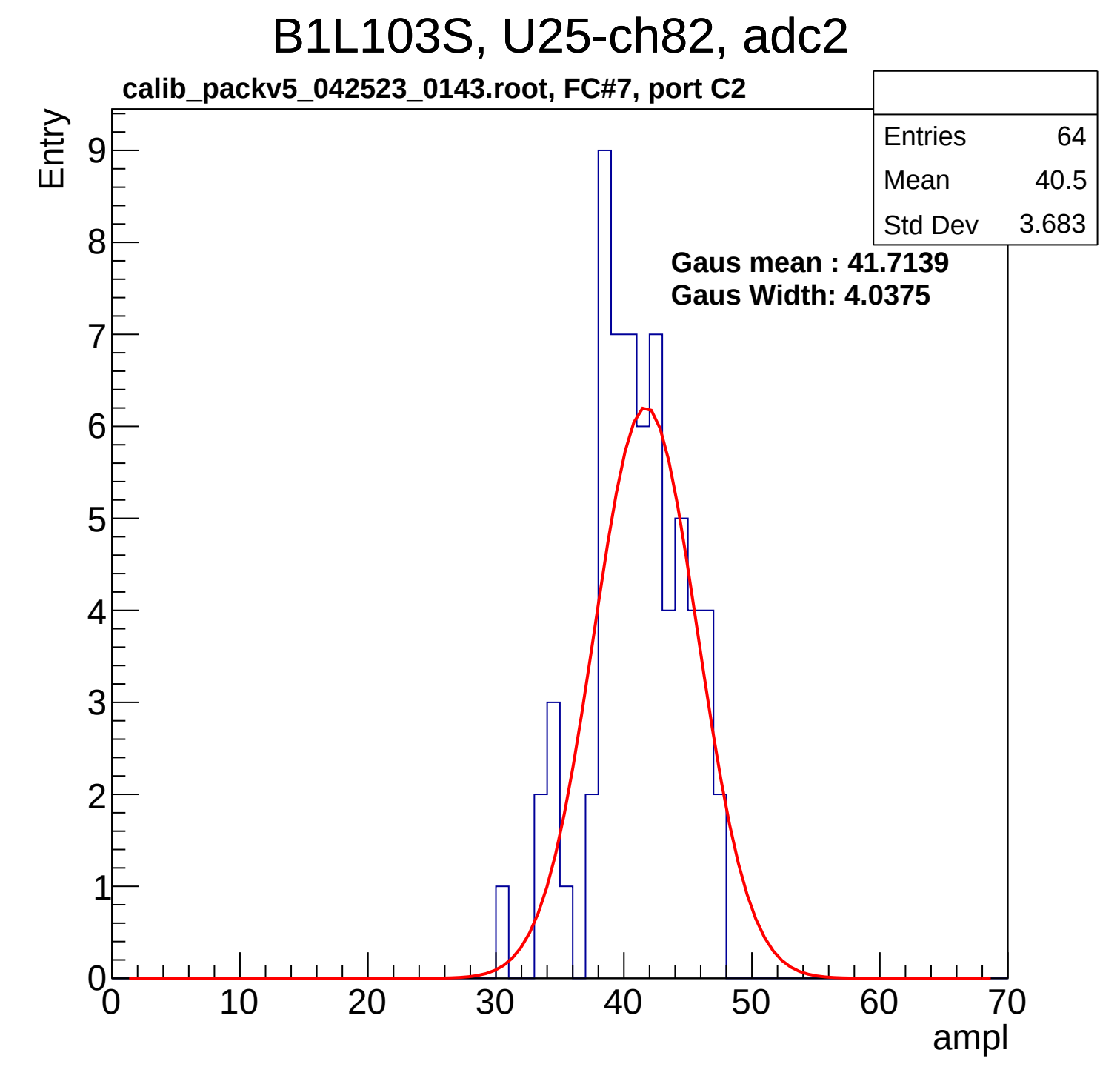

B1L103S, U25-ch81, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 55.67
Std Dev 19.69
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch81, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 6
Std Dev 8.485
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 26.75
Std Dev 4.97
Gaus mean : 28.8036
Gaus Width: 4.7339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 34.59
Std Dev 4.171
Gaus mean : 33.4156
Gaus Width: 5.8214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.5
Std Dev 3.683
Gaus mean : 41.7139
Gaus Width: 4.0375
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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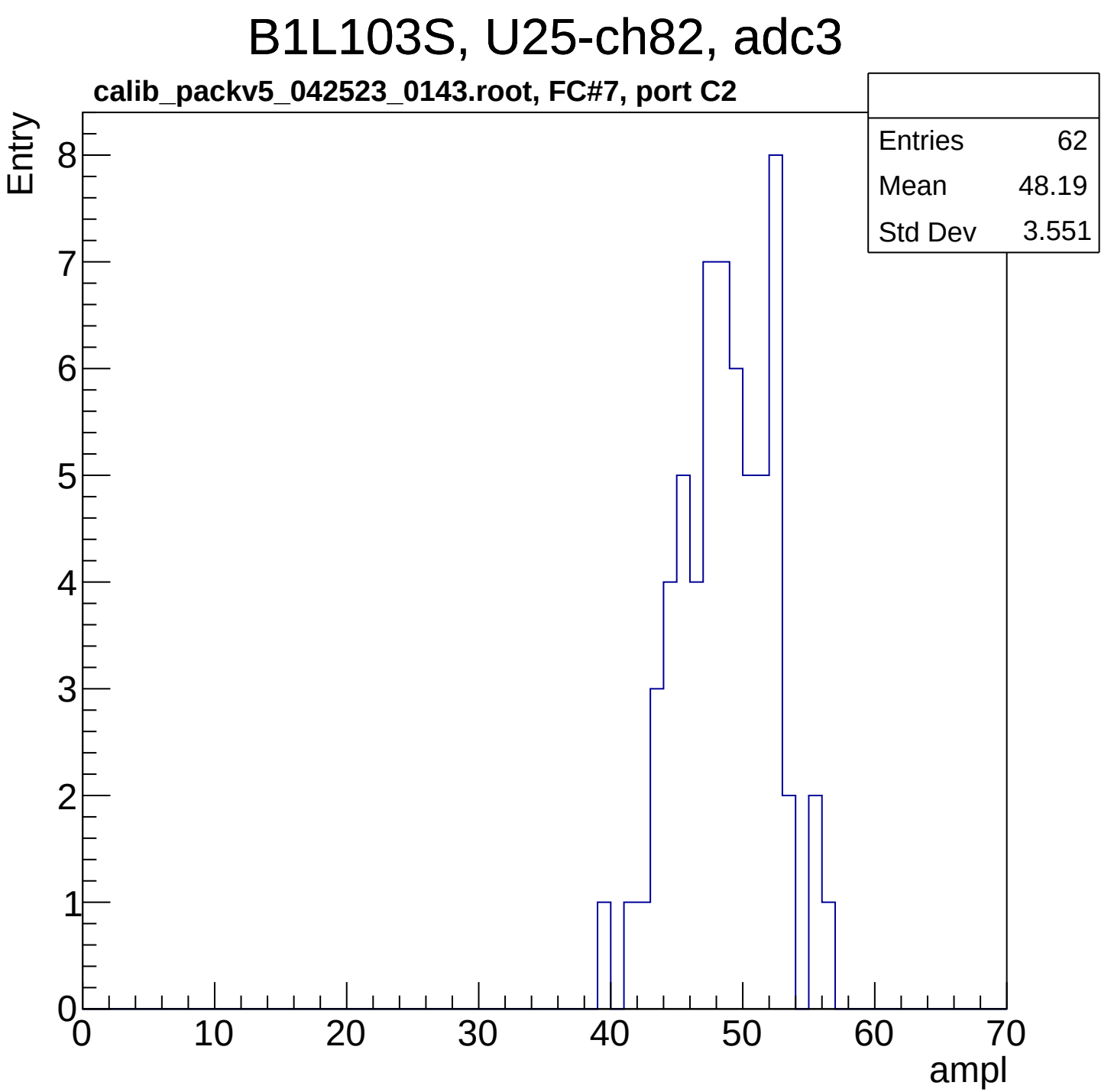
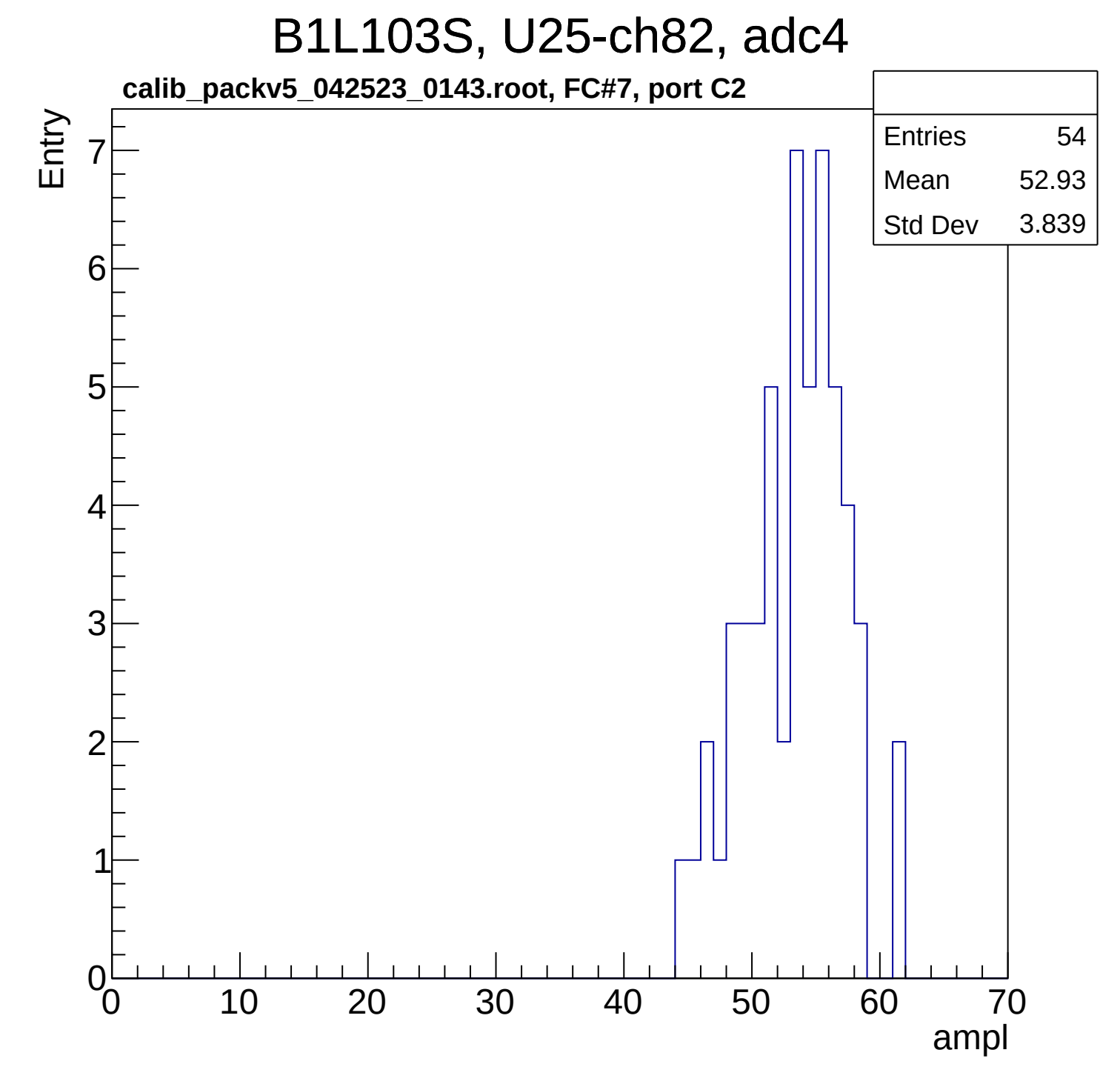
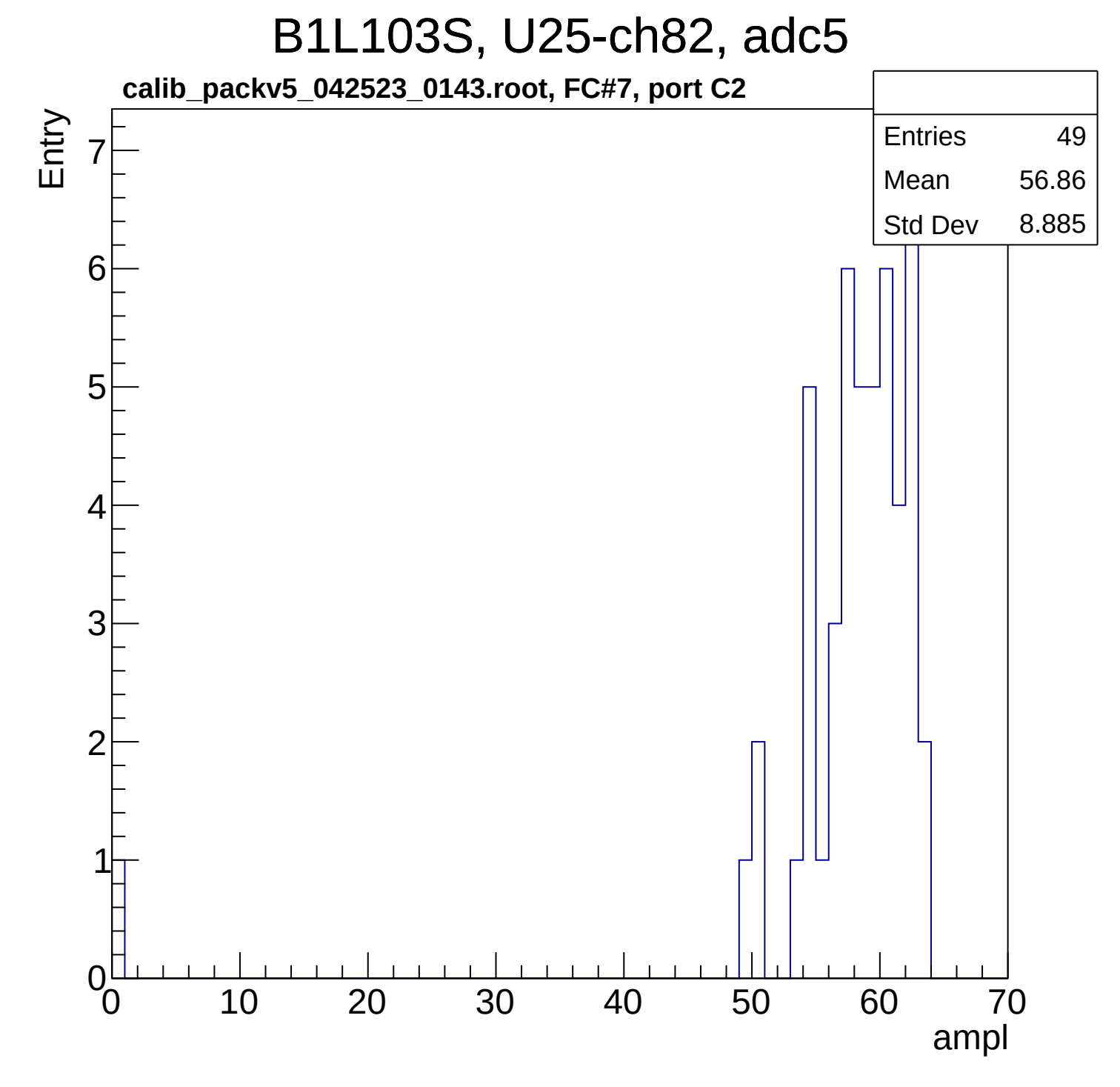

B1L103S, U25-ch82, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 48.19
Std Dev 3.551
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 52.93
Std Dev 3.839
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 56.86
Std Dev 8.885
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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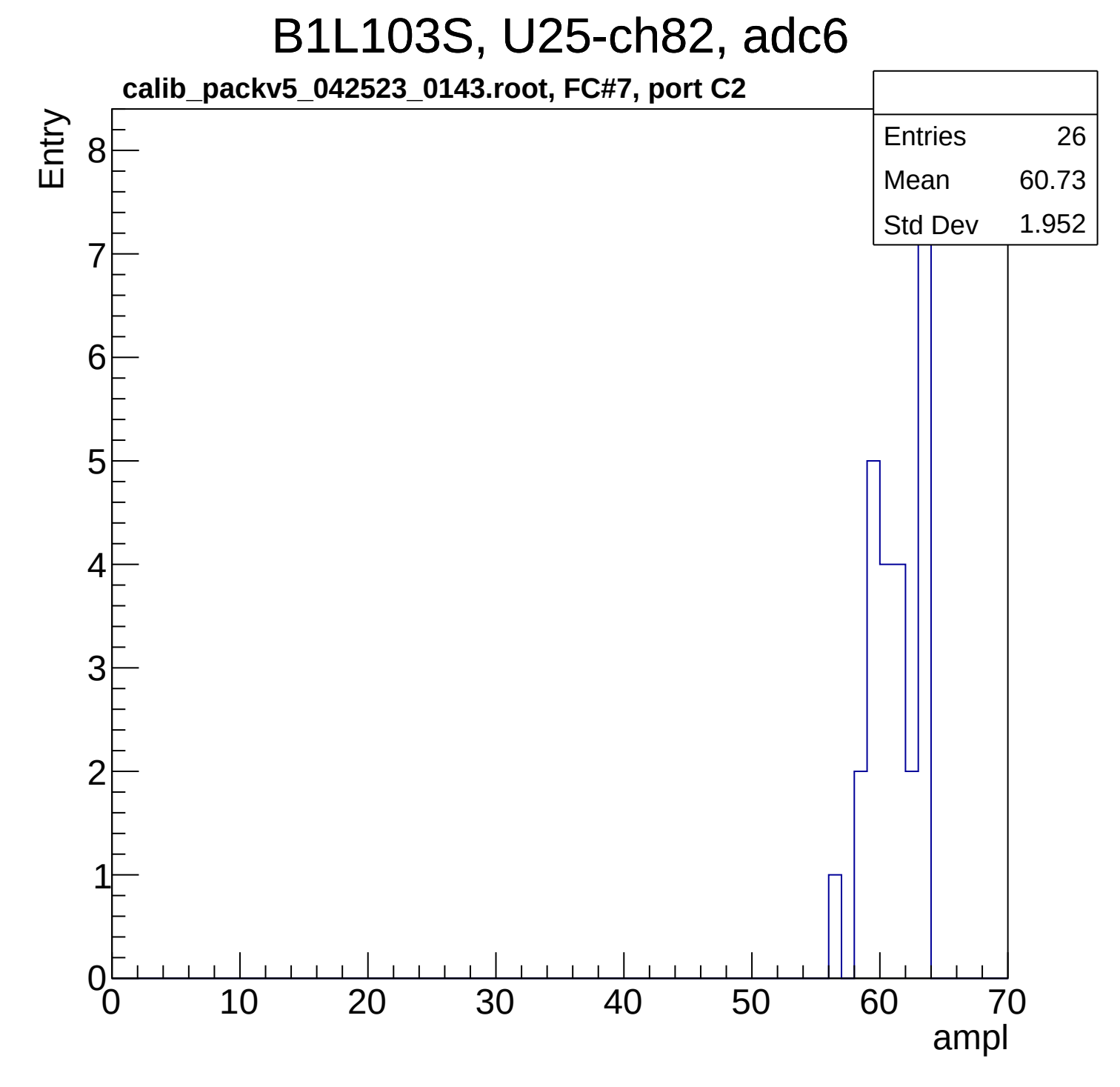
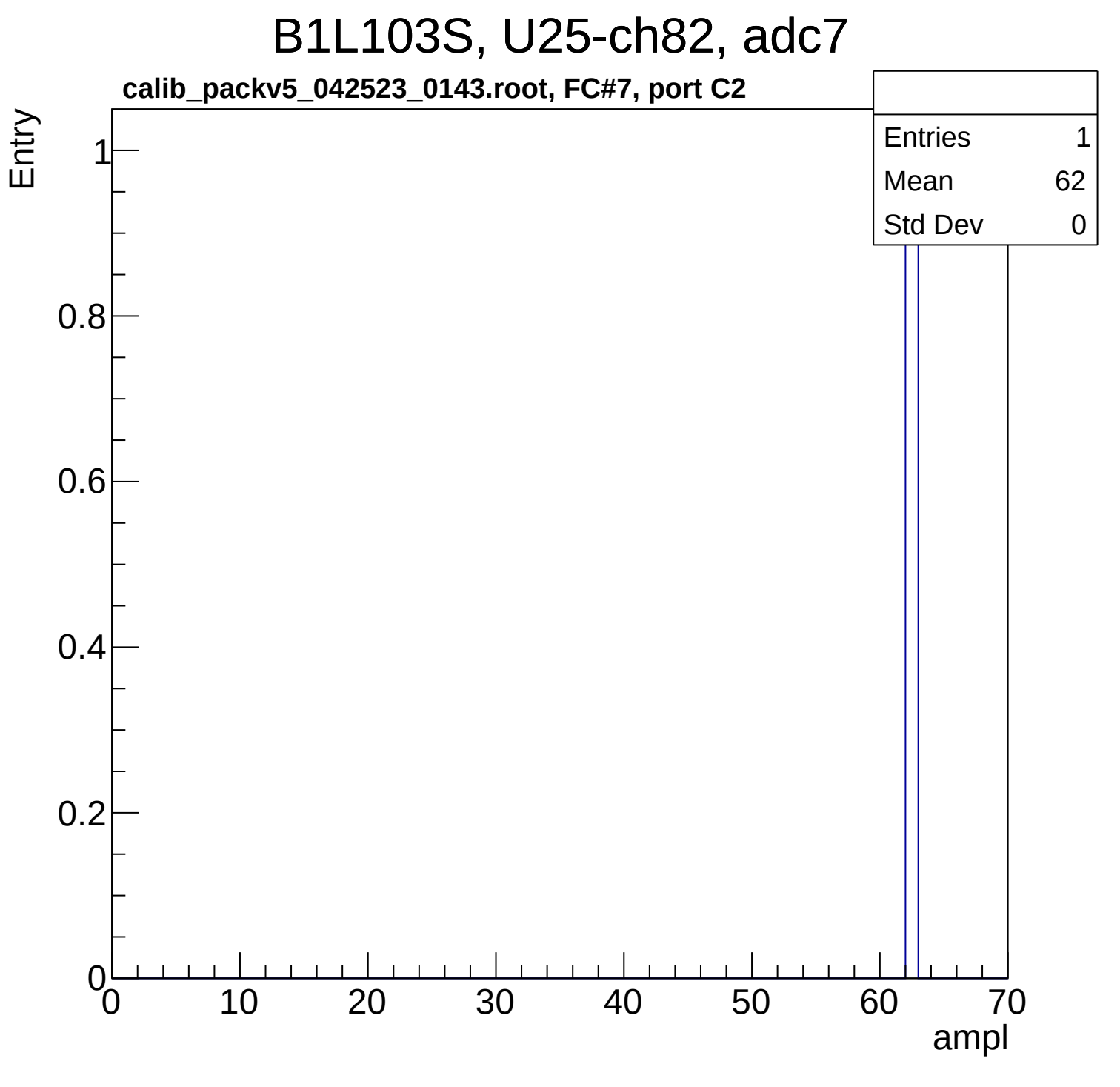
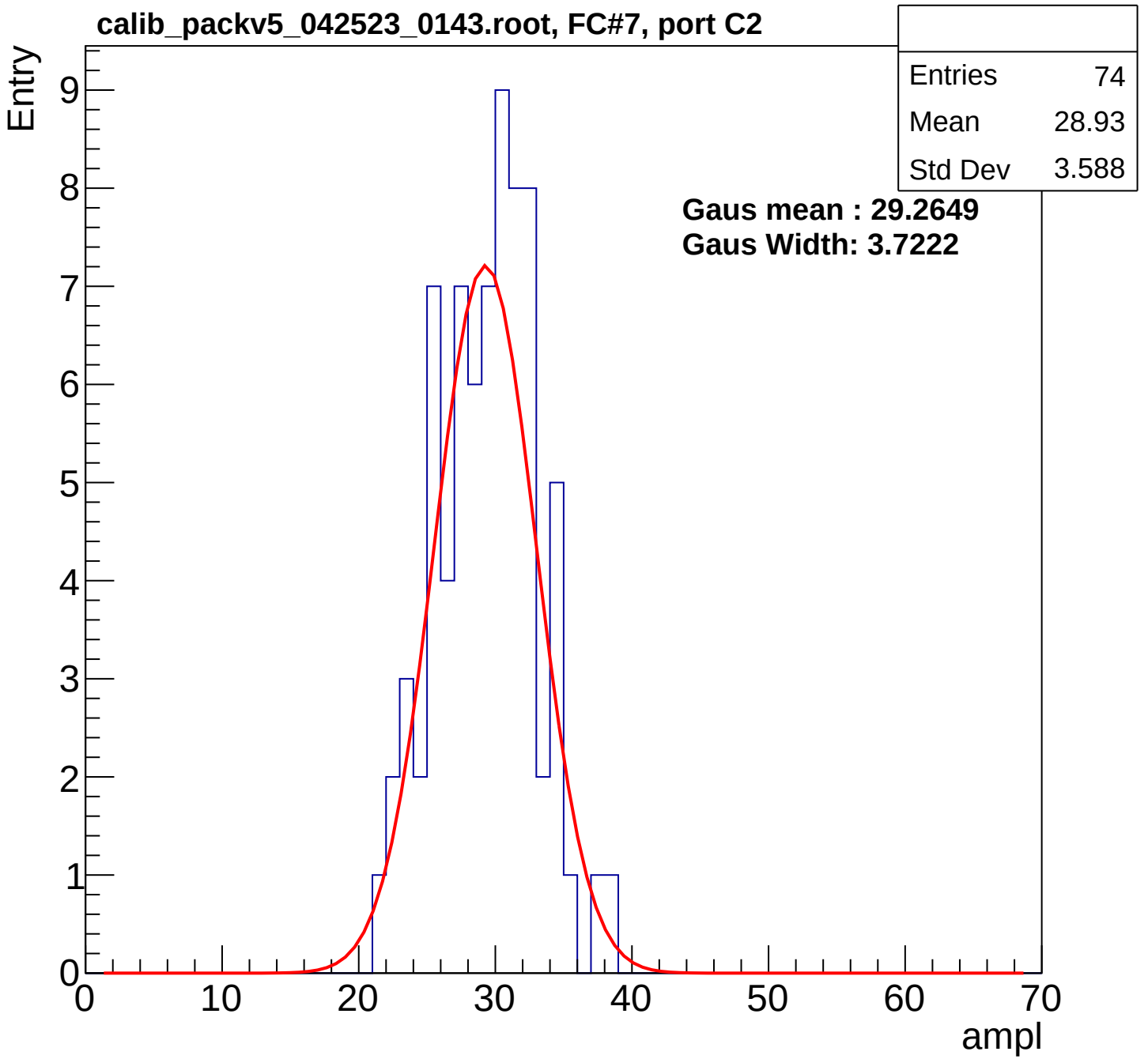

B1L103S, U25-ch82, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 60.73
Std Dev 1.952
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch82, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch83, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 28.93
Std Dev 3.588
Gaus mean : 29.2649
Gaus Width: 3.7222
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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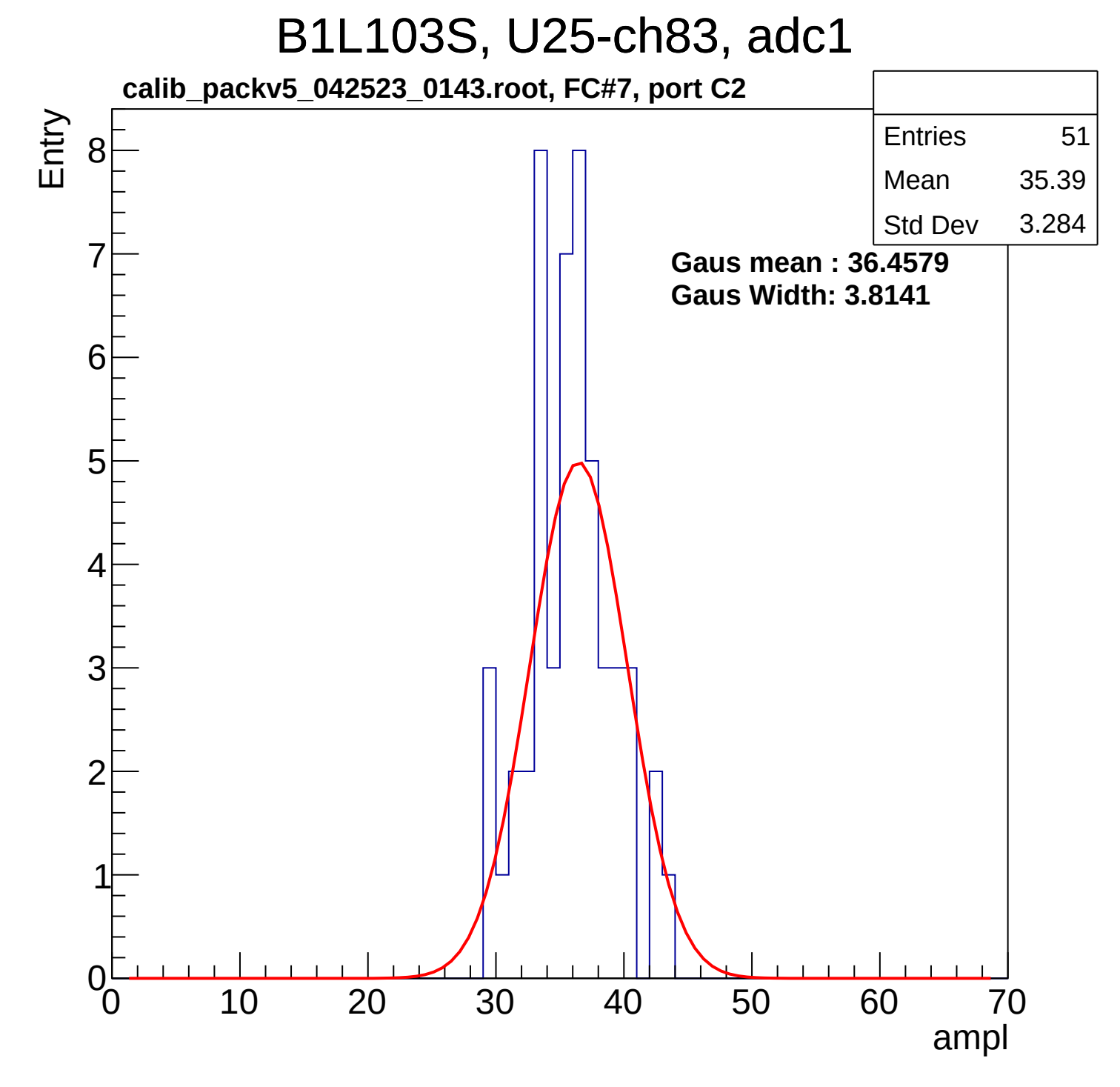
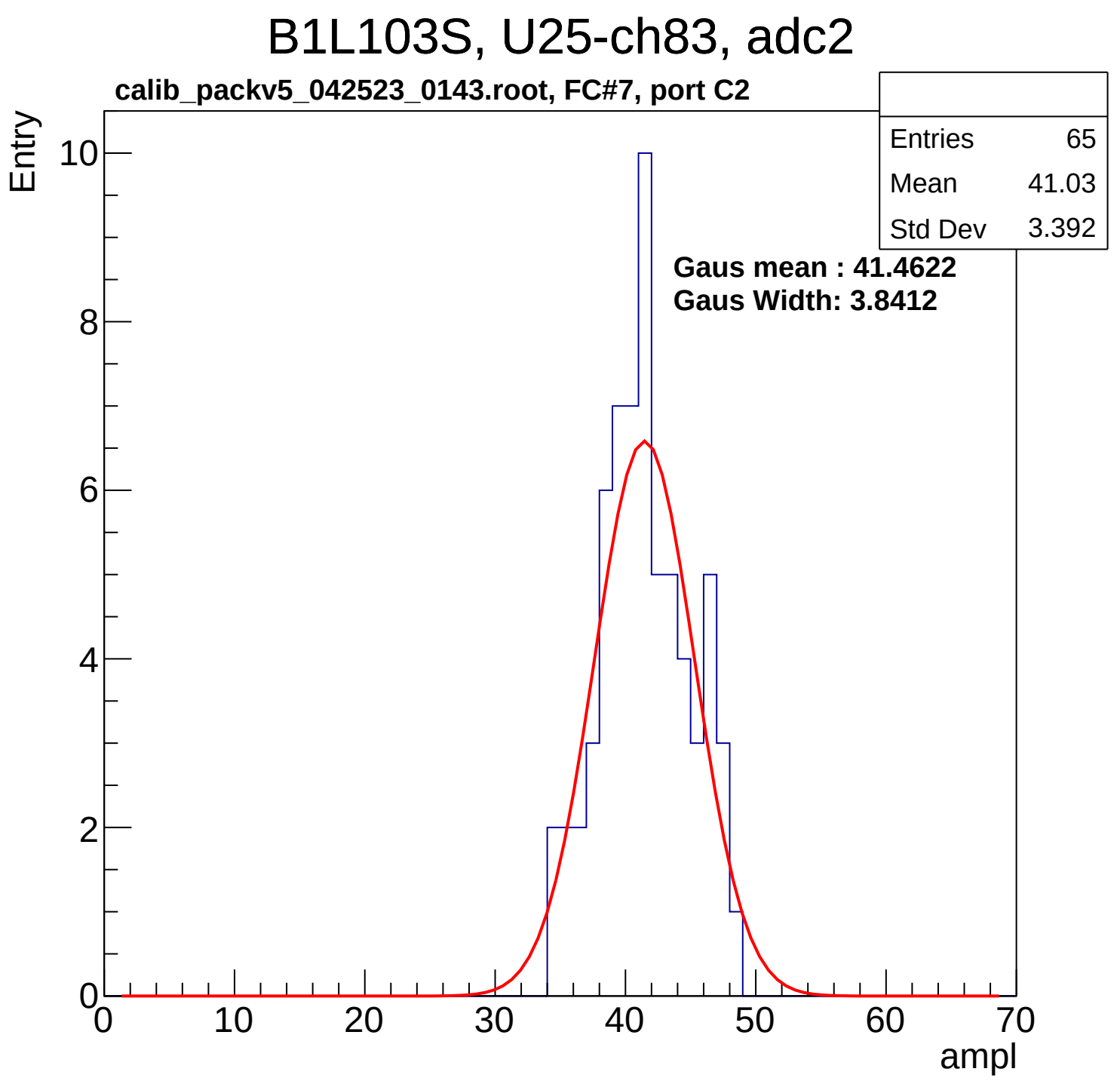
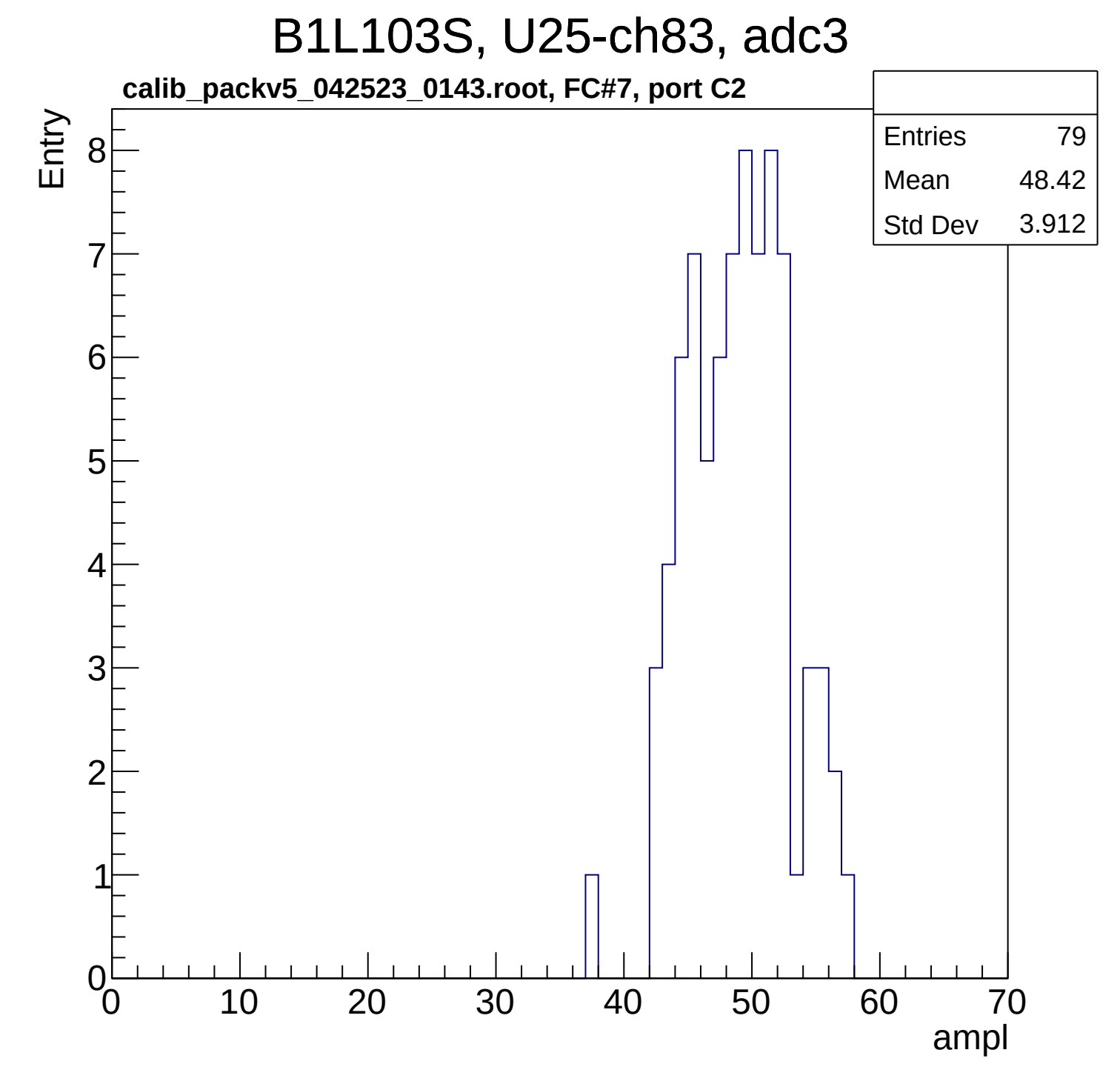
60
70


B1L103S, U25-ch83, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 35.39
Std Dev 3.284
Gaus mean : 36.4579
Gaus Width: 3.8141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch83, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.03
Std Dev 3.392
Gaus mean : 41.4622
Gaus Width: 3.8412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch83, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 48.42
Std Dev 3.912
Entry
ampl
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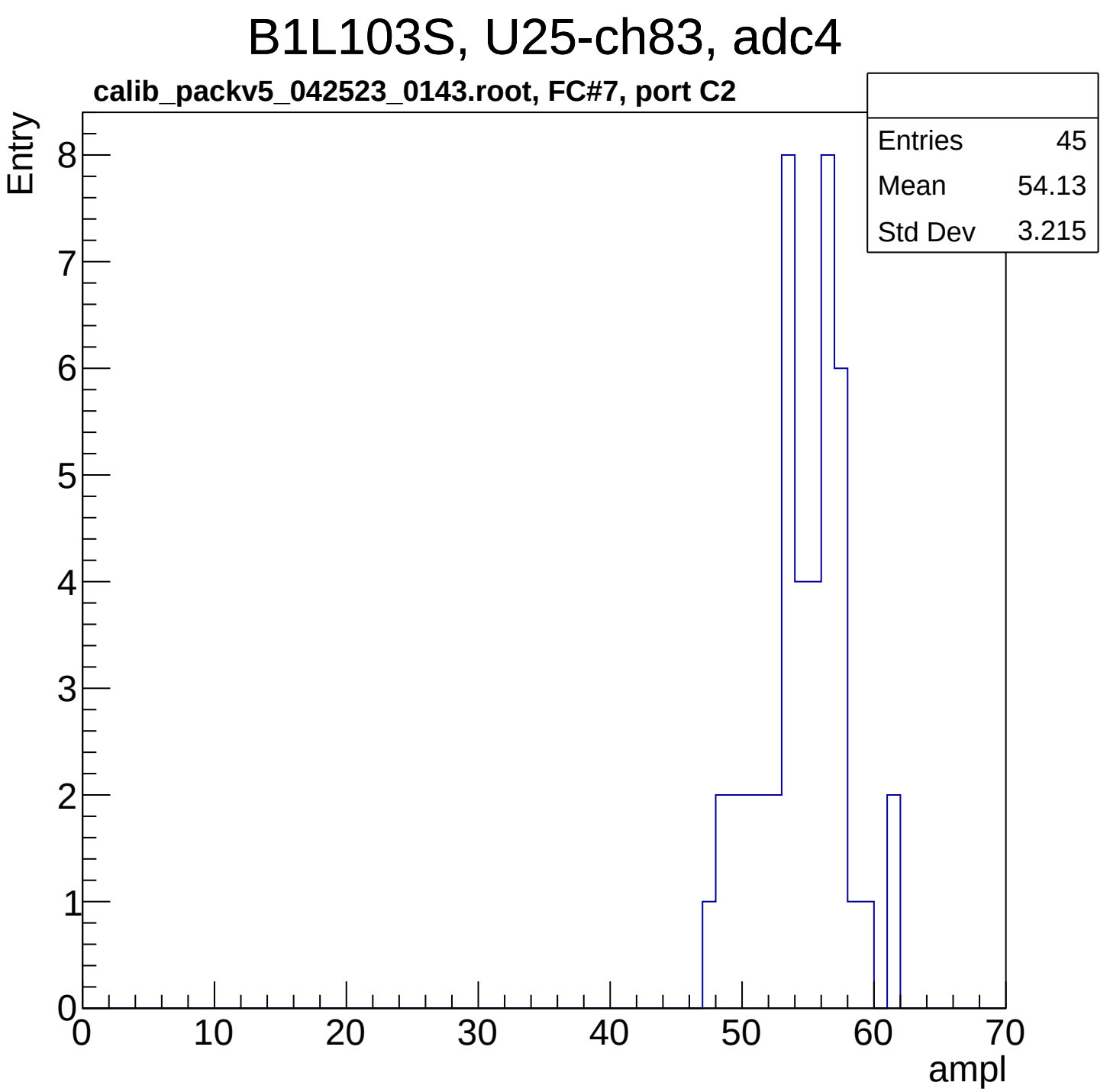
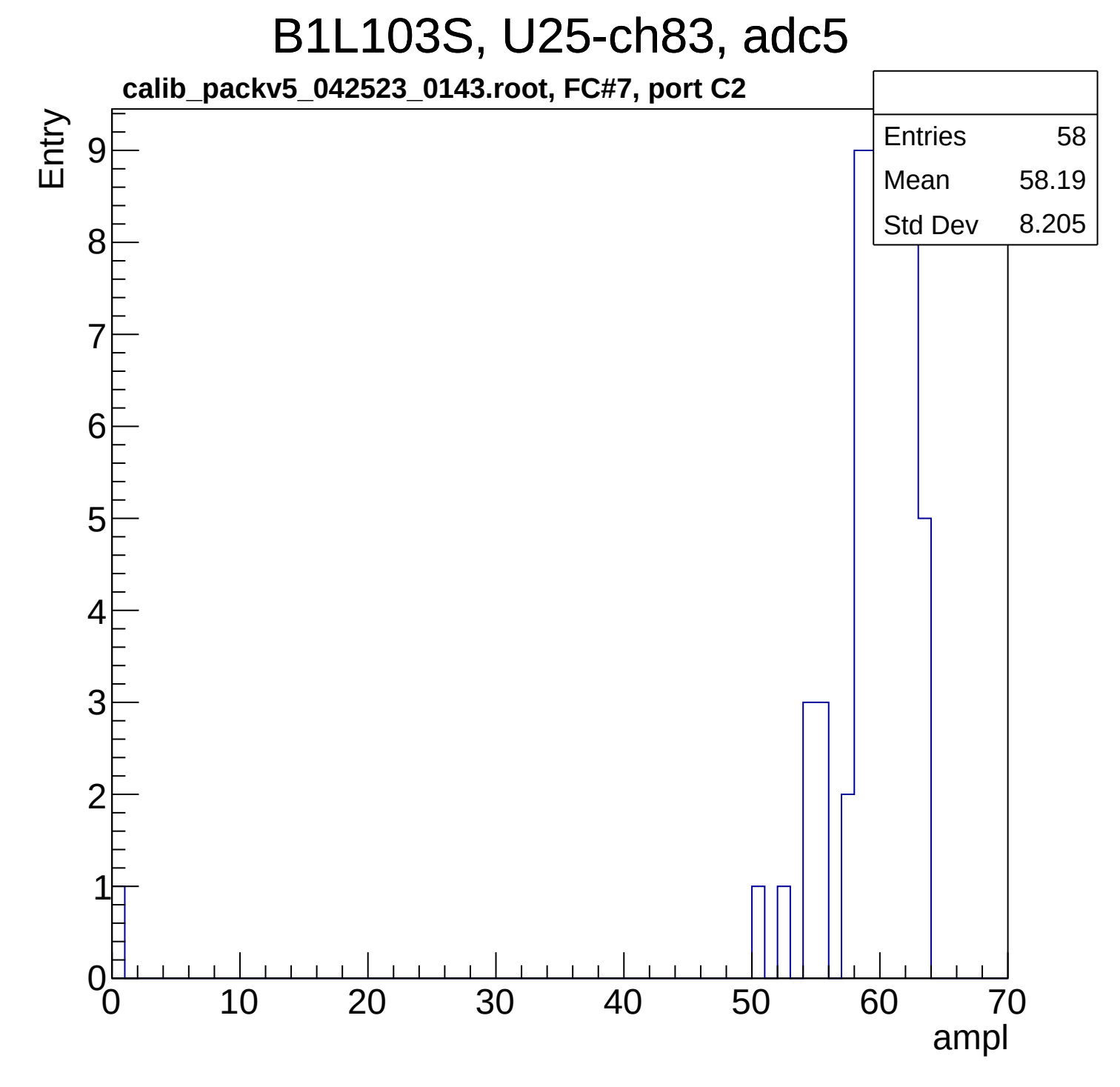
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch83, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 54.13
Std Dev 3.215
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch83, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 58.19
Std Dev 8.205
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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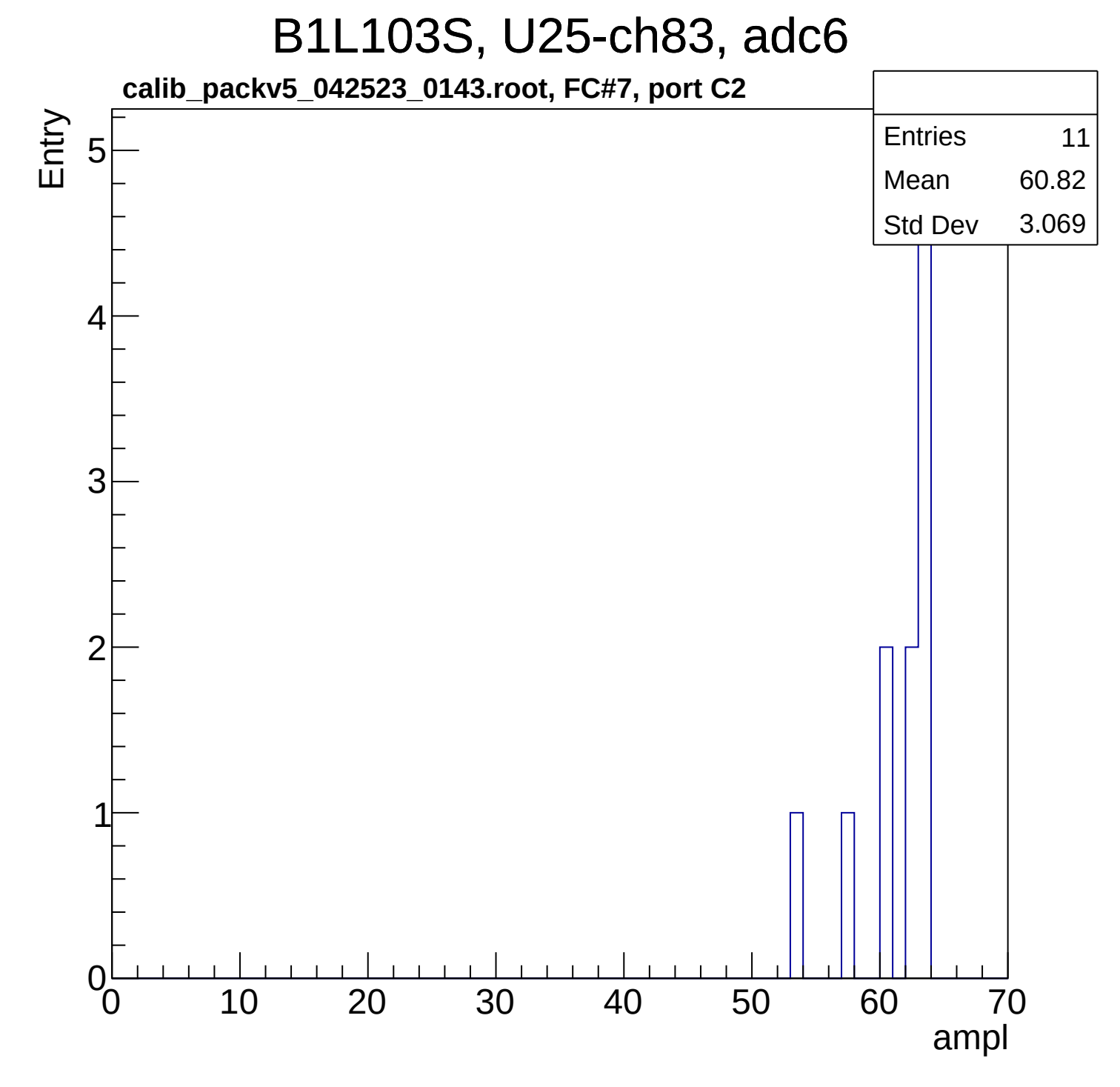
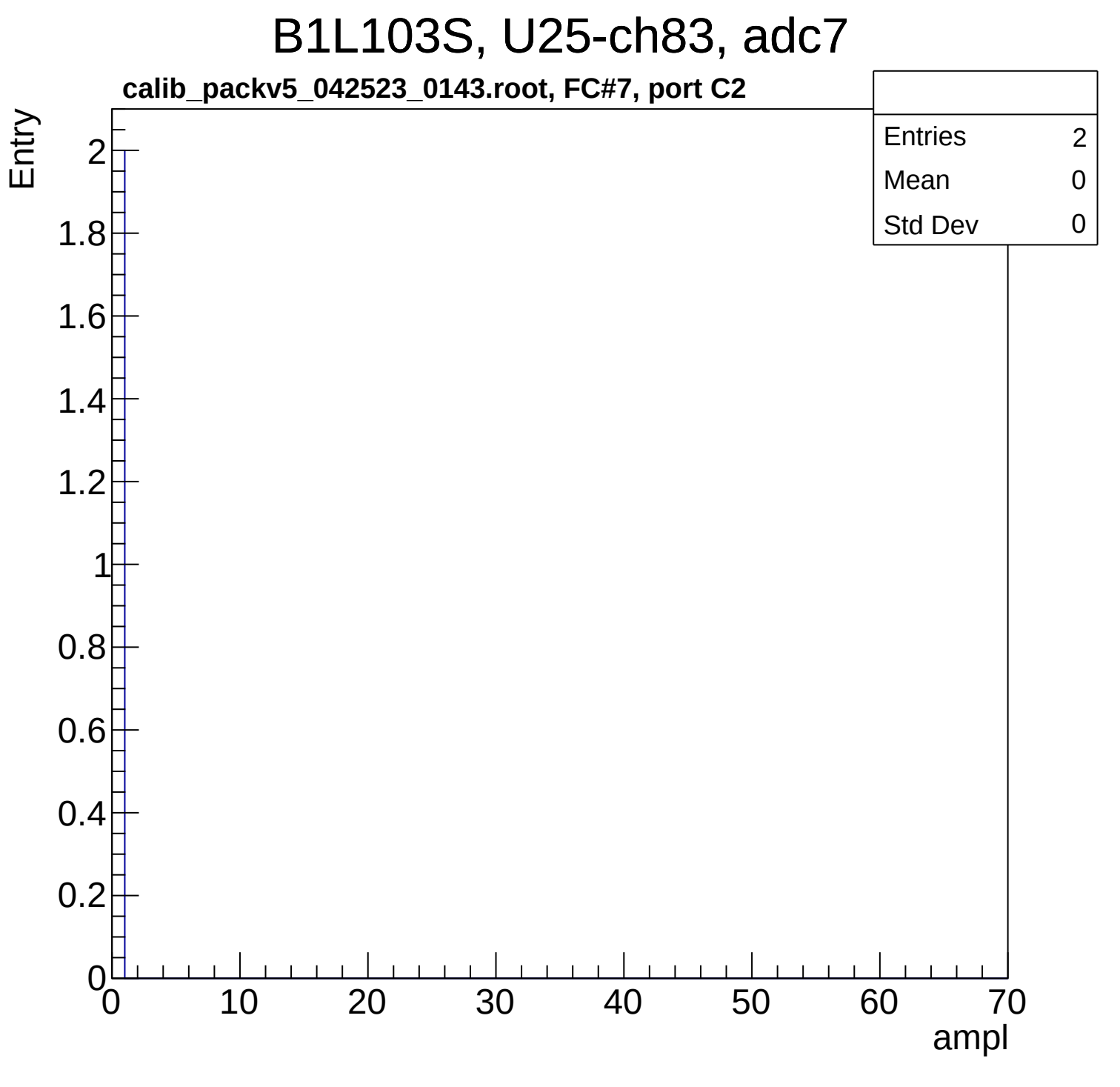
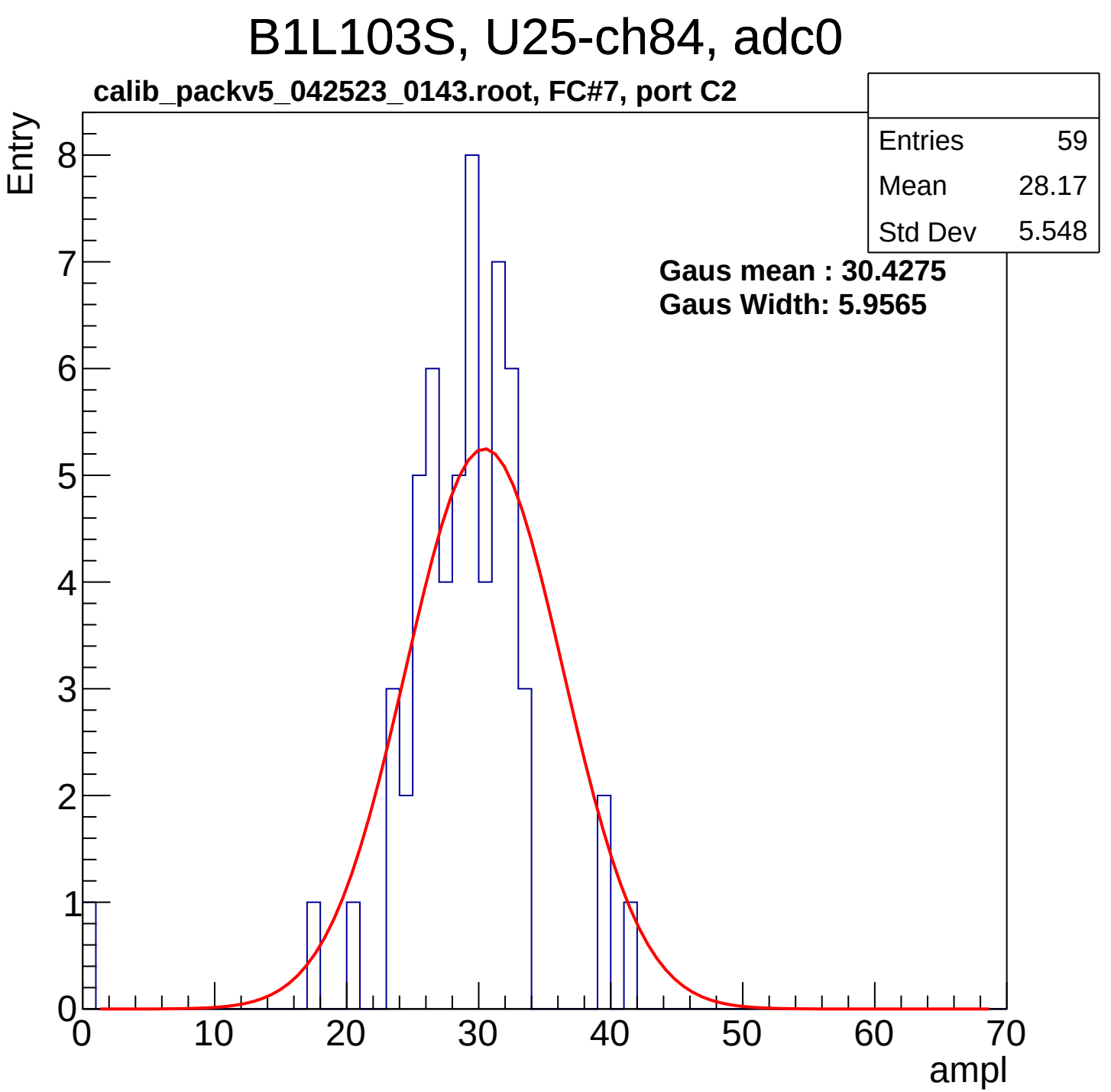
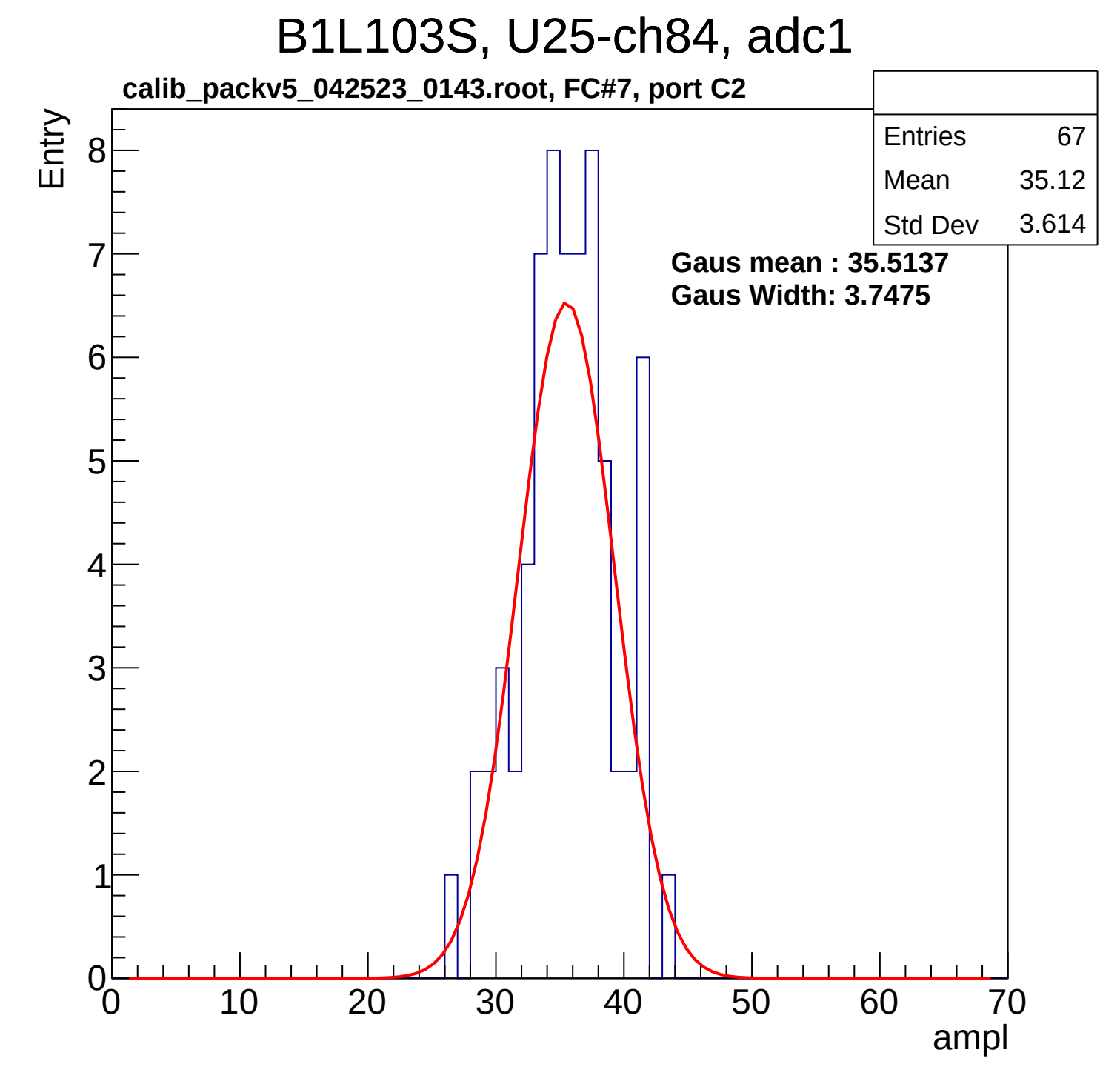
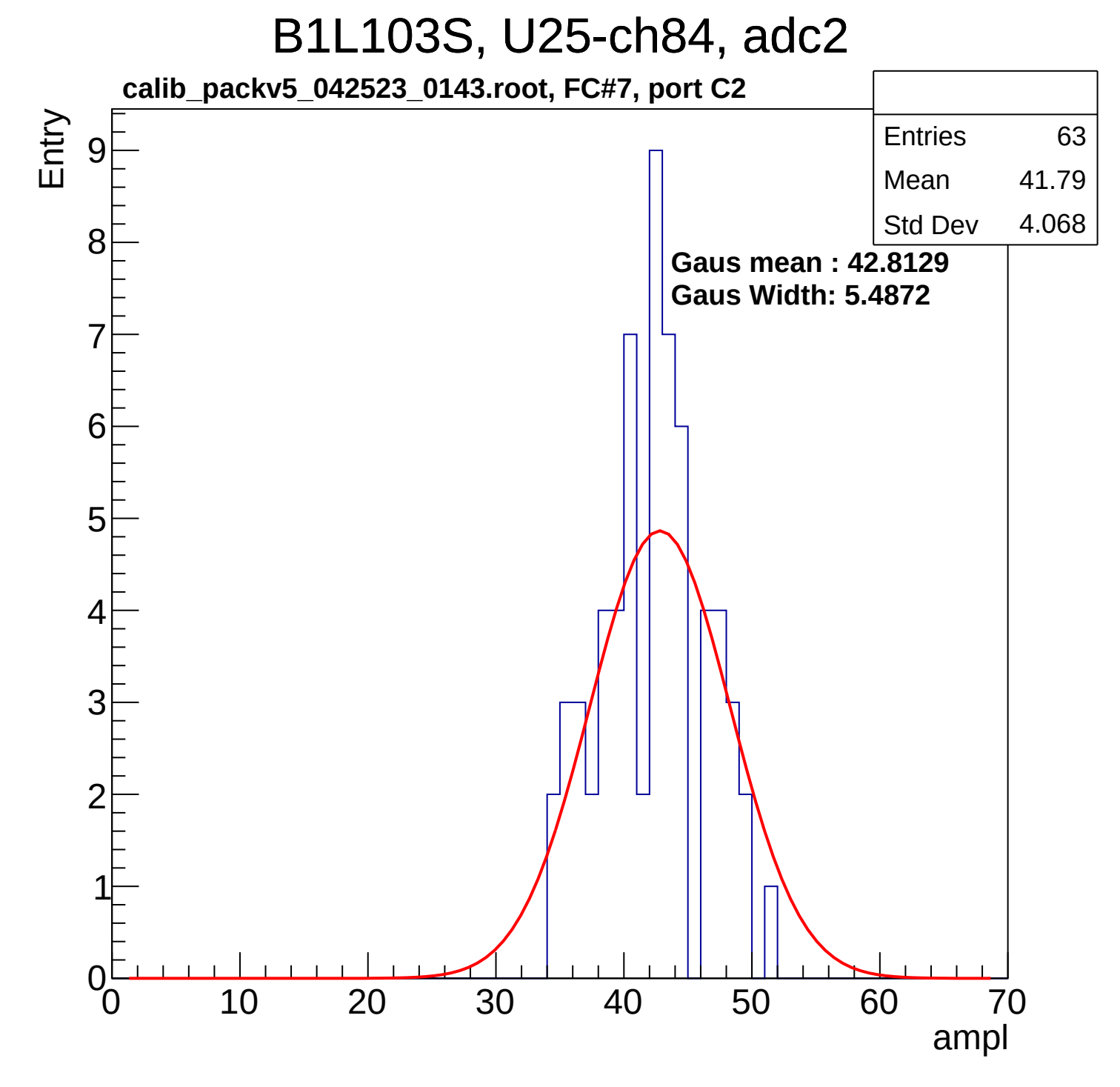
20
30
40
50
60
70

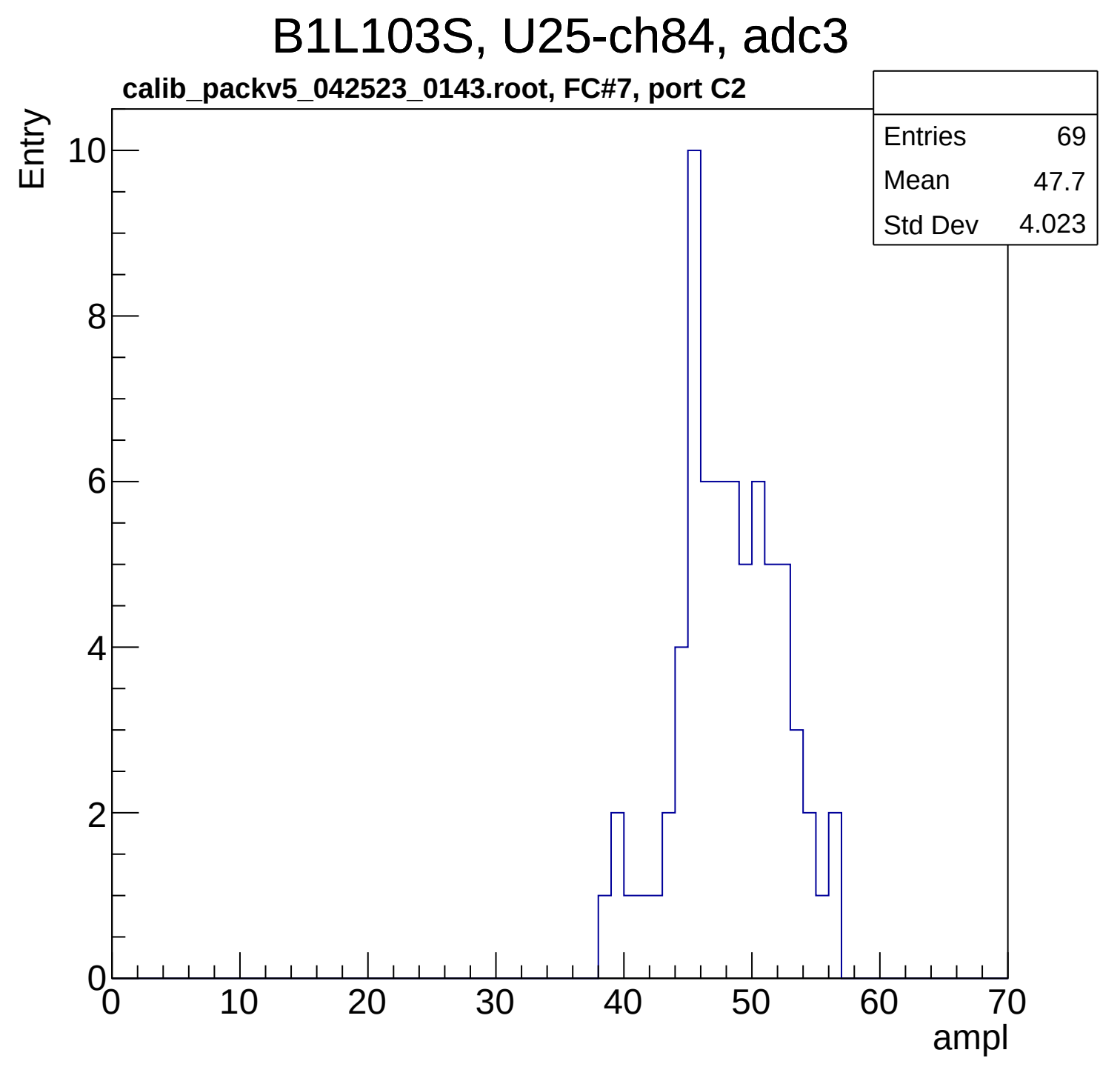

B1L103S, U25-ch83, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 60.82
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

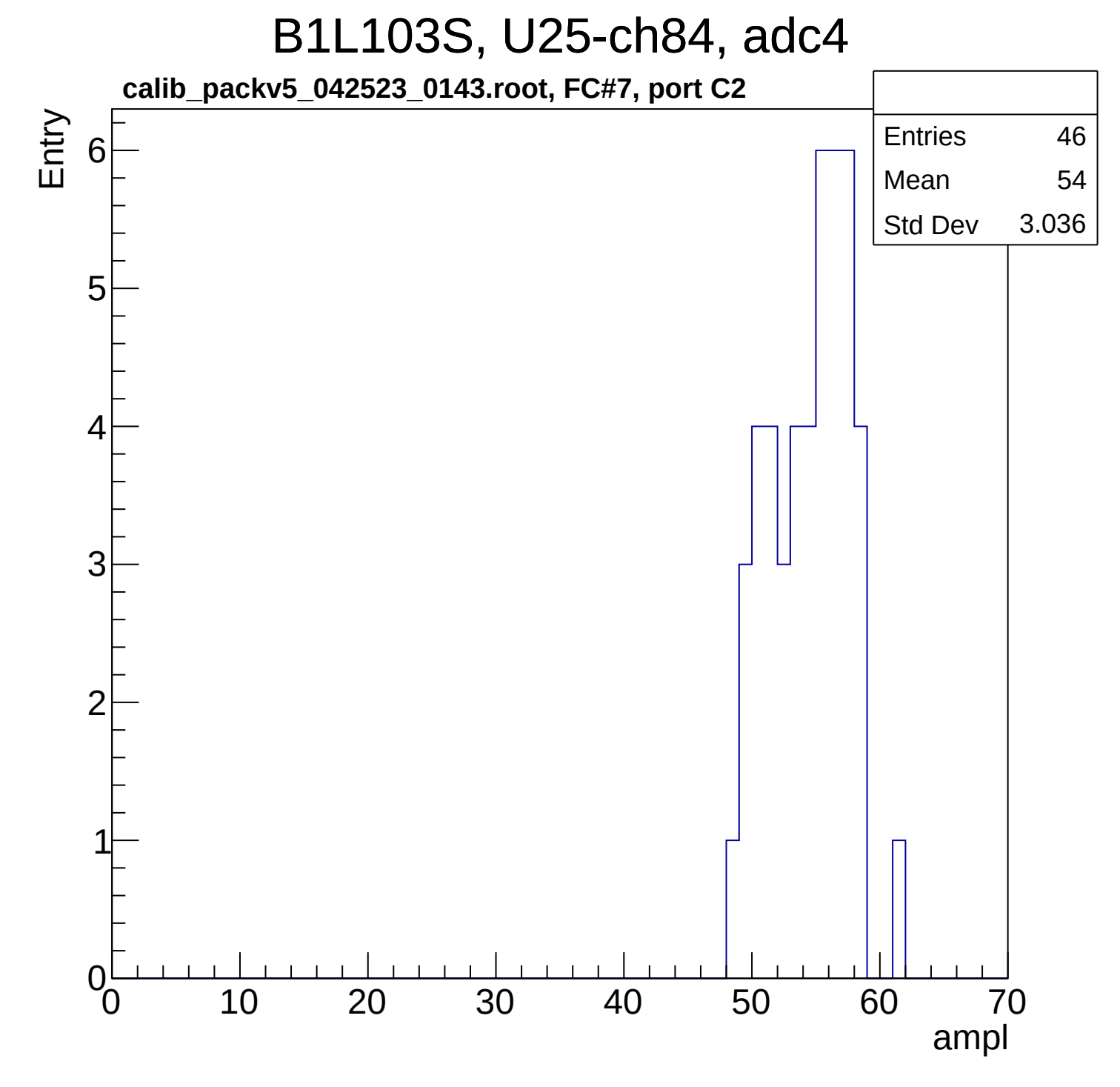

B1L103S, U25-ch83, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

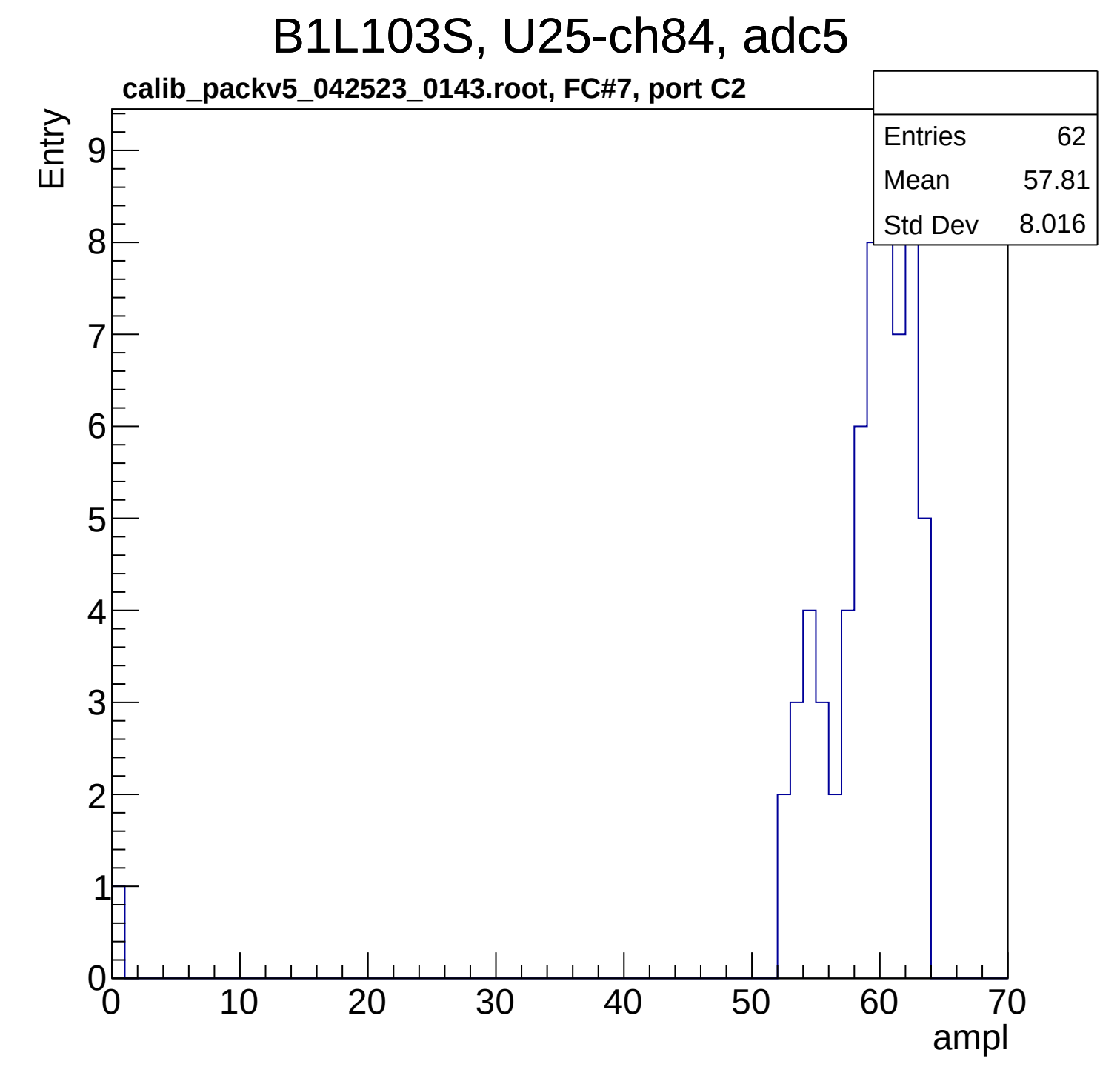

B1L103S, U25-ch84, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 28.17
Std Dev 5.548
Gaus mean : 30.4275
Gaus Width: 5.9565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

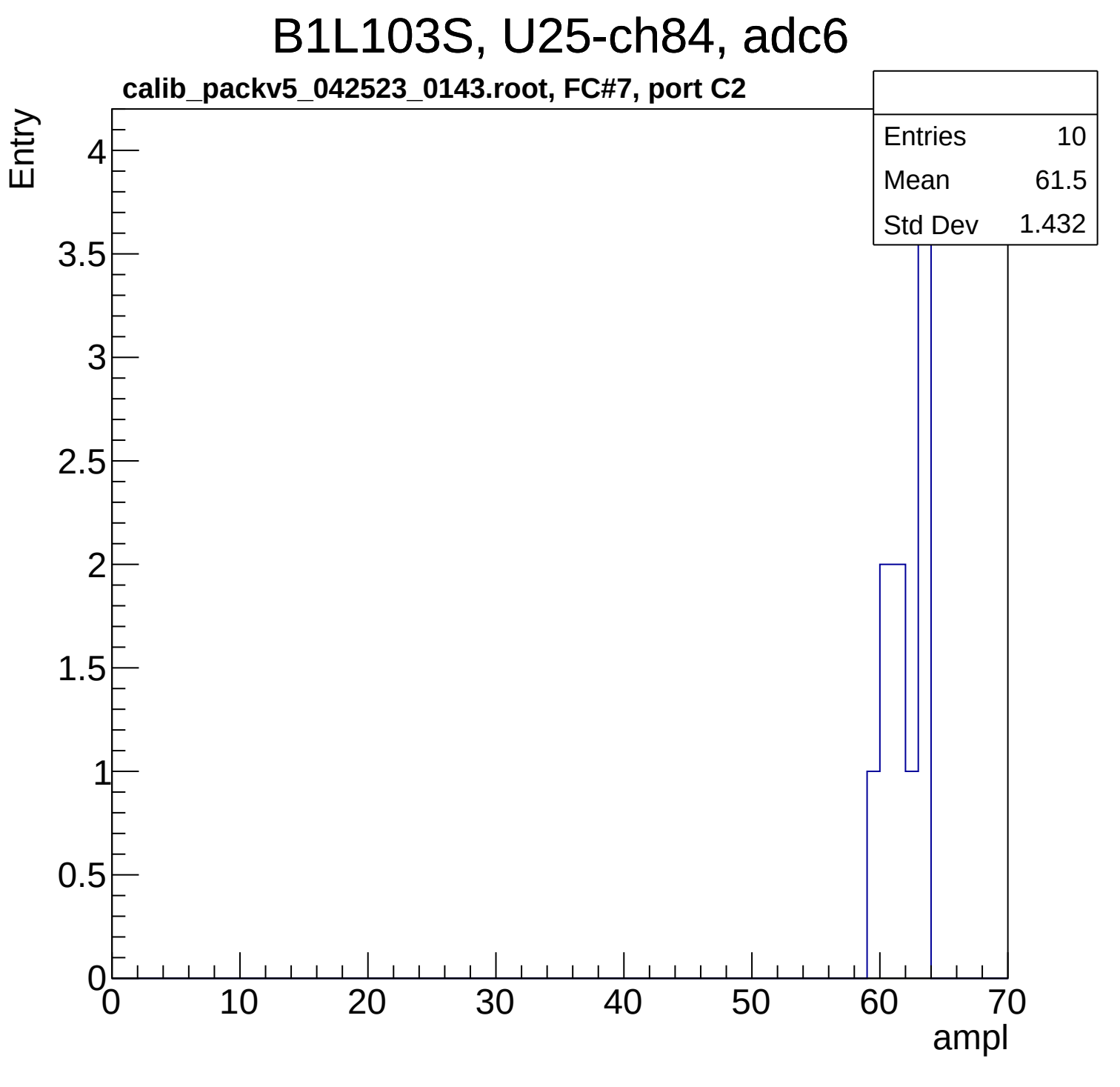

B1L103S, U25-ch84, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 35.12
Std Dev 3.614
Gaus mean : 35.5137
Gaus Width: 3.7475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

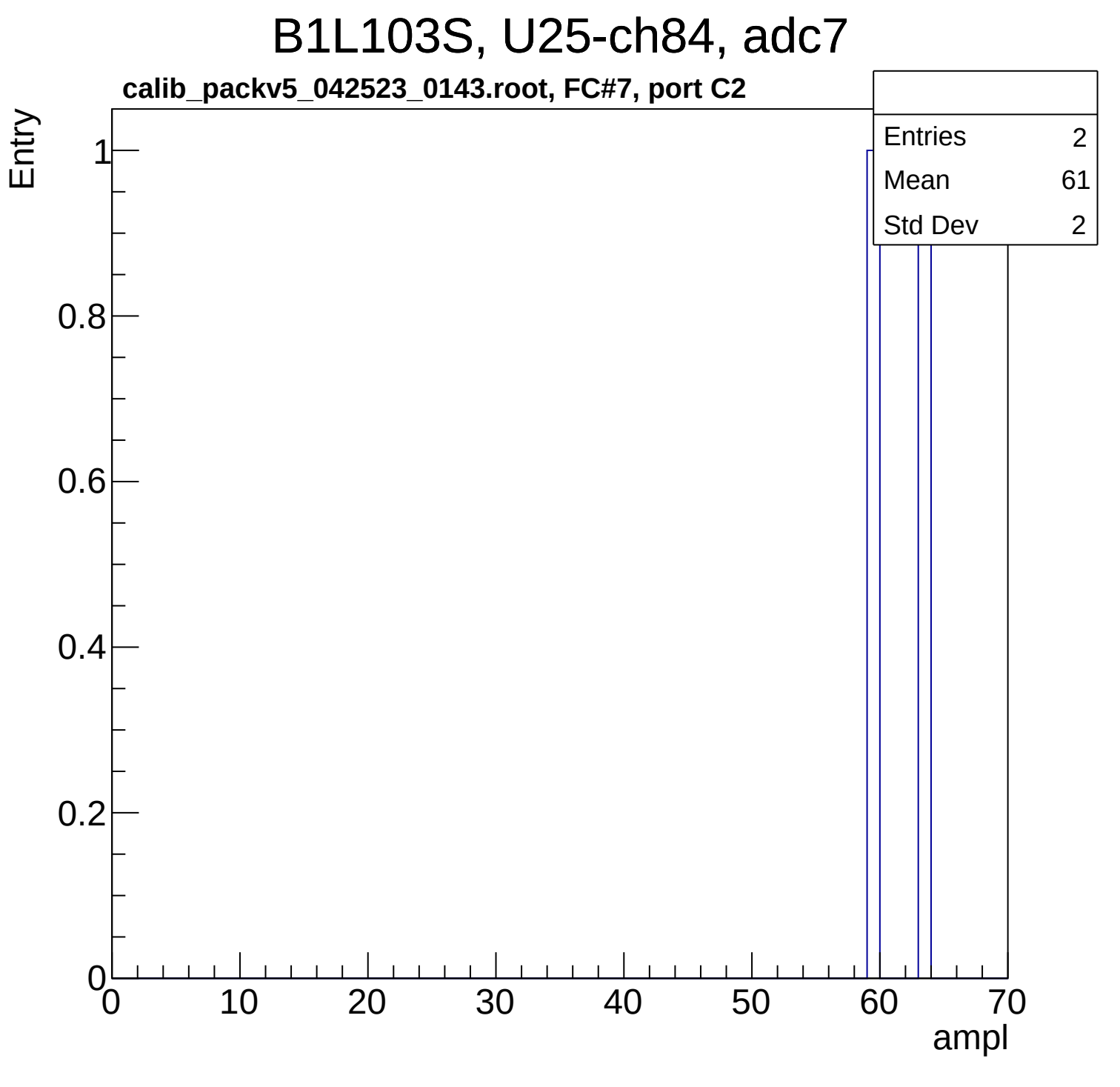

B1L103S, U25-ch84, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 41.79
Std Dev 4.068
Gaus mean : 42.8129
Gaus Width: 5.4872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch84, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 47.7
Std Dev 4.023
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch84, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 54
Std Dev 3.036
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch84, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 57.81
Std Dev 8.016
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch84, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.5
Std Dev 1.432
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U25-ch84, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61
Std Dev 2
Entry
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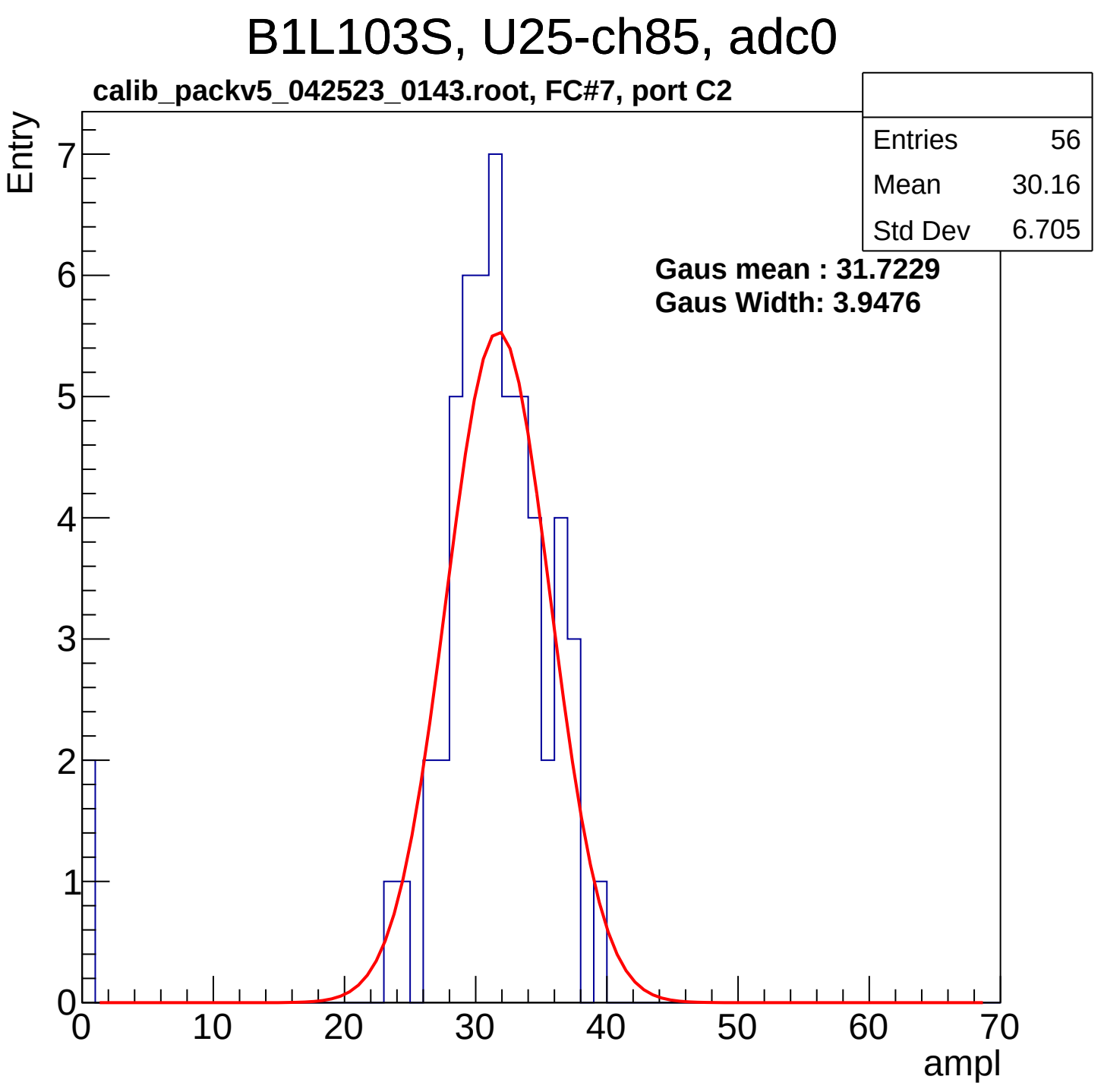
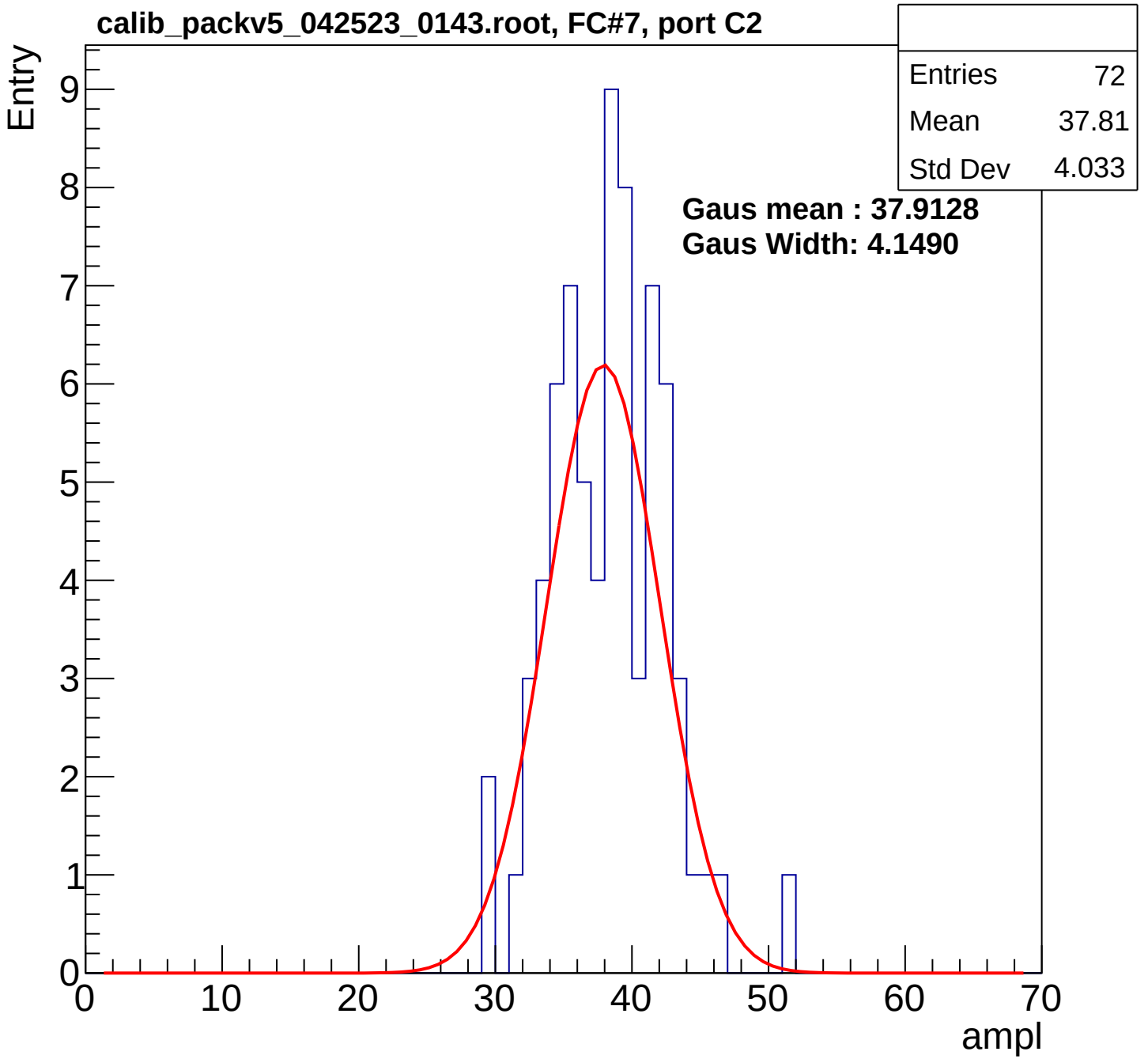
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch85, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 30.16
Std Dev 6.705
Gaus mean : 31.7229
Gaus Width: 3.9476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch85, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 37.81
Std Dev 4.033
Gaus mean : 37.9128
Gaus Width: 4.1490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
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60
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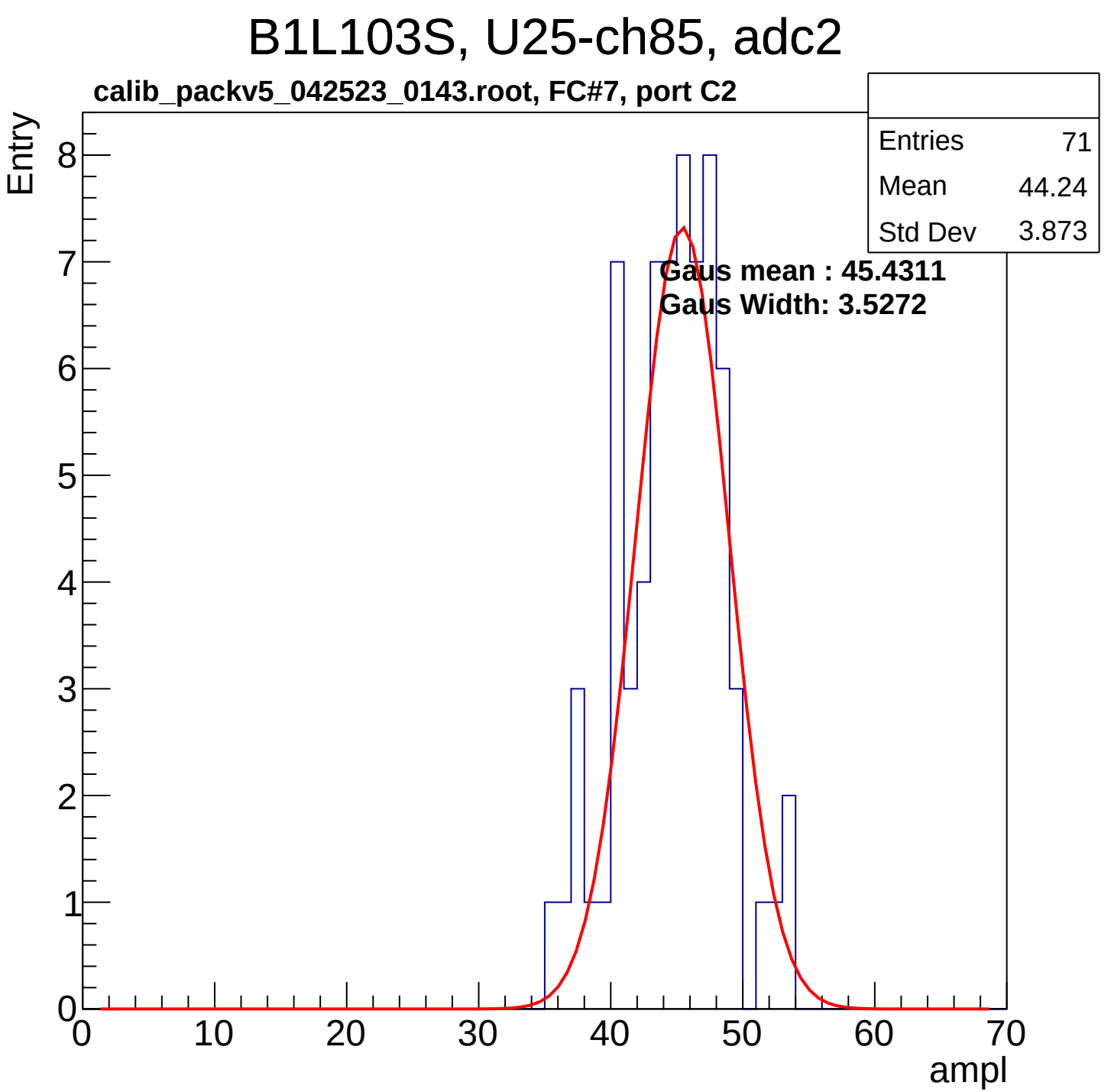
B1L103S, U25-ch85, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 44.24
Std Dev 3.873
Gaus mean : 45.4311
Gaus Width: 3.5272
Entry
ampl
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1
2
3
4
5
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0
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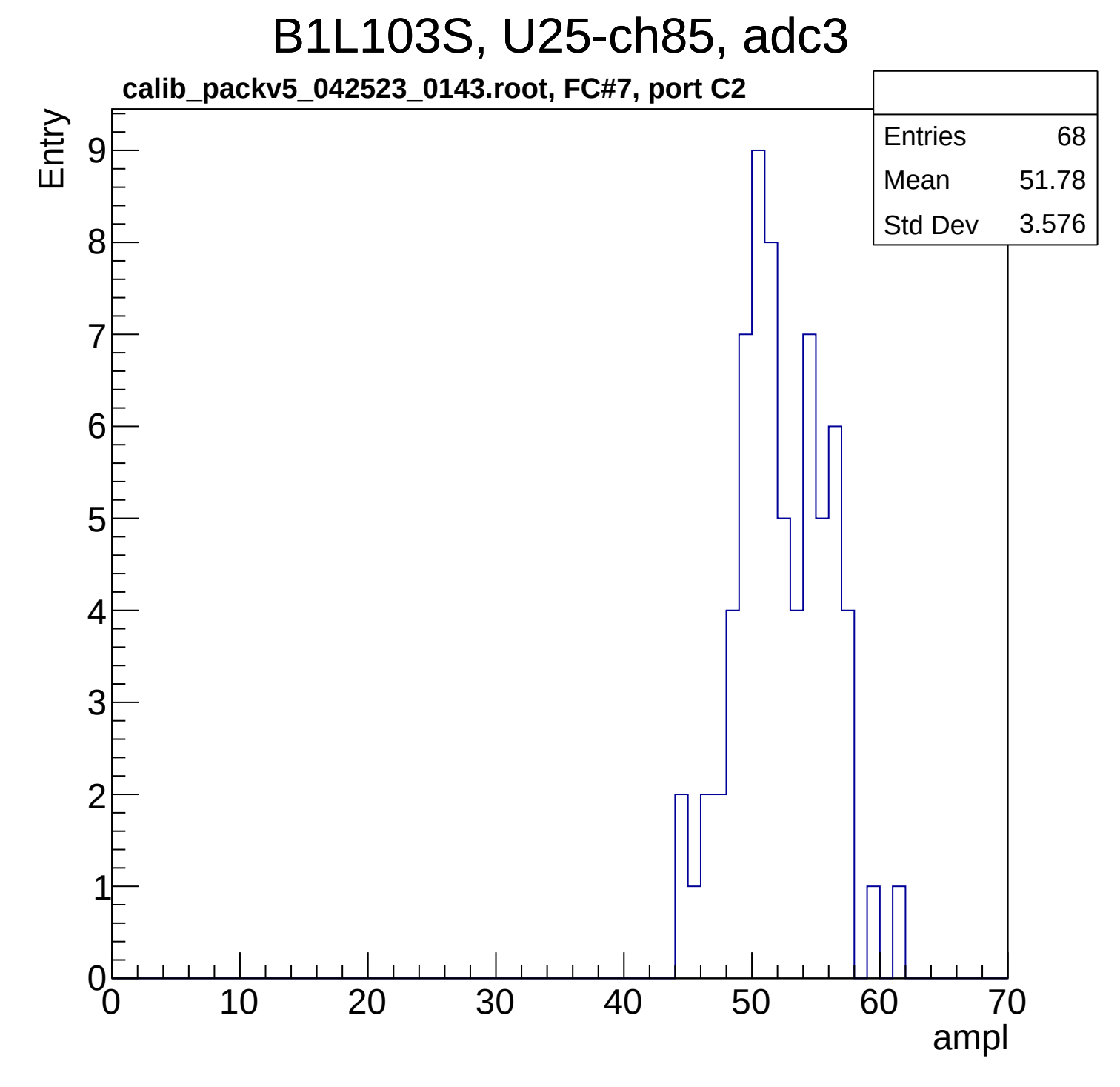
B1L103S, U25-ch85, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 51.78
Std Dev 3.576
Entry
ampl
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2
3
4
5
6
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9
0
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60
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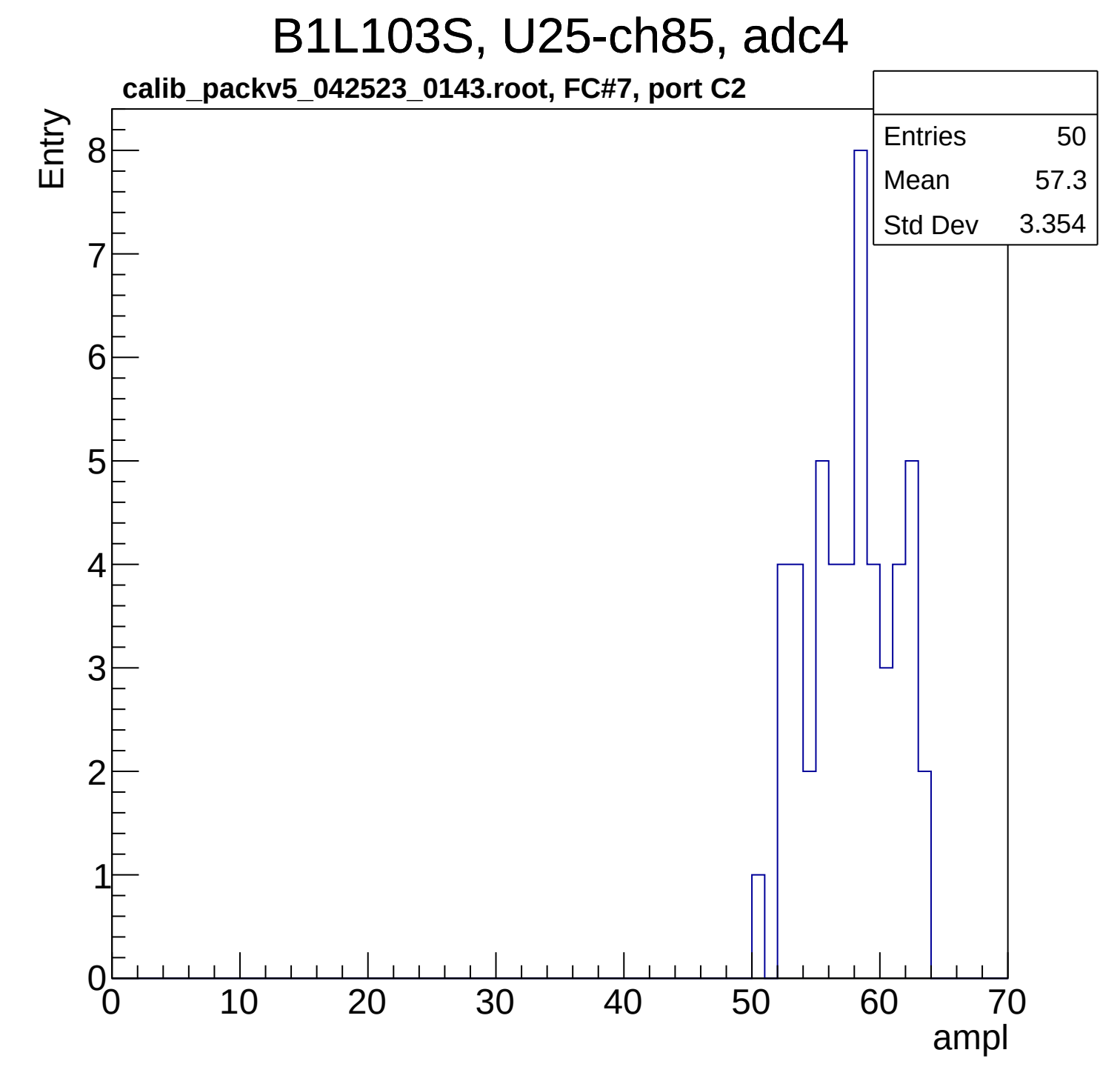
B1L103S, U25-ch85, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 57.3
Std Dev 3.354
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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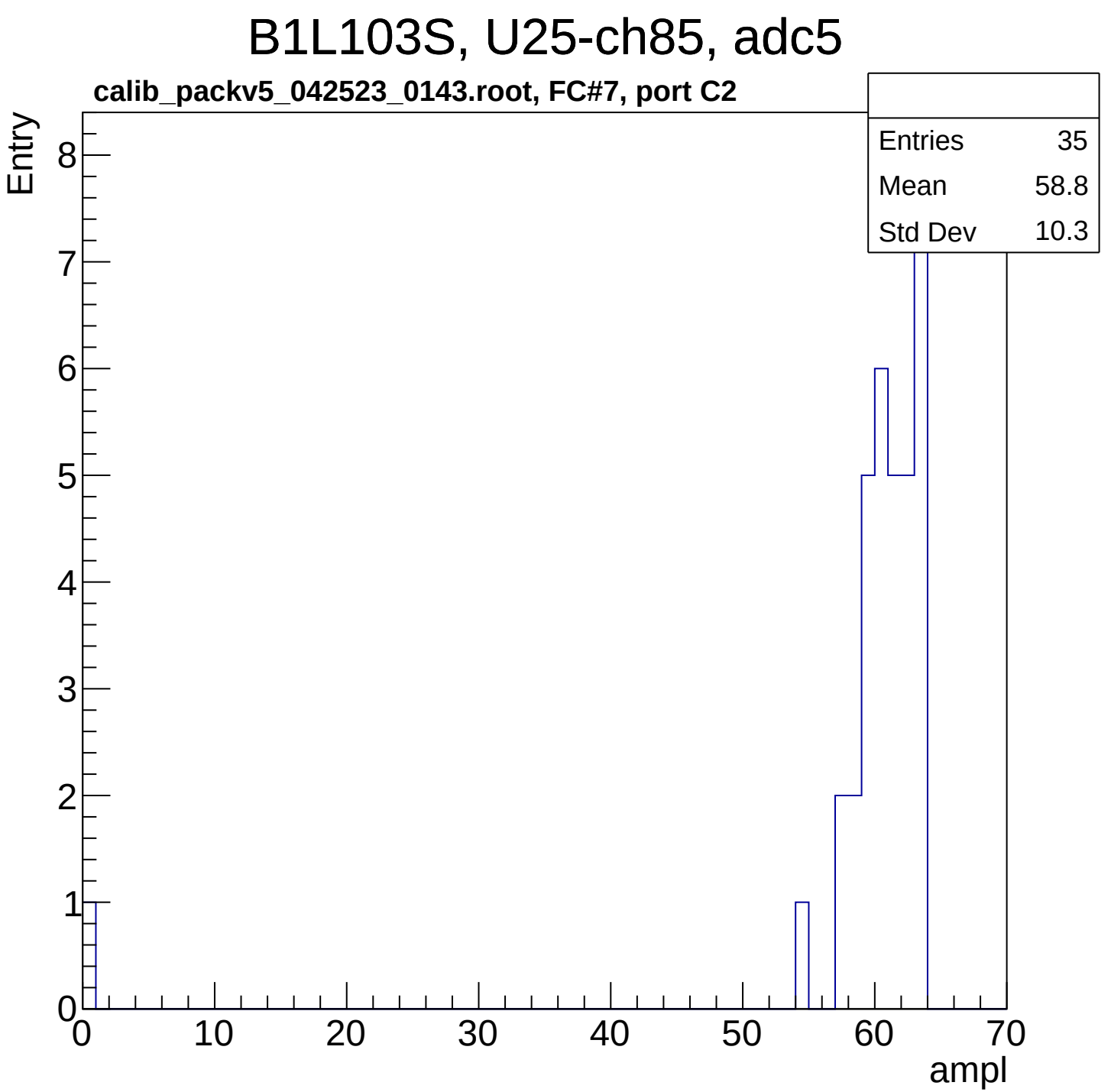
B1L103S, U25-ch85, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 58.8
Std Dev 10.3
Entry
ampl
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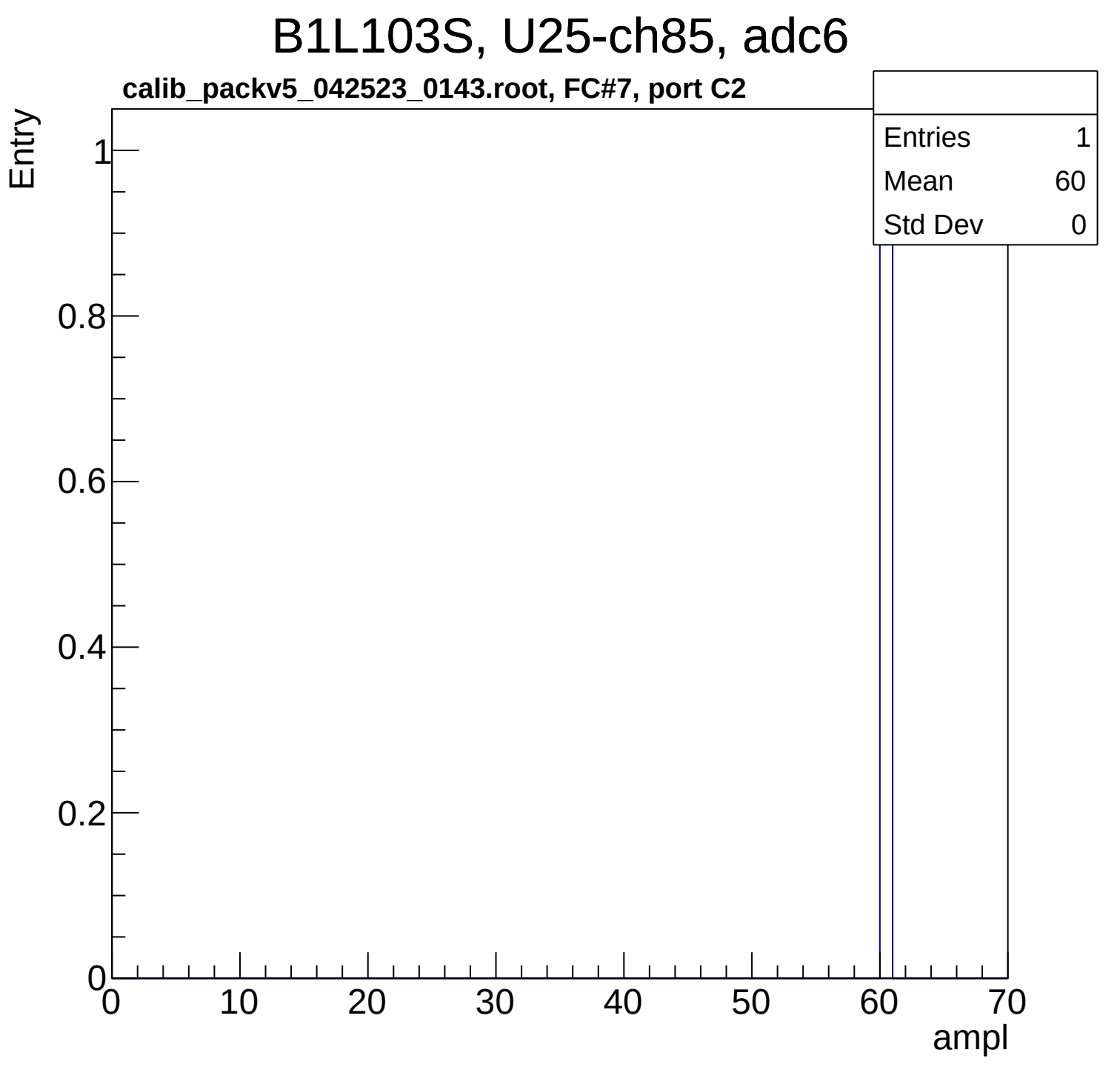
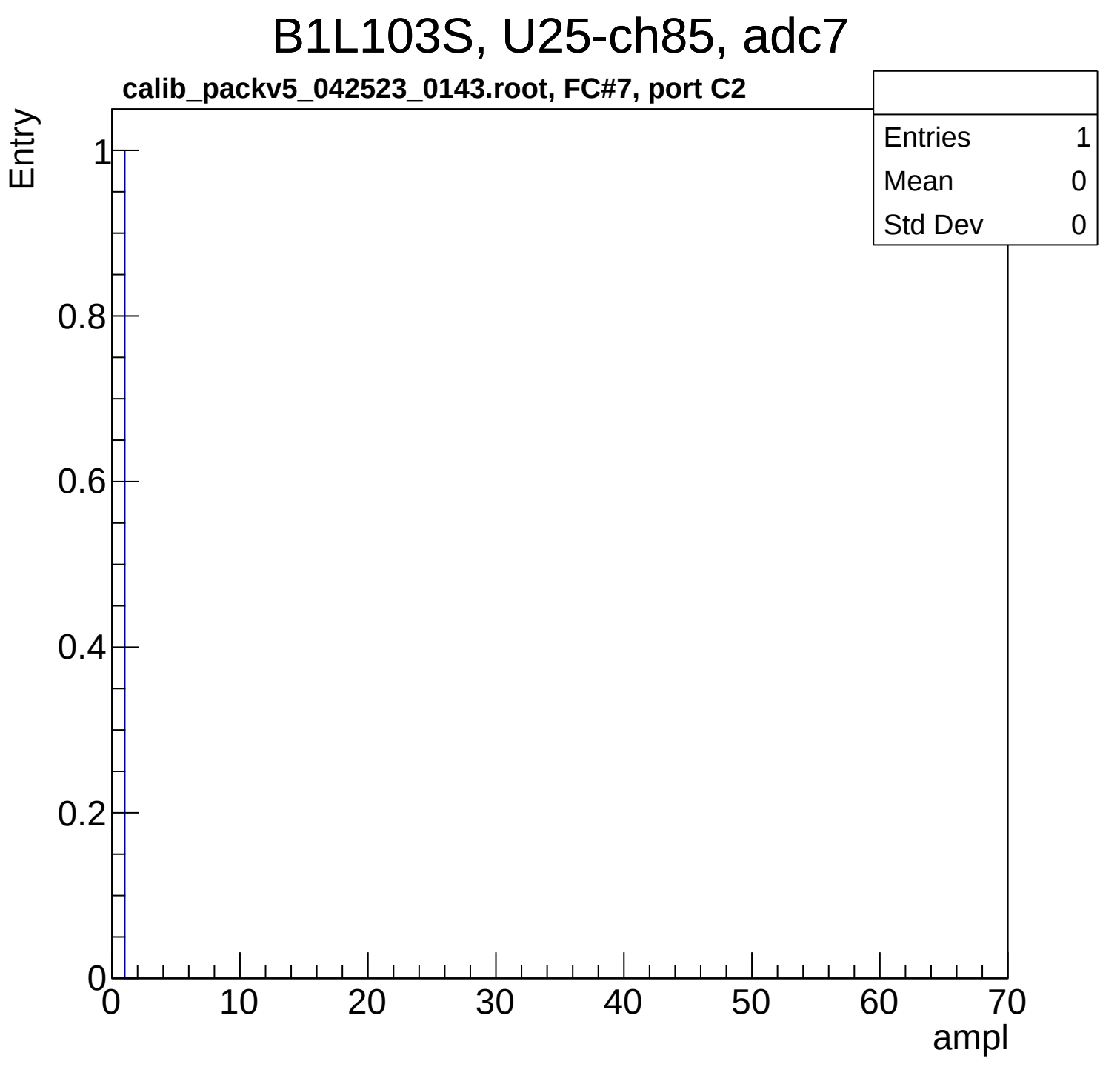
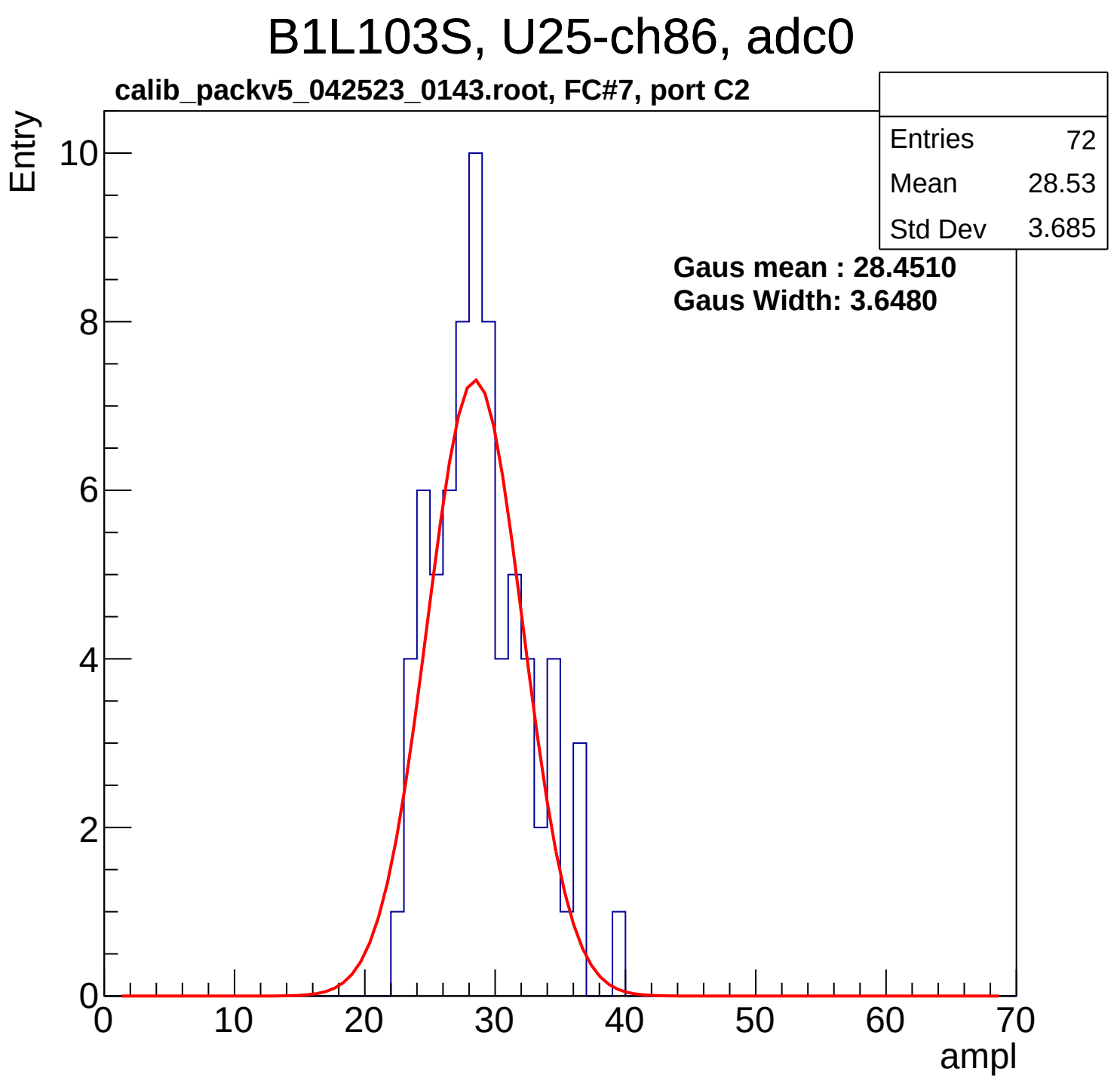
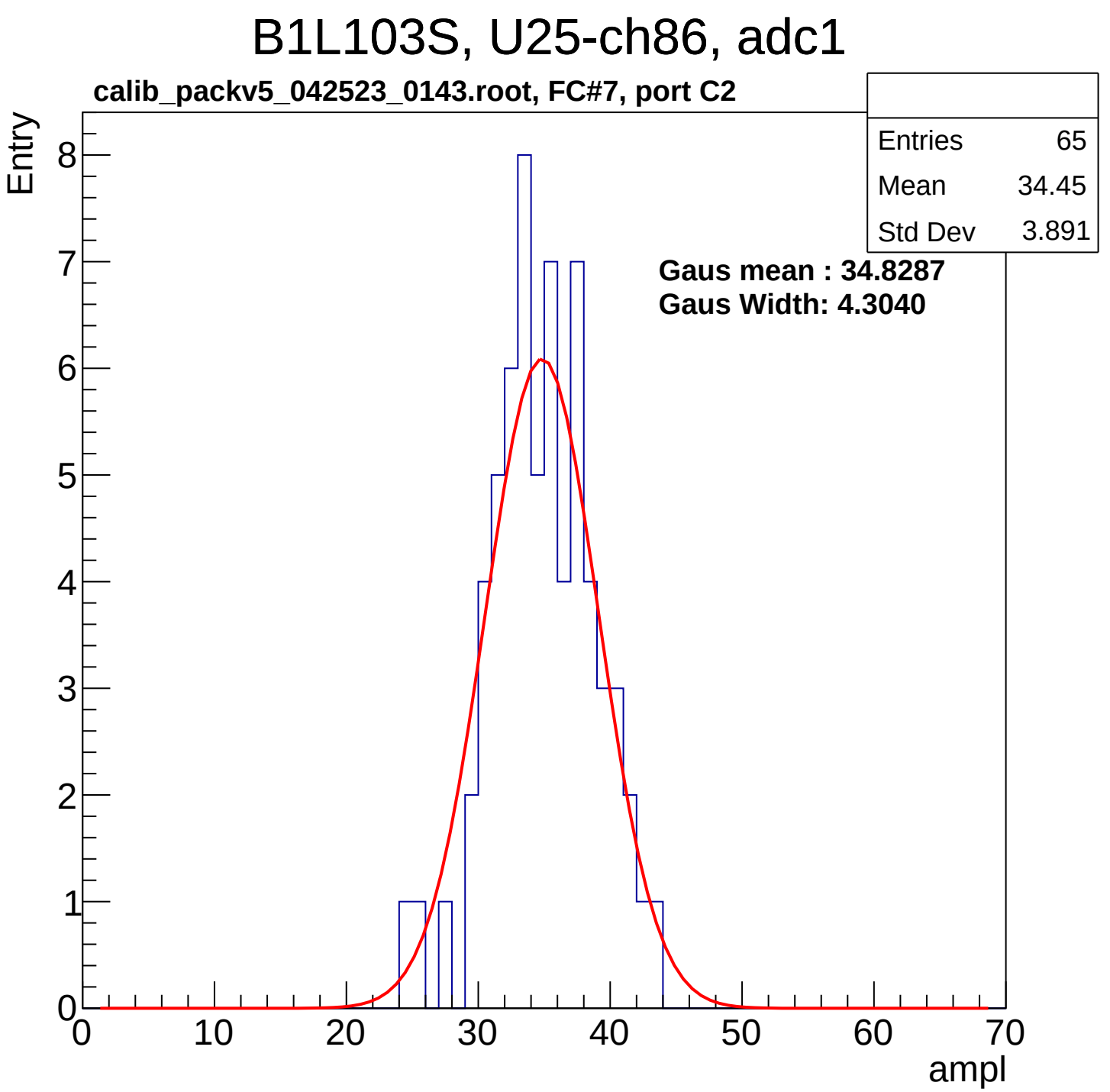
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch85, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch85, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch86, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 28.53
Std Dev 3.685
Gaus mean : 28.4510
Gaus Width: 3.6480
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch86, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 34.45
Std Dev 3.891
Gaus mean : 34.8287
Gaus Width: 4.3040
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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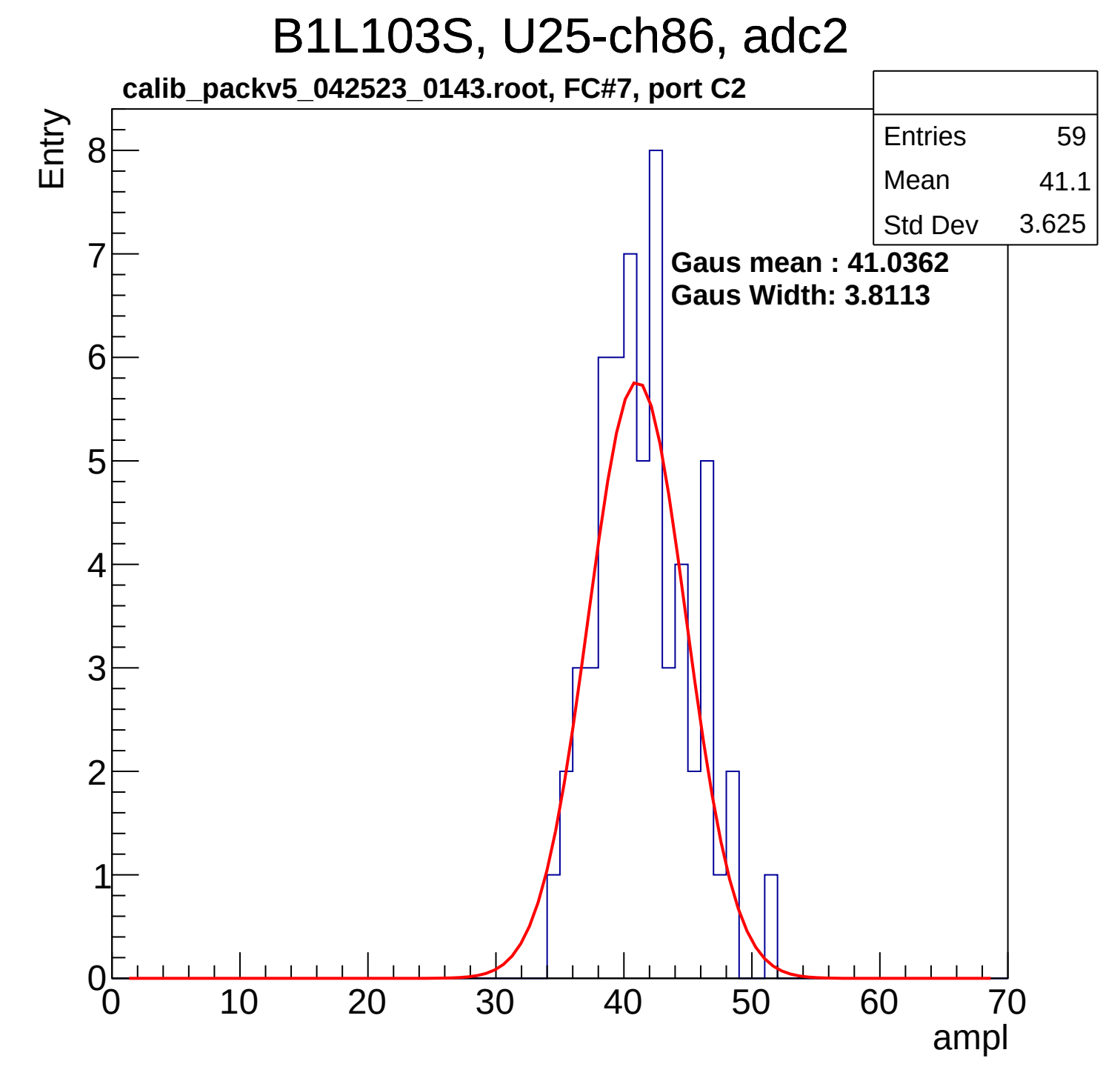
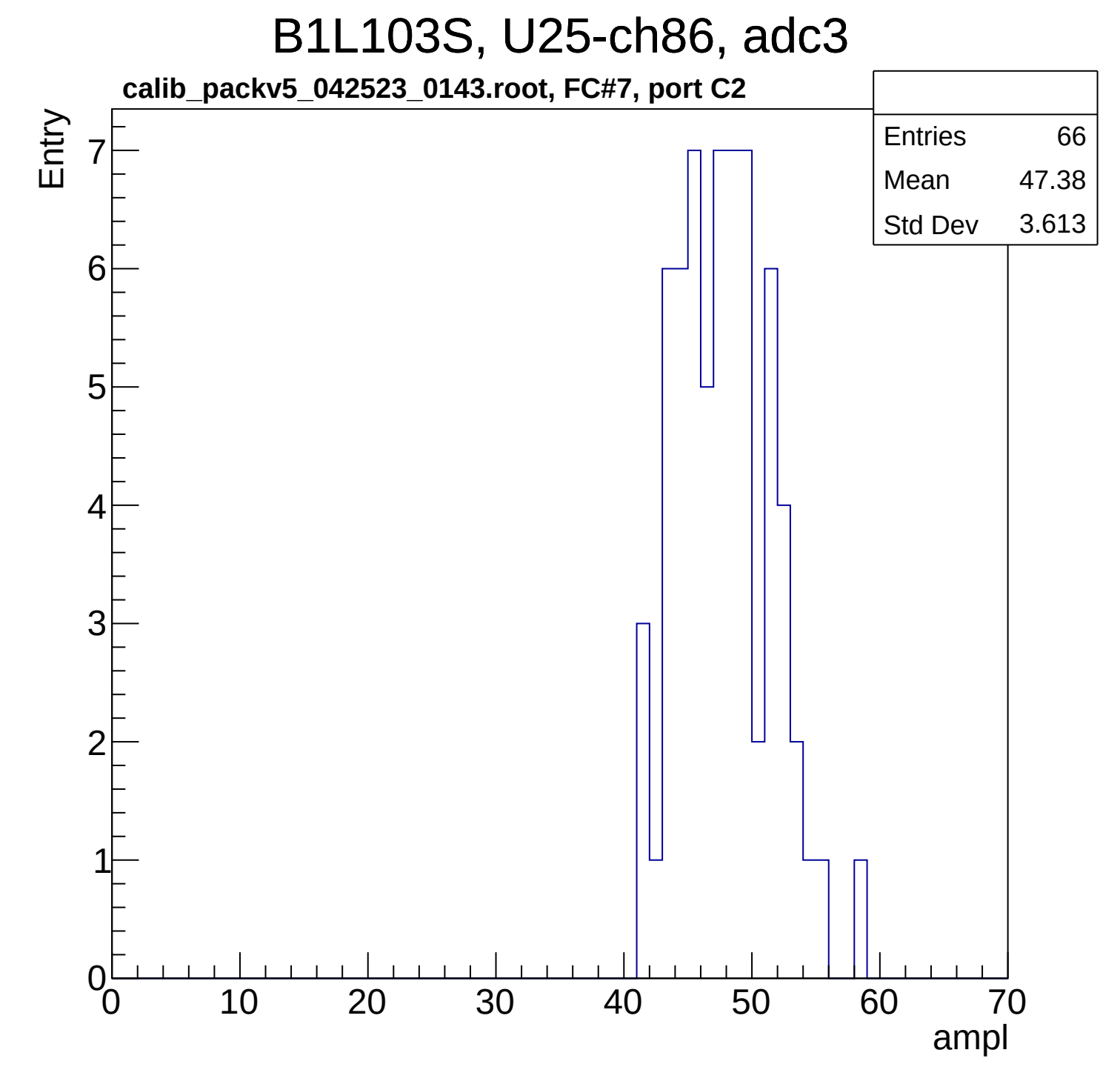
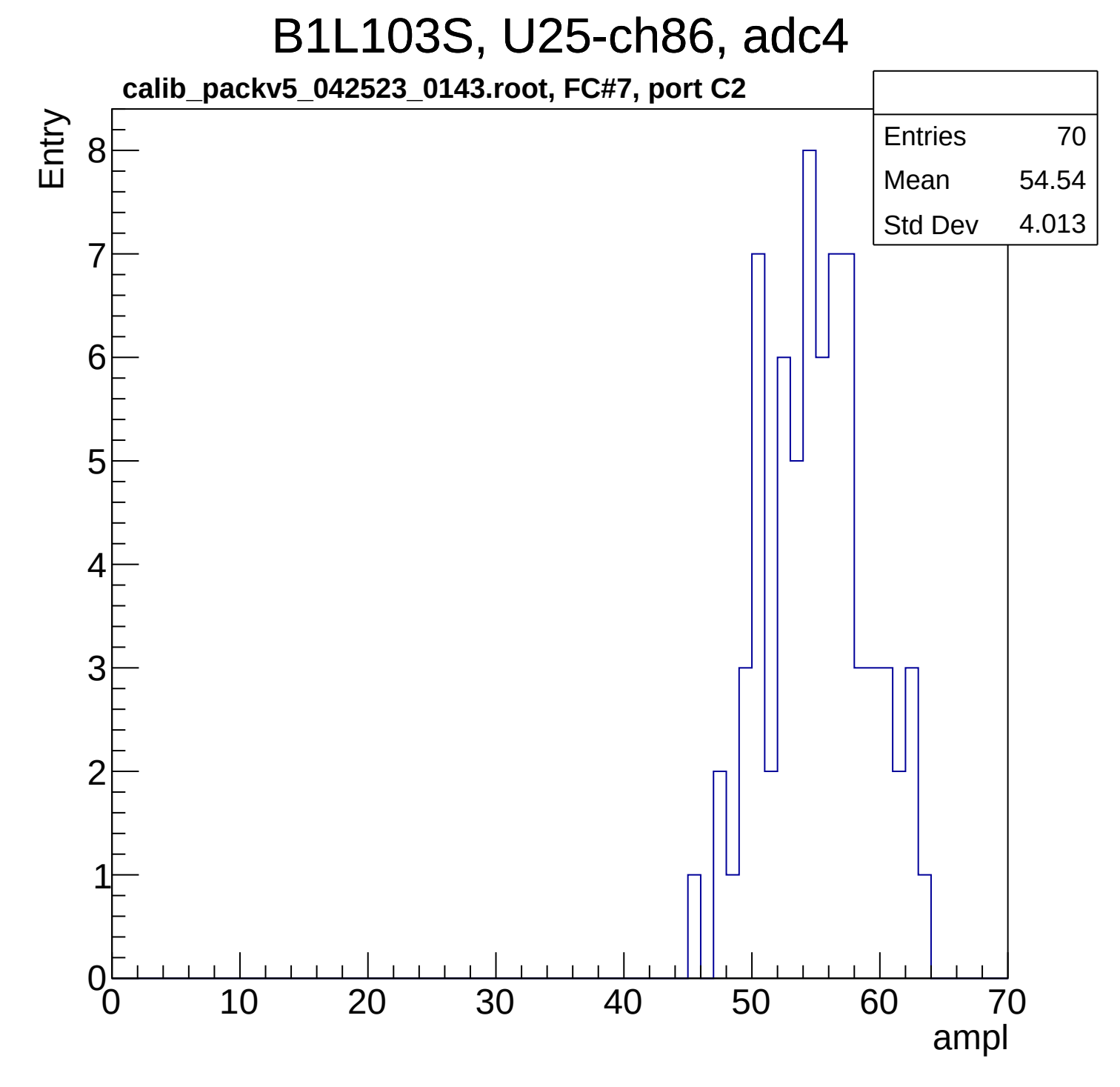

B1L103S, U25-ch86, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 41.1
Std Dev 3.625
Gaus mean : 41.0362
Gaus Width: 3.8113
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch86, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 47.38
Std Dev 3.613
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch86, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 54.54
Std Dev 4.013
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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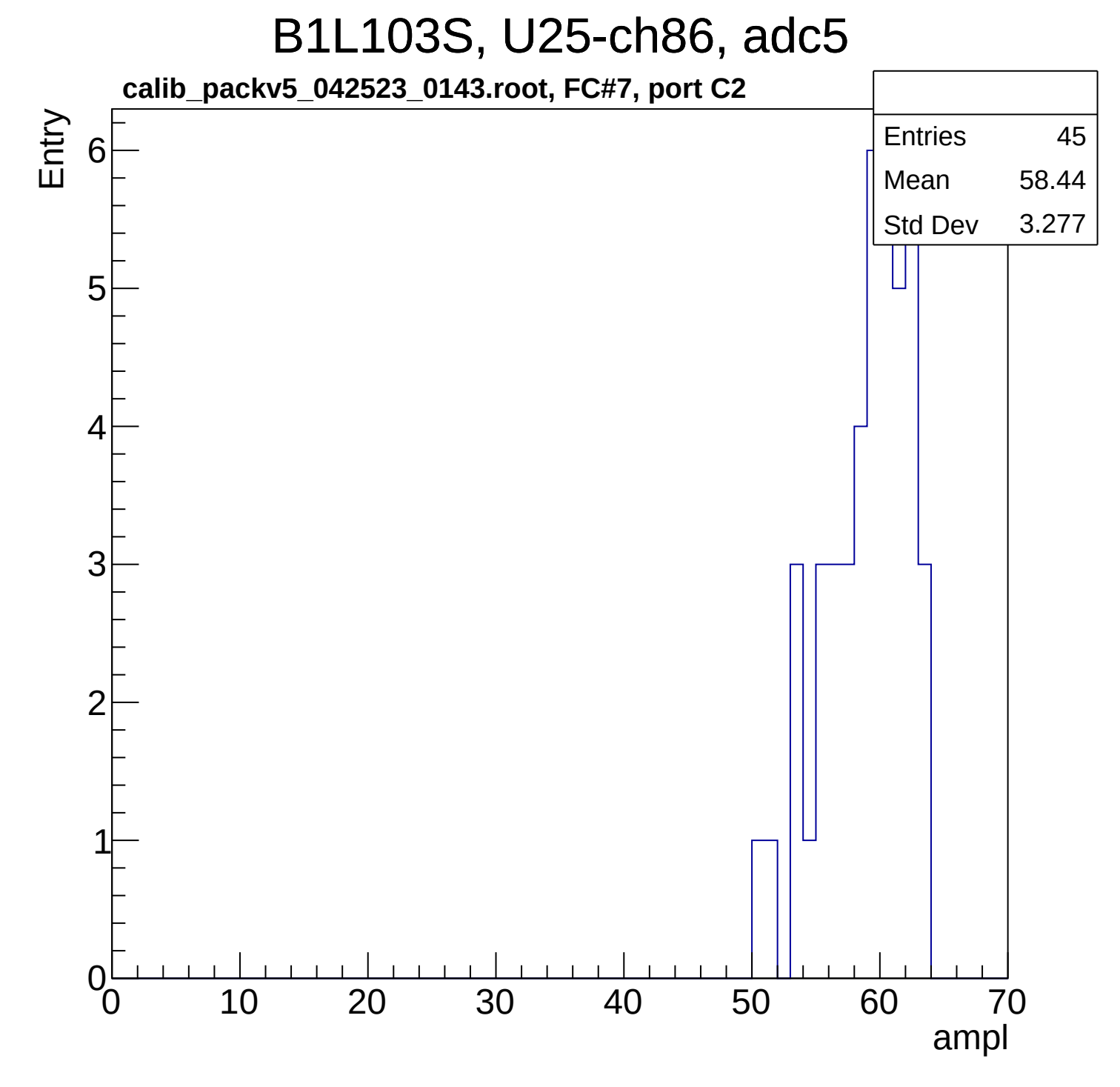
B1L103S, U25-ch86, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 58.44
Std Dev 3.277
Entry
ampl
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3
4
5
6
0
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60
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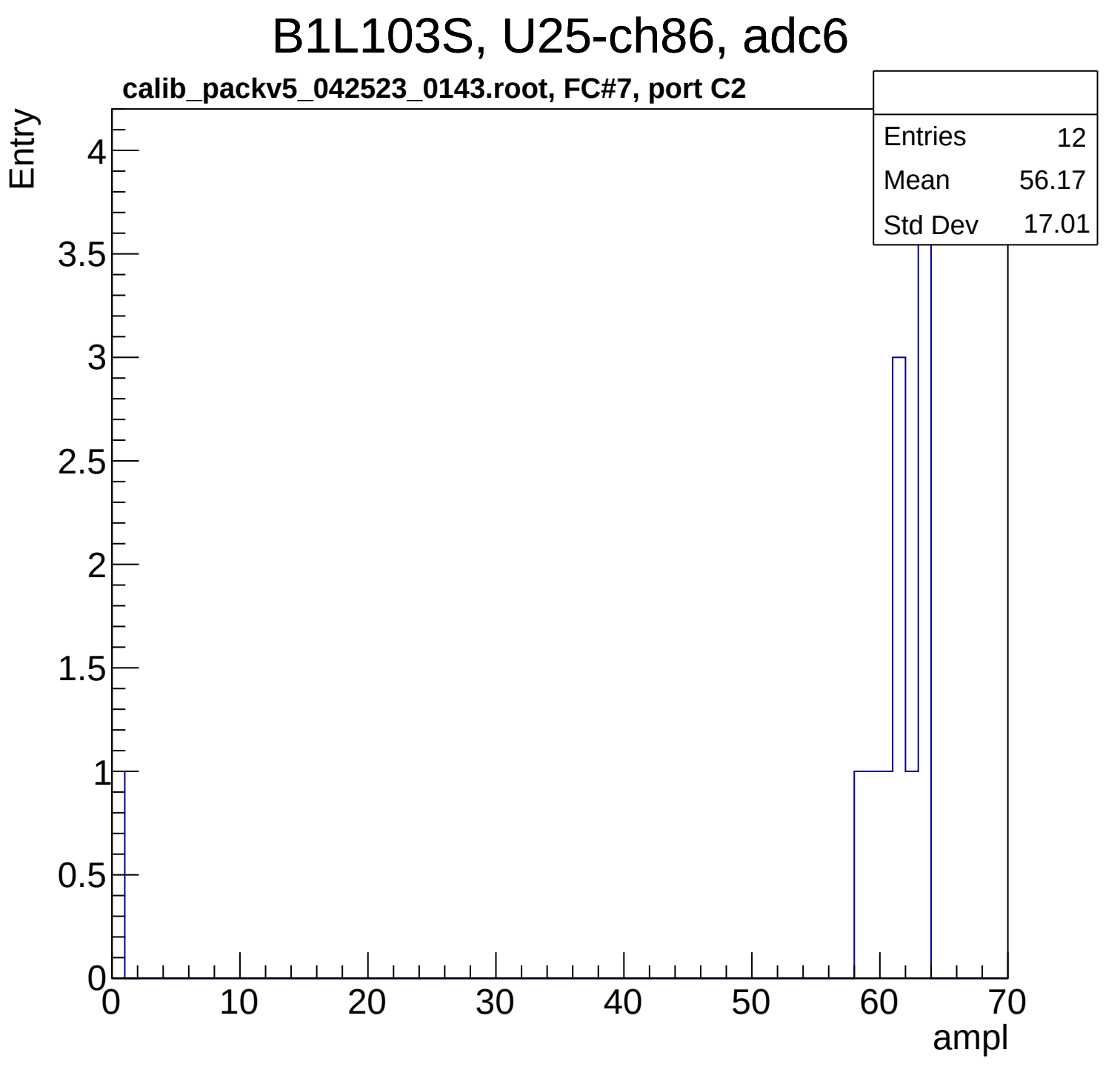
B1L103S, U25-ch86, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 56.17
Std Dev 17.01
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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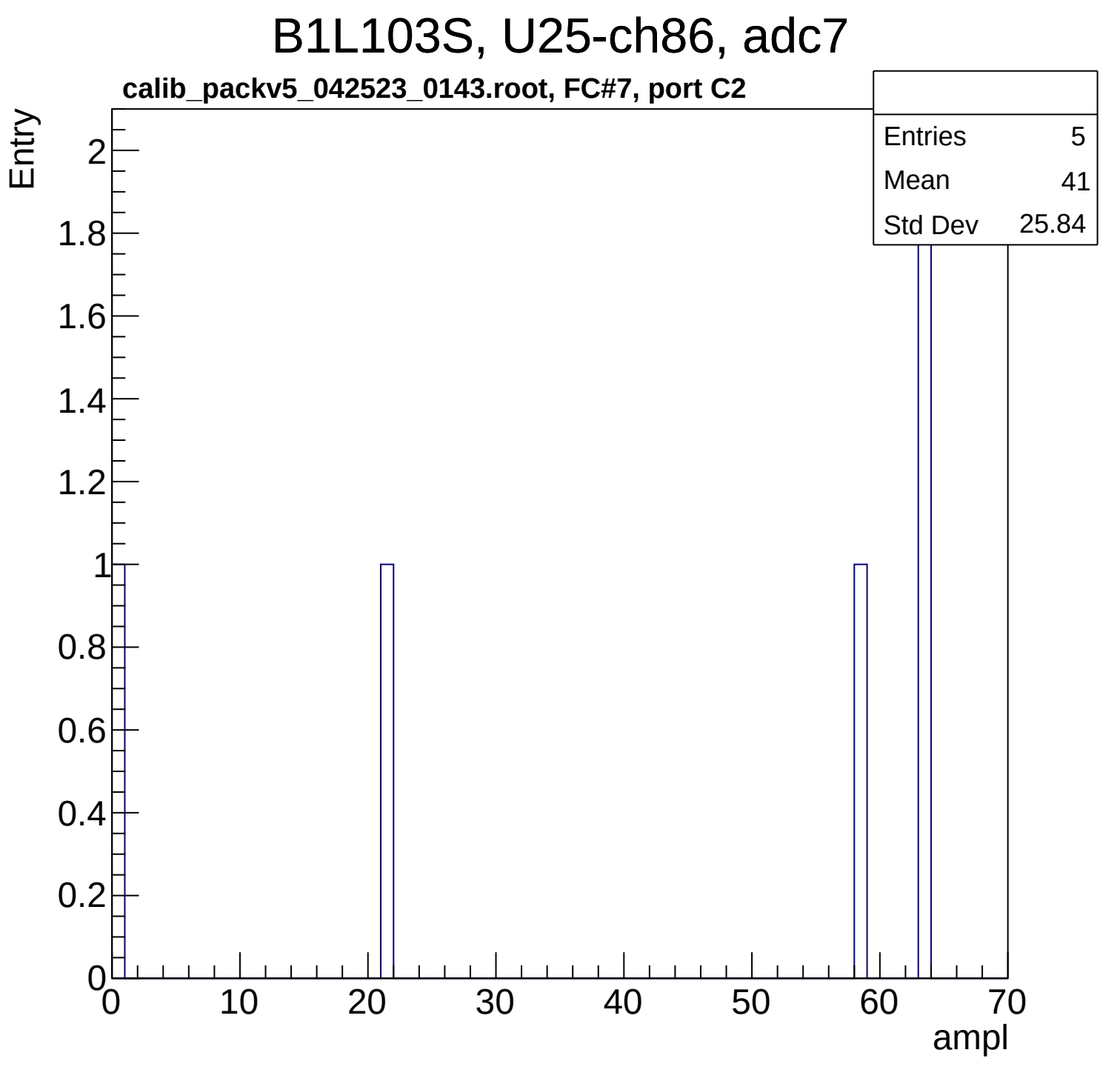
B1L103S, U25-ch86, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 41
Std Dev 25.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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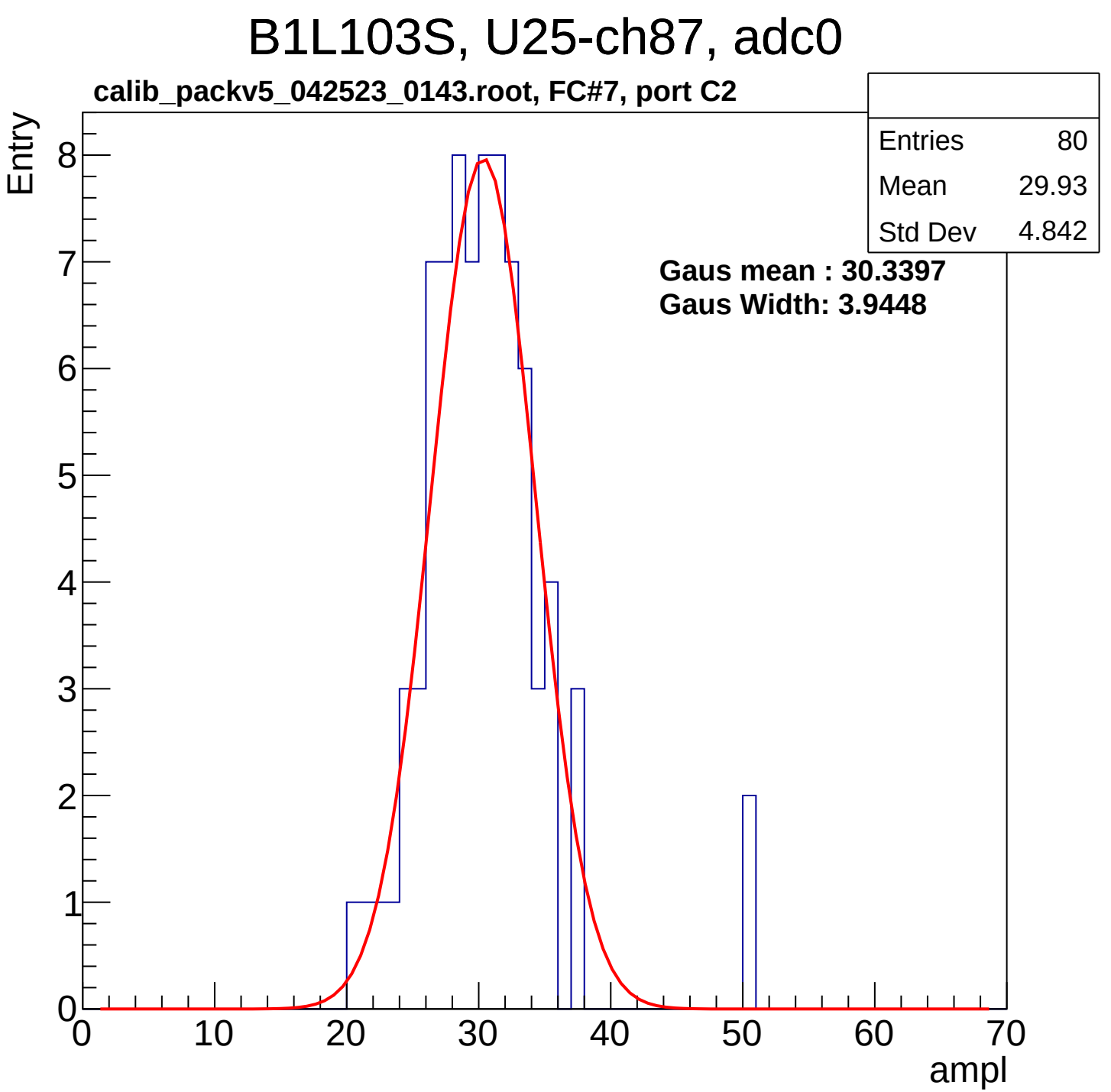
B1L103S, U25-ch87, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 29.93
Std Dev 4.842
Gaus mean : 30.3397
Gaus Width: 3.9448
Entry
ampl
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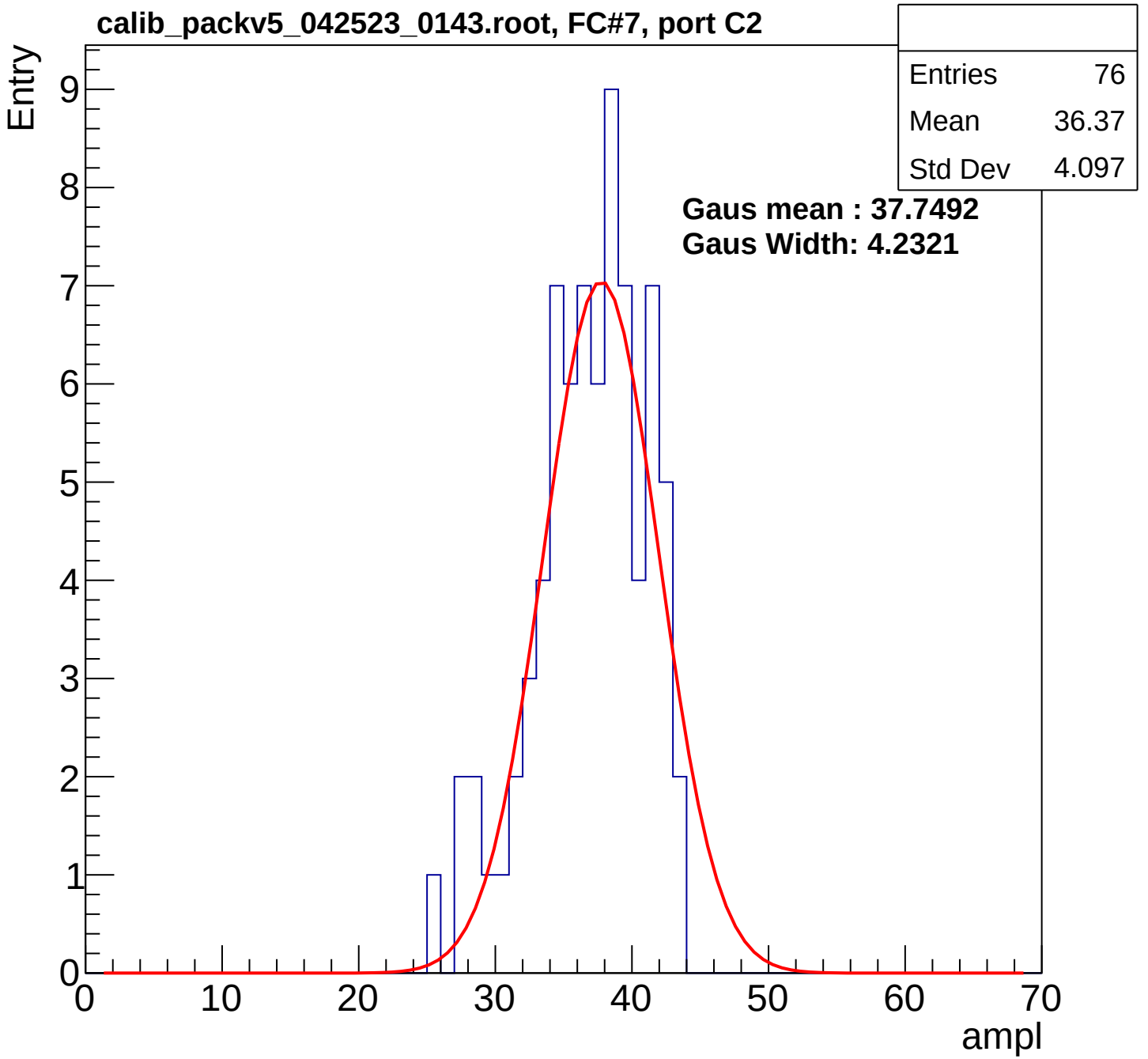
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch87, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36.37
Std Dev 4.097
Gaus mean : 37.7492
Gaus Width: 4.2321
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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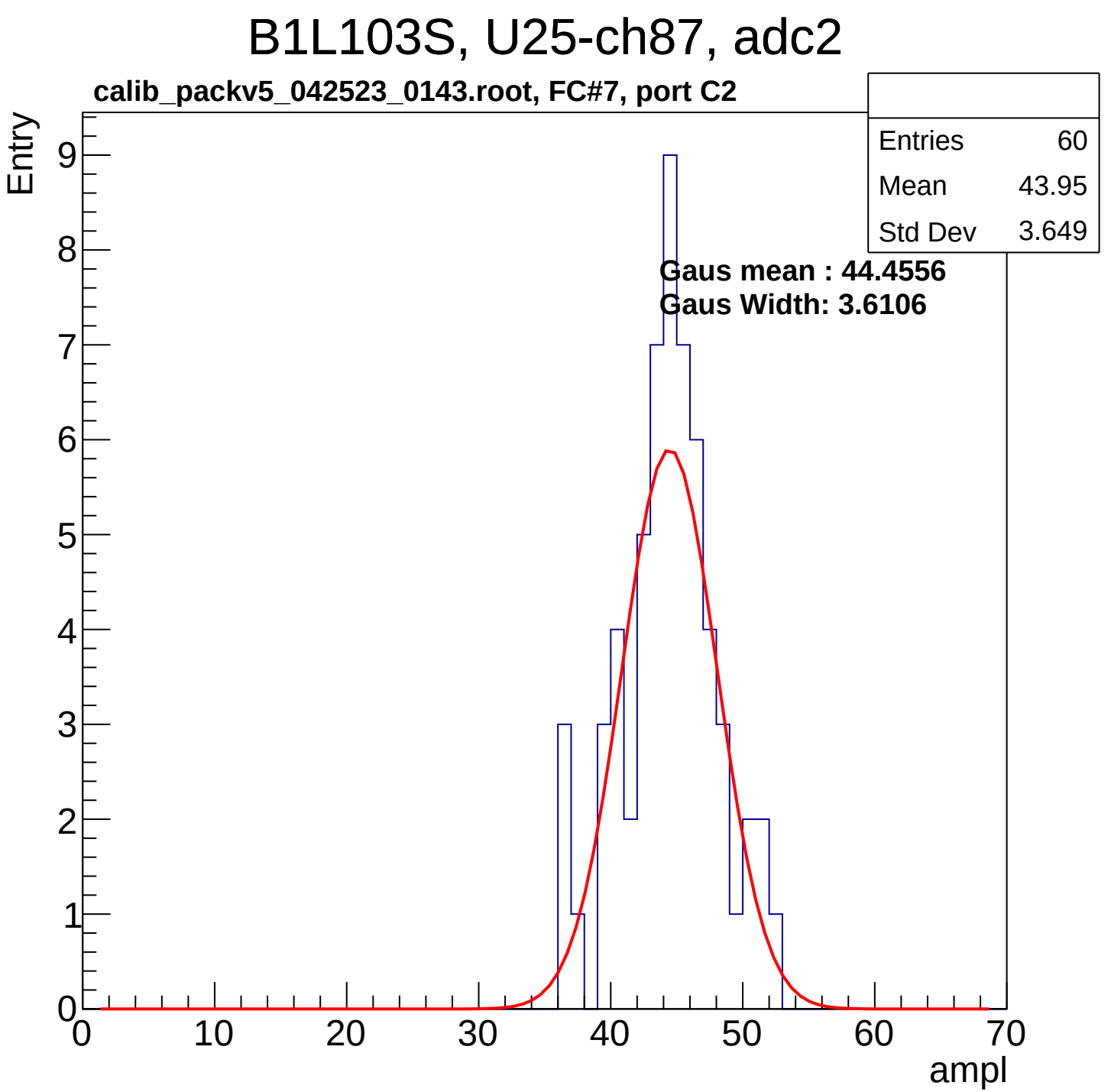
B1L103S, U25-ch87, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.95
Std Dev 3.649
Gaus mean : 44.4556
Gaus Width: 3.6106
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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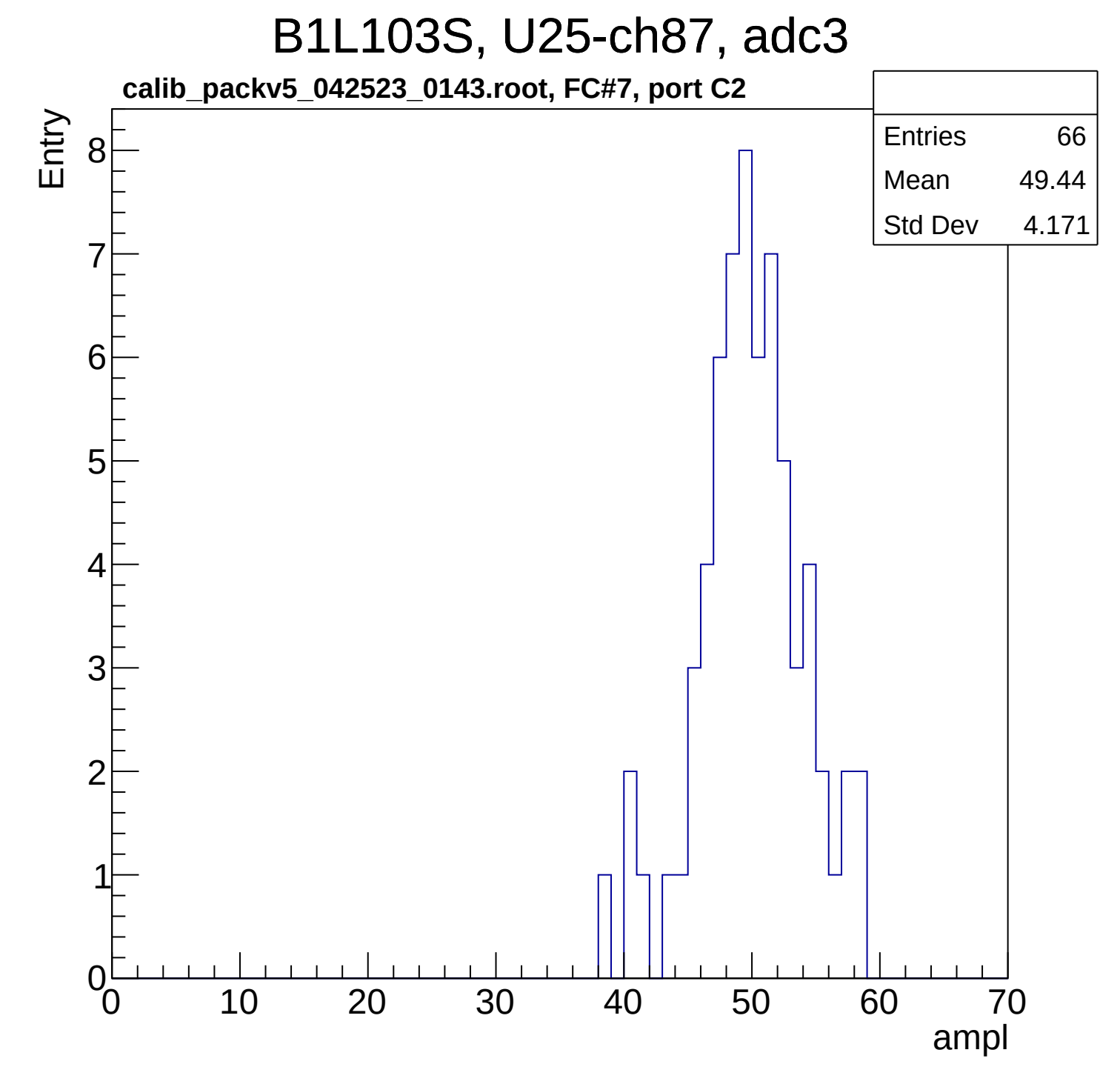
B1L103S, U25-ch87, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 49.44
Std Dev 4.171
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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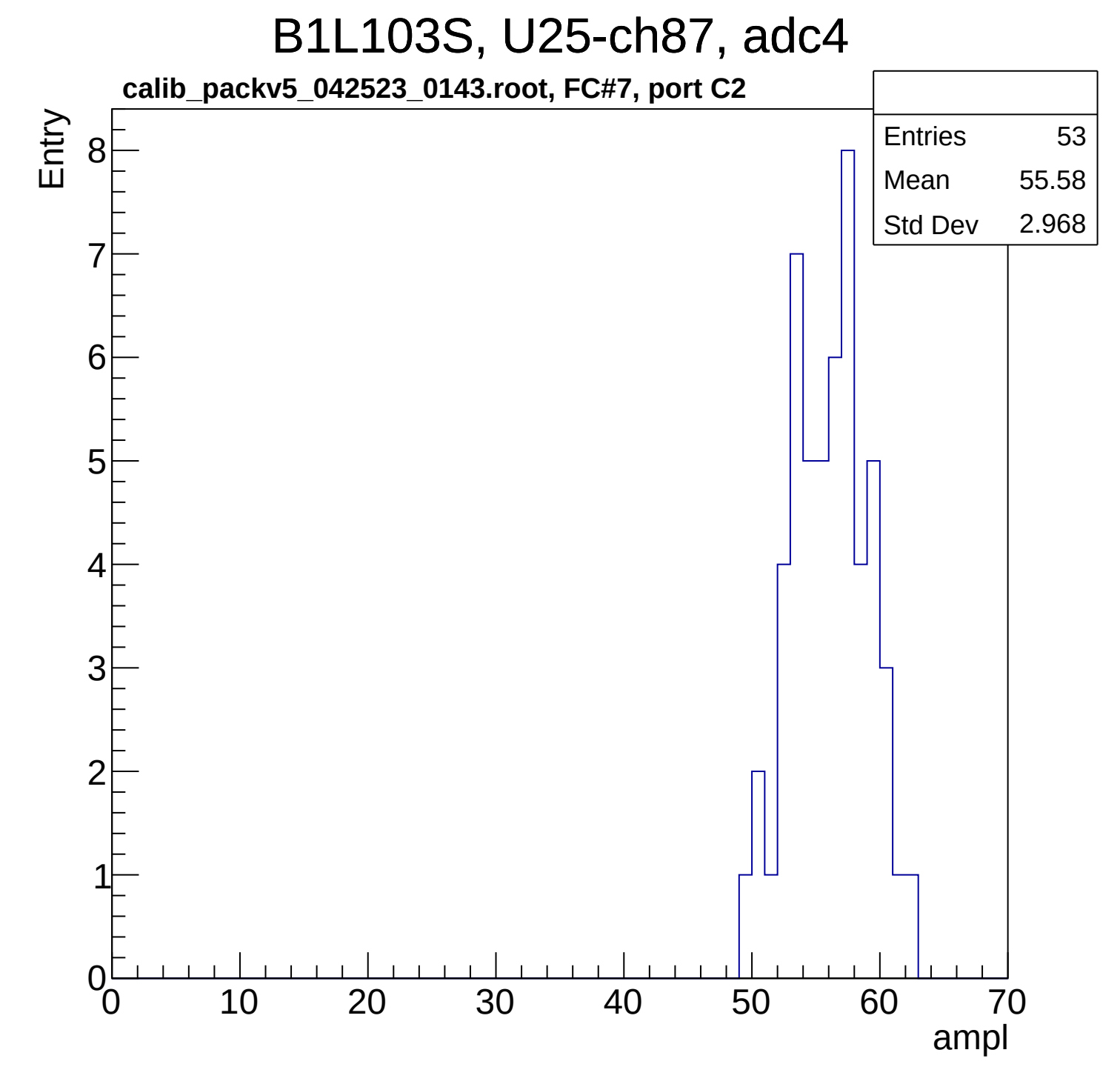
B1L103S, U25-ch87, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 55.58
Std Dev 2.968
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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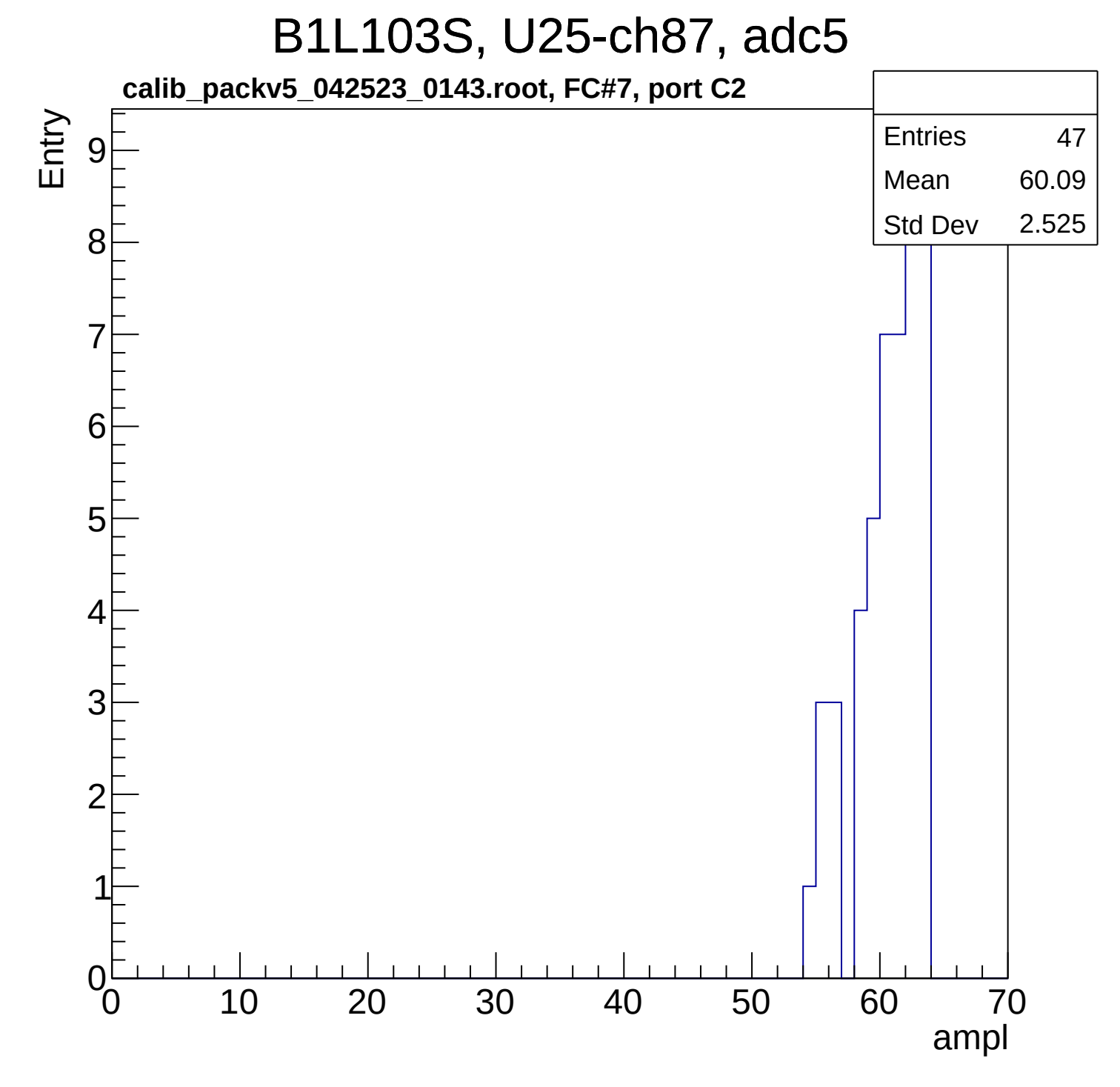
B1L103S, U25-ch87, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 60.09
Std Dev 2.525
Entry
ampl
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4
5
6
7
8
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0
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20
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60
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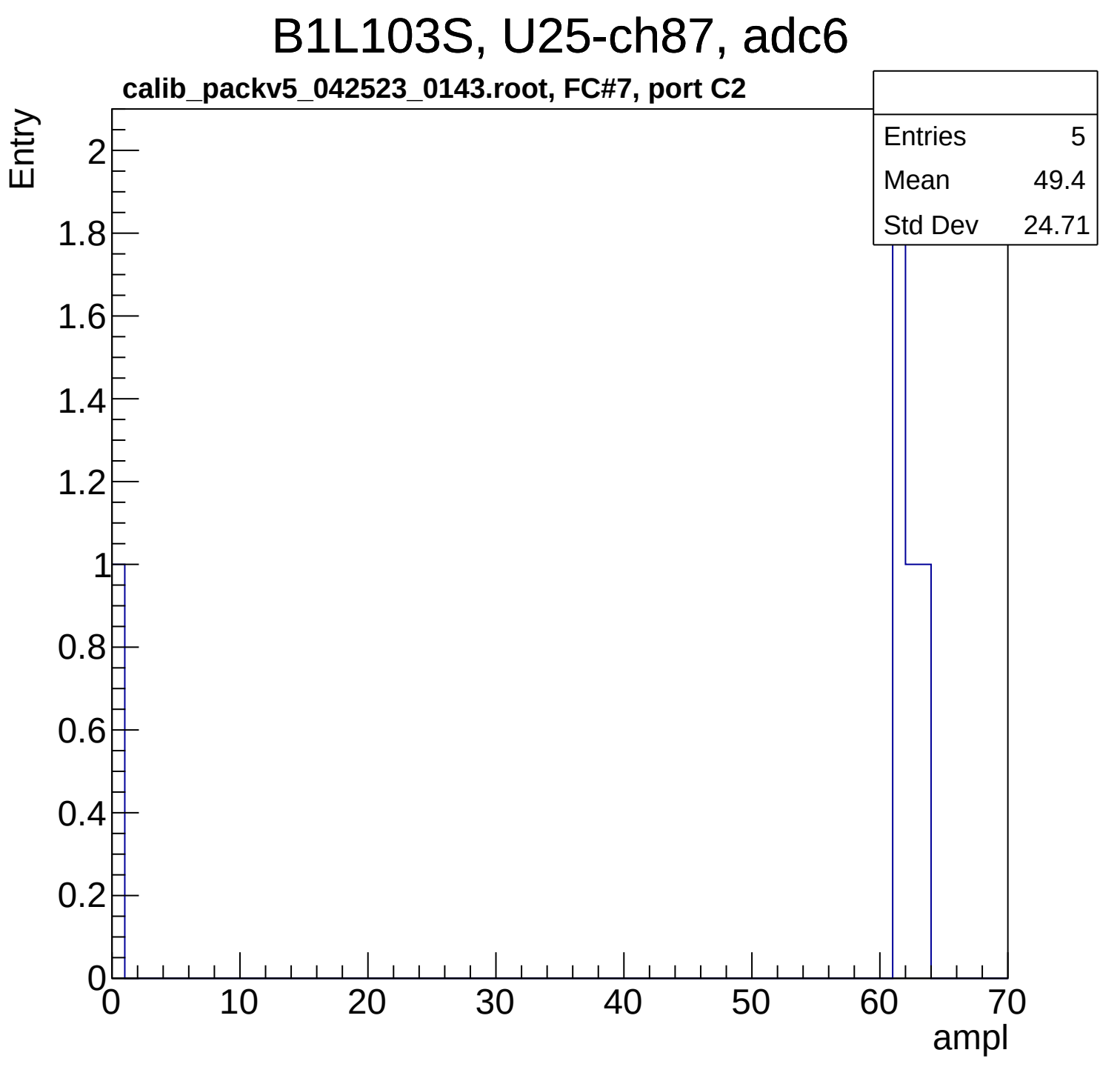
B1L103S, U25-ch87, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.4
Std Dev 24.71
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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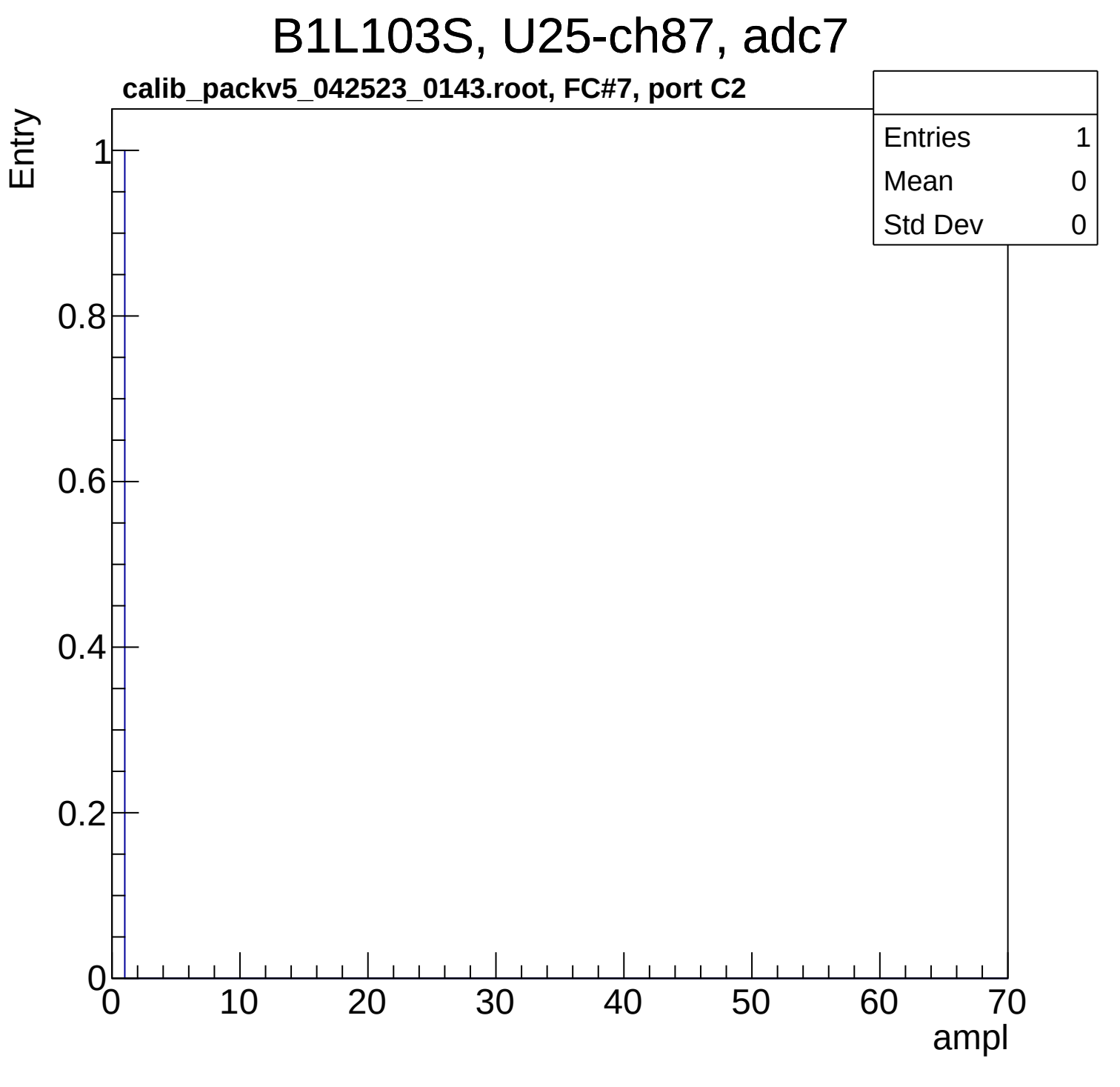
30
40
50
60
70


B1L103S, U25-ch87, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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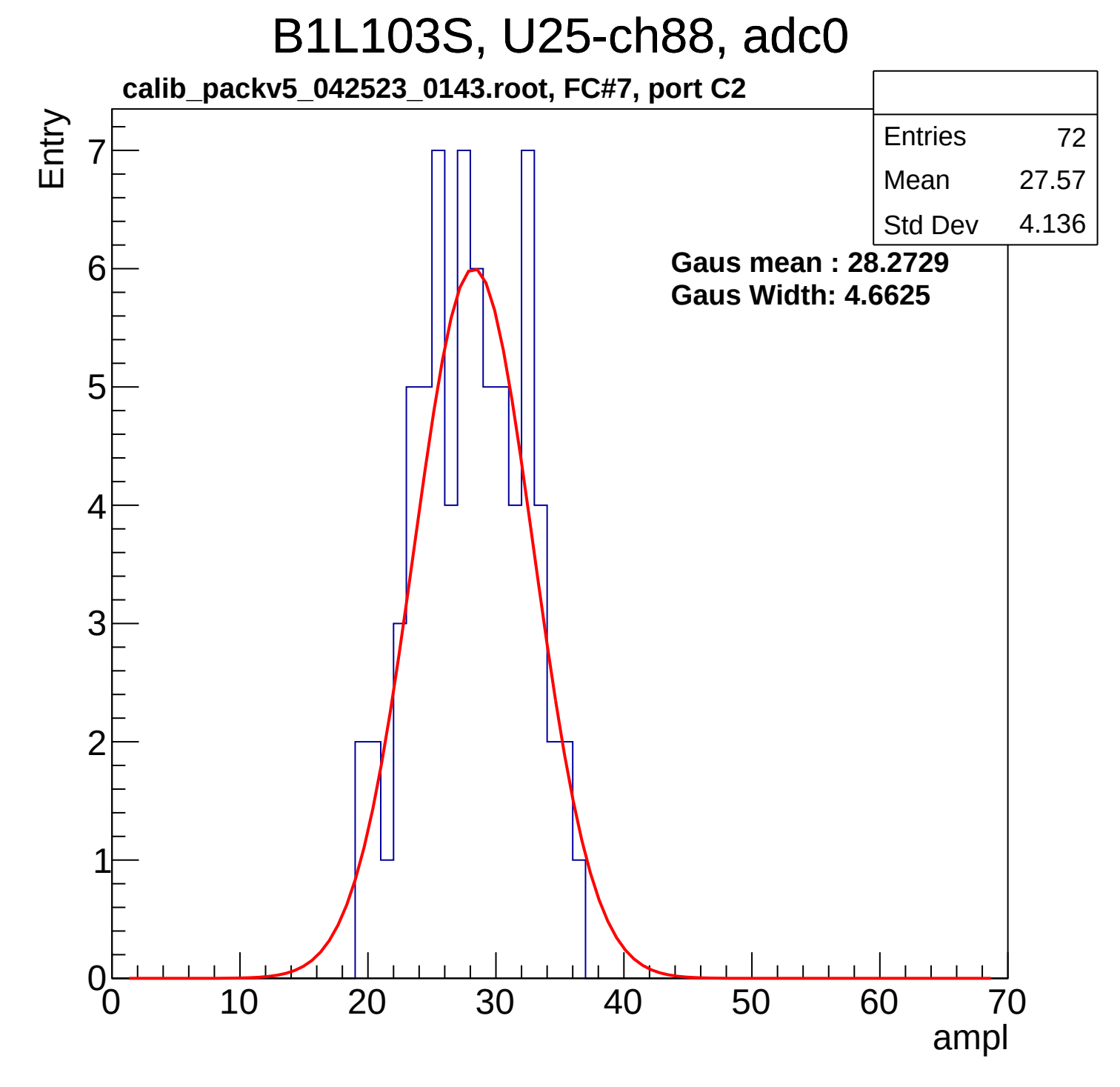
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch88, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 27.57
Std Dev 4.136
Gaus mean : 28.2729
Gaus Width: 4.6625
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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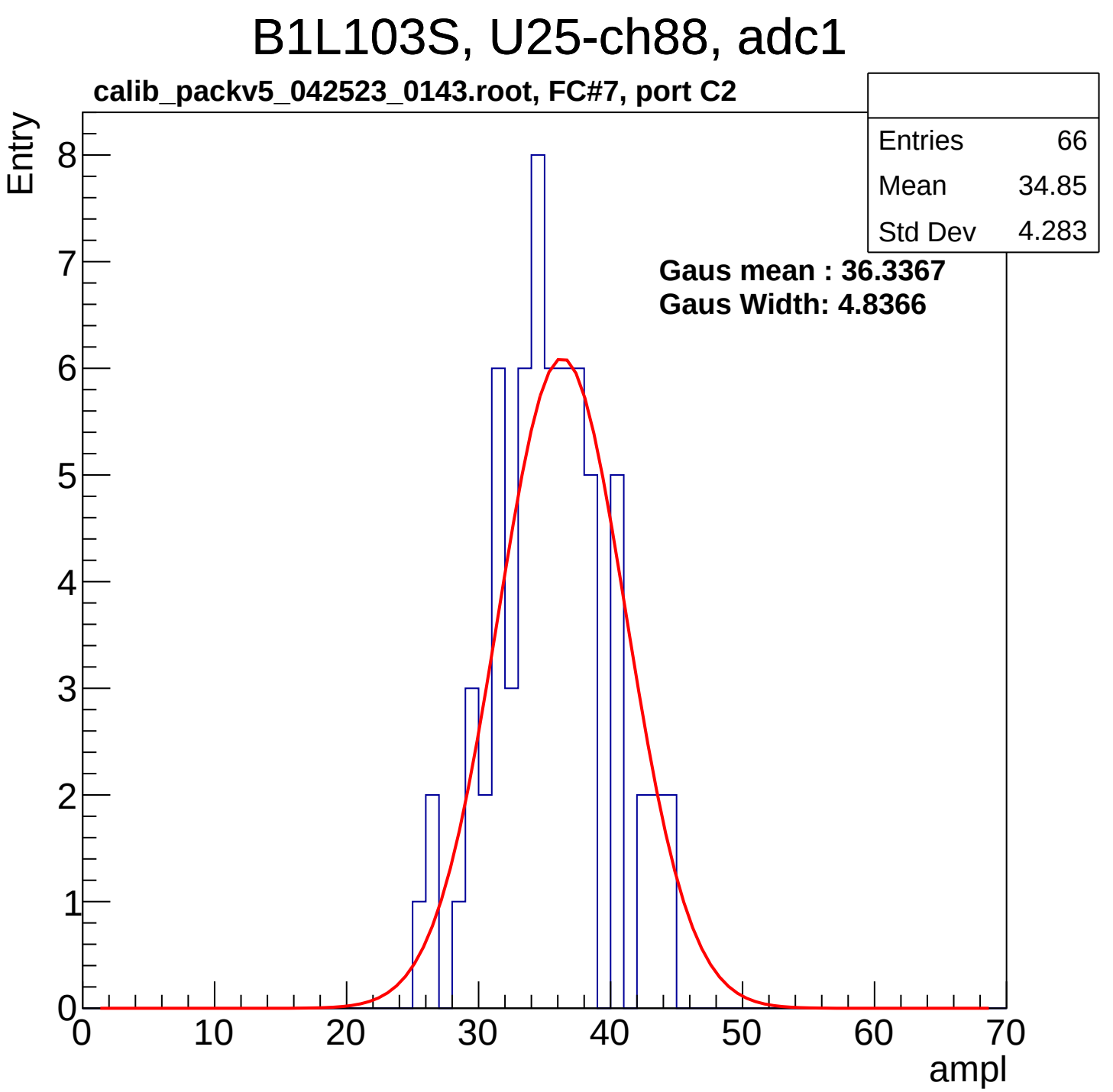
B1L103S, U25-ch88, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 34.85
Std Dev 4.283
Gaus mean : 36.3367
Gaus Width: 4.8366
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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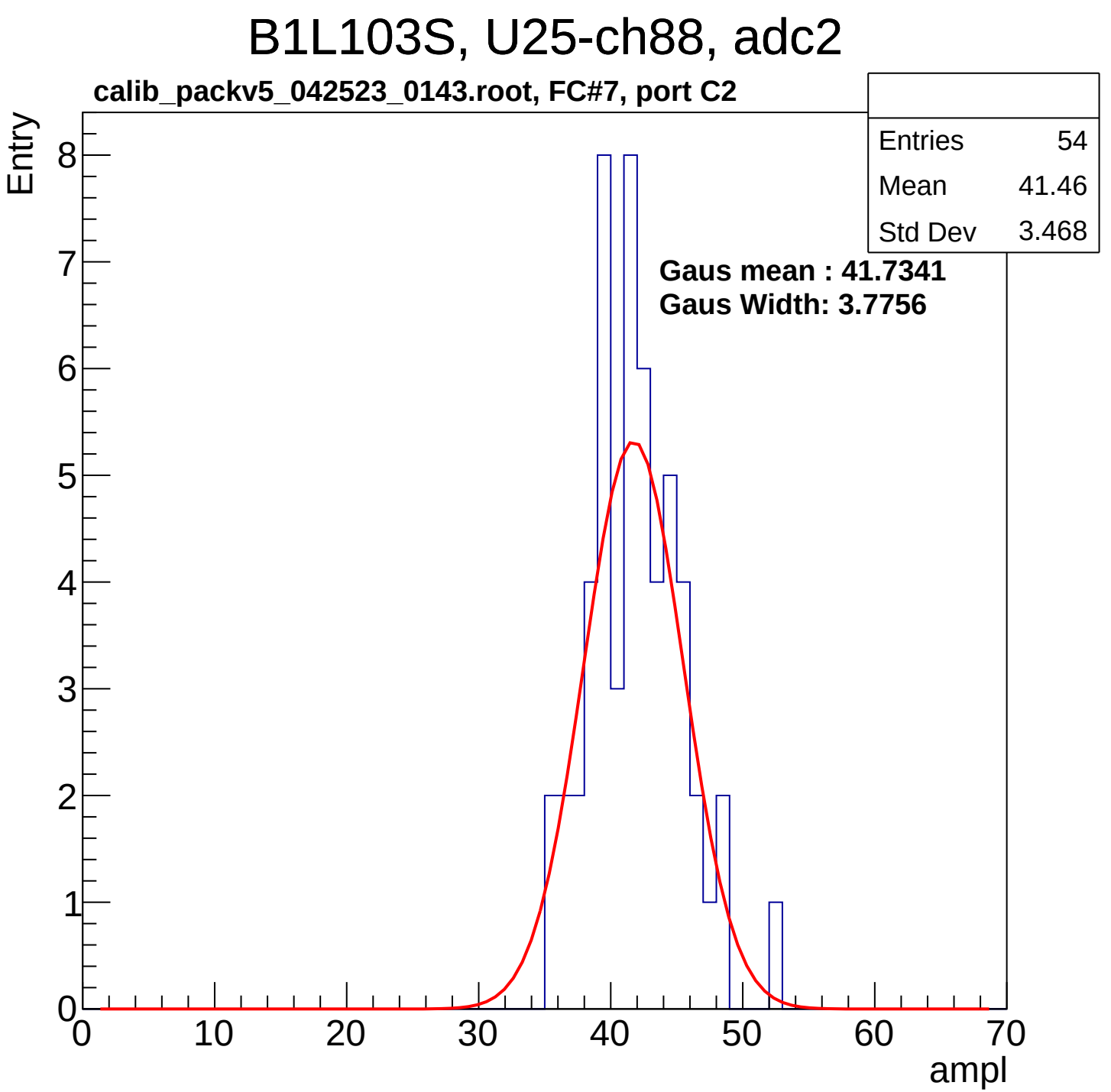
40
50
60
70


B1L103S, U25-ch88, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 41.46
Std Dev 3.468
Gaus mean : 41.7341
Gaus Width: 3.7756
Entry
ampl
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1
2
3
4
5
6
7
8
0
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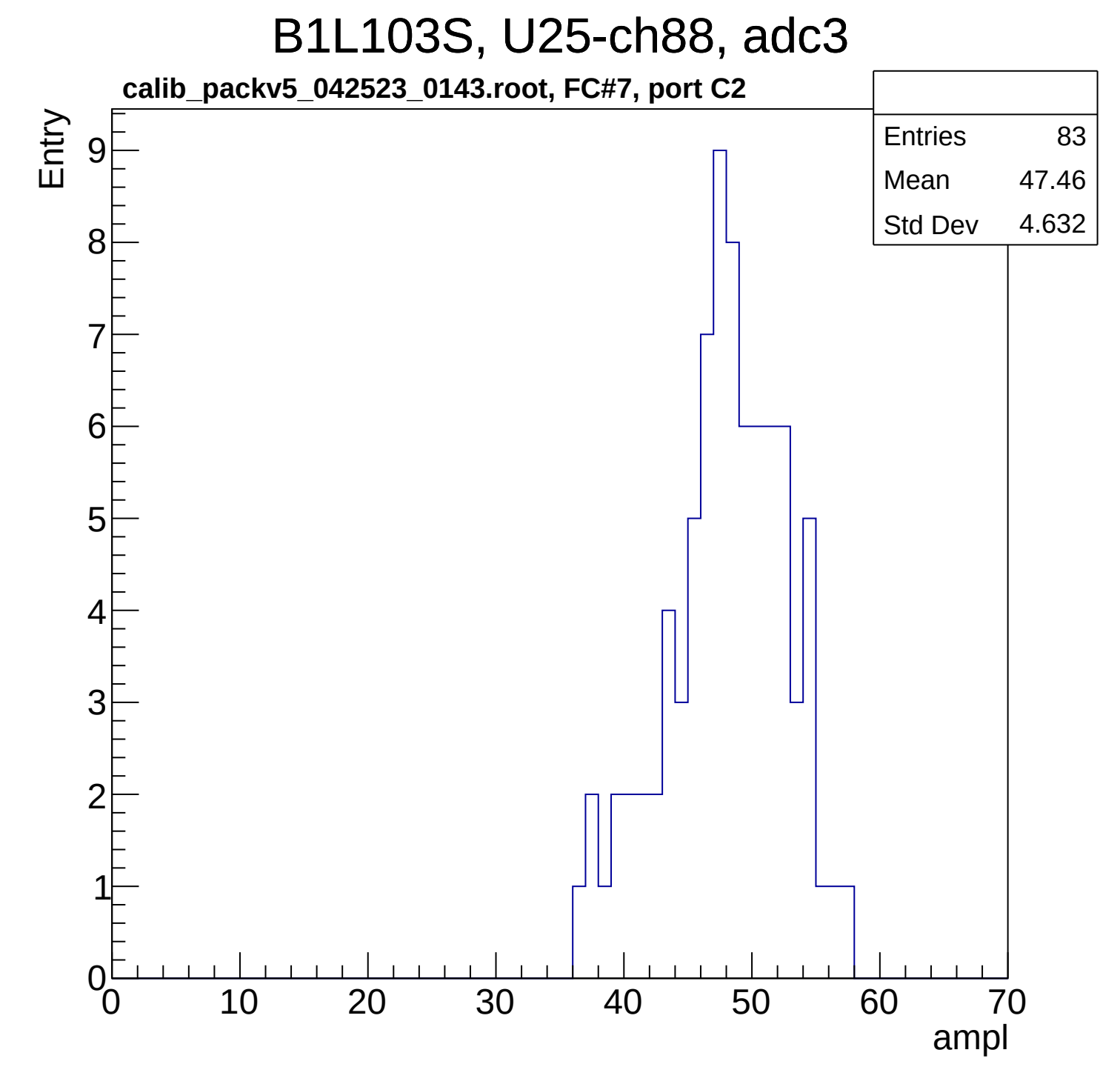
B1L103S, U25-ch88, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 47.46
Std Dev 4.632
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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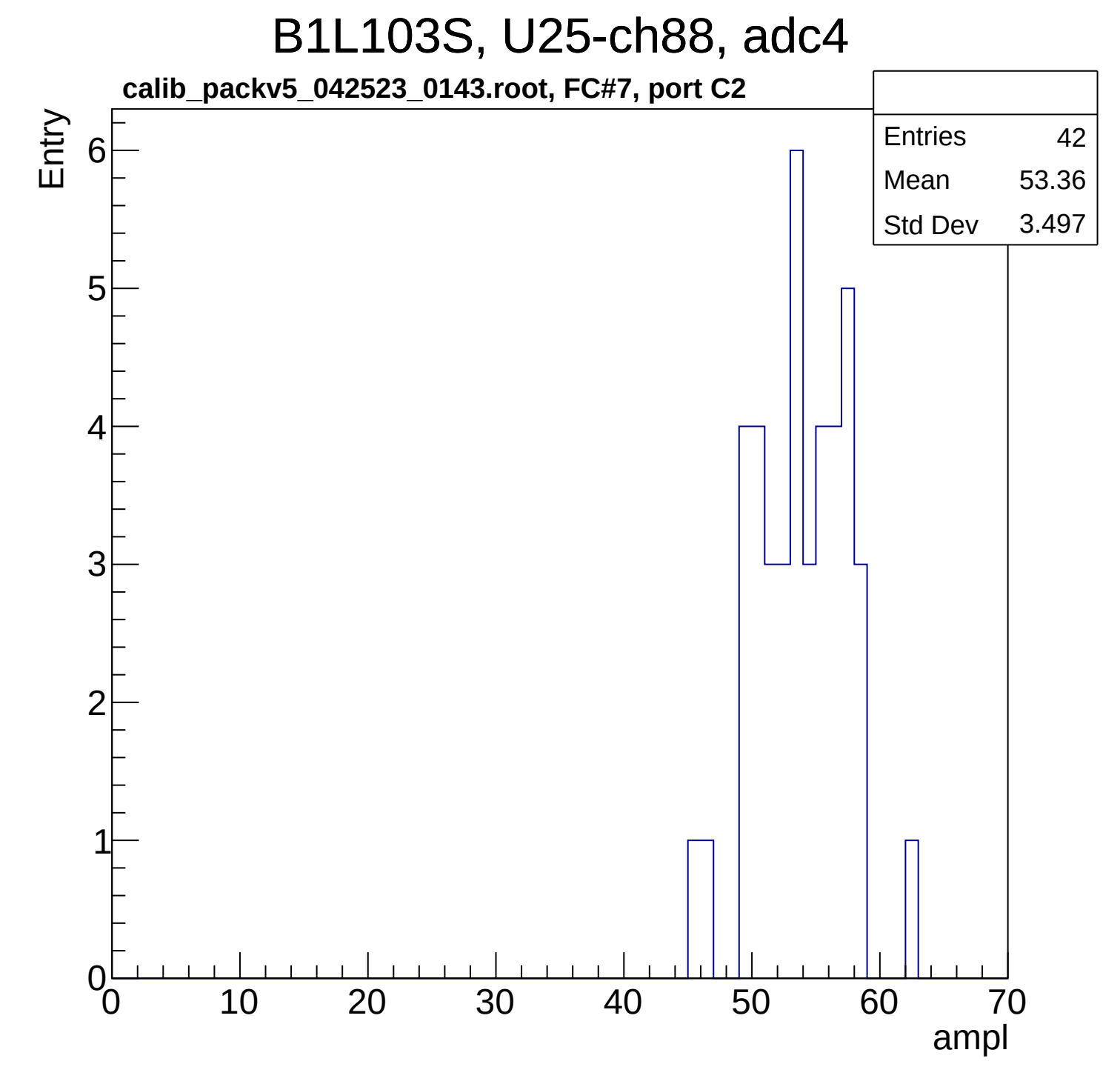
B1L103S, U25-ch88, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 53.36
Std Dev 3.497
Entry
ampl
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3
4
5
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0
10
20
30
40
50
60
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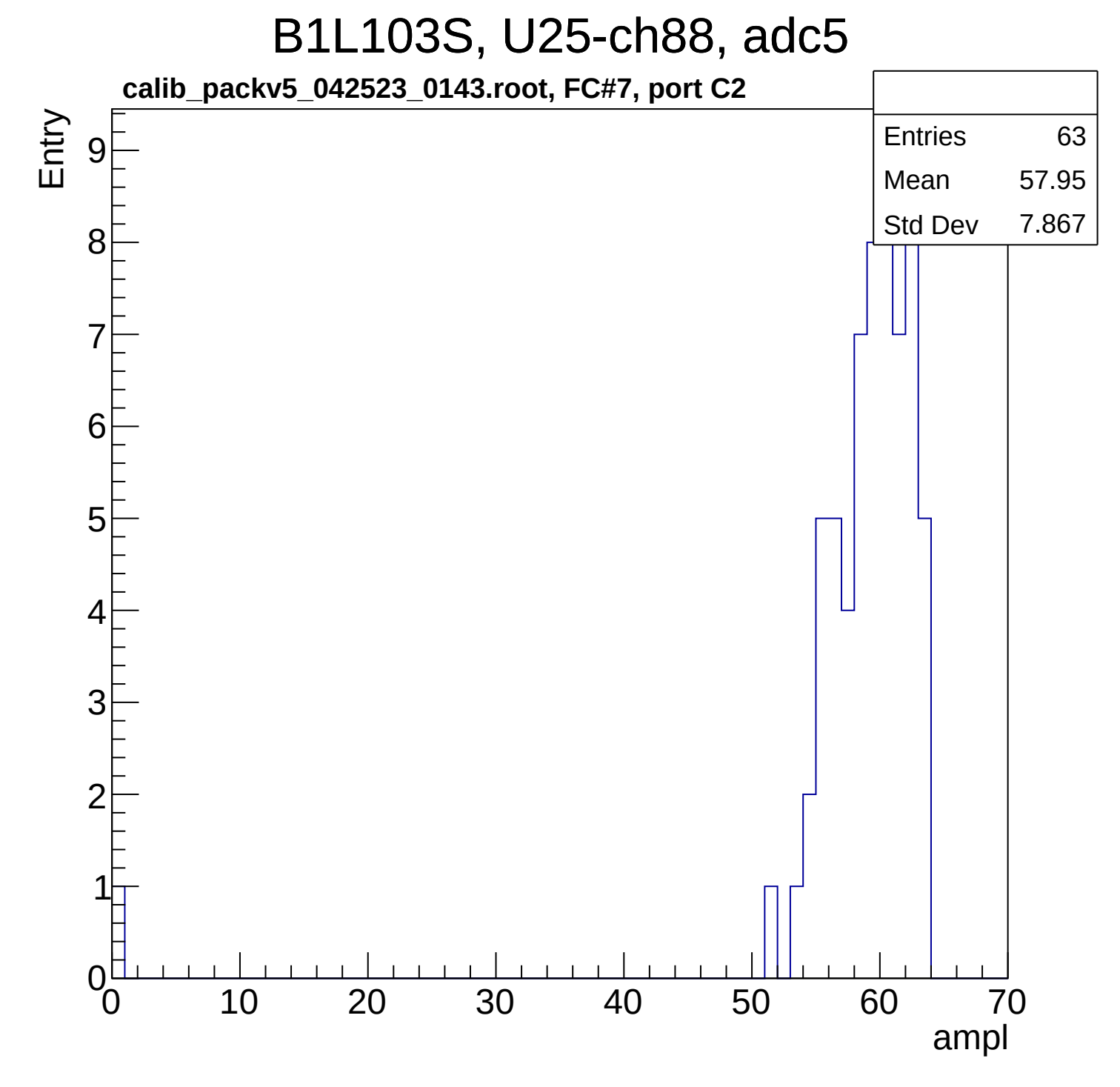
B1L103S, U25-ch88, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 57.95
Std Dev 7.867
Entry
ampl
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2
3
4
5
6
7
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9
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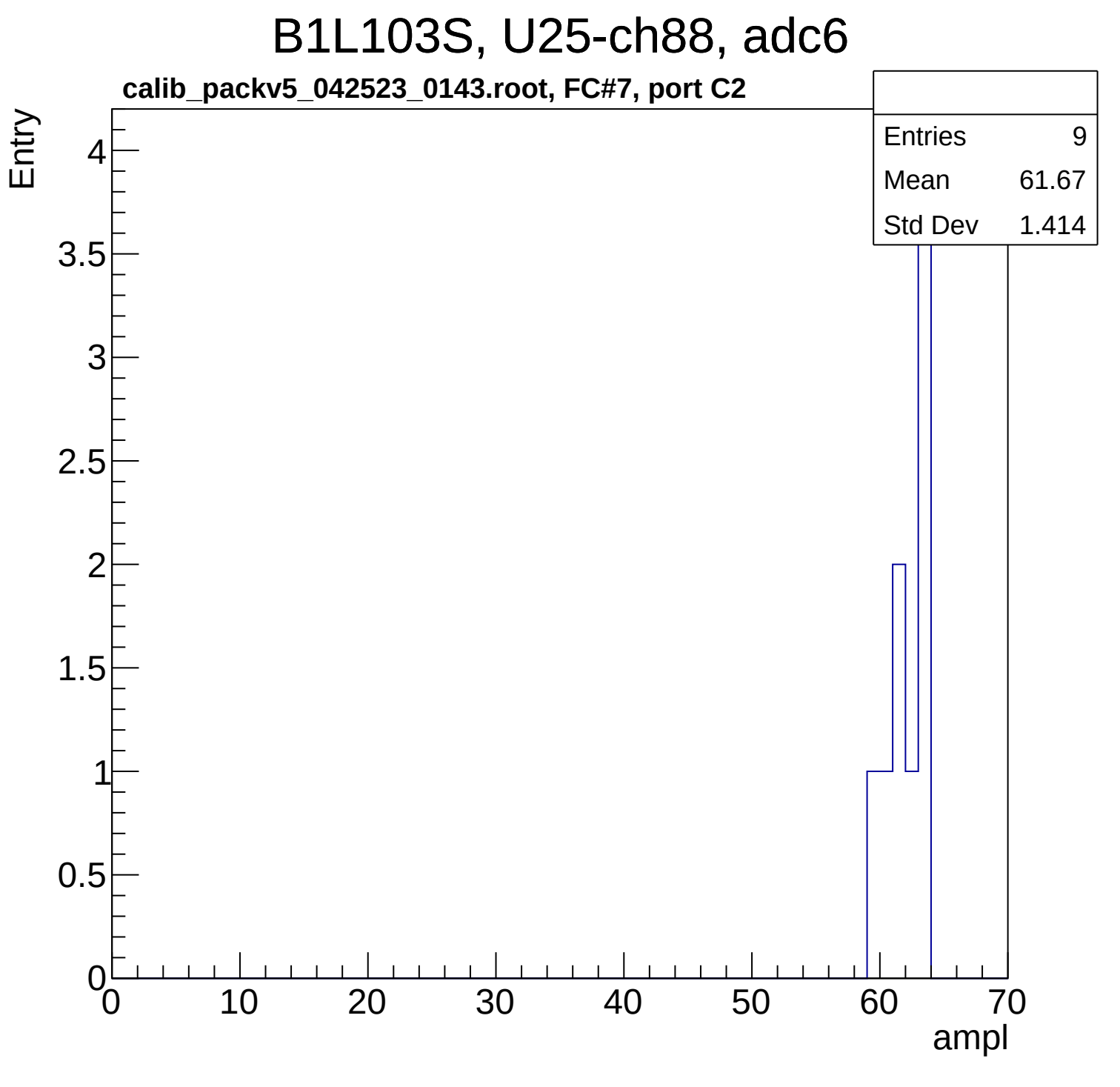
B1L103S, U25-ch88, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 61.67
Std Dev 1.414
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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60
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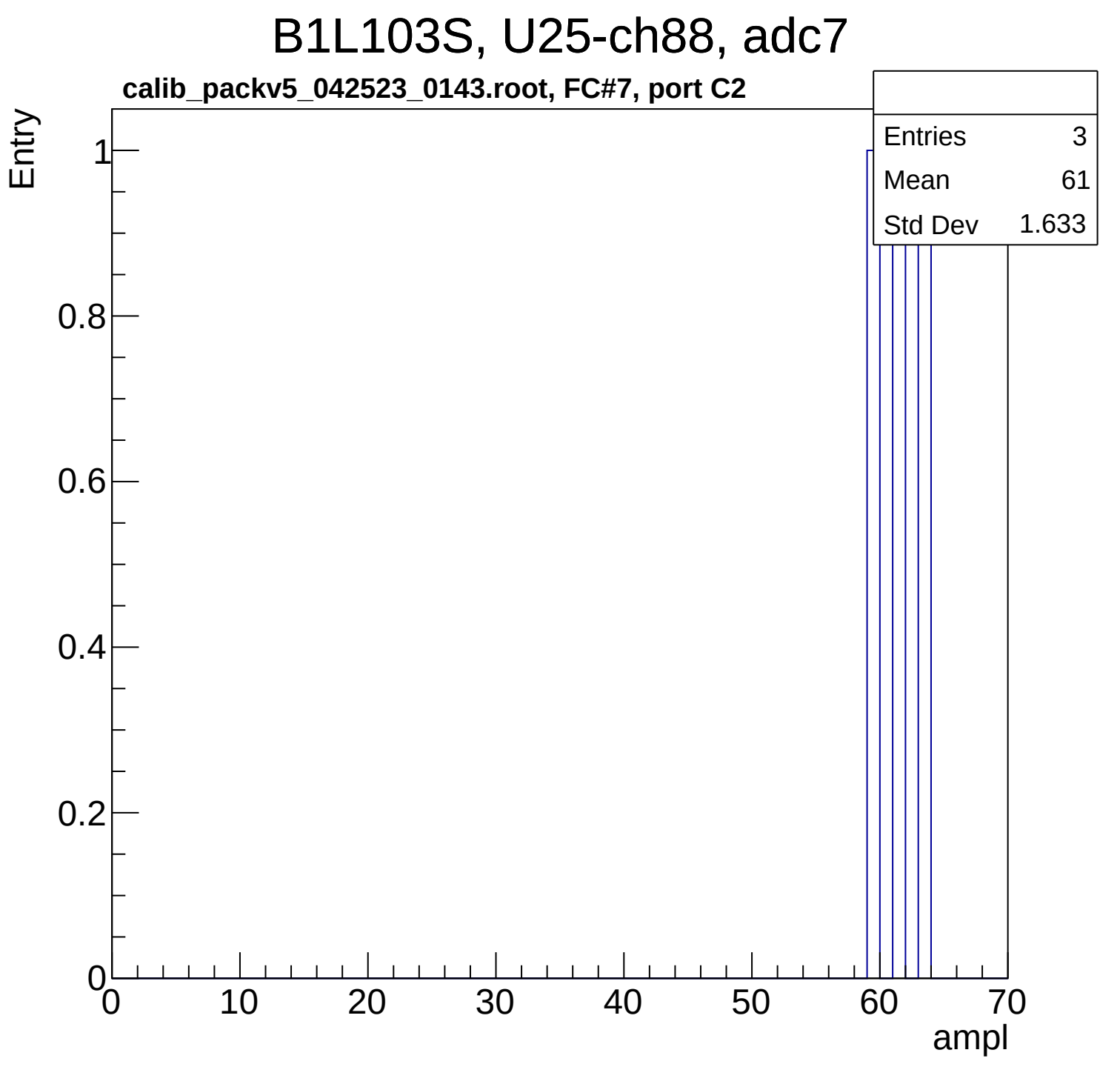
B1L103S, U25-ch88, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61
Std Dev 1.633
Entry
ampl
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0.2
0.4
0.6
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1
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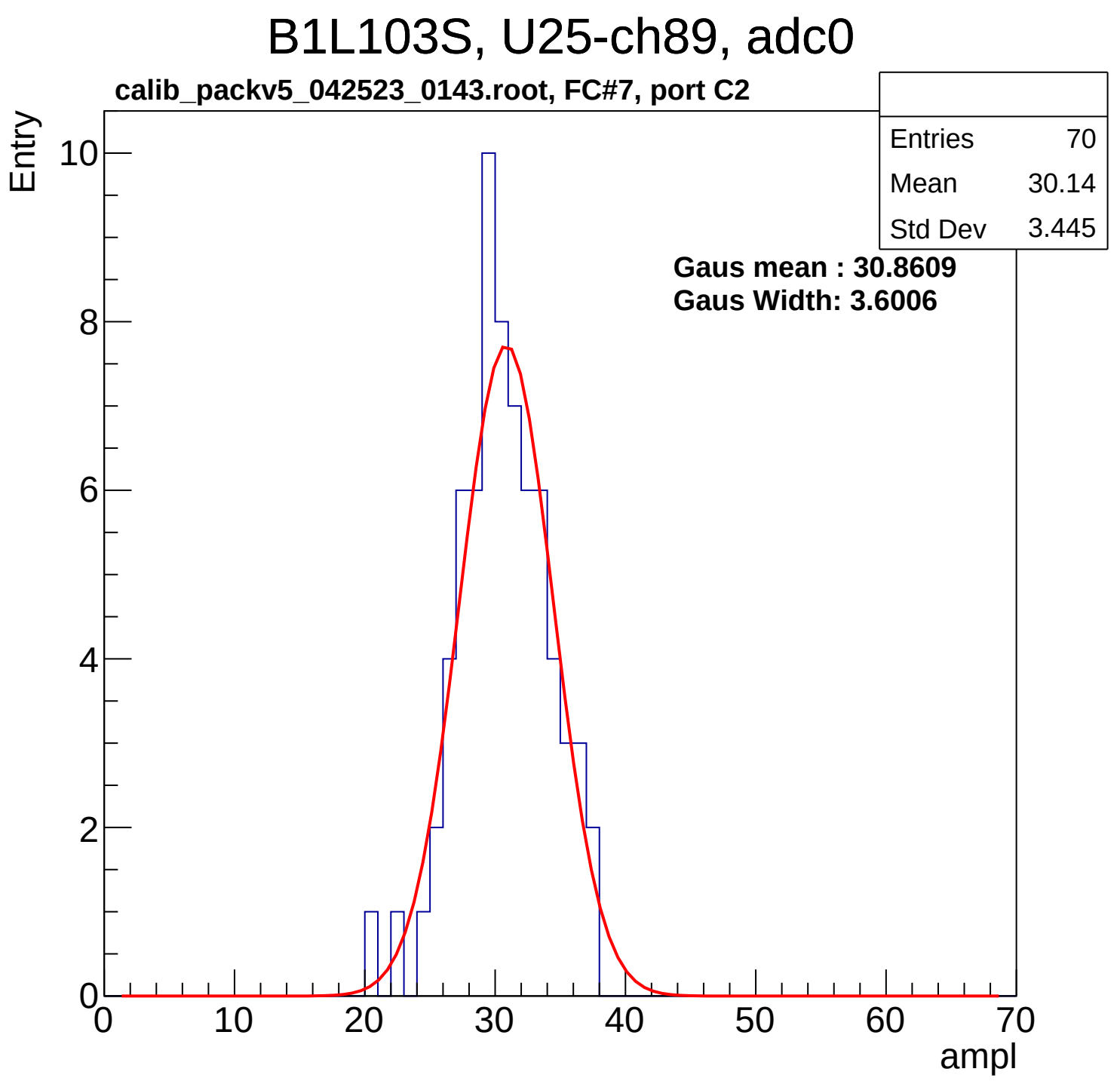
40
50
60
70


B1L103S, U25-ch89, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 30.14
Std Dev 3.445
Gaus mean : 30.8609
Gaus Width: 3.6006
Entry
ampl
0
2
4
6
8
10
0
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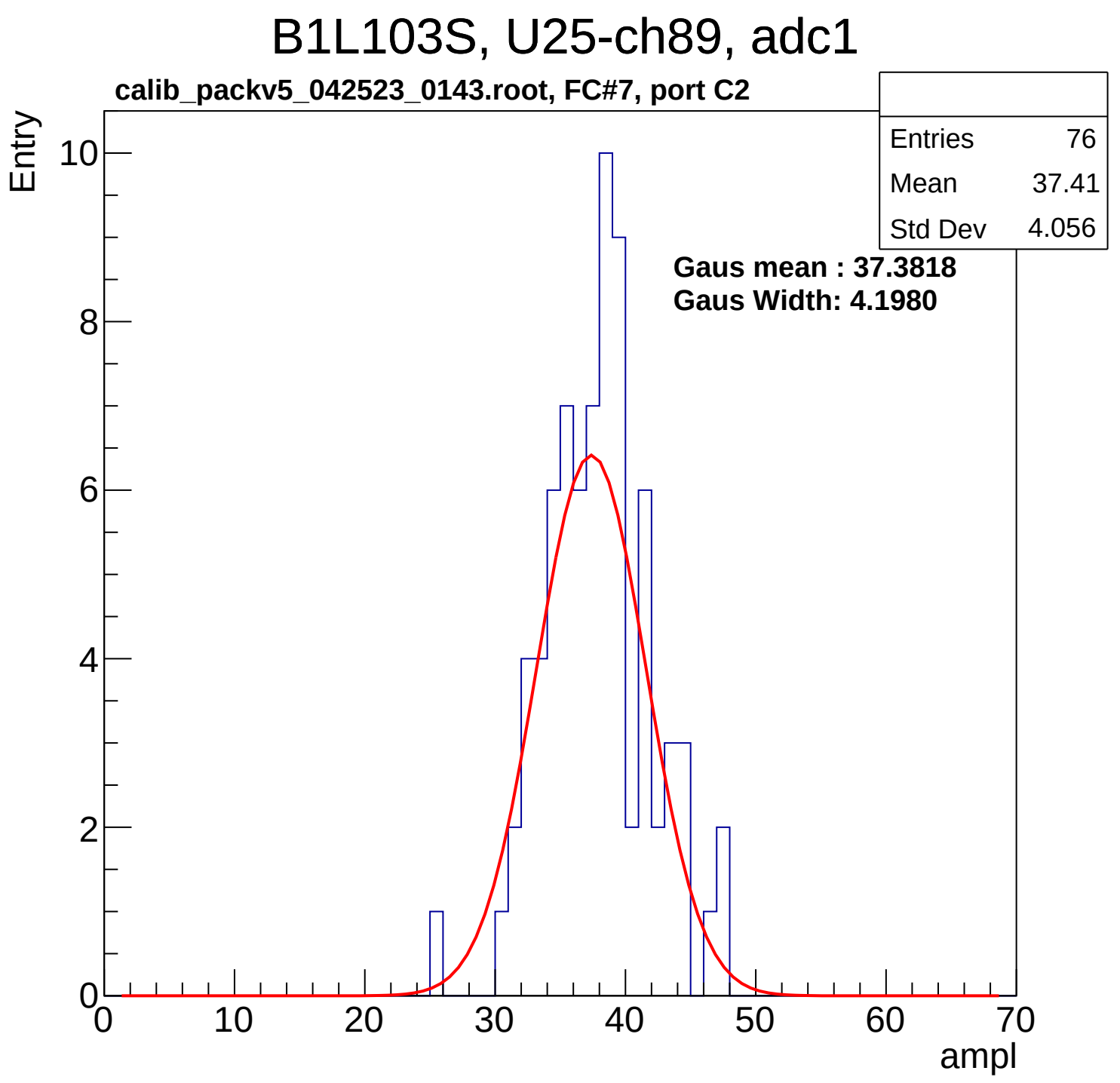
20
30
40
50
60
70


B1L103S, U25-ch89, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 37.41
Std Dev 4.056
Gaus mean : 37.3818
Gaus Width: 4.1980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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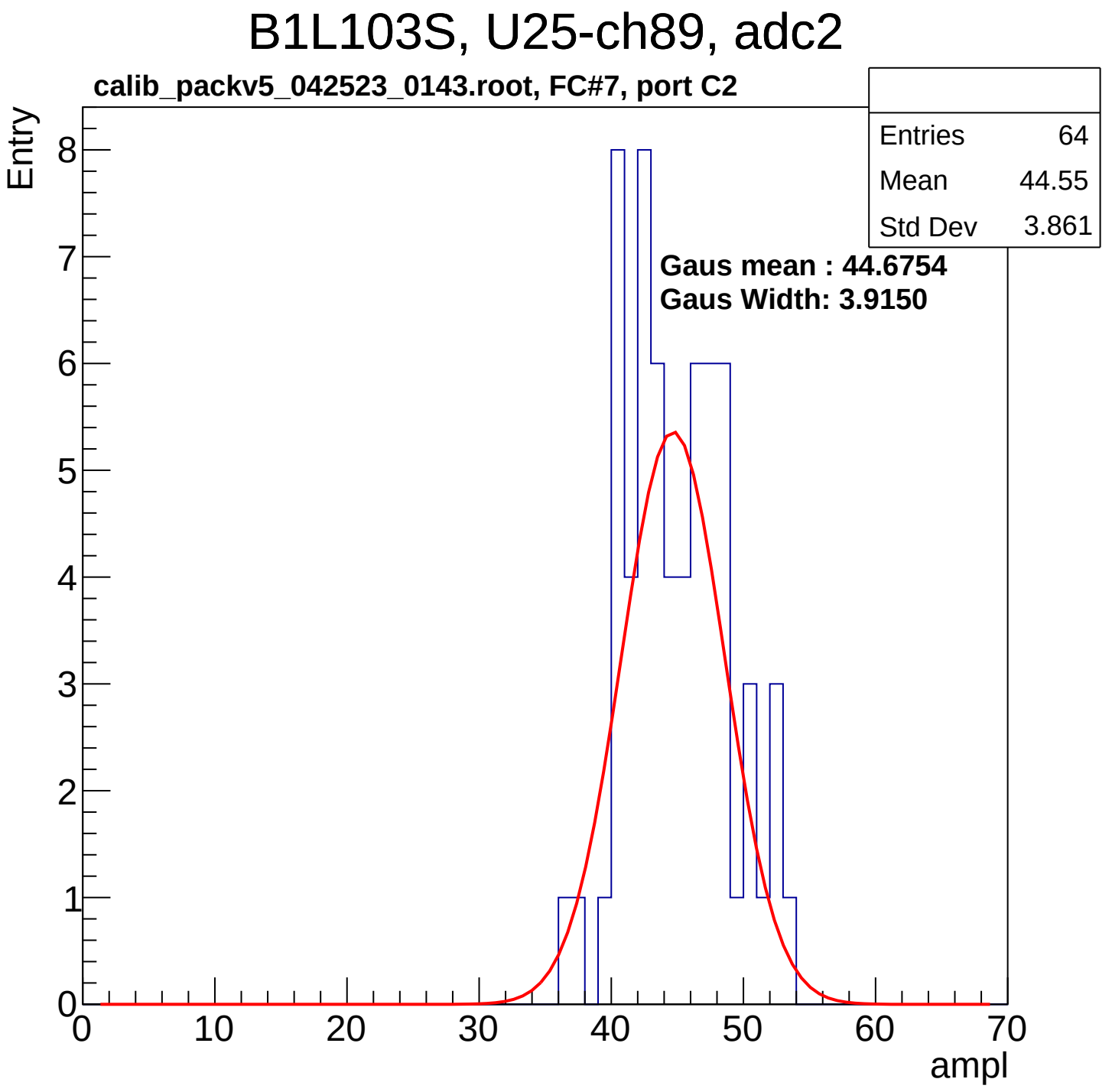
B1L103S, U25-ch89, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 44.55
Std Dev 3.861
Gaus mean : 44.6754
Gaus Width: 3.9150
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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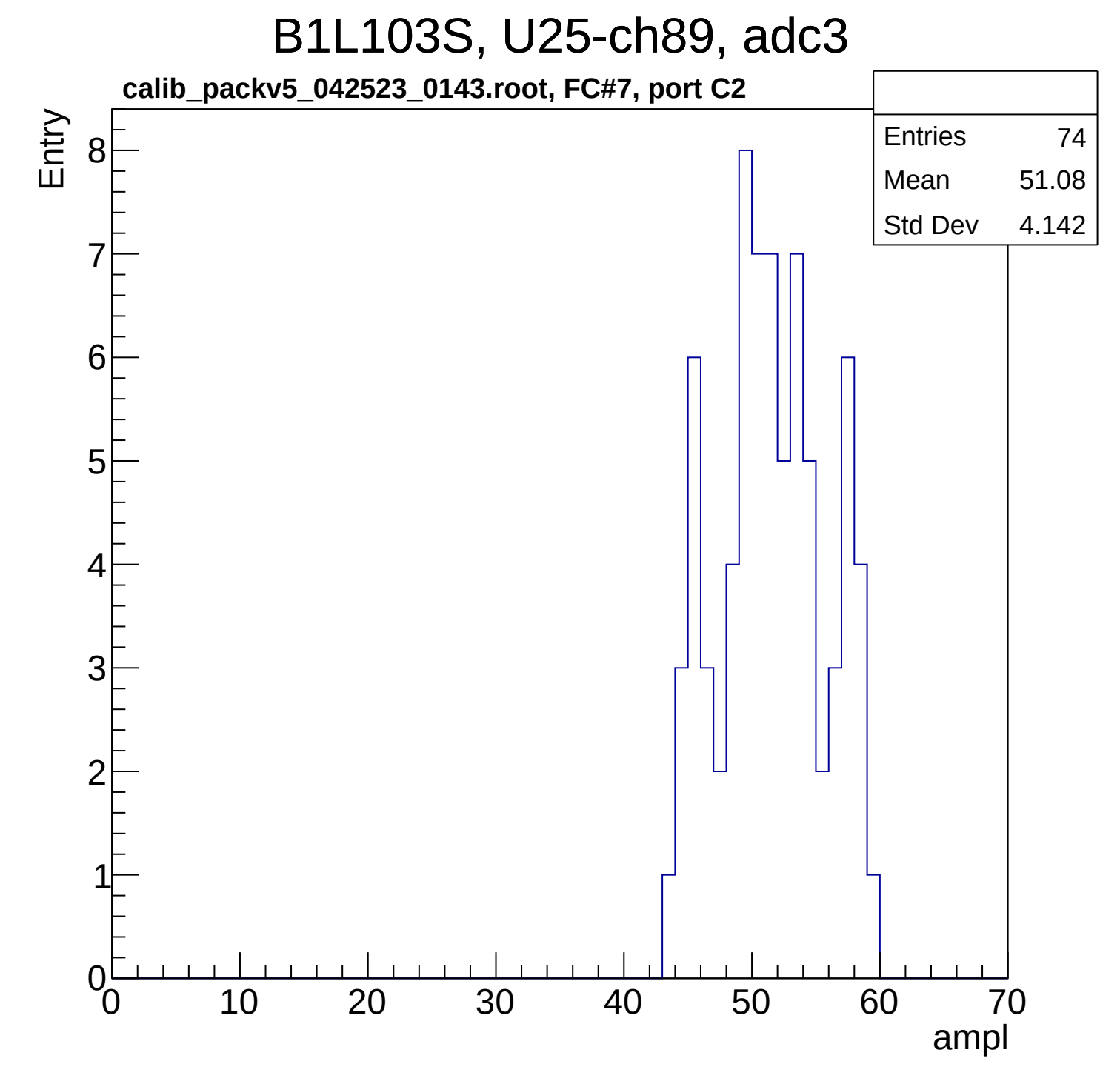
B1L103S, U25-ch89, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 51.08
Std Dev 4.142
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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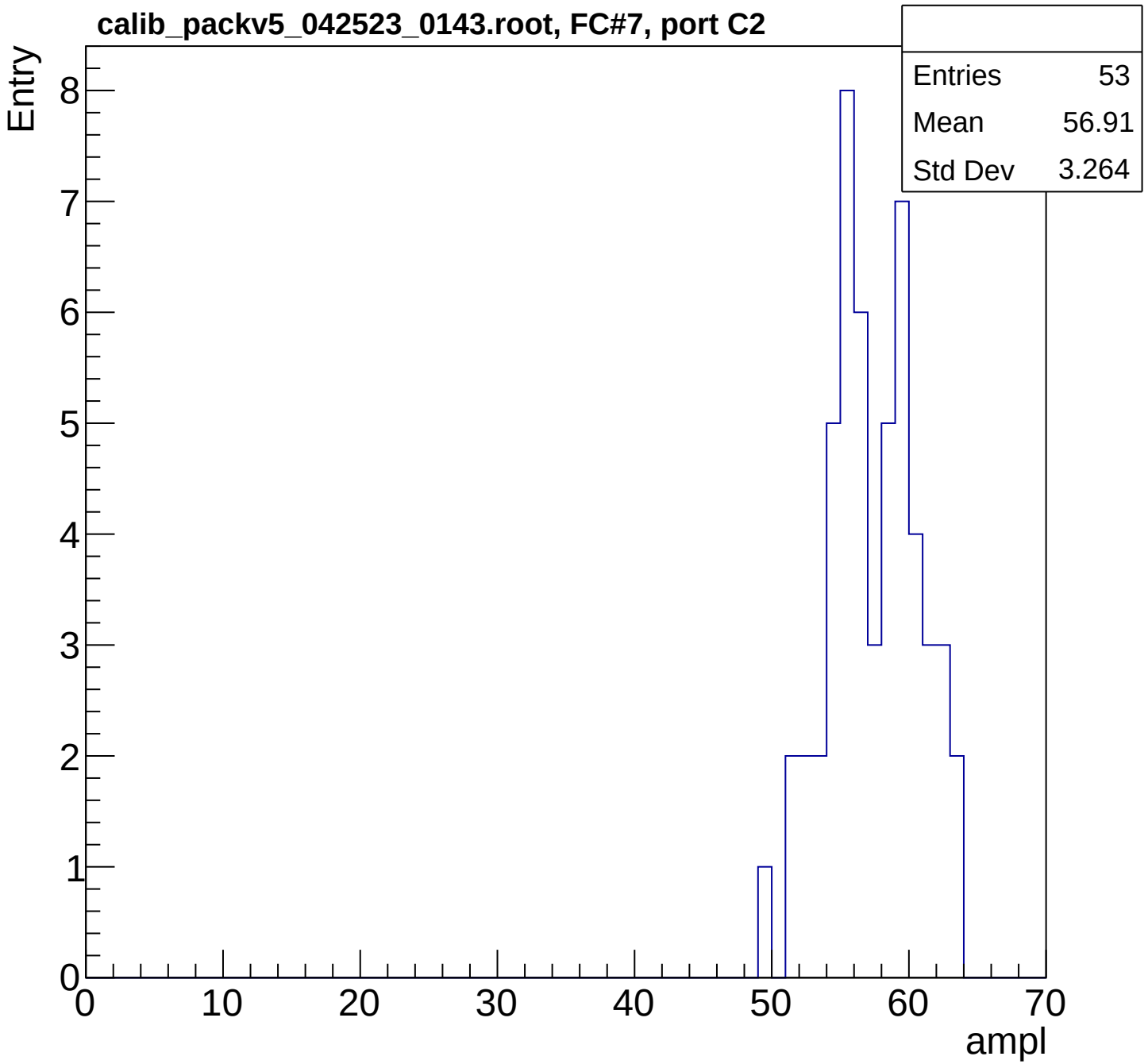
B1L103S, U25-ch89, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 56.91
Std Dev 3.264
Entry
ampl
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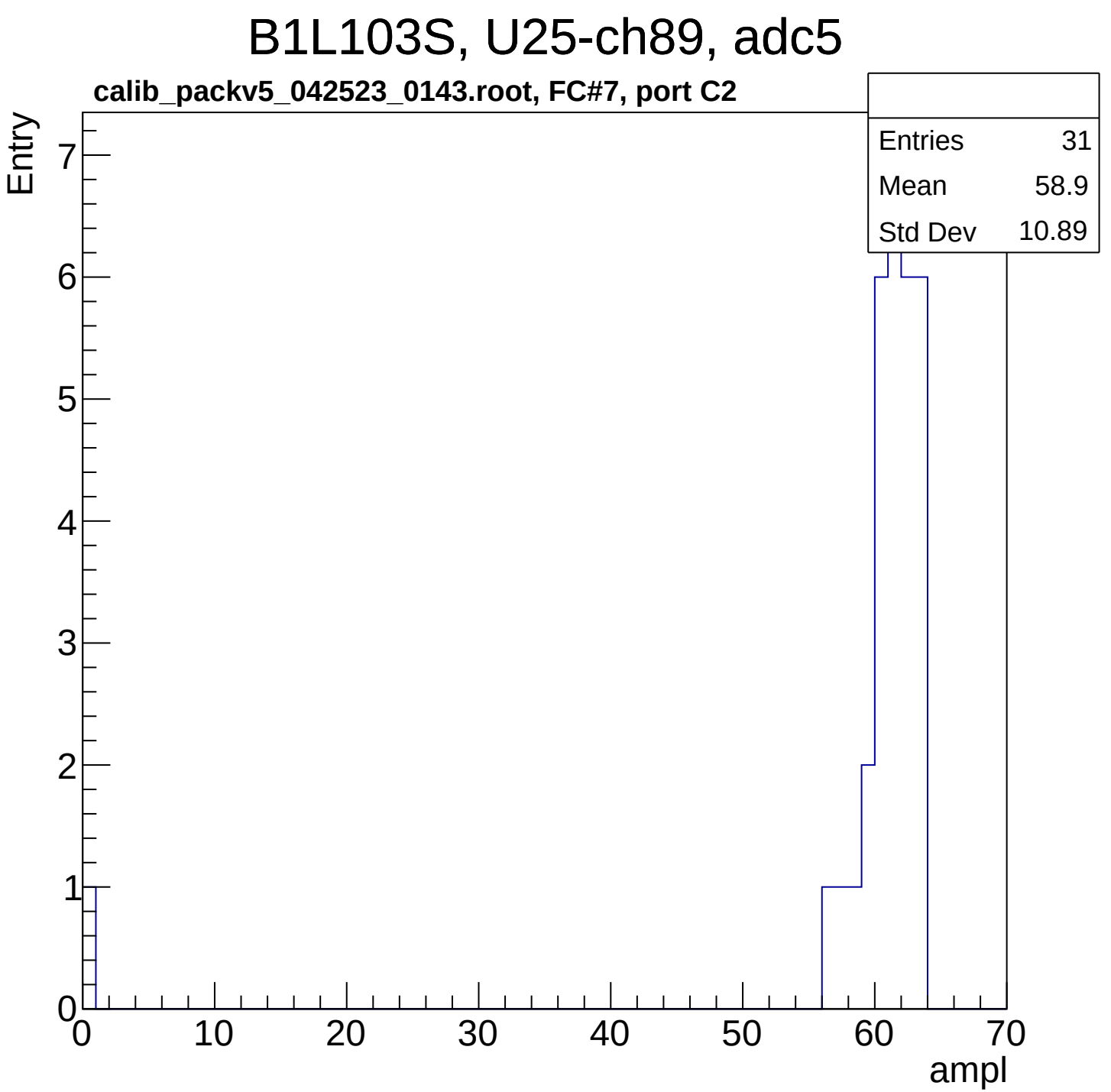
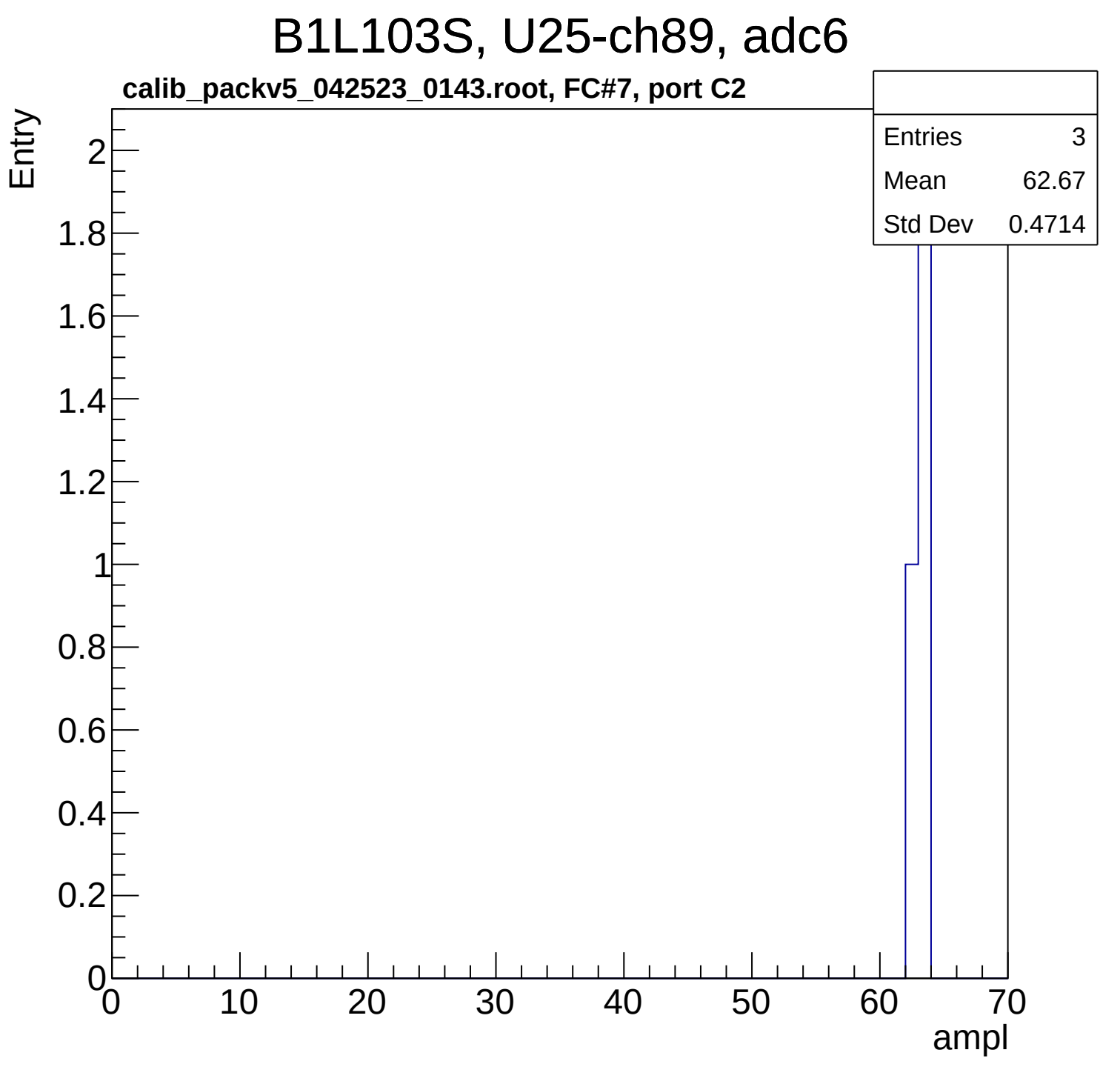
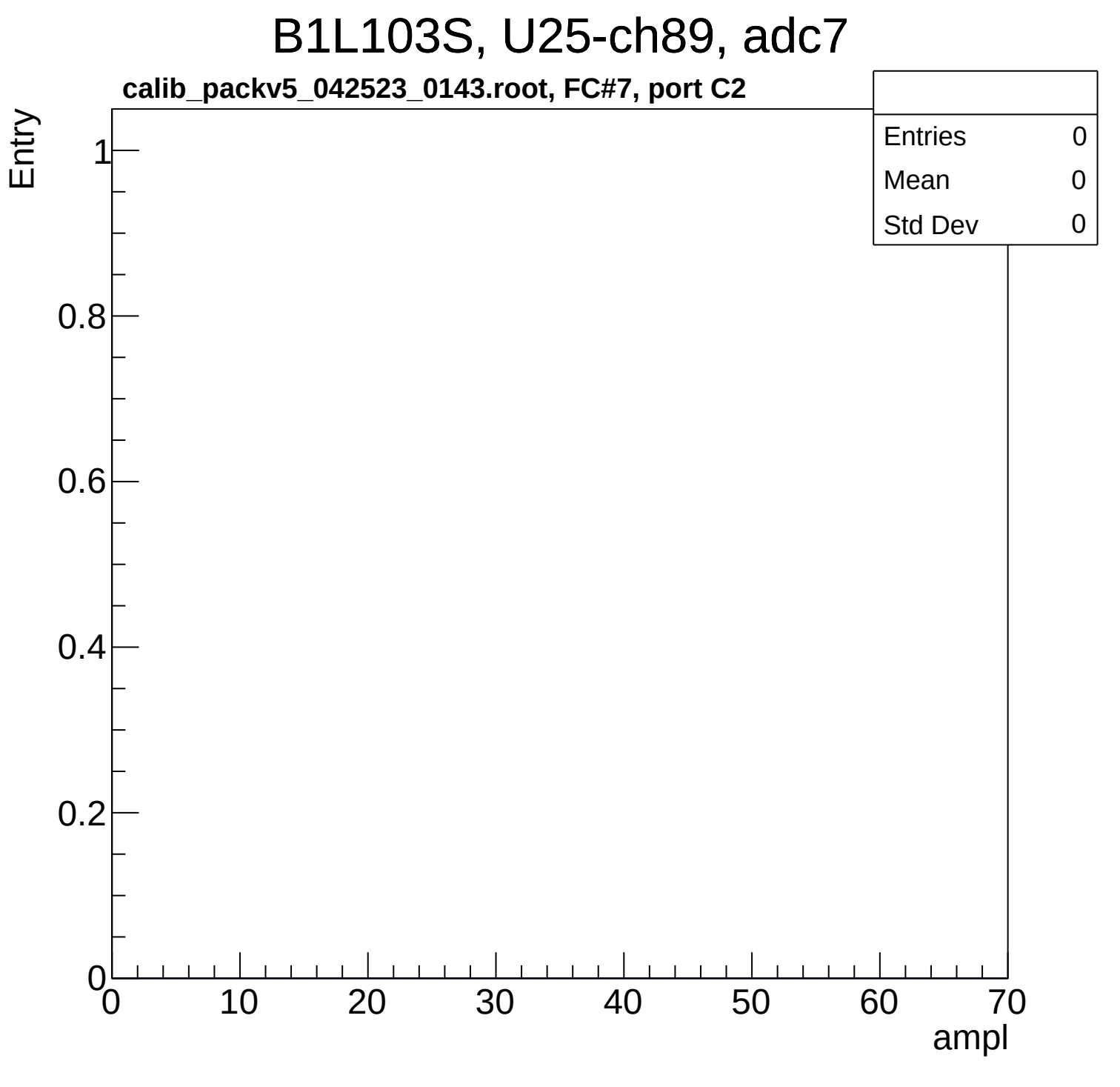
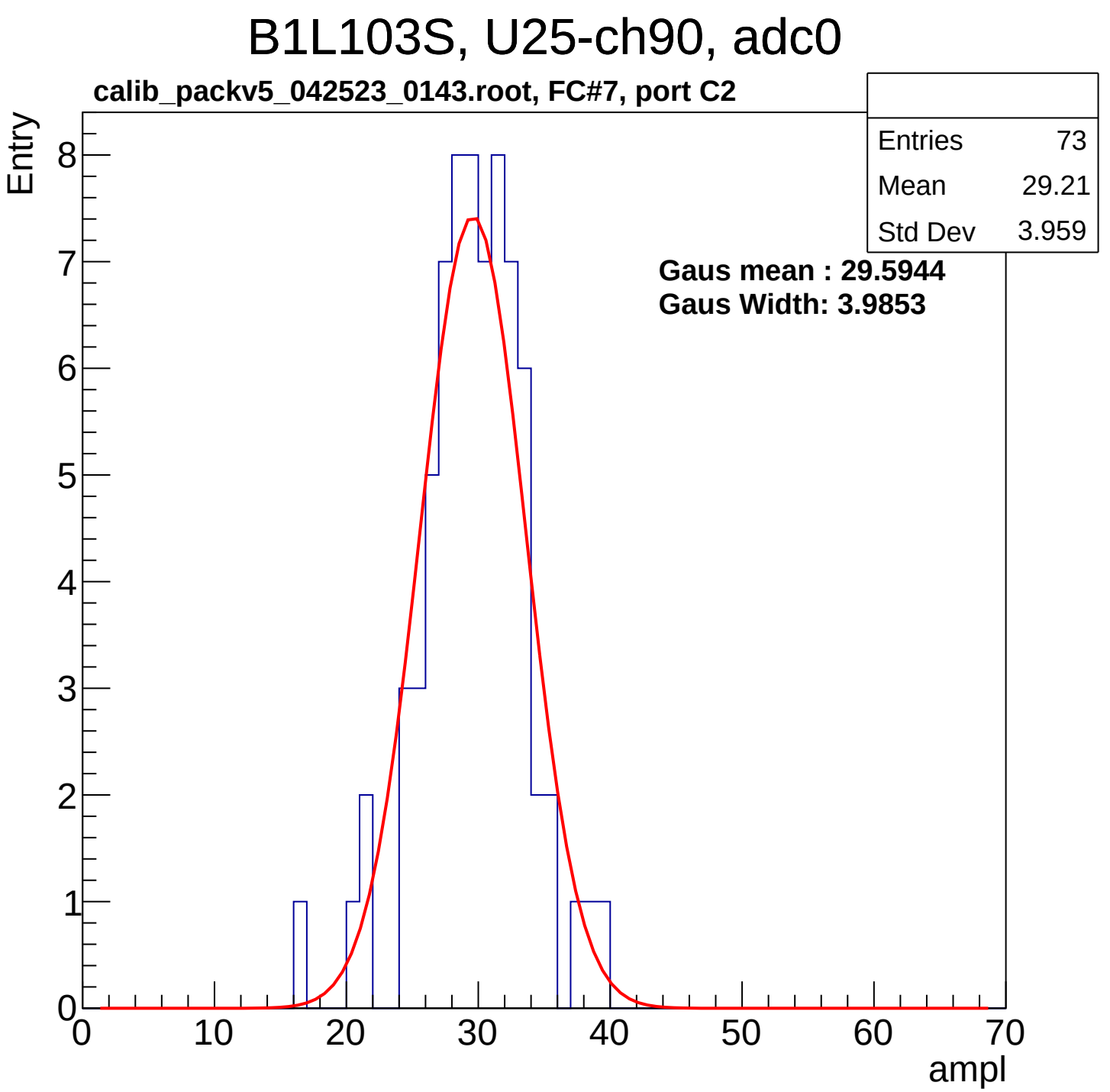
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch89, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 31
Mean 58.9
Std Dev 10.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch89, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch89, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch90, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 29.21
Std Dev 3.959
Gaus mean : 29.5944
Gaus Width: 3.9853
Entry
ampl
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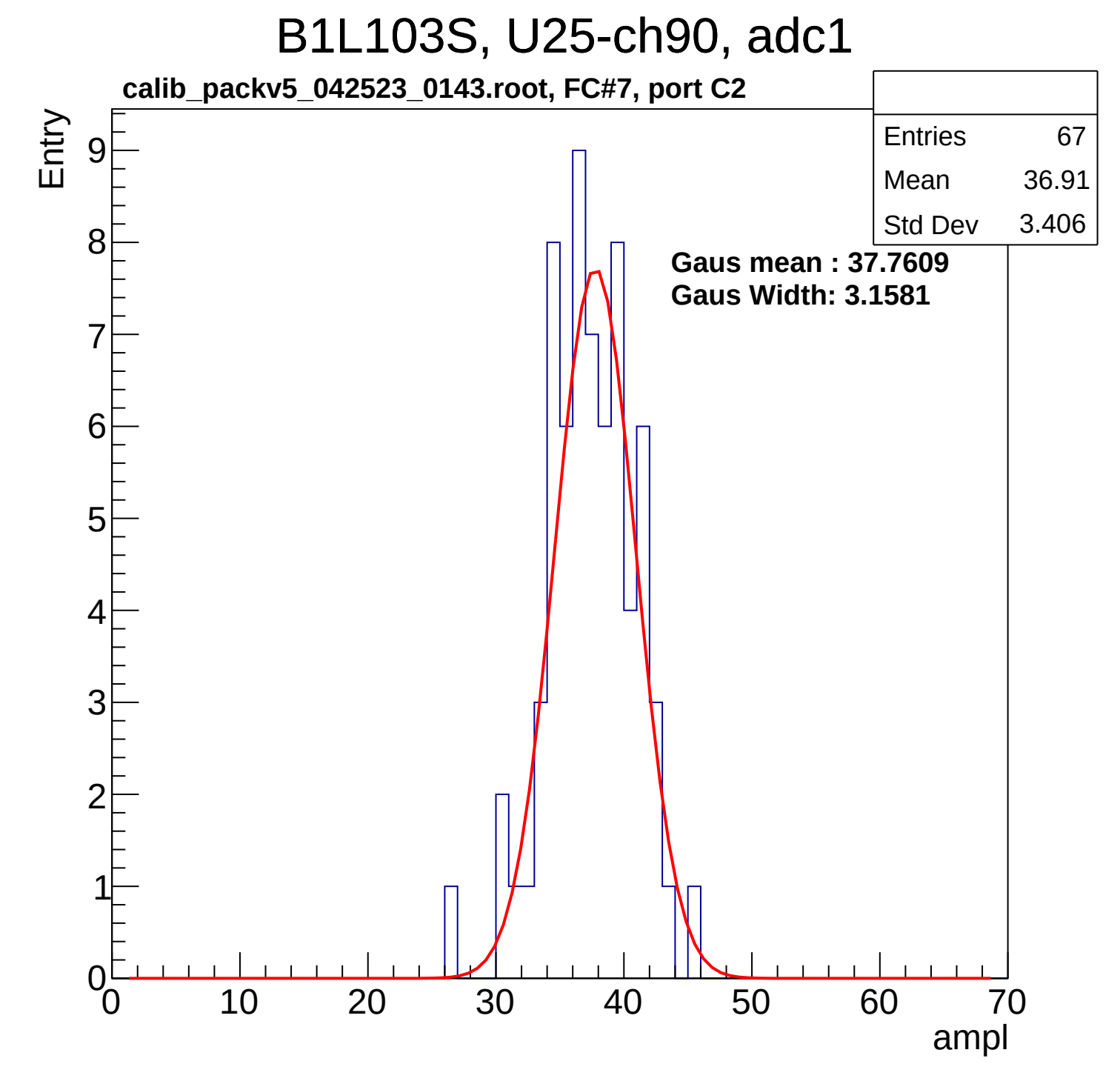
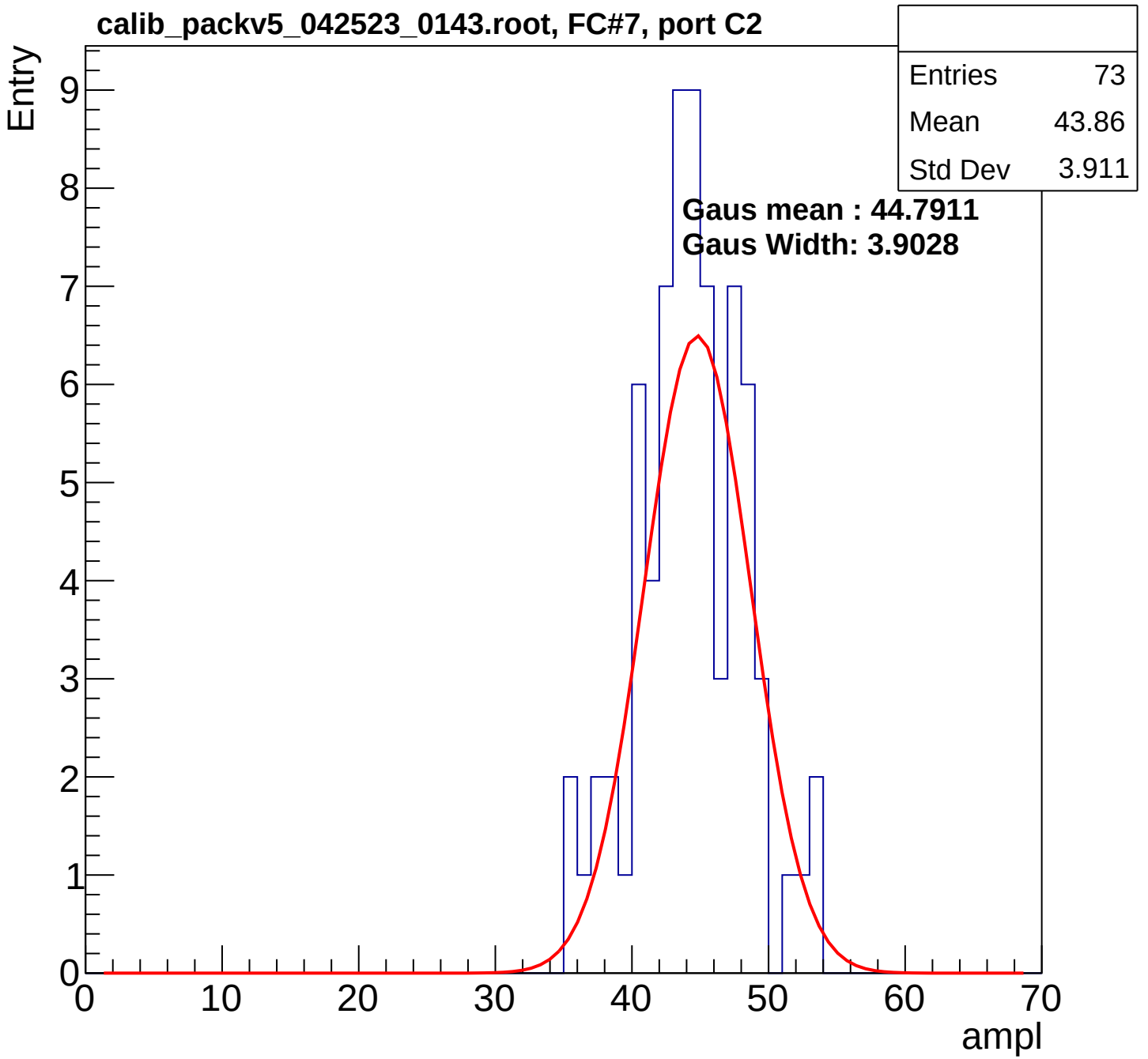
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch90, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.91
Std Dev 3.406
Gaus mean : 37.7609
Gaus Width: 3.1581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch90, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 43.86
Std Dev 3.911
Gaus mean : 44.7911
Gaus Width: 3.9028
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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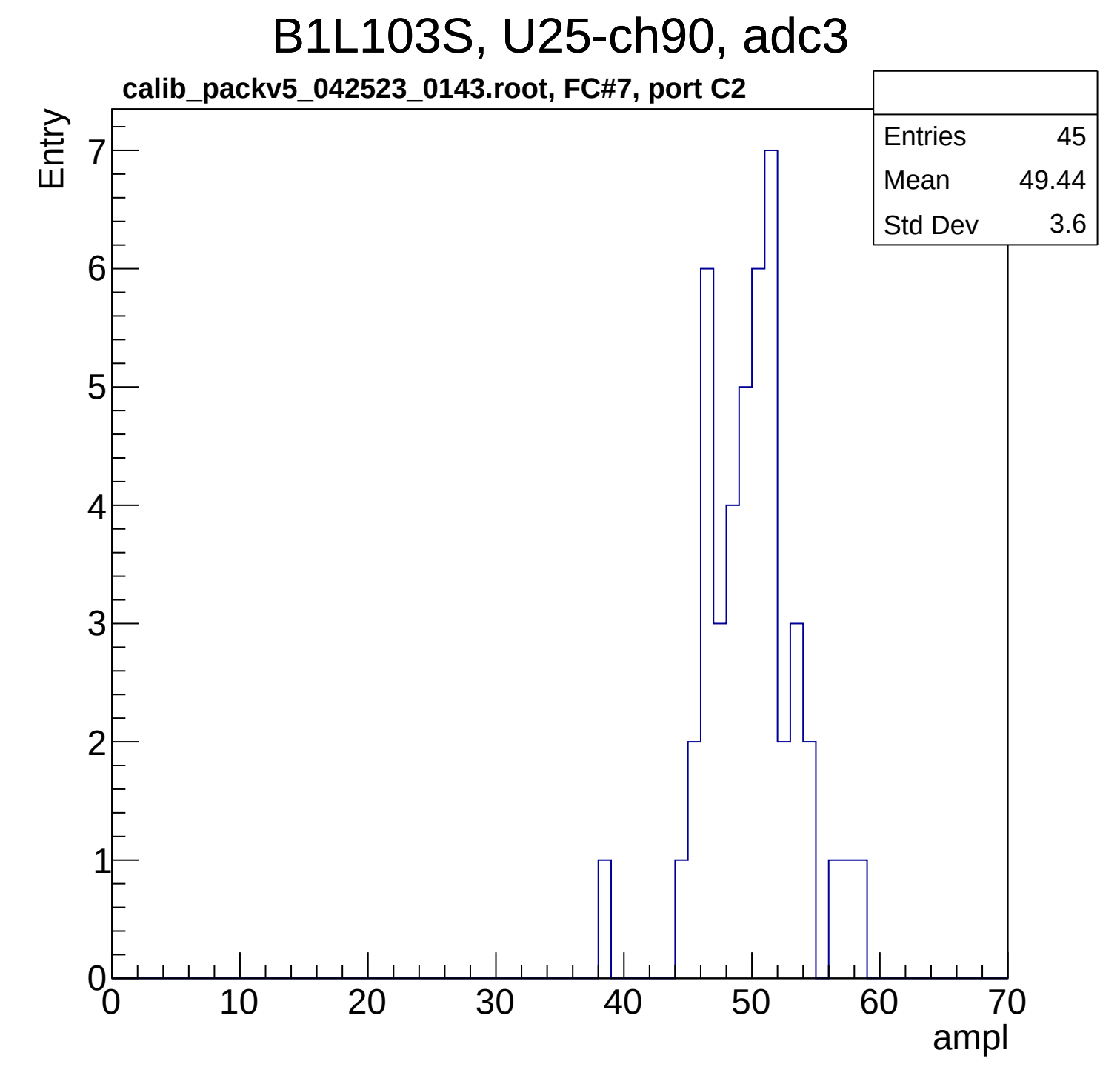
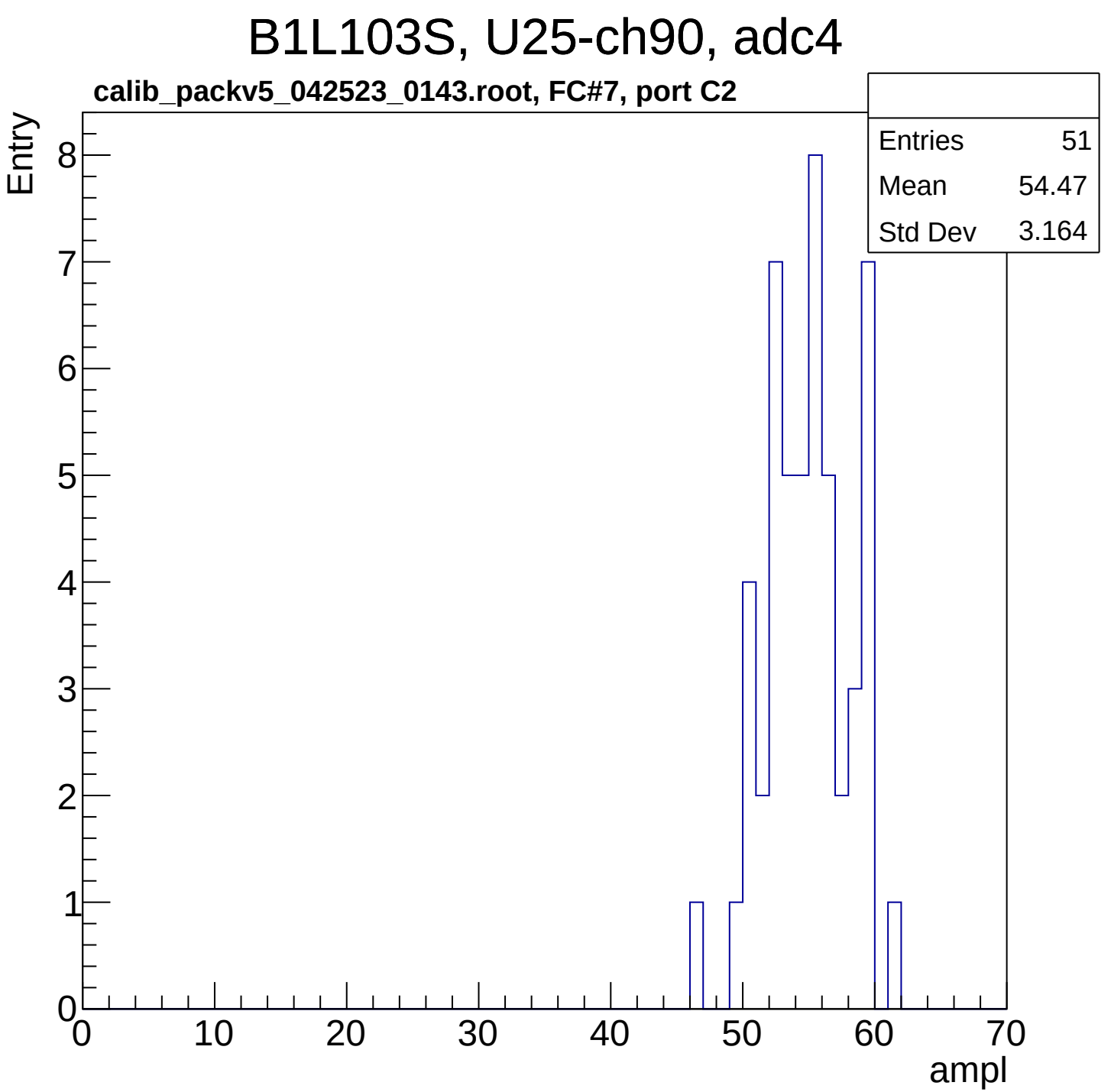

B1L103S, U25-ch90, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 49.44
Std Dev 3.6
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch90, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 54.47
Std Dev 3.164
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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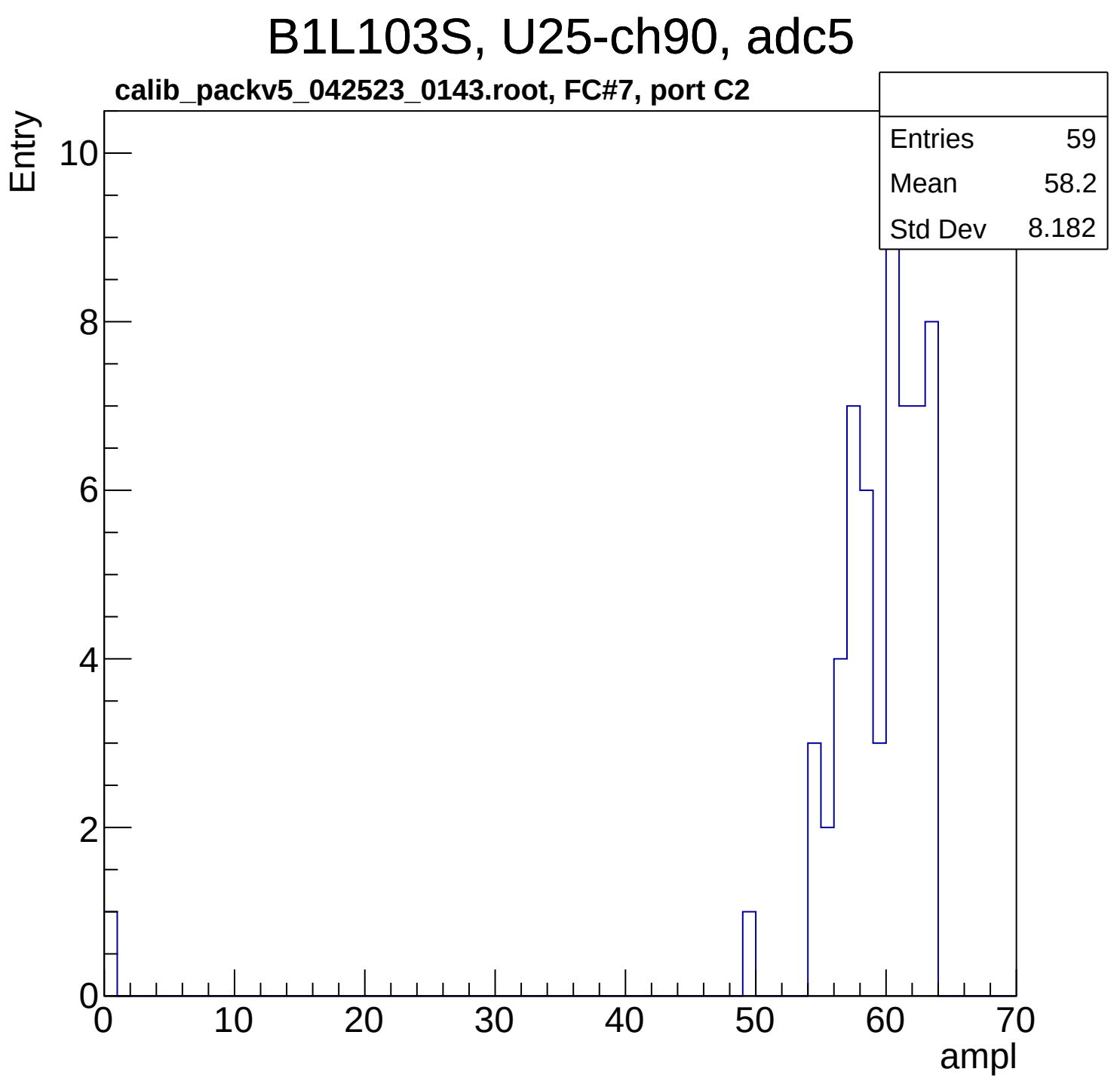
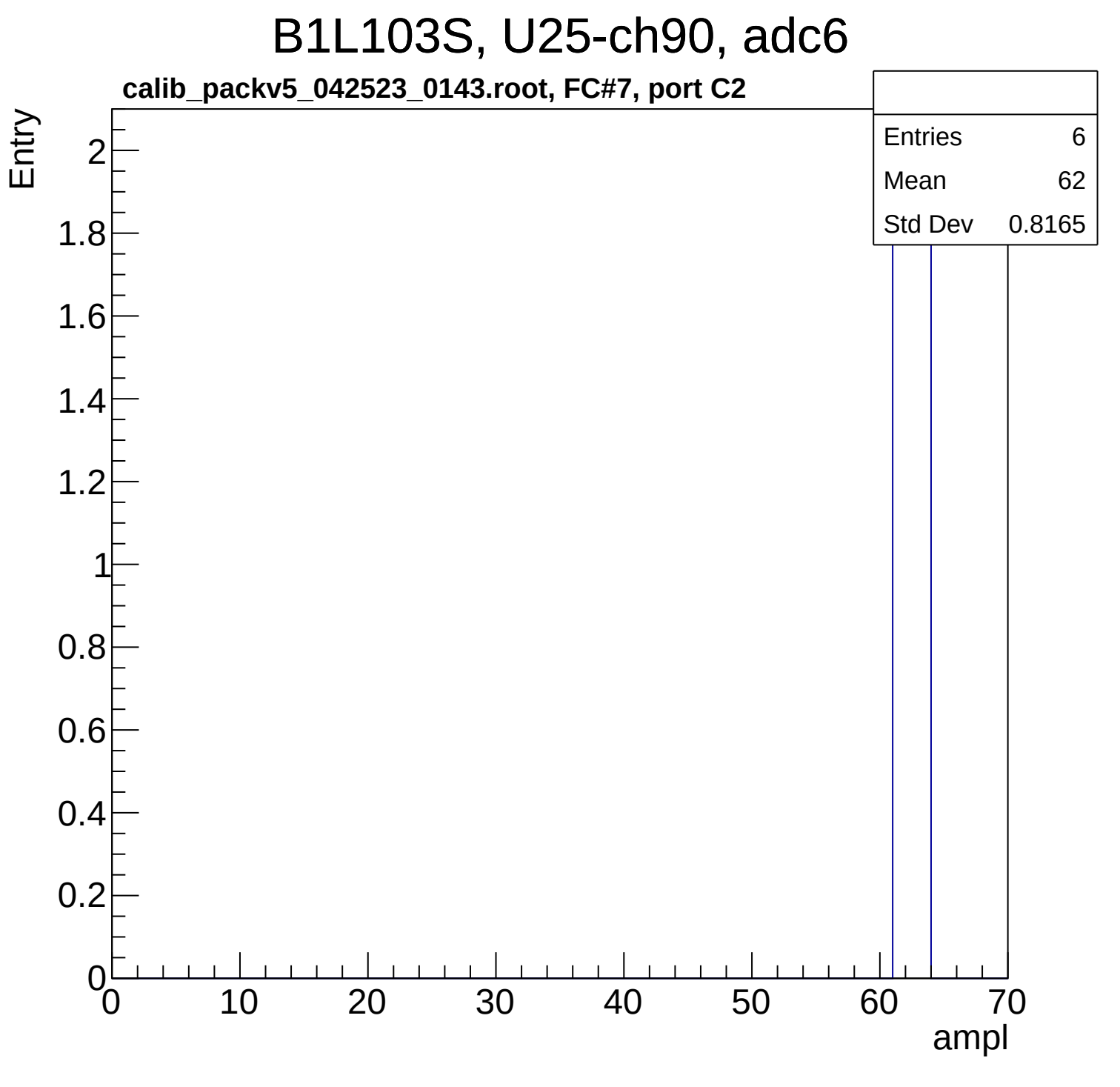
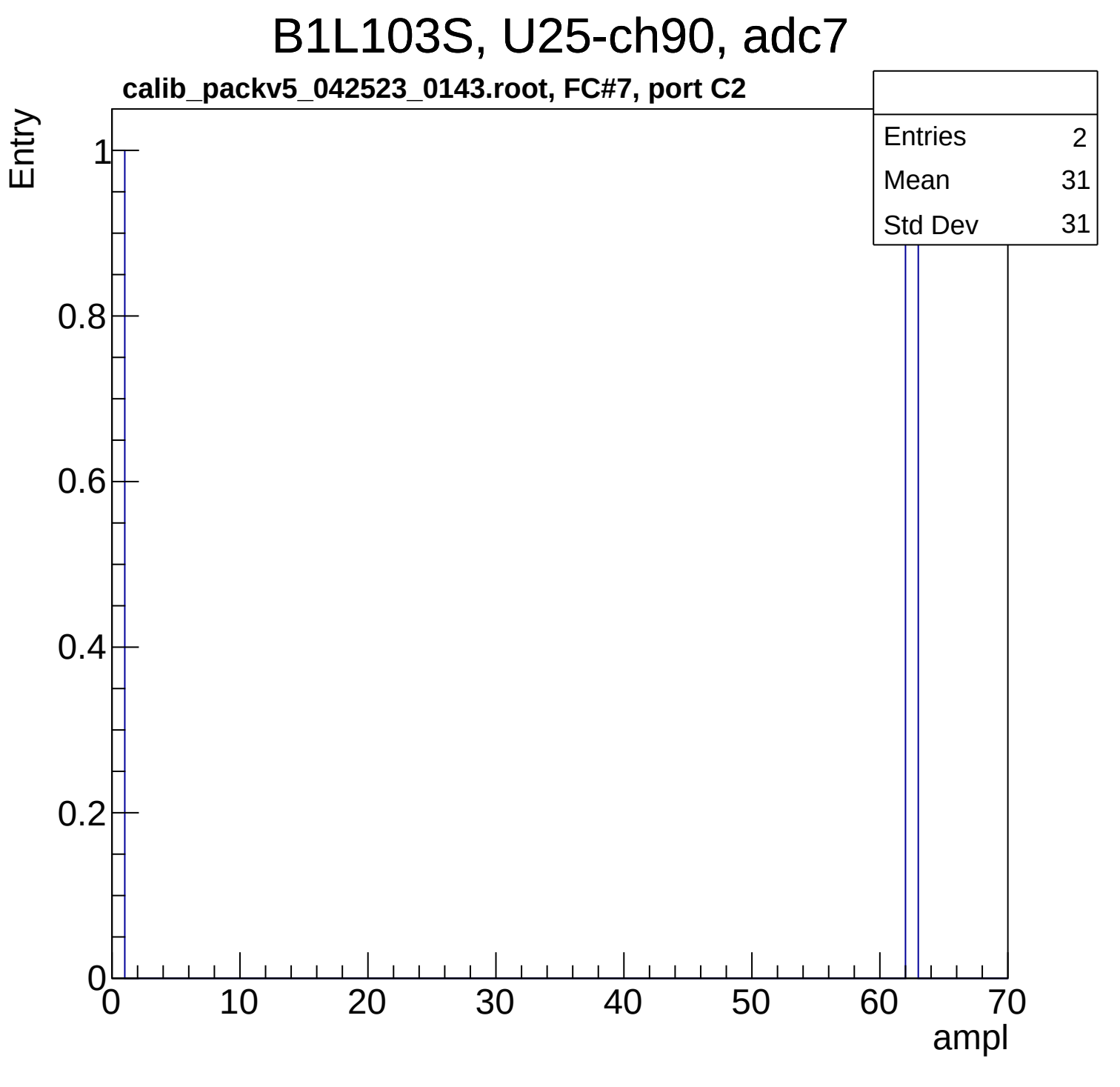
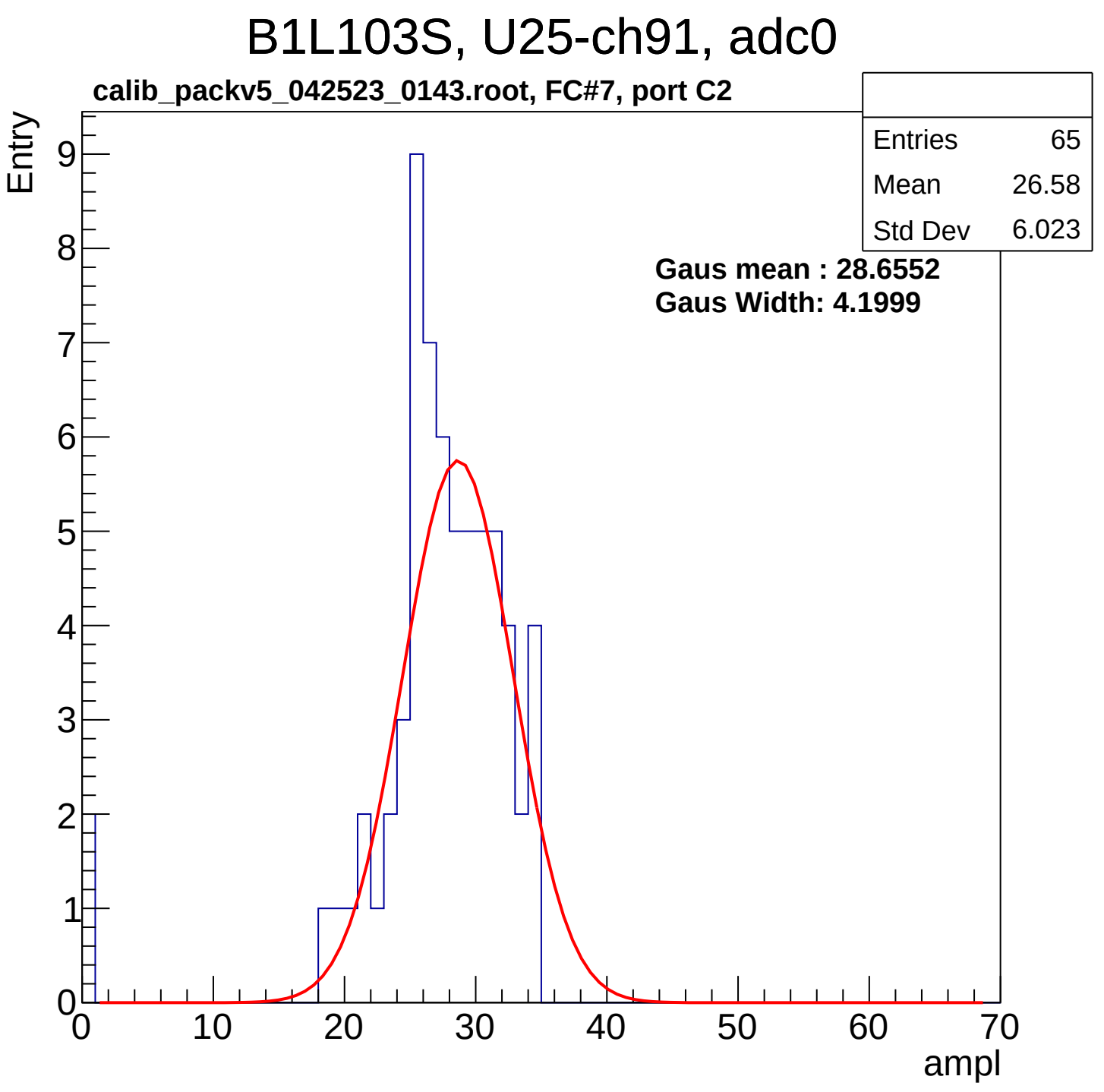

B1L103S, U25-ch90, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 58.2
Std Dev 8.182
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch90, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 62
Std Dev 0.8165
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch90, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch91, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 26.58
Std Dev 6.023
Gaus mean : 28.6552
Gaus Width: 4.1999
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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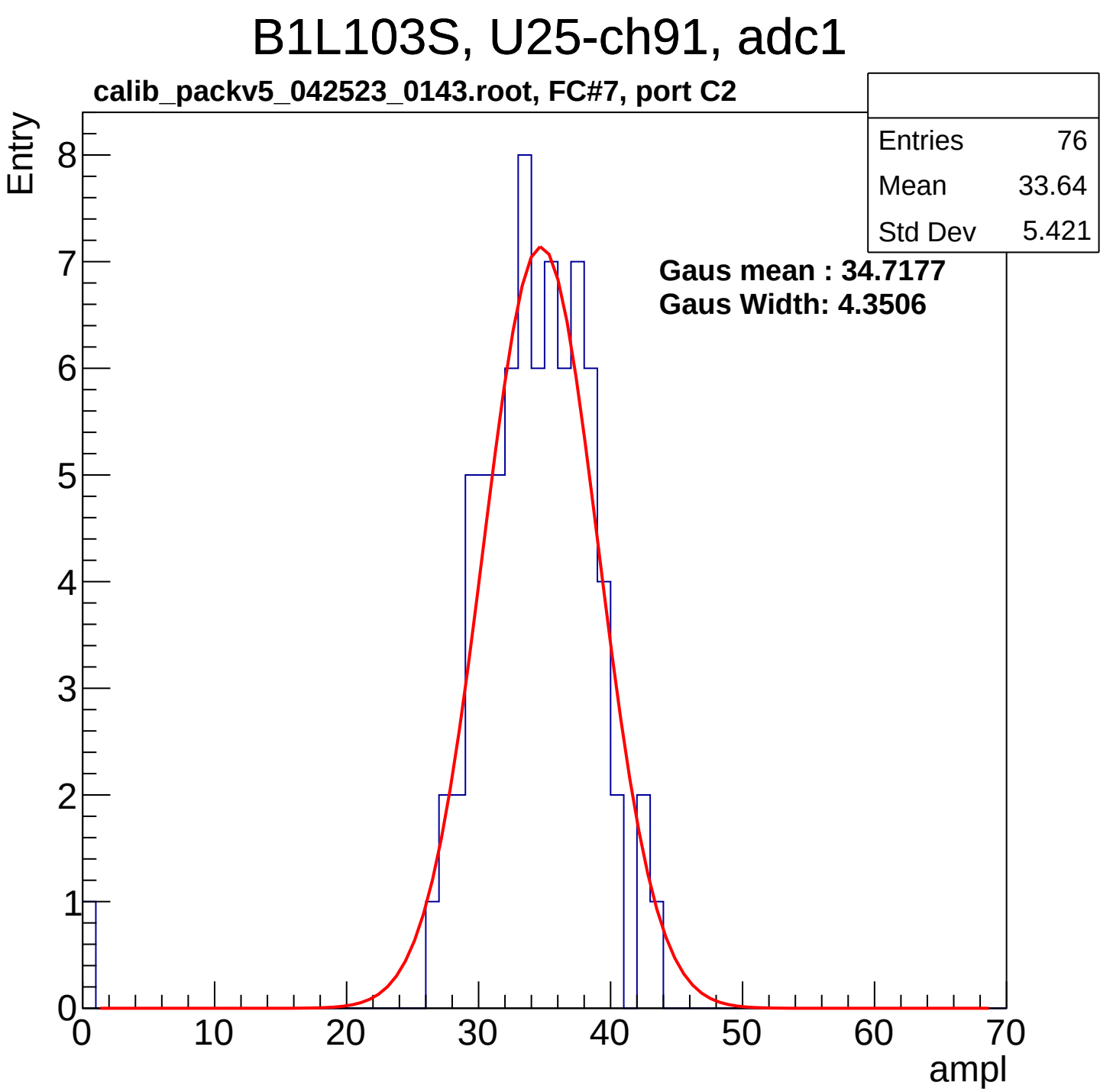
60
70


B1L103S, U25-ch91, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 33.64
Std Dev 5.421
Gaus mean : 34.7177
Gaus Width: 4.3506
Entry
ampl
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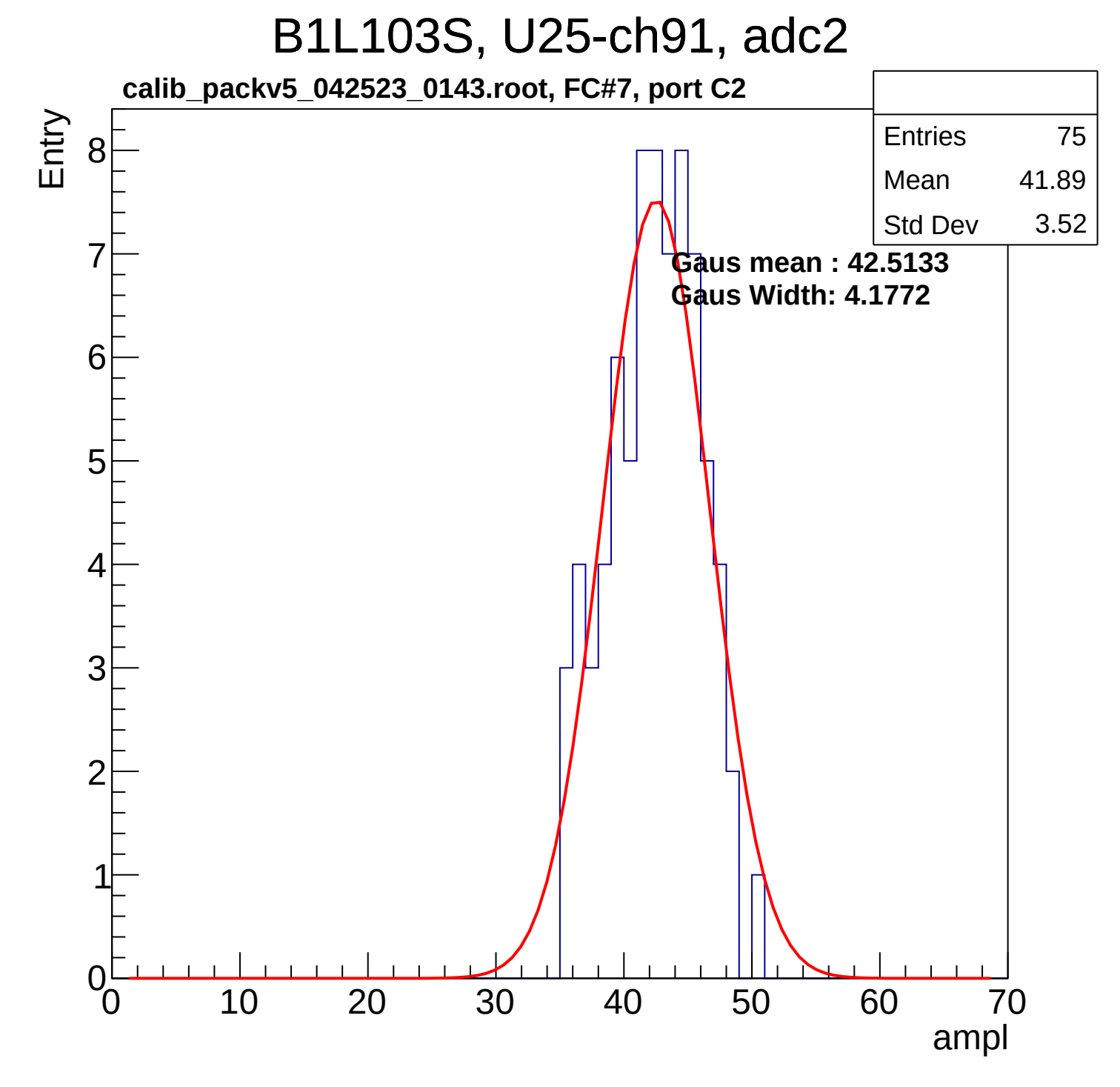
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch91, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 41.89
Std Dev 3.52
Gaus mean : 42.5133
Gaus Width: 4.1772
Entry
ampl
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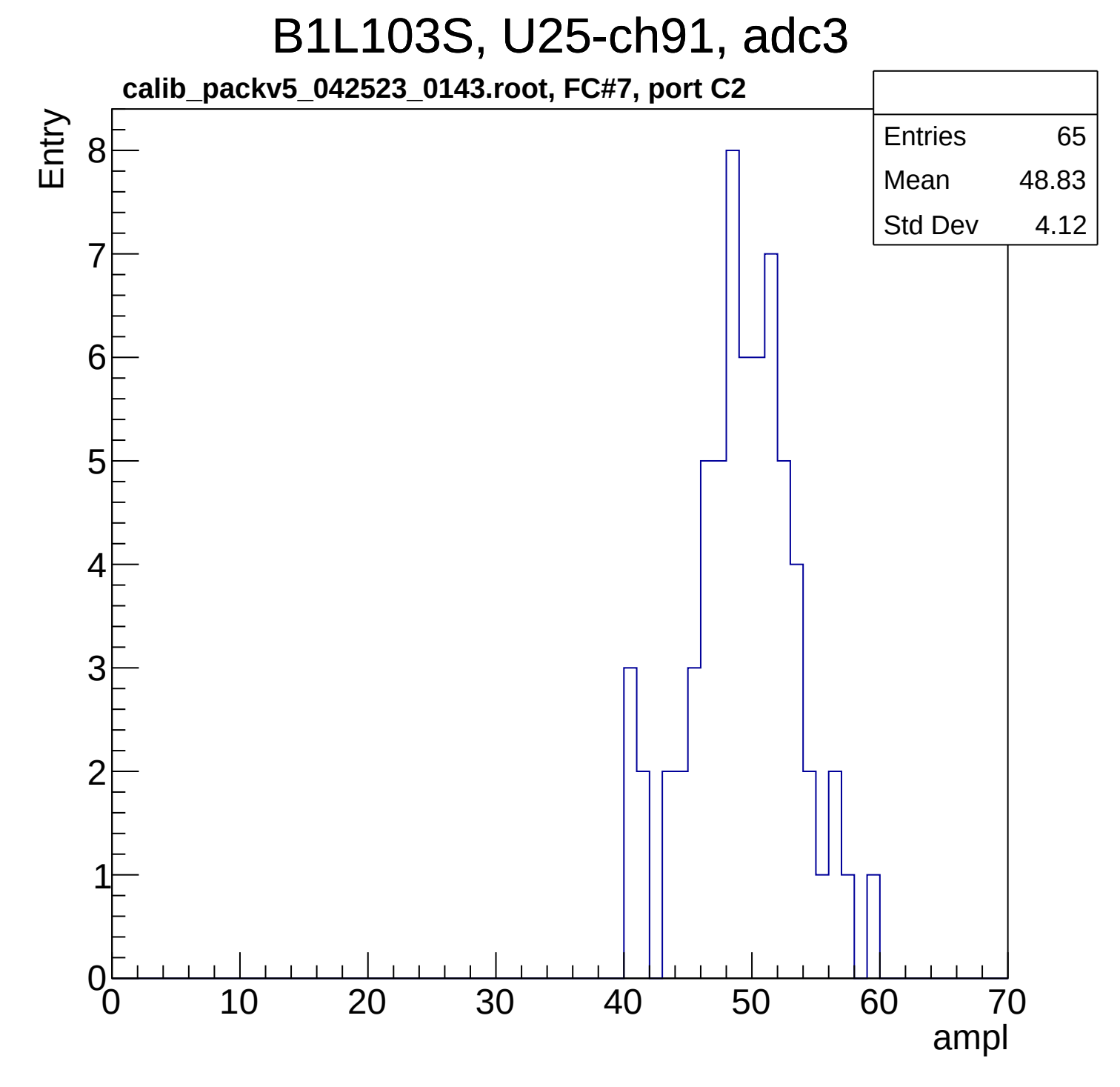
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch91, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 48.83
Std Dev 4.12
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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60
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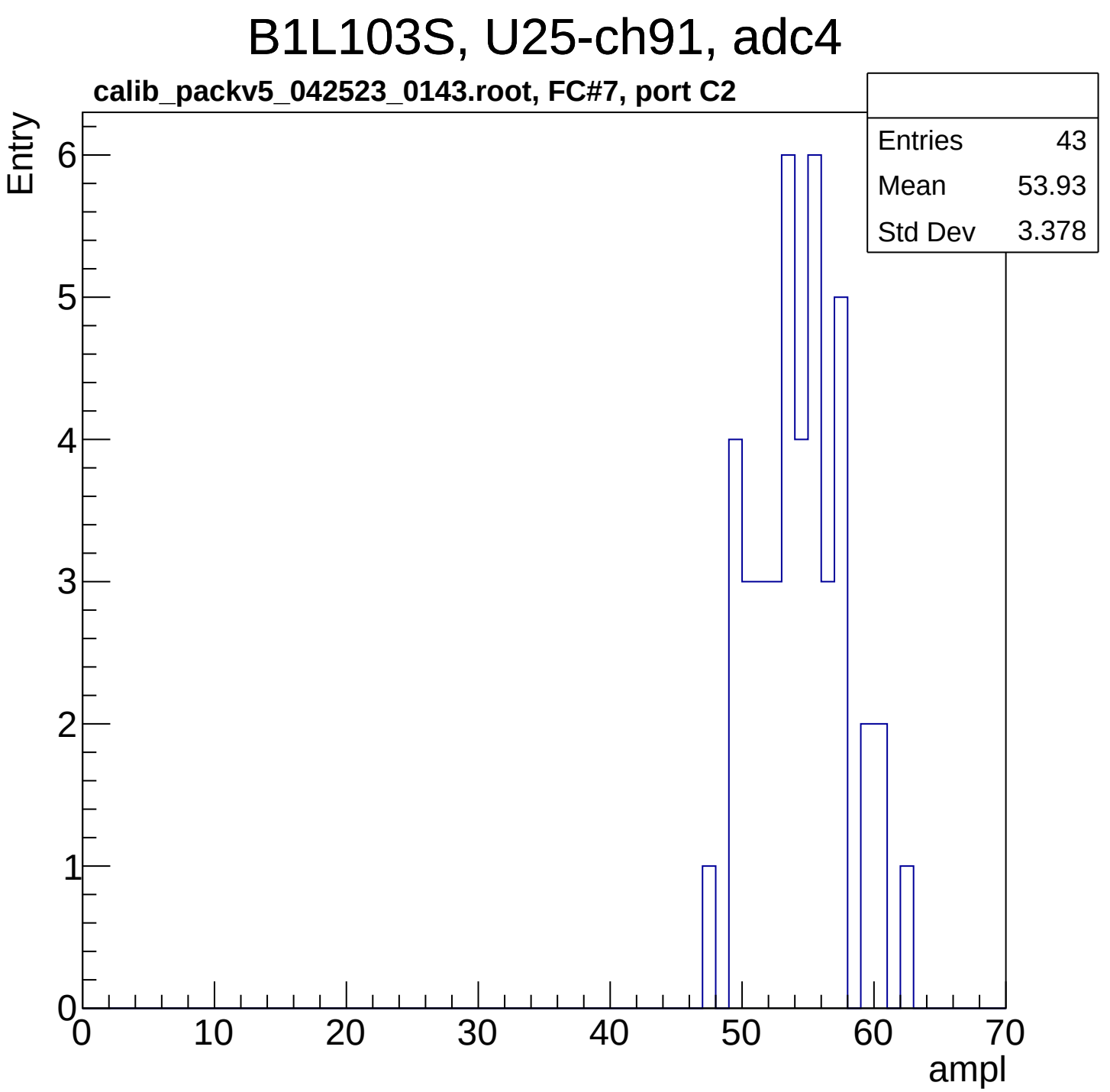
B1L103S, U25-ch91, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 53.93
Std Dev 3.378
Entry
ampl
0
1
2
3
4
5
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0
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20
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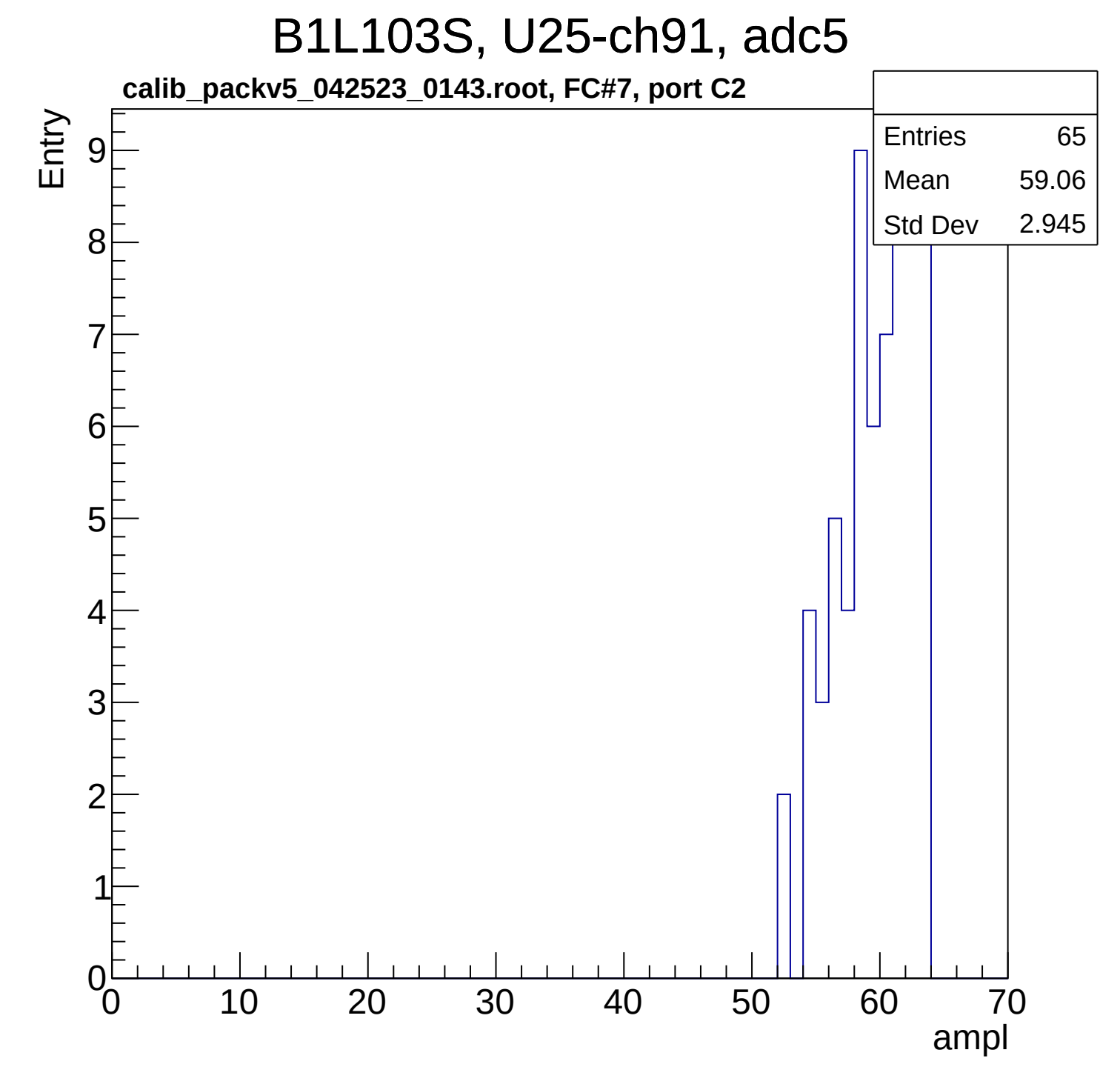
40
50
60
70


B1L103S, U25-ch91, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 59.06
Std Dev 2.945
Entry
ampl
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0
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20
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40
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60
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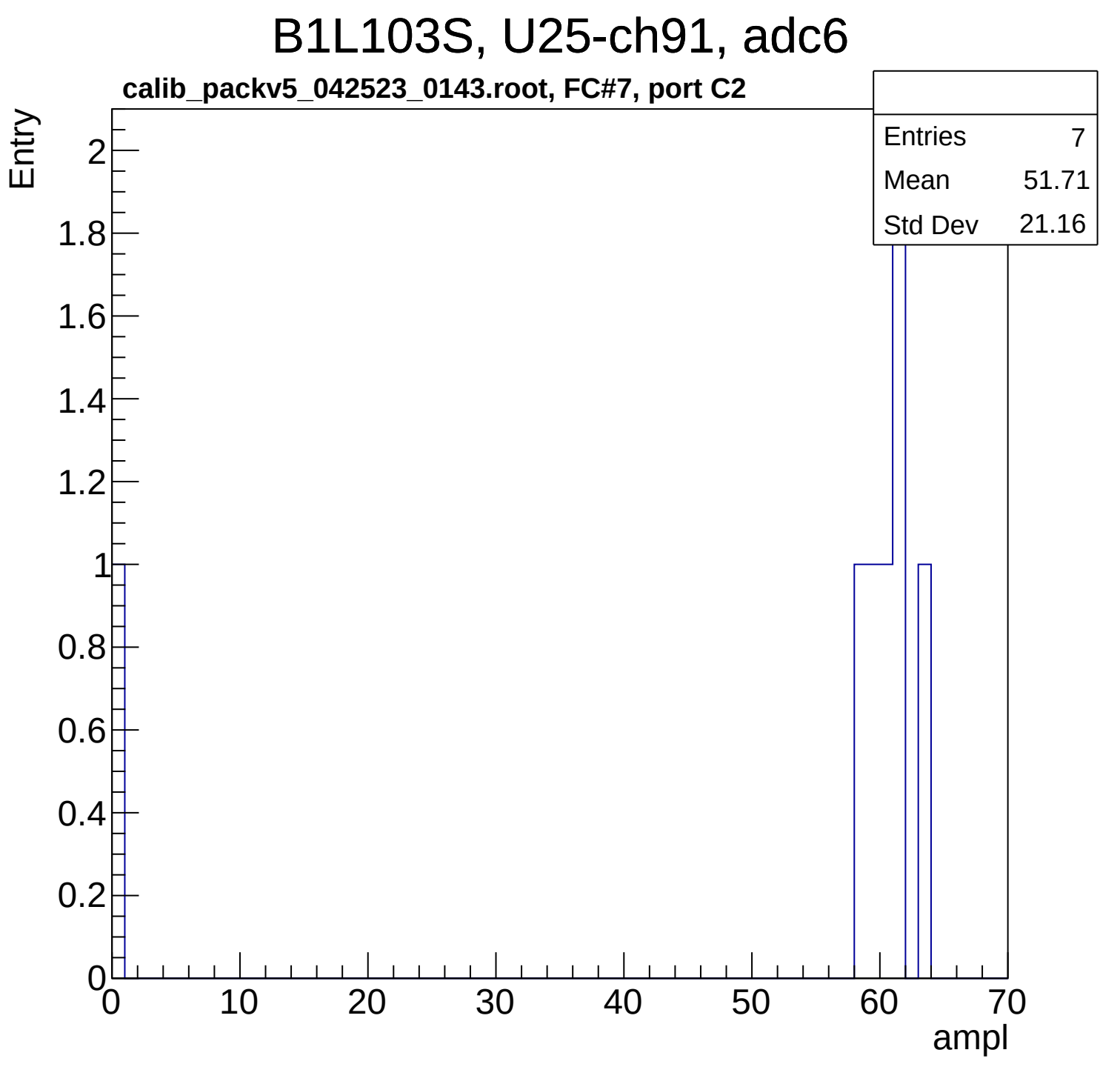
B1L103S, U25-ch91, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 51.71
Std Dev 21.16
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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20
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40
50
60
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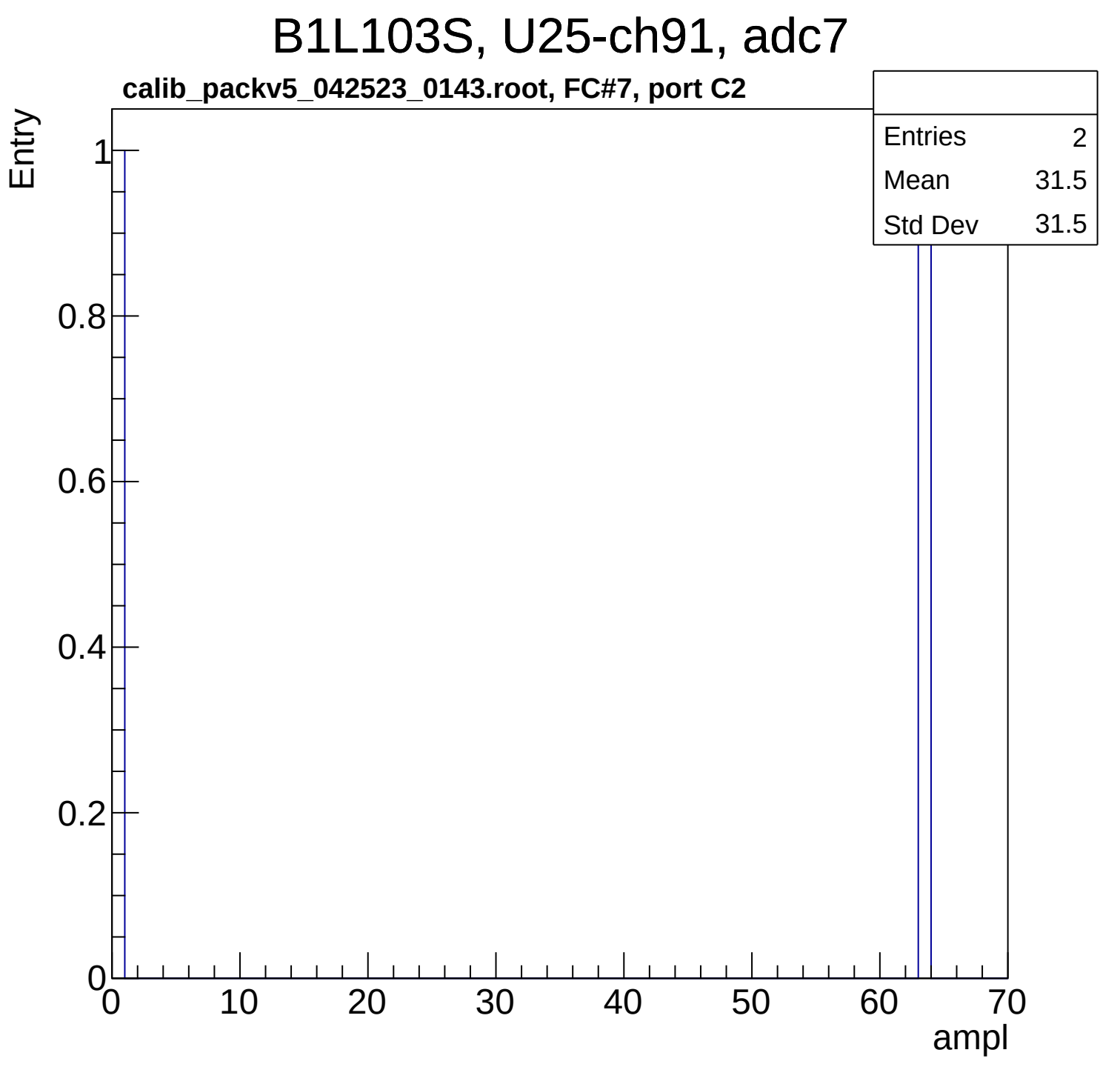
B1L103S, U25-ch91, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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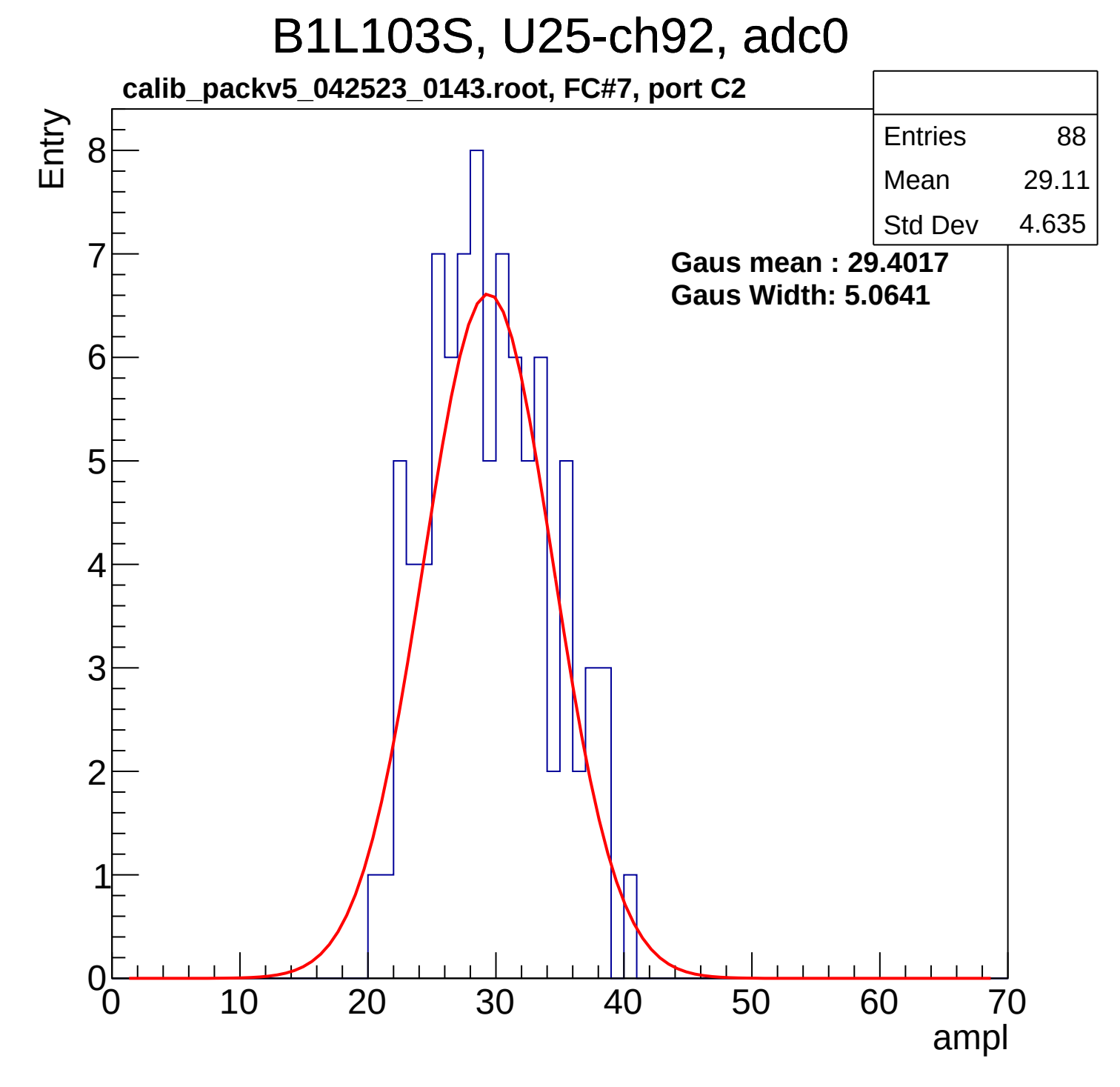
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch92, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 29.11
Std Dev 4.635
Gaus mean : 29.4017
Gaus Width: 5.0641
Entry
ampl
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1
2
3
4
5
6
7
8
0
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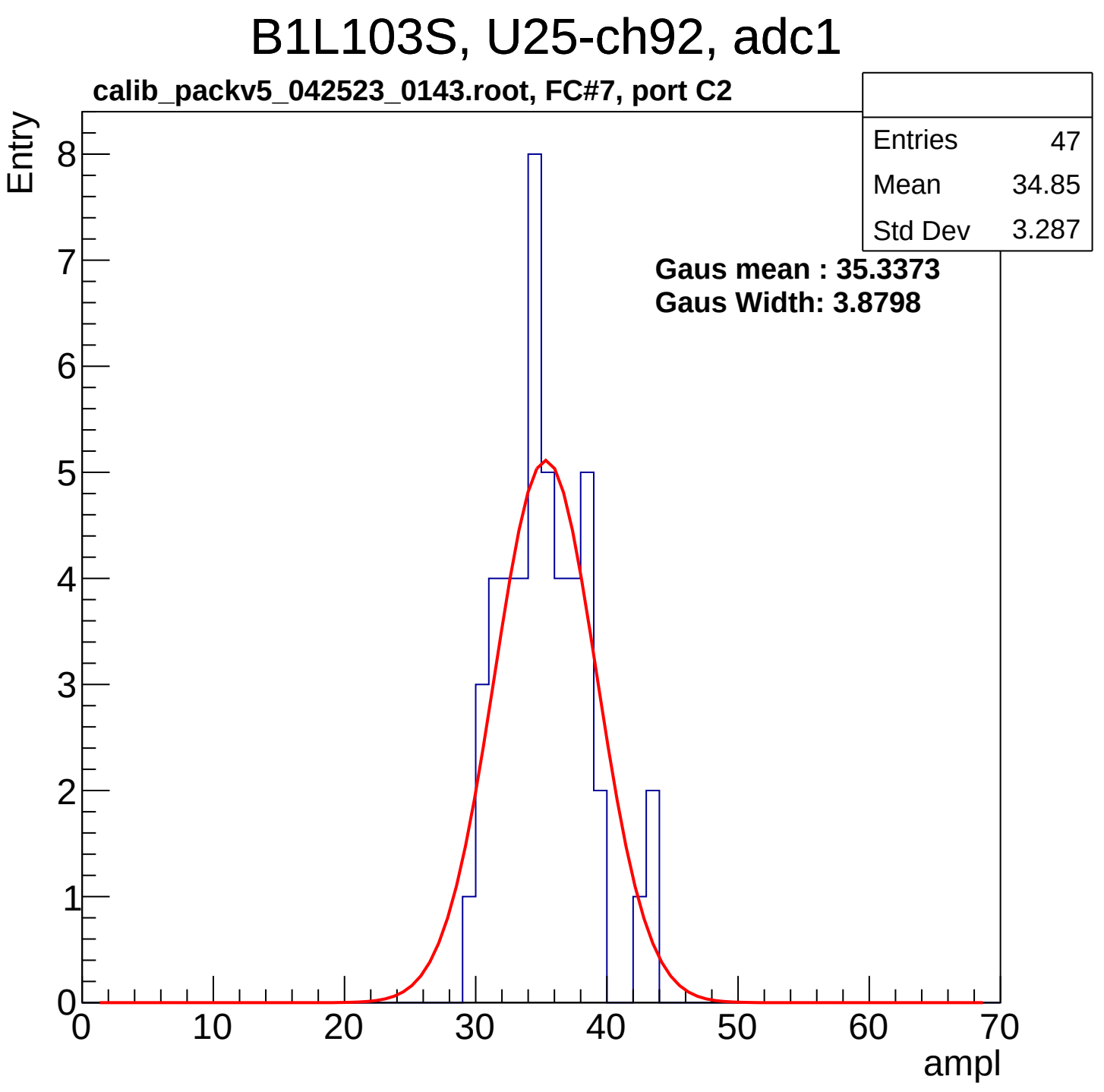
20
30
40
50
60
70


B1L103S, U25-ch92, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 34.85
Std Dev 3.287
Gaus mean : 35.3373
Gaus Width: 3.8798
Entry
ampl
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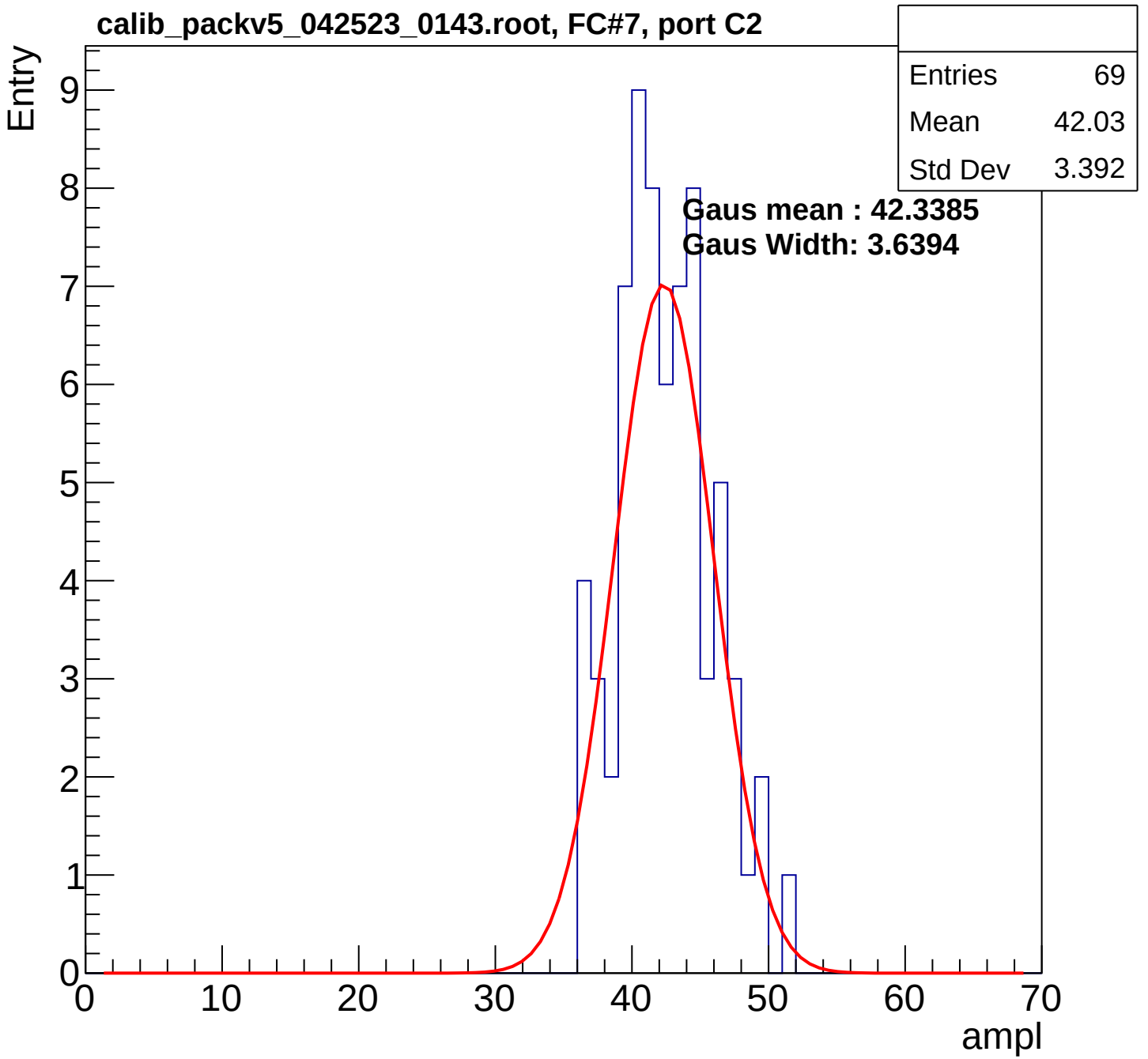
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch92, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 42.03
Std Dev 3.392
Gaus mean : 42.3385
Gaus Width: 3.6394
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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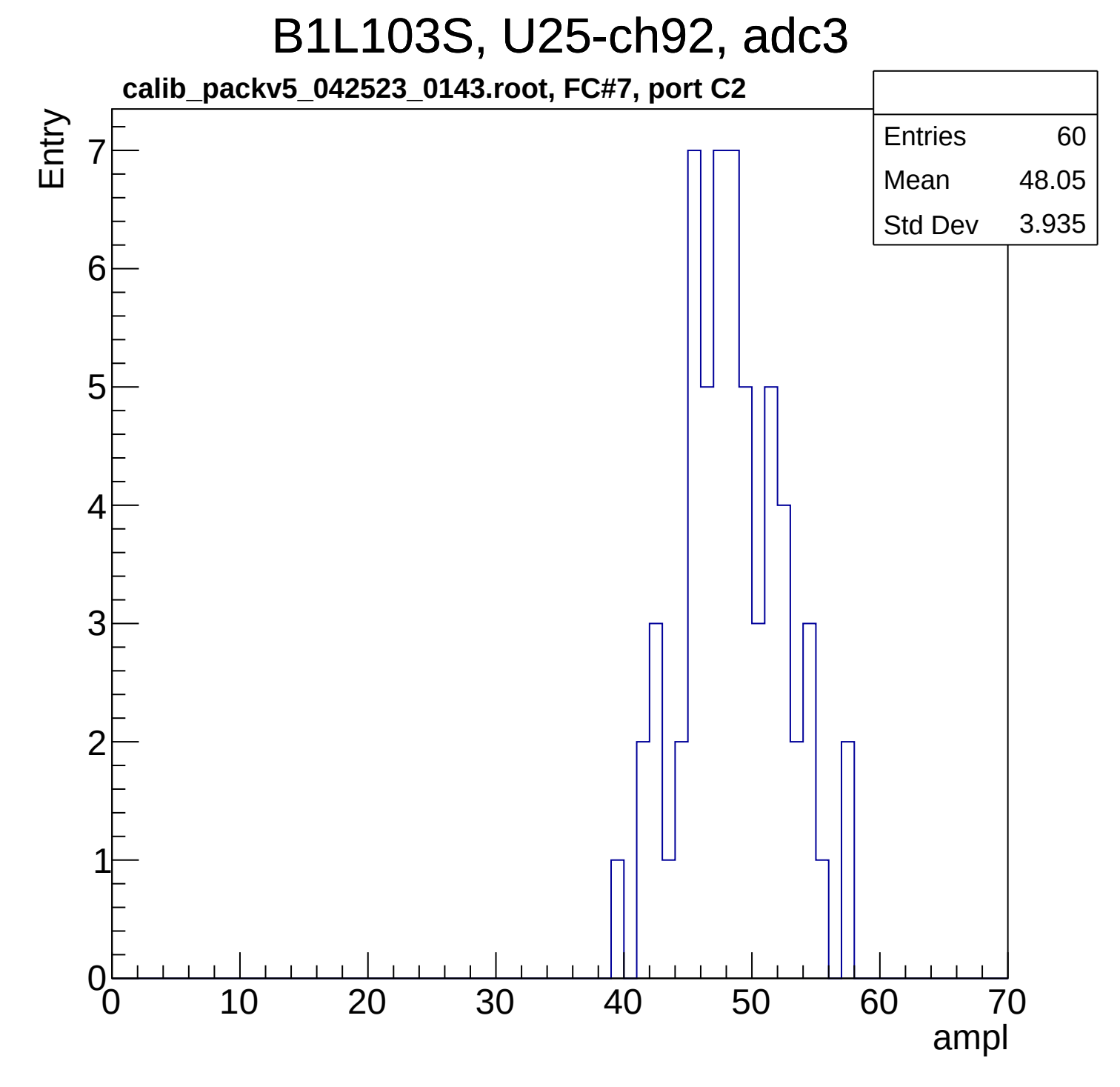
B1L103S, U25-ch92, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 48.05
Std Dev 3.935
Entry
ampl
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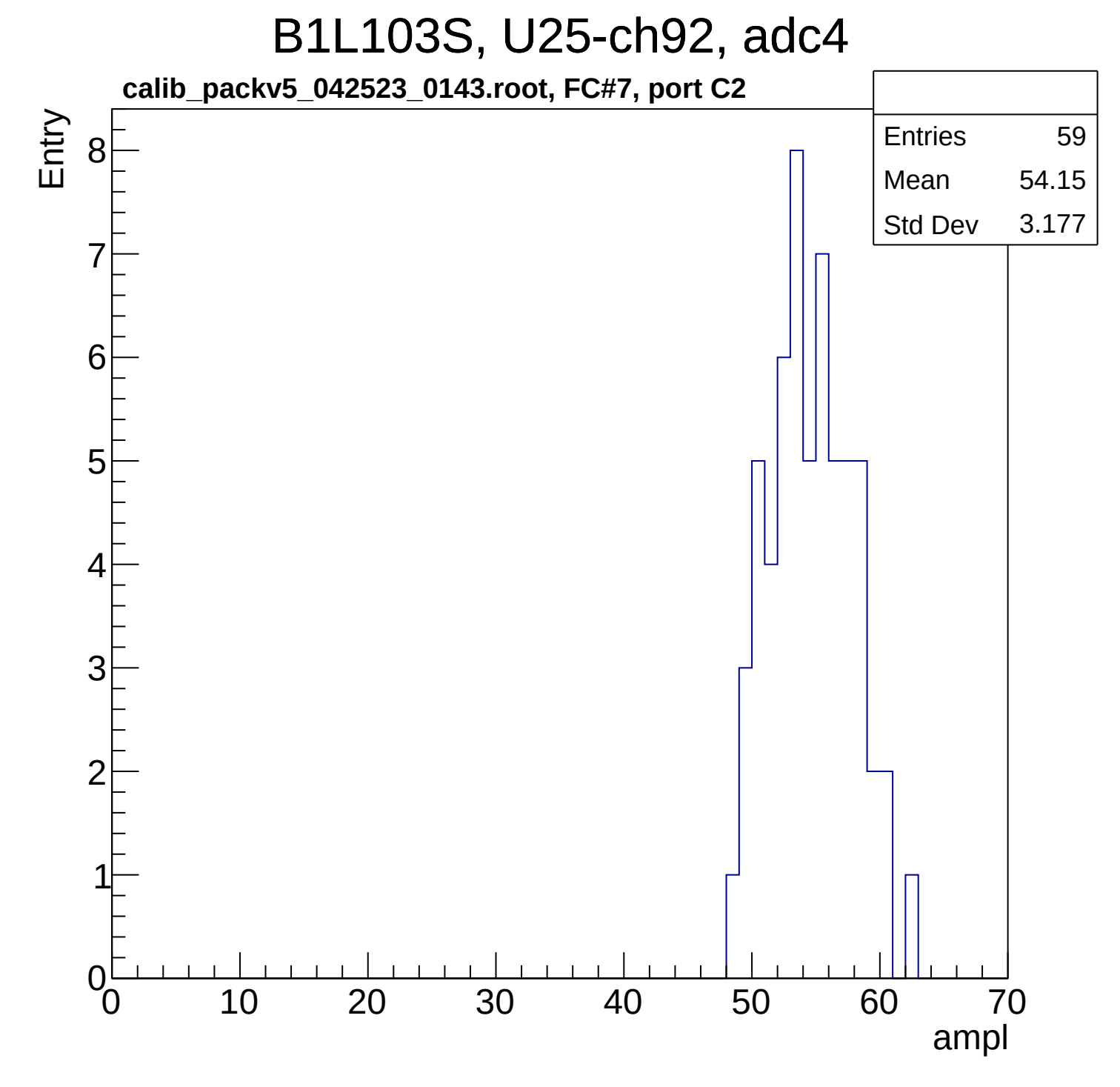
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch92, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.15
Std Dev 3.177
Entry
ampl
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2
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4
5
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7
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50
60
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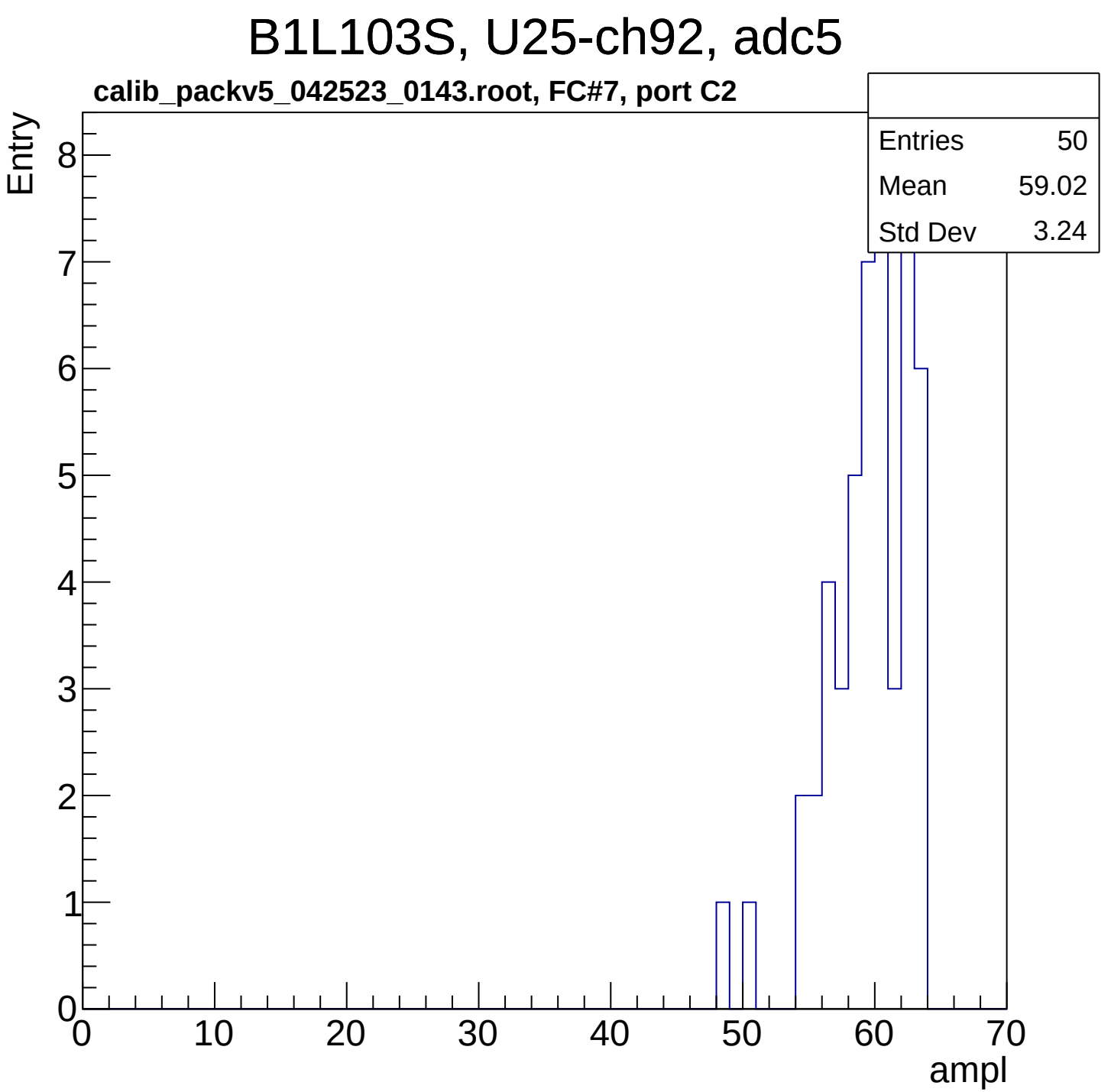
B1L103S, U25-ch92, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 59.02
Std Dev 3.24
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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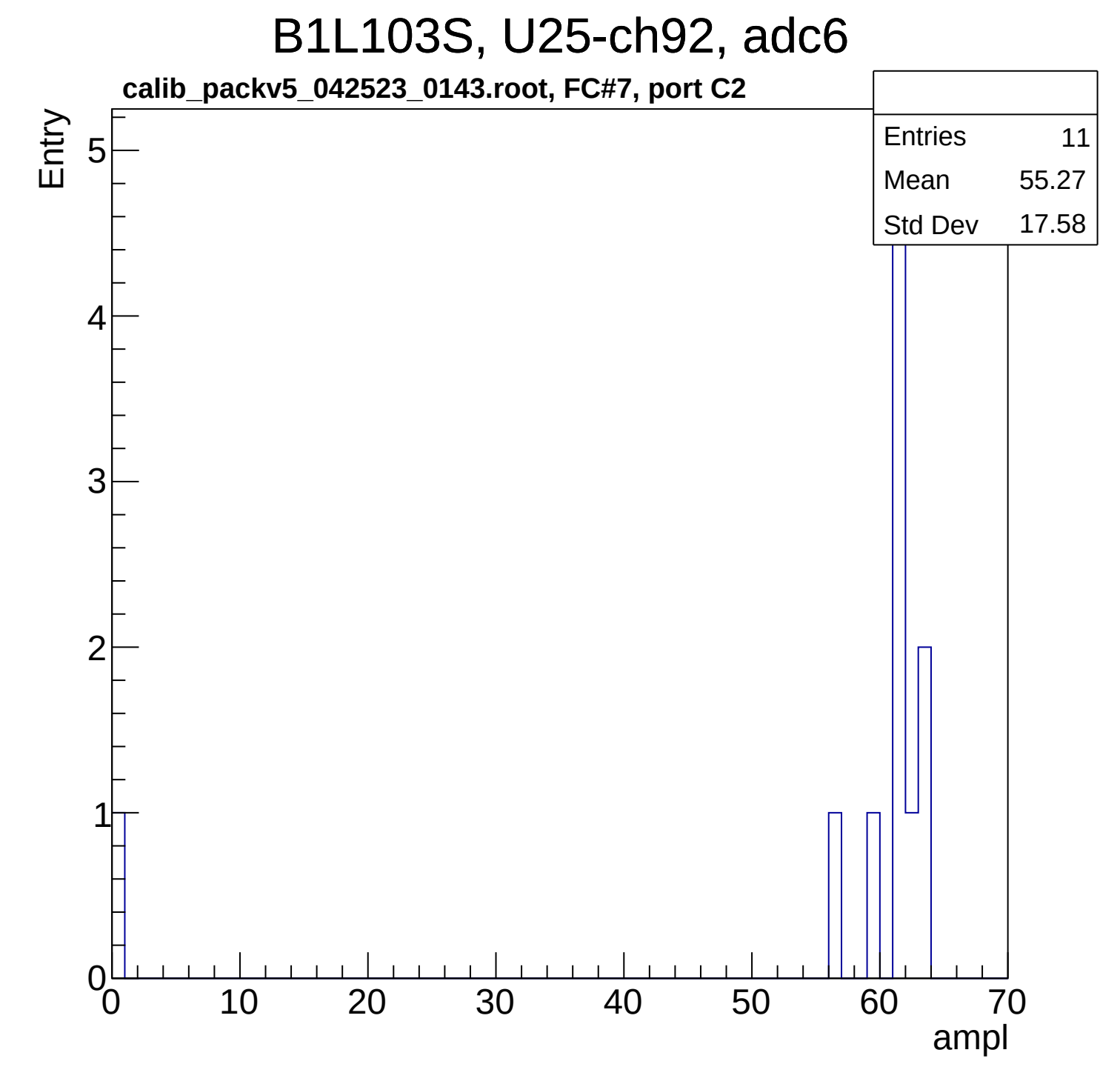
40
50
60
70


B1L103S, U25-ch92, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 55.27
Std Dev 17.58
Entry
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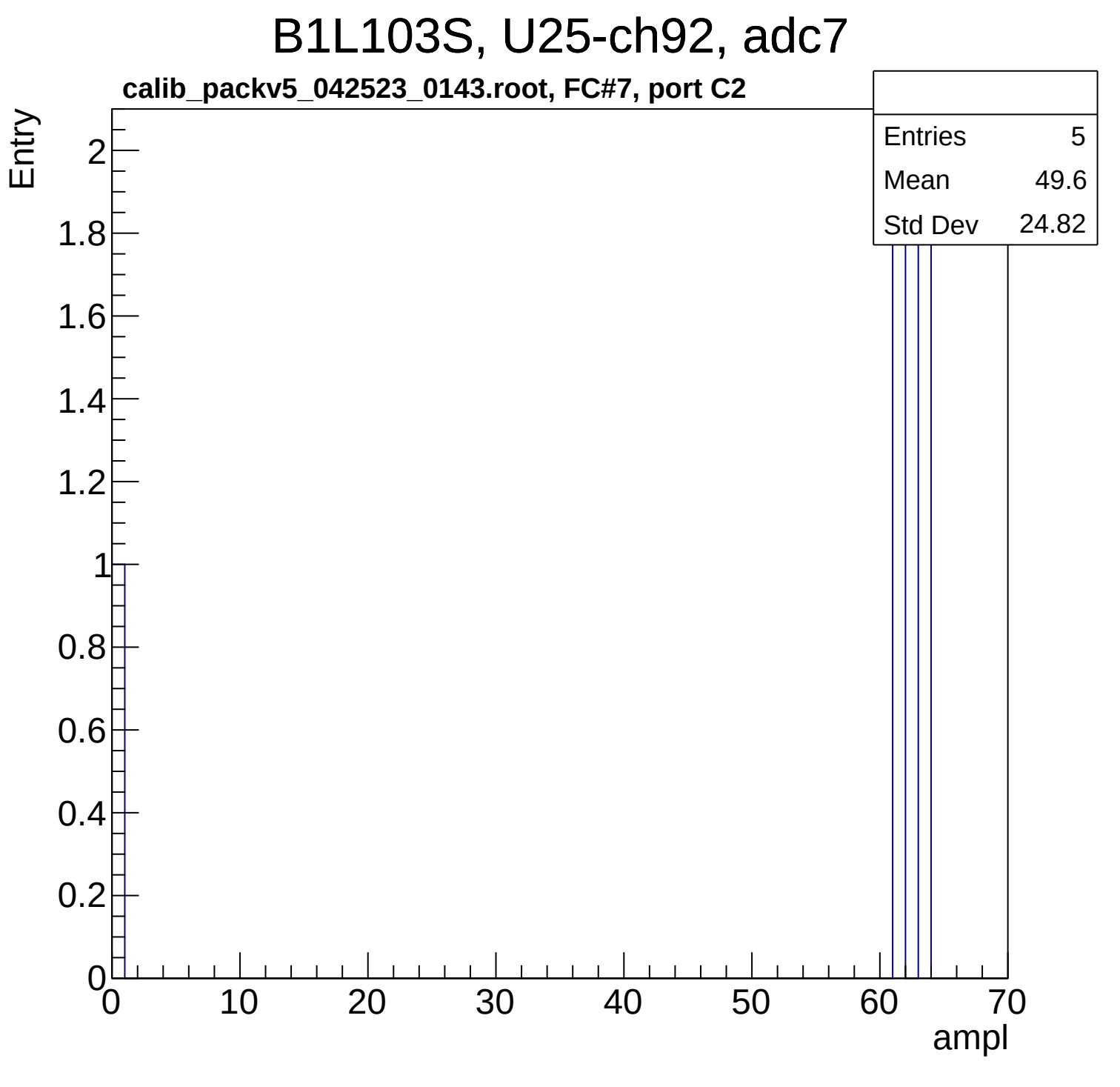
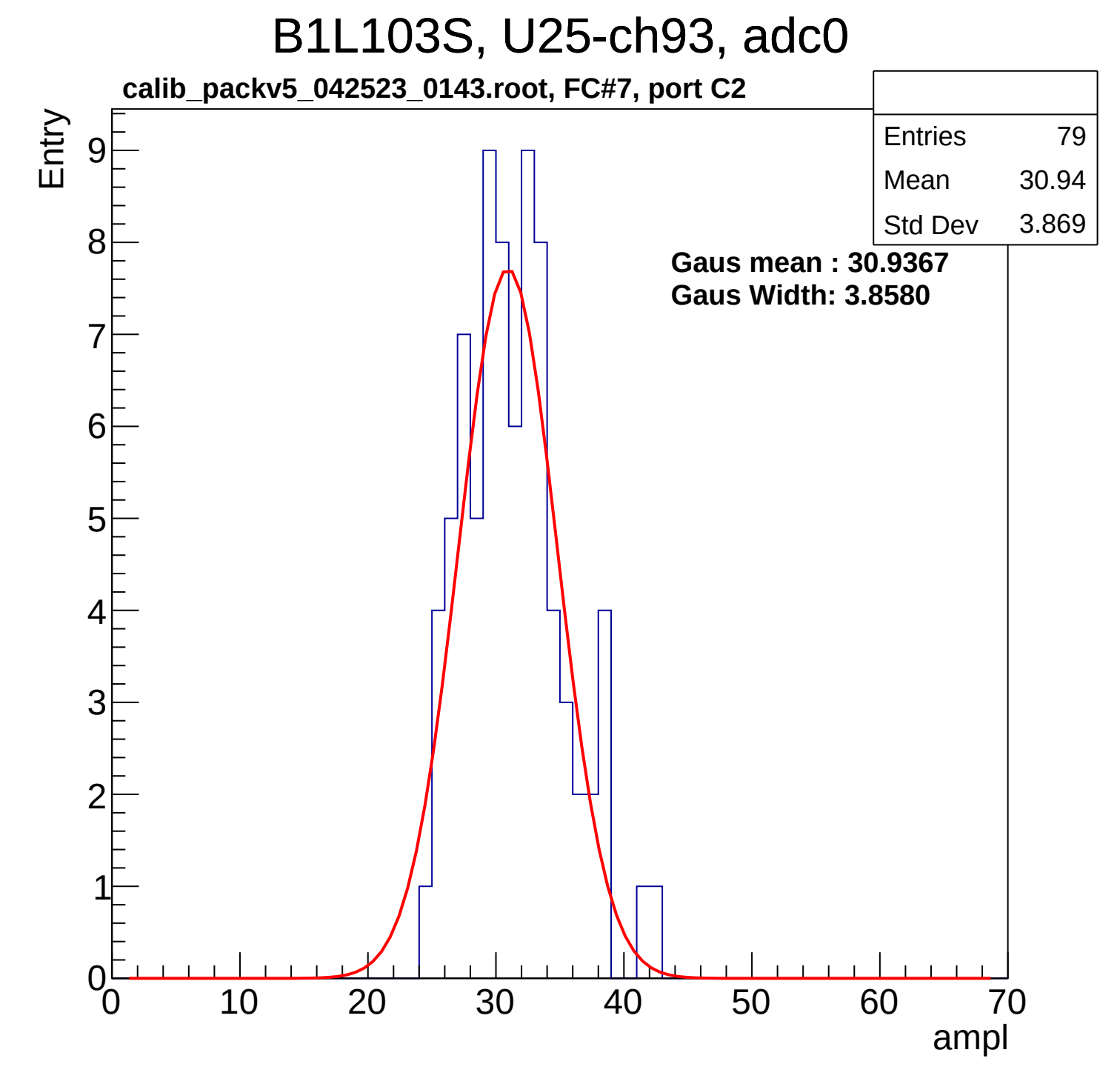
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch92, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.6
Std Dev 24.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch93, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 30.94
Std Dev 3.869
Gaus mean : 30.9367
Gaus Width: 3.8580
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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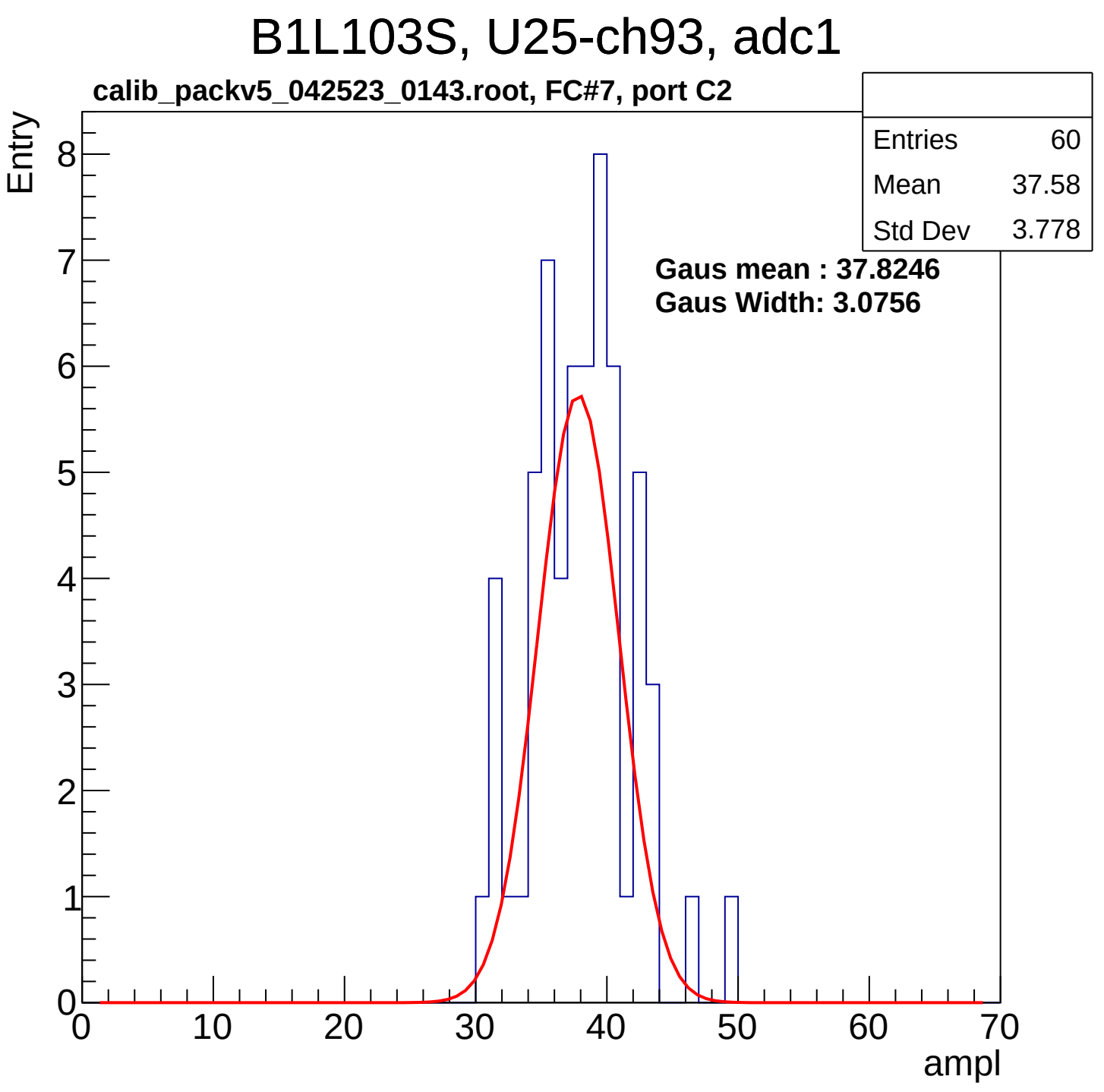
B1L103S, U25-ch93, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 37.58
Std Dev 3.778
Gaus mean : 37.8246
Gaus Width: 3.0756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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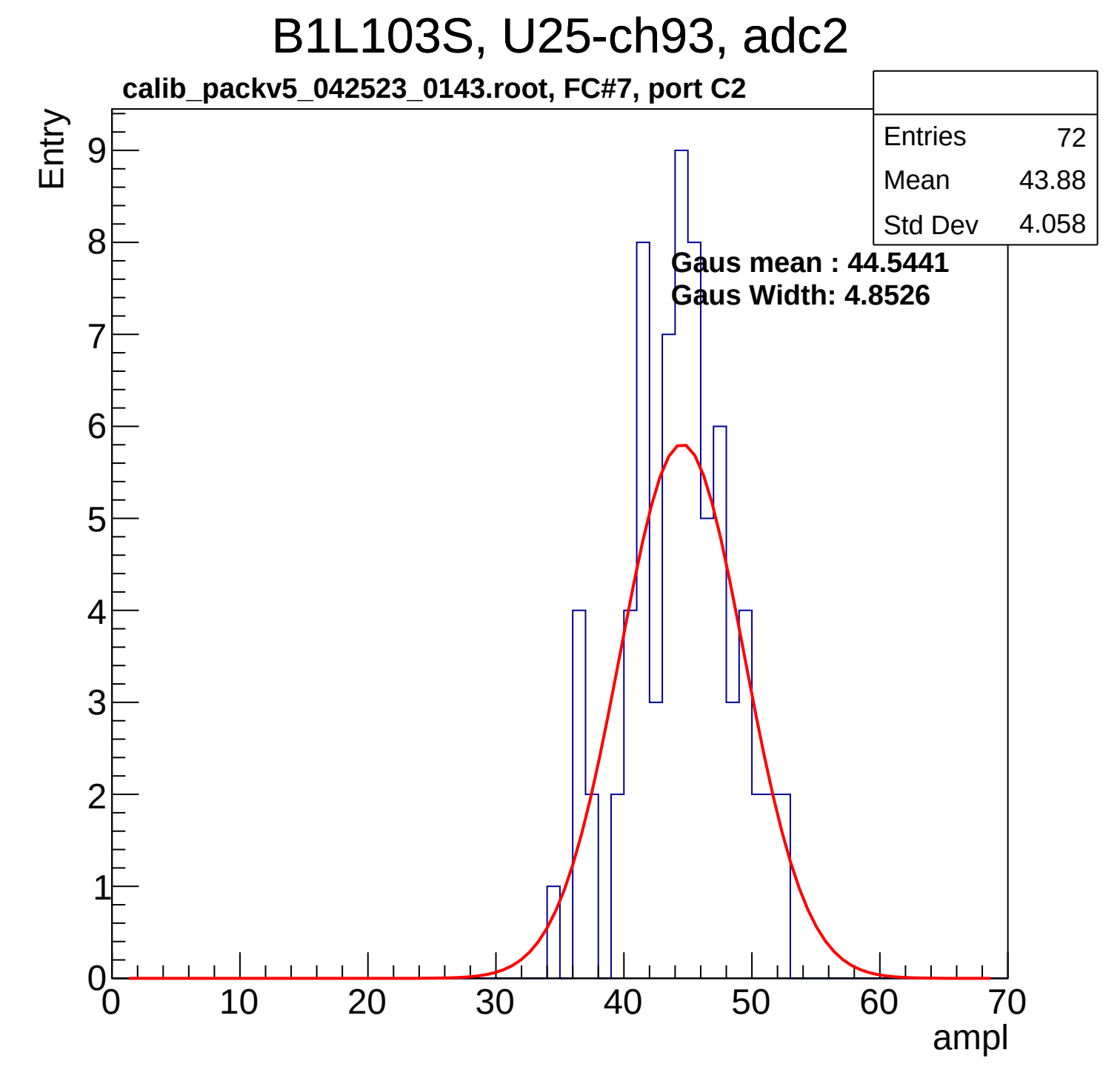
B1L103S, U25-ch93, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 43.88
Std Dev 4.058
Gaus mean : 44.5441
Gaus Width: 4.8526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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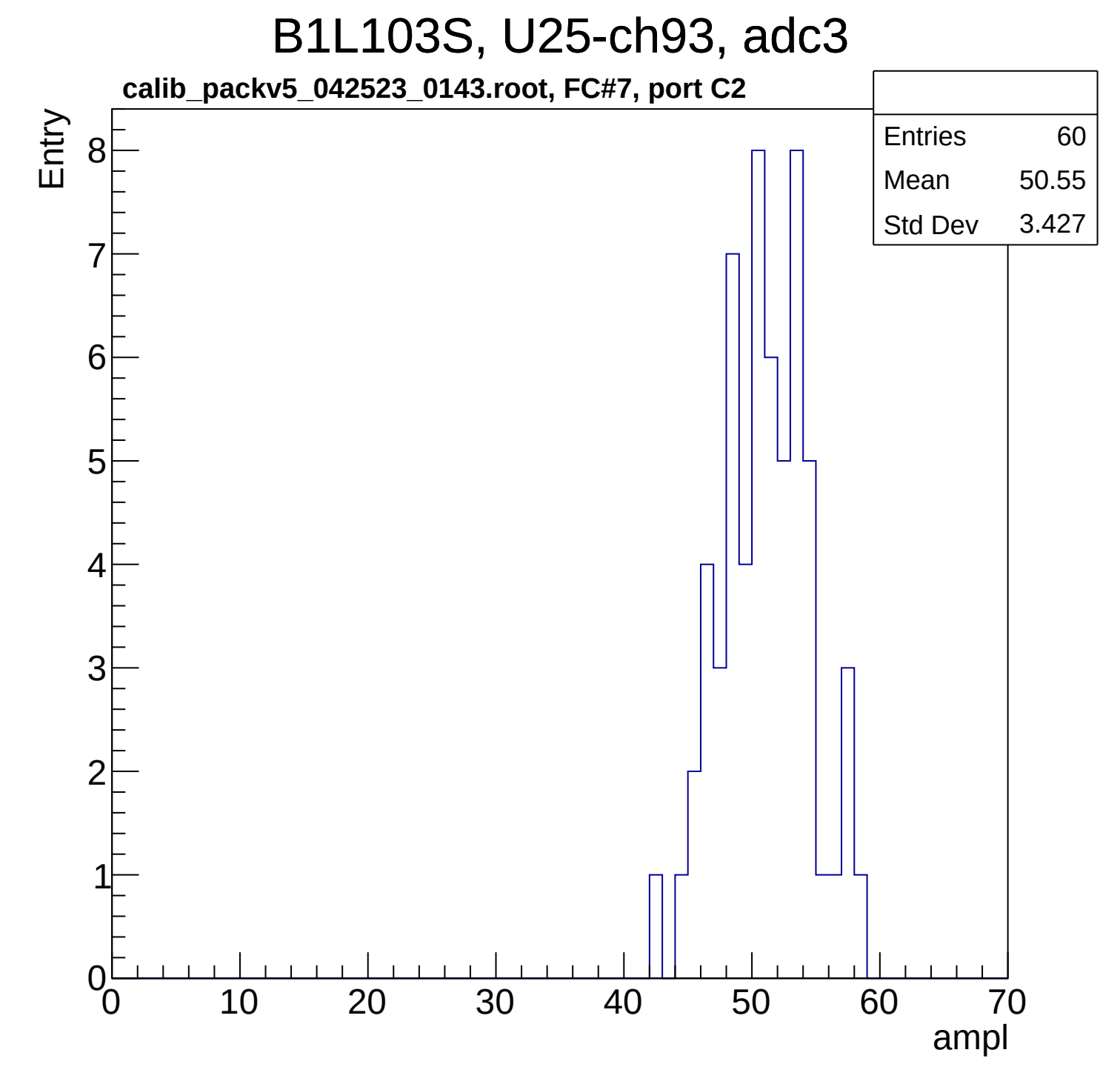
B1L103S, U25-ch93, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 50.55
Std Dev 3.427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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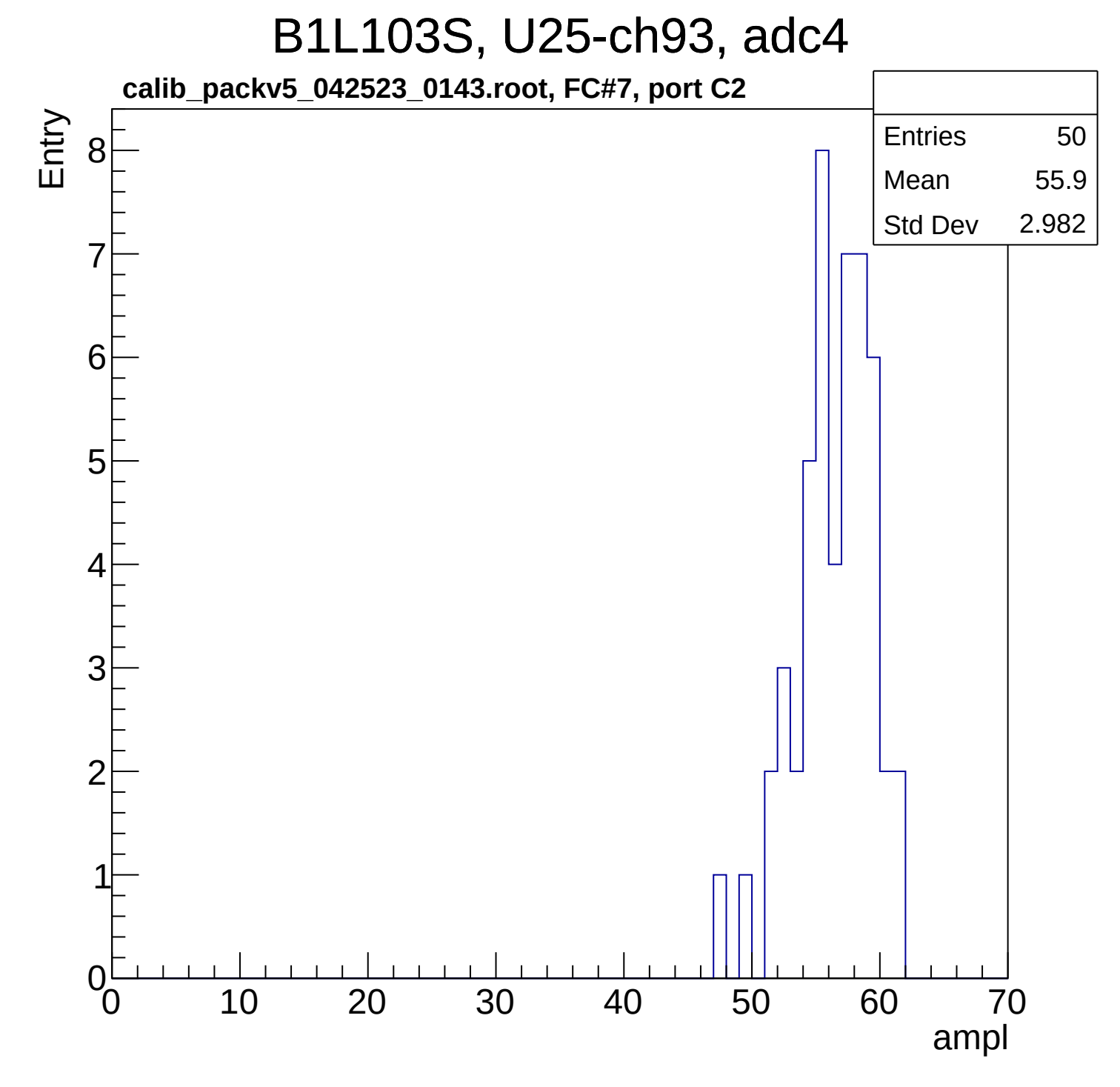
B1L103S, U25-ch93, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 55.9
Std Dev 2.982
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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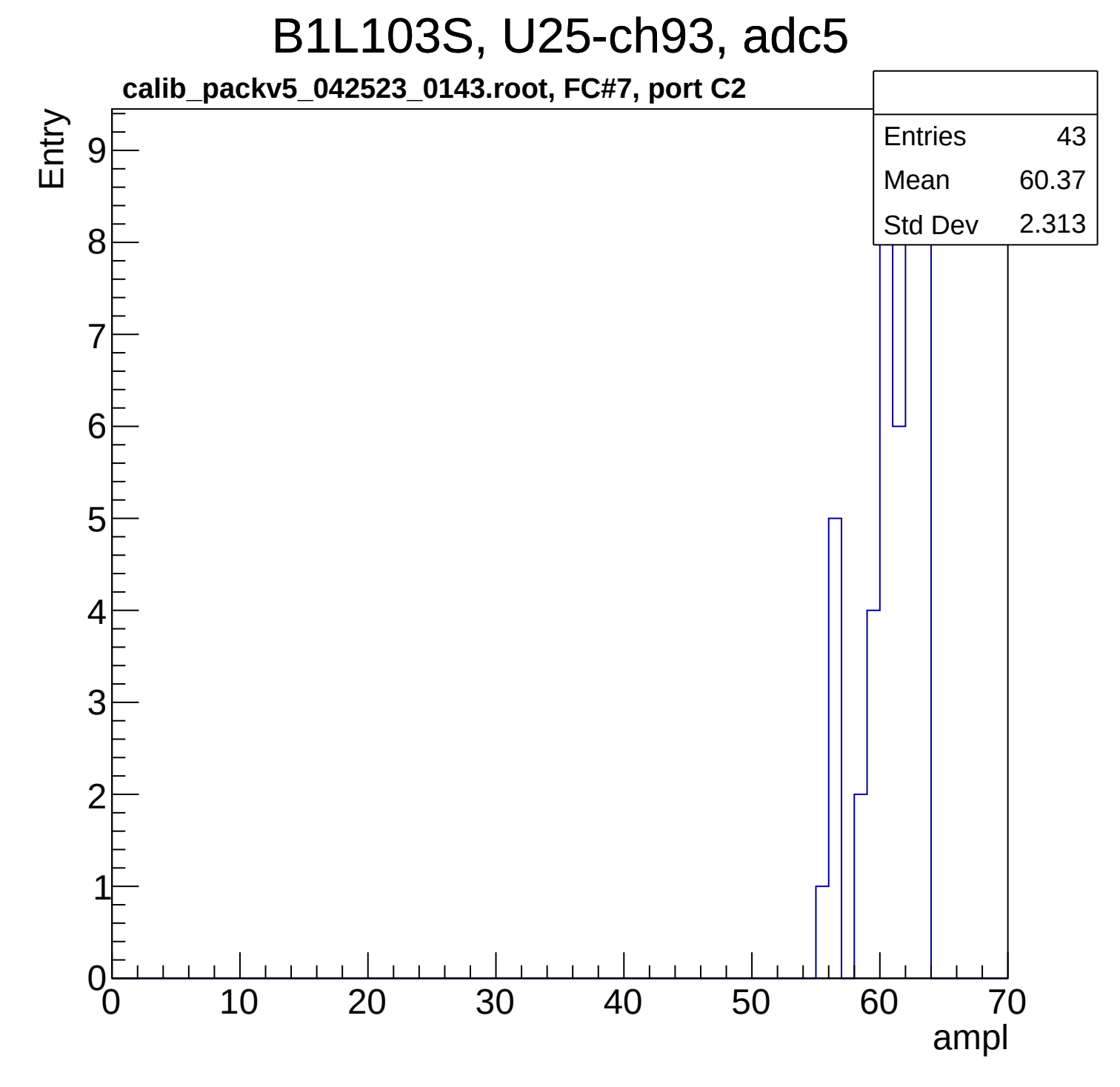
B1L103S, U25-ch93, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 60.37
Std Dev 2.313
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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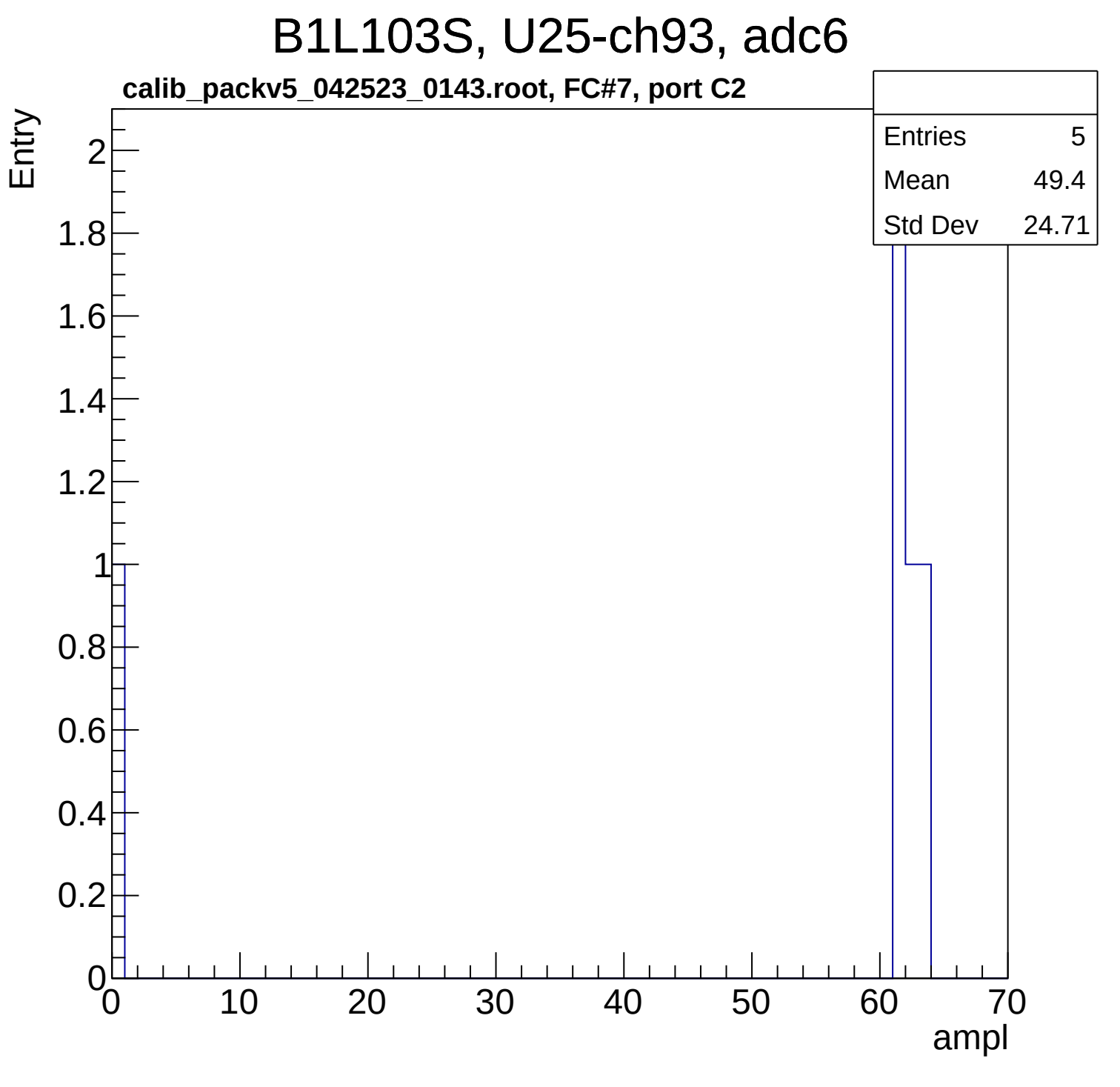
B1L103S, U25-ch93, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49.4
Std Dev 24.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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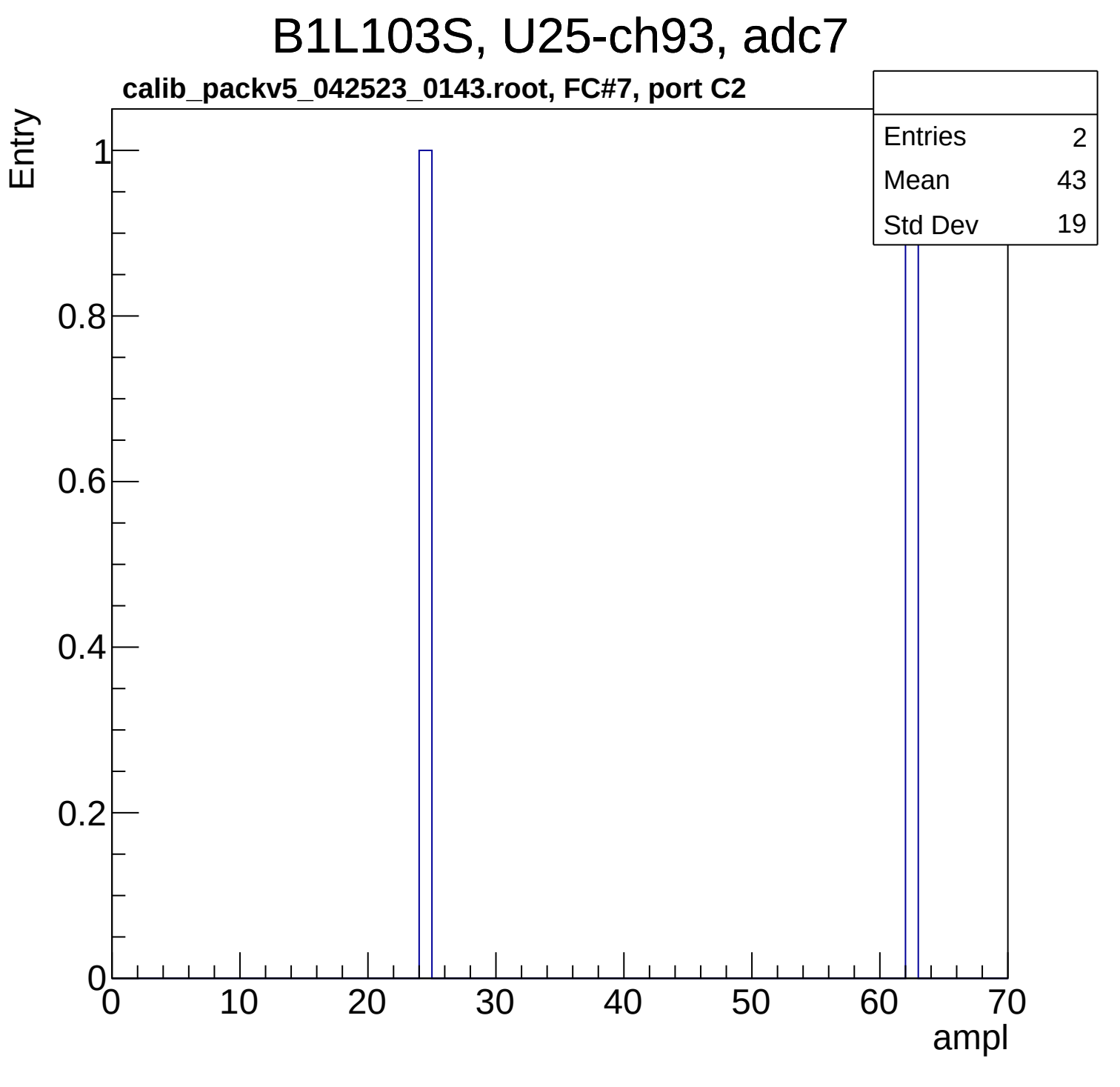
B1L103S, U25-ch93, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 43
Std Dev 19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
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60
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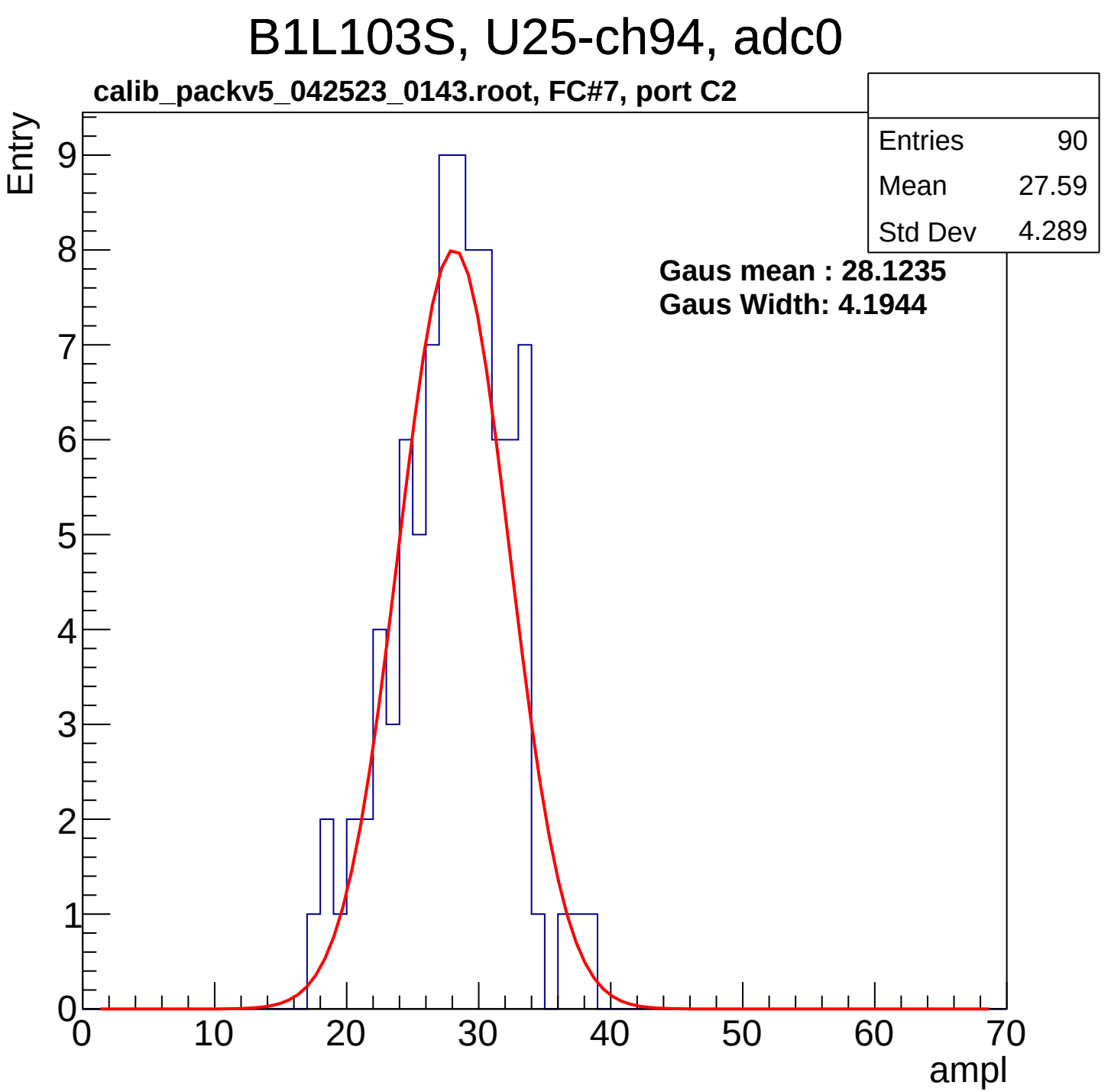
B1L103S, U25-ch94, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 27.59
Std Dev 4.289
Gaus mean : 28.1235
Gaus Width: 4.1944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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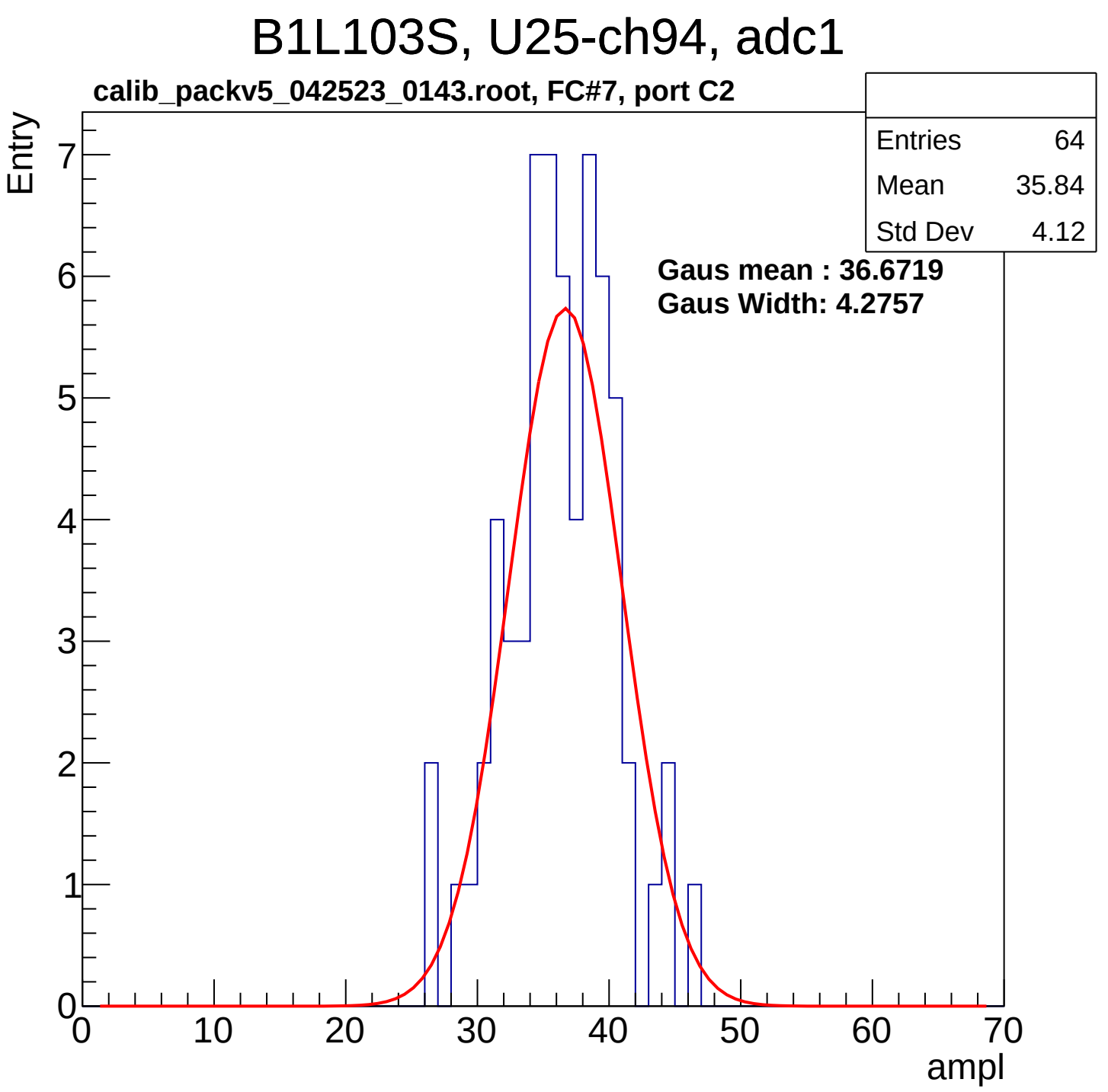
B1L103S, U25-ch94, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 35.84
Std Dev 4.12
Gaus mean : 36.6719
Gaus Width: 4.2757
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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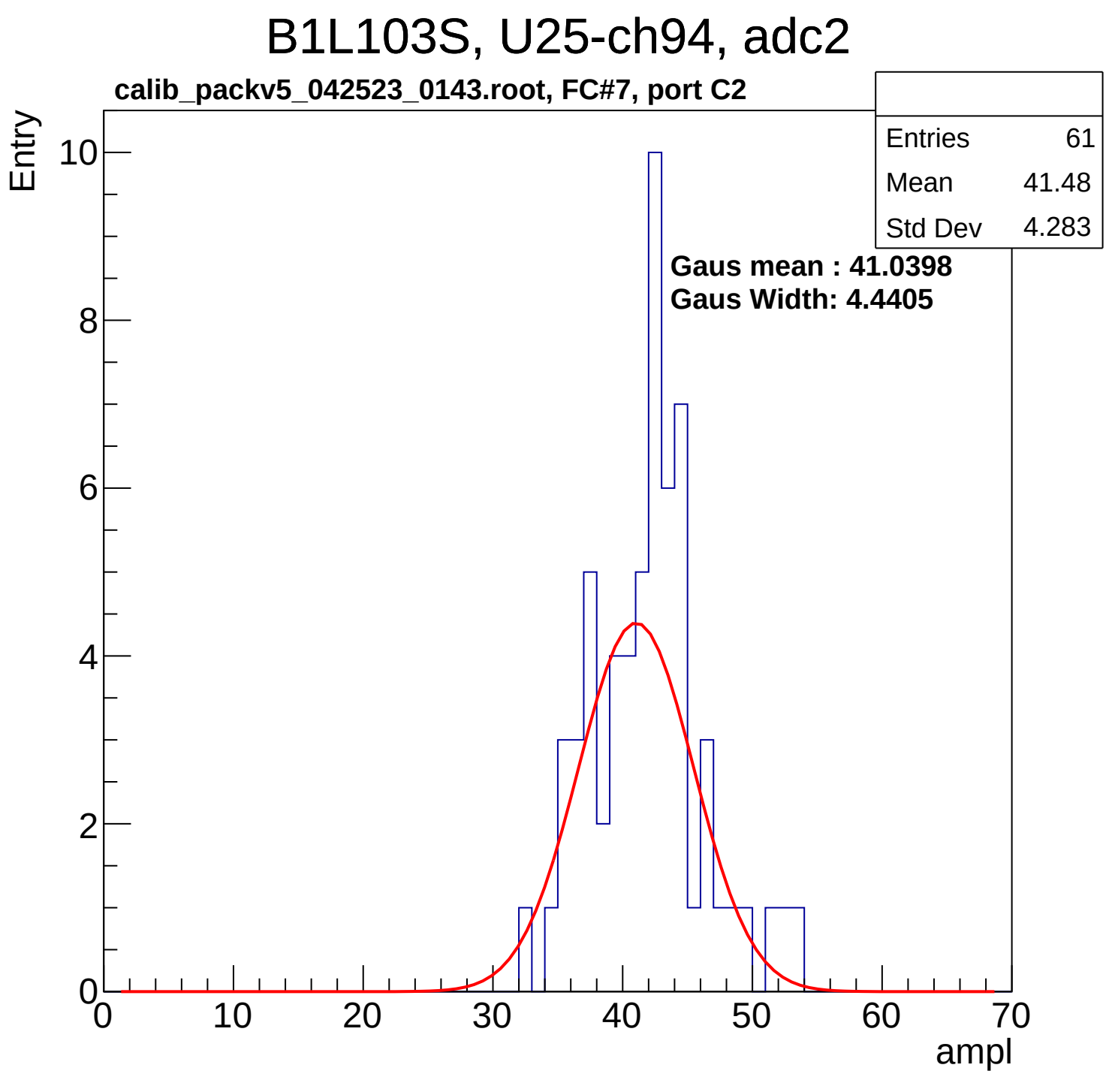
B1L103S, U25-ch94, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 41.48
Std Dev 4.283
Gaus mean : 41.0398
Gaus Width: 4.4405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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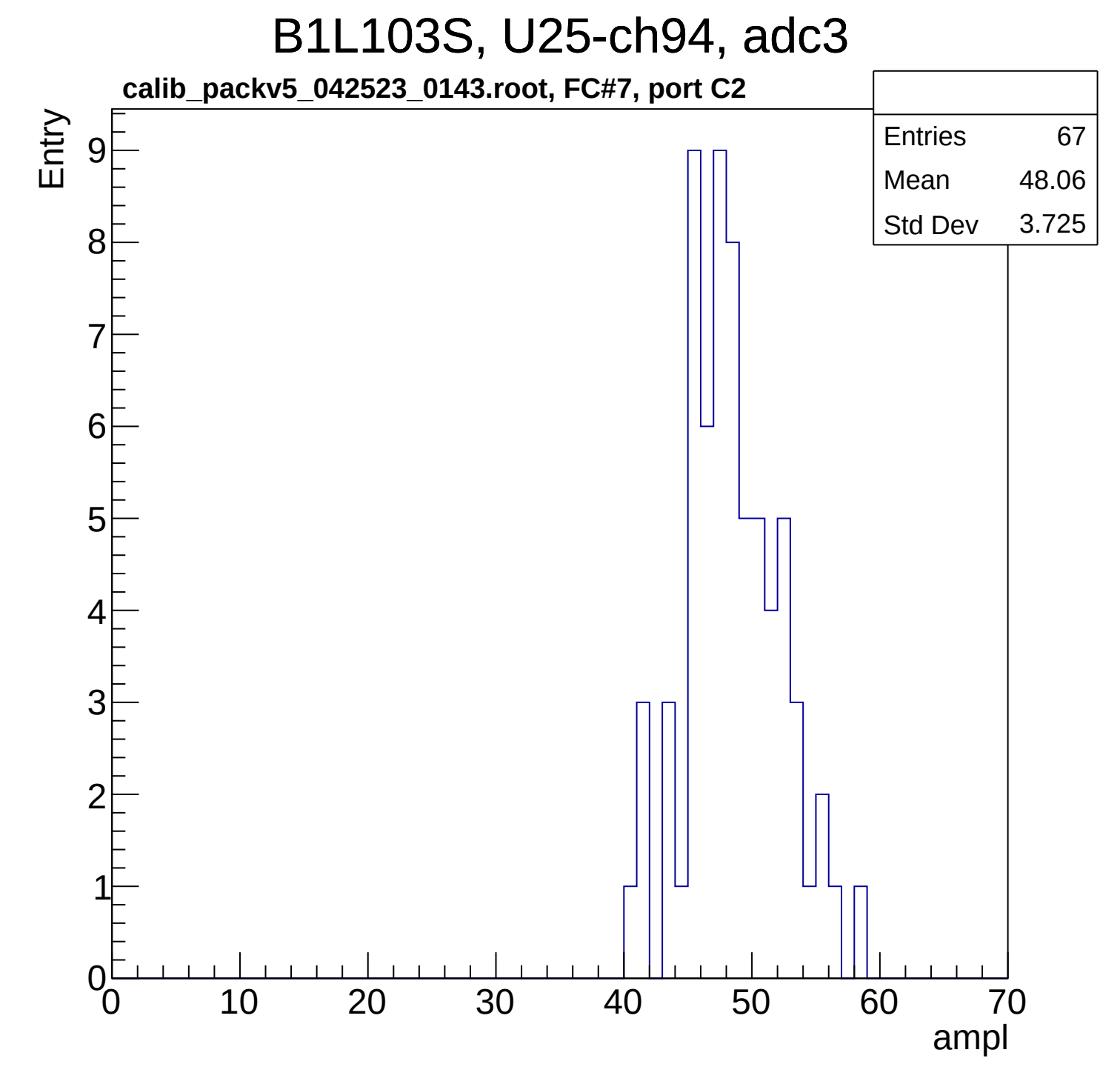
B1L103S, U25-ch94, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.06
Std Dev 3.725
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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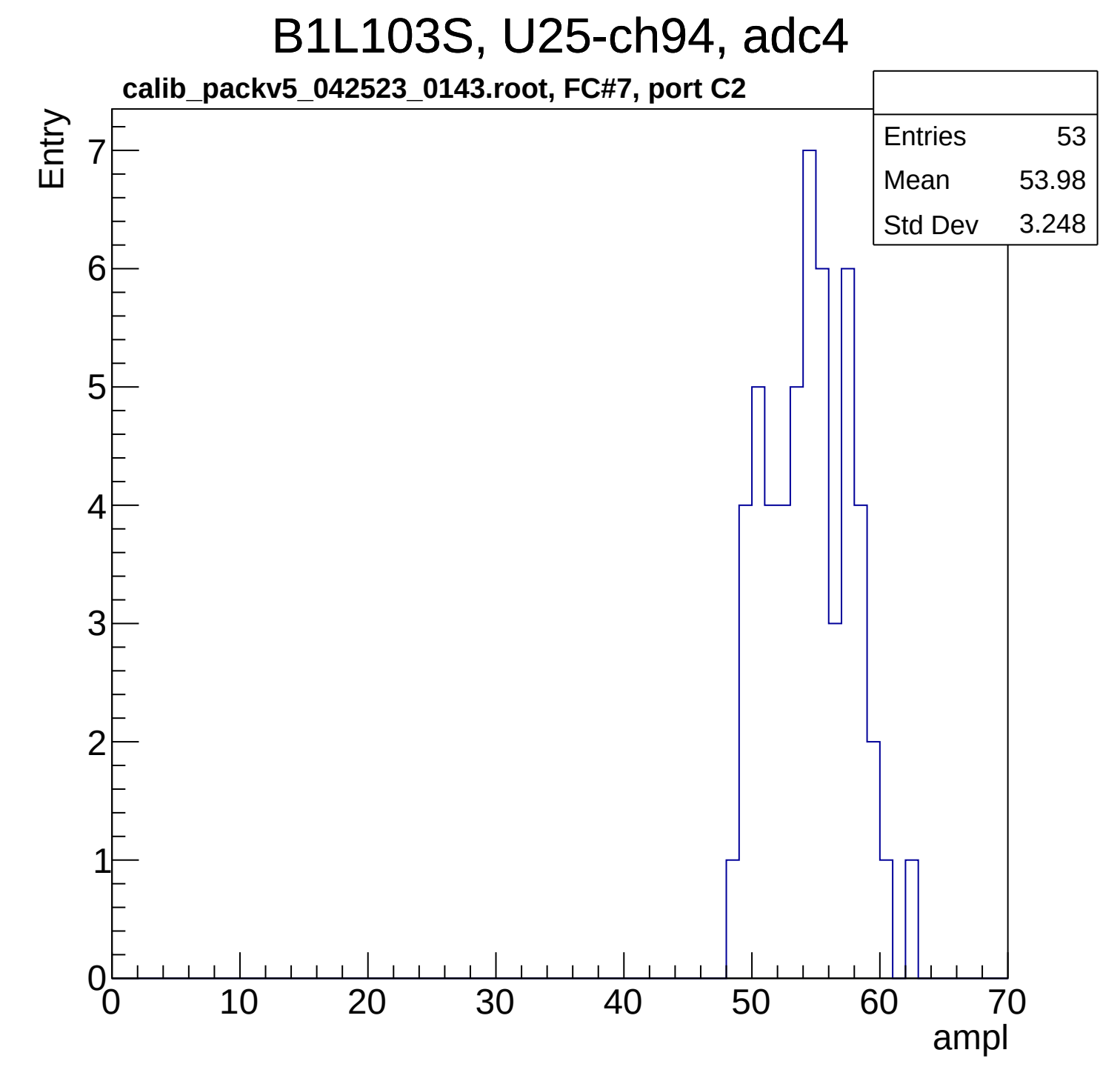
B1L103S, U25-ch94, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 53.98
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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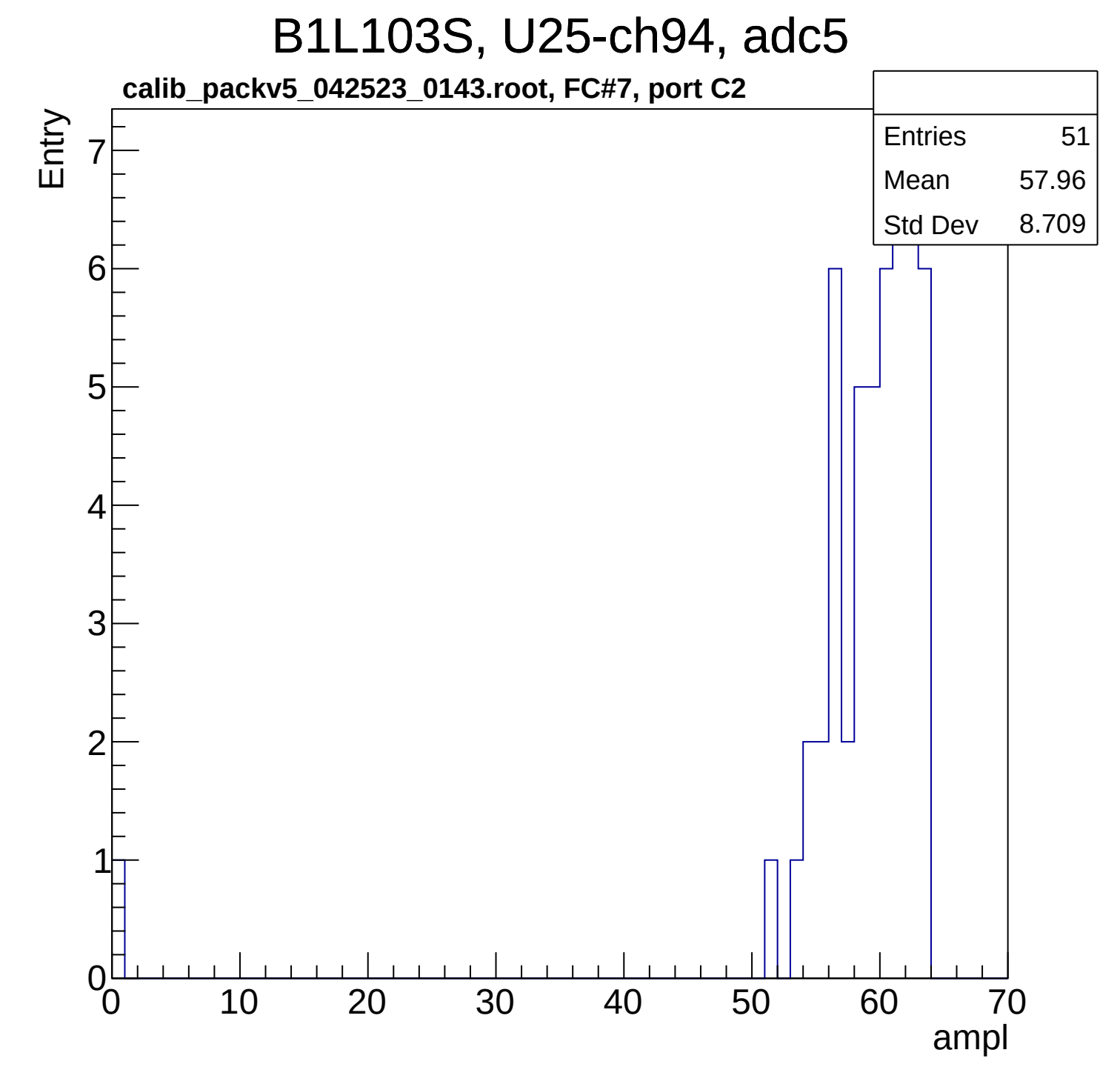
B1L103S, U25-ch94, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.96
Std Dev 8.709
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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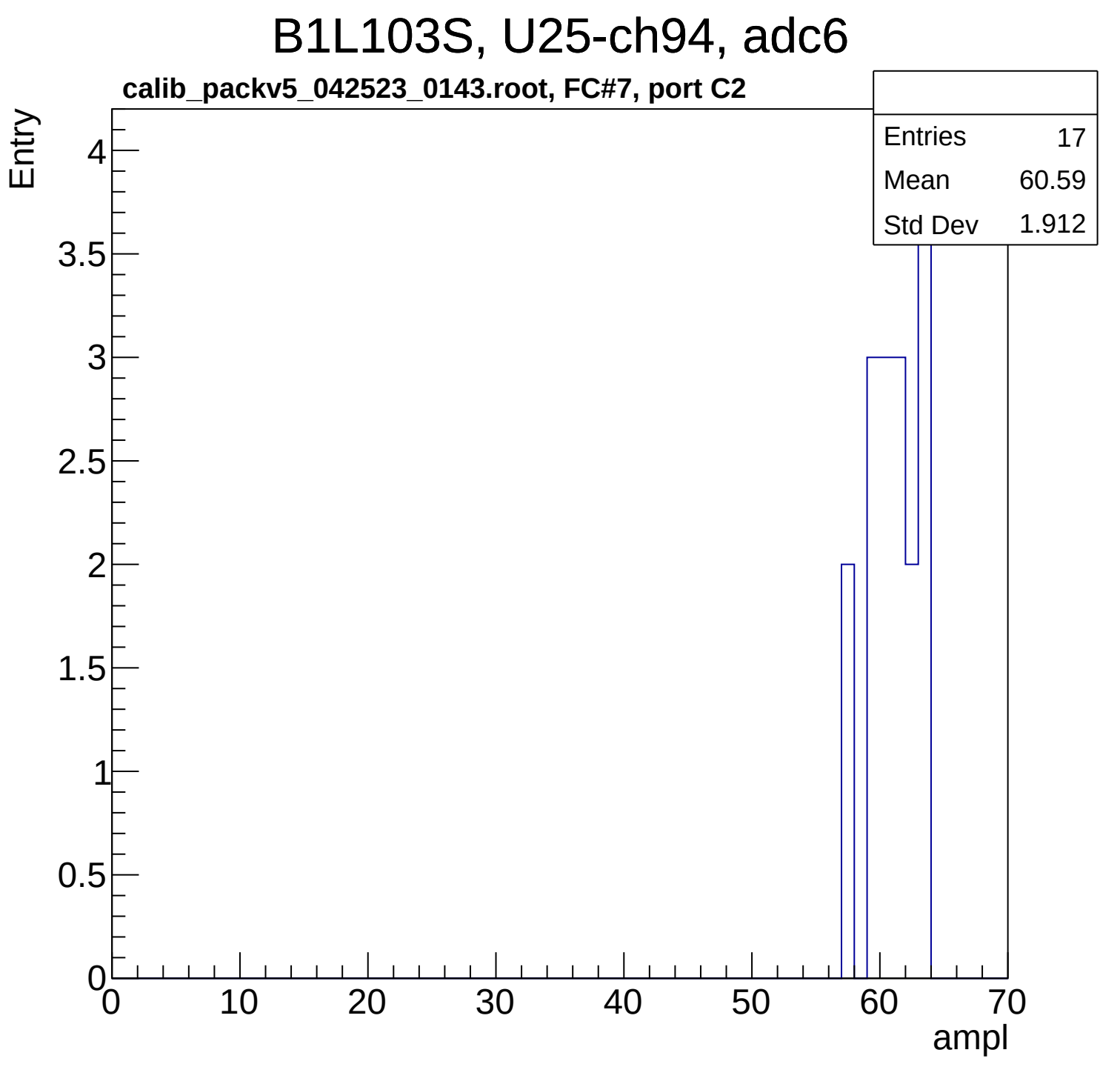
B1L103S, U25-ch94, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 17
Mean 60.59
Std Dev 1.912
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
50
60
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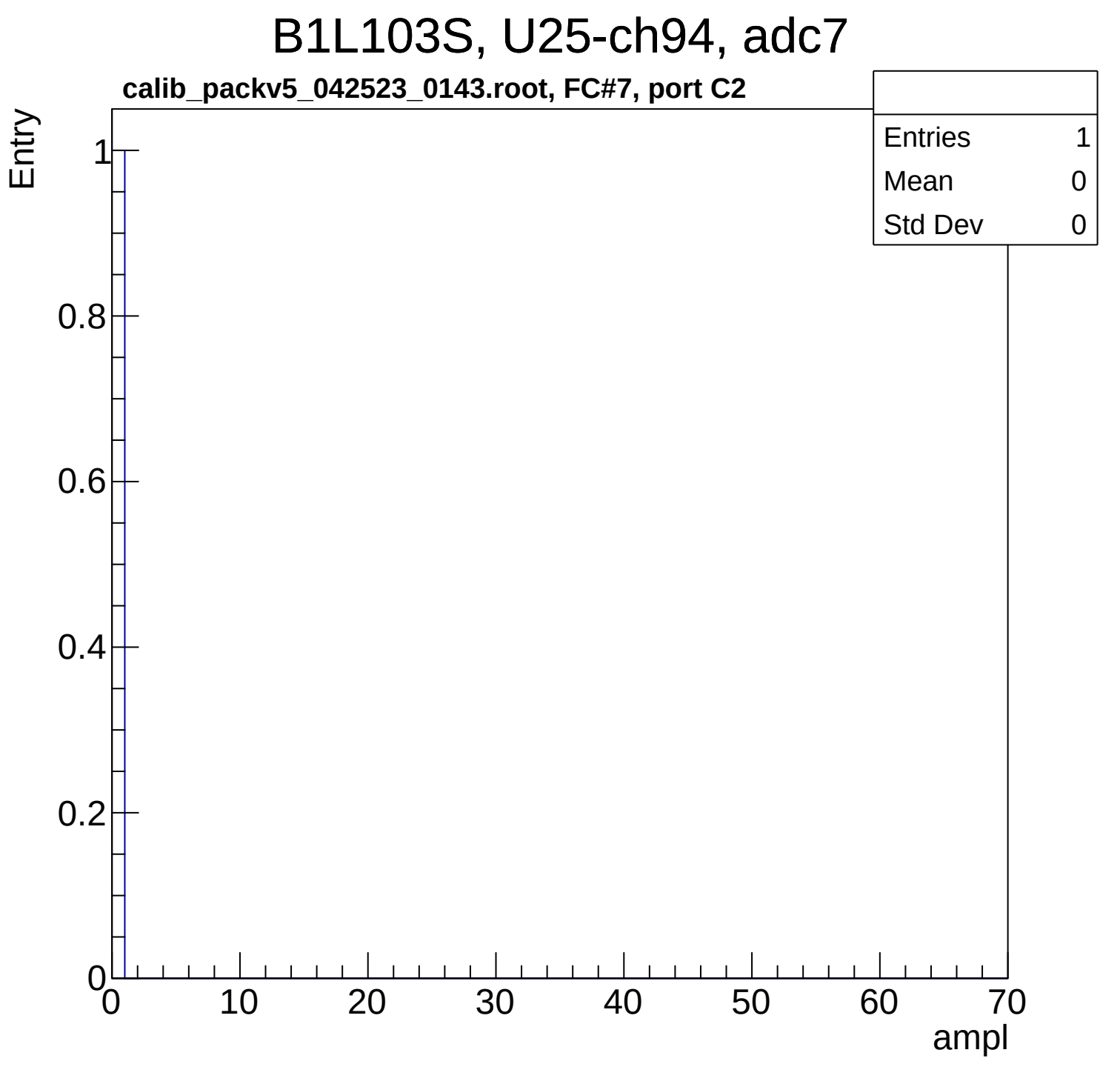
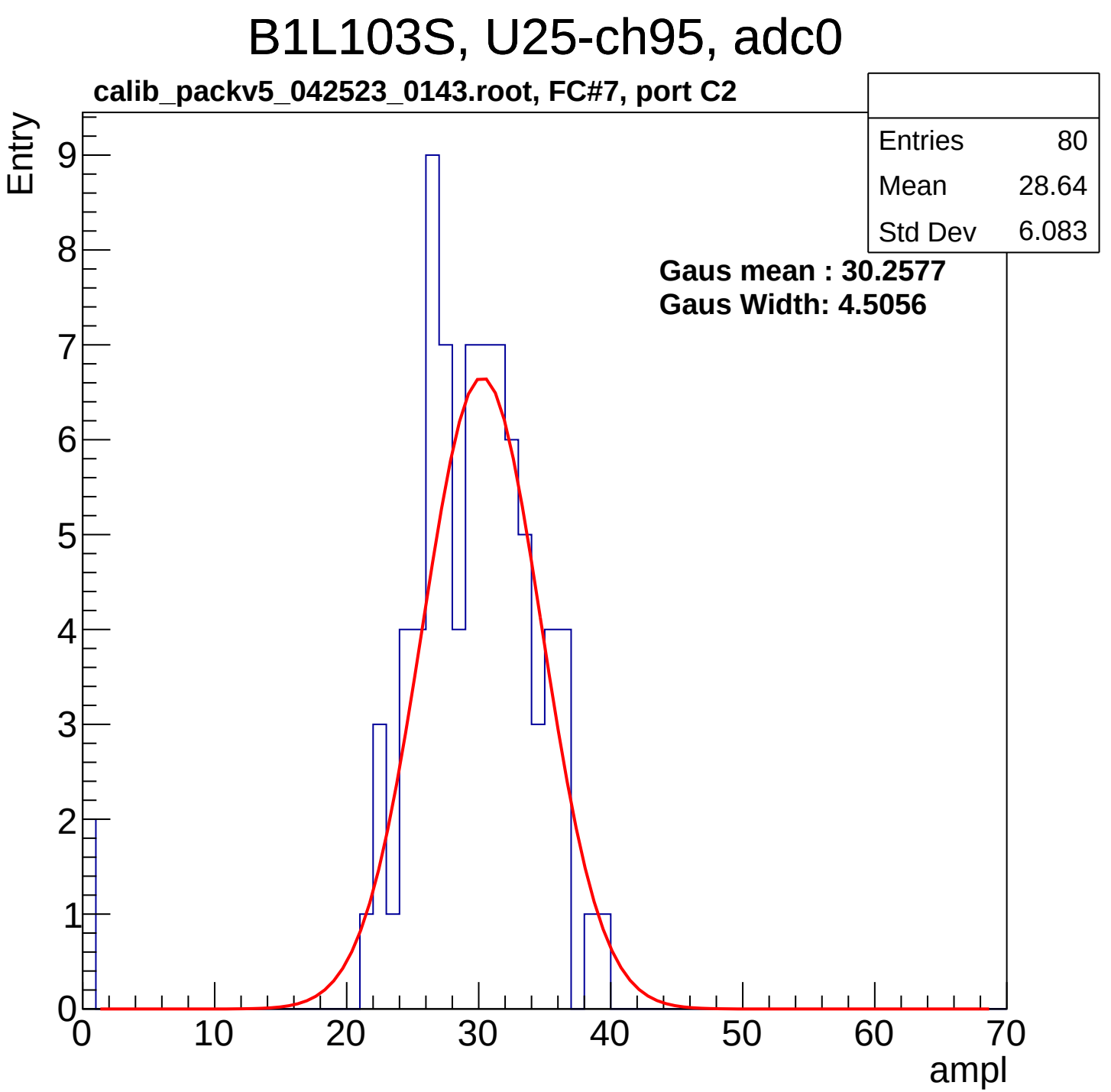
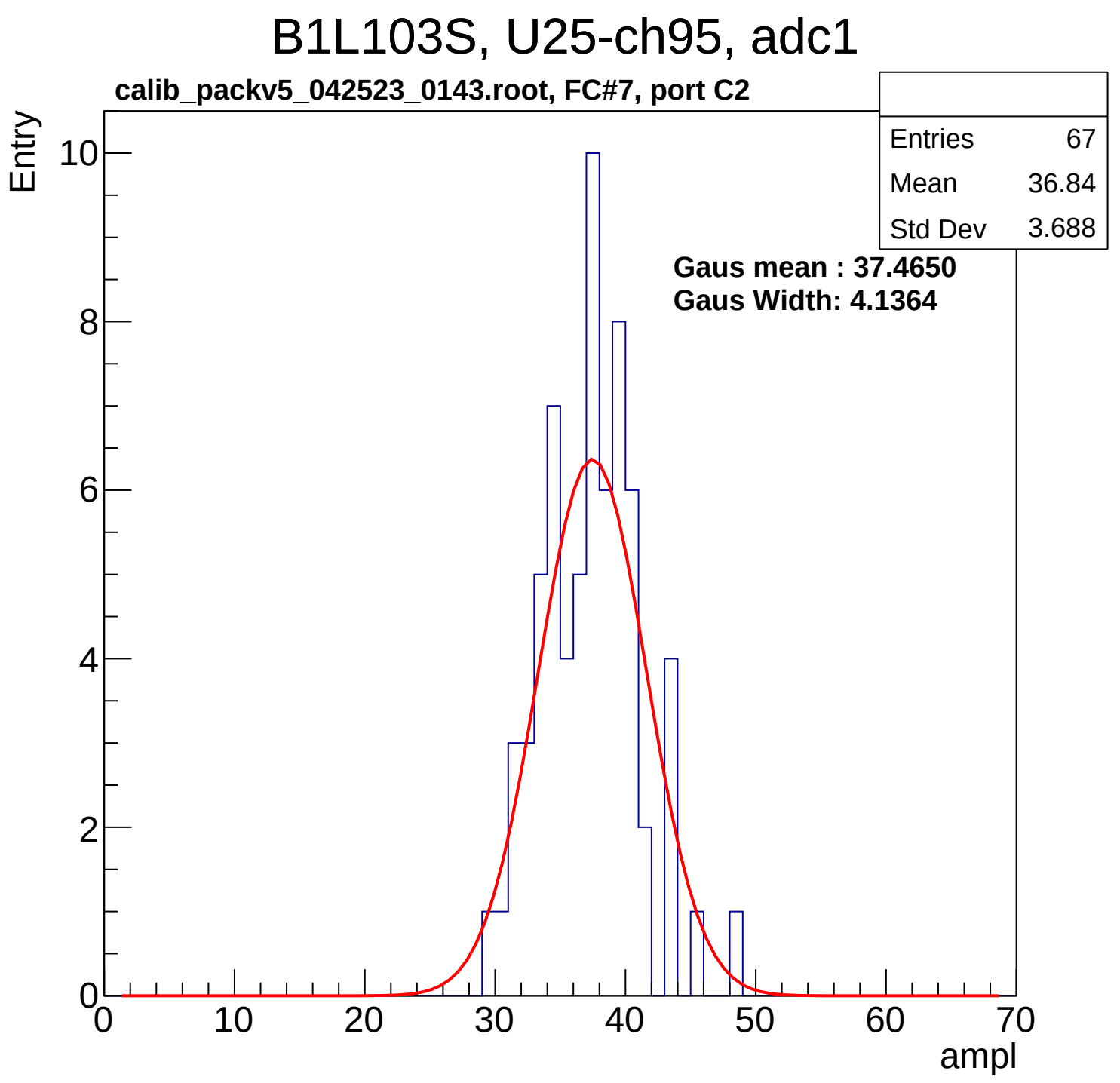
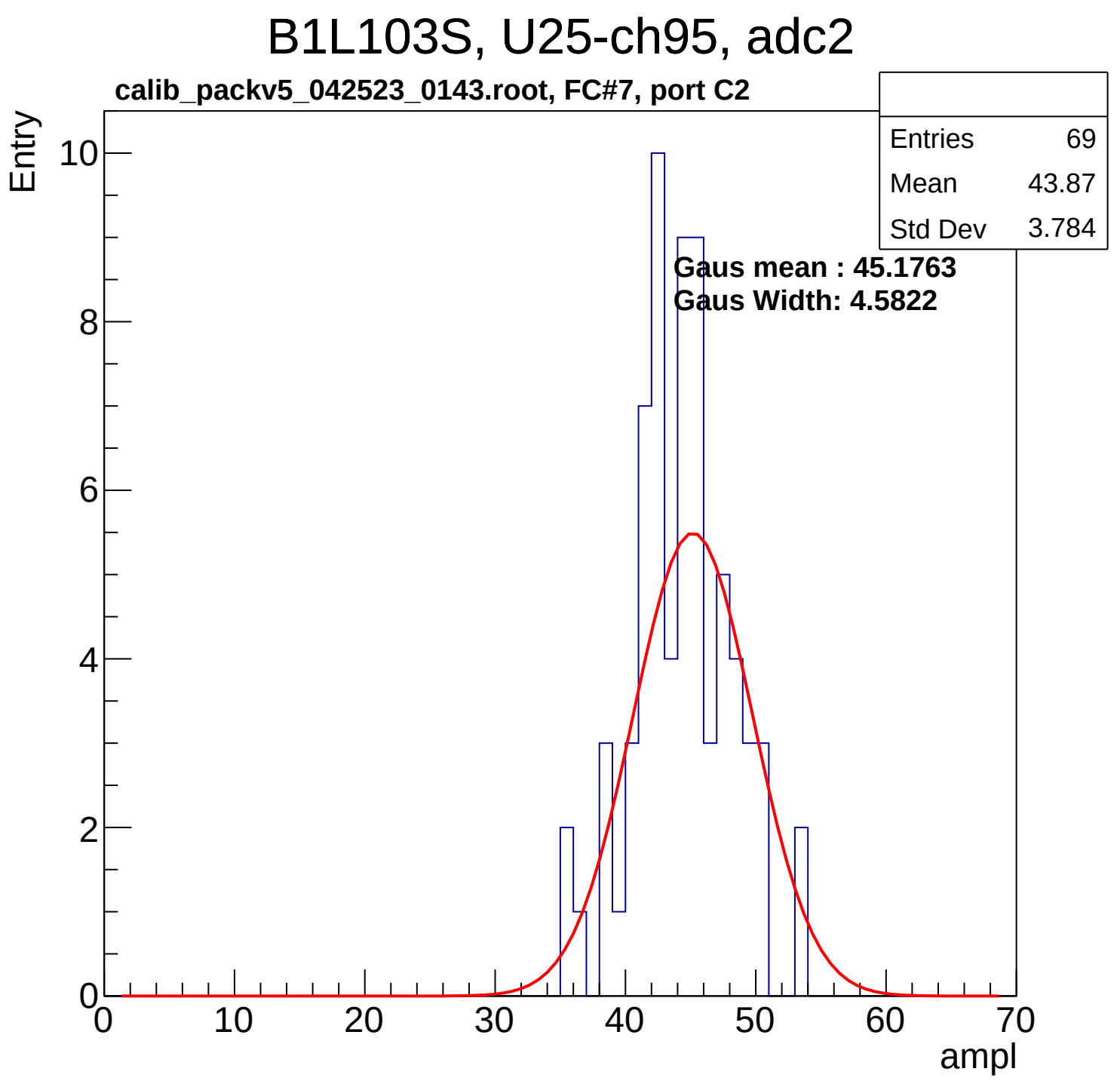

B1L103S, U25-ch94, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch95, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 28.64
Std Dev 6.083
Gaus mean : 30.2577
Gaus Width: 4.5056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch95, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.84
Std Dev 3.688
Gaus mean : 37.4650
Gaus Width: 4.1364
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch95, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 43.87
Std Dev 3.784
Gaus mean : 45.1763
Gaus Width: 4.5822
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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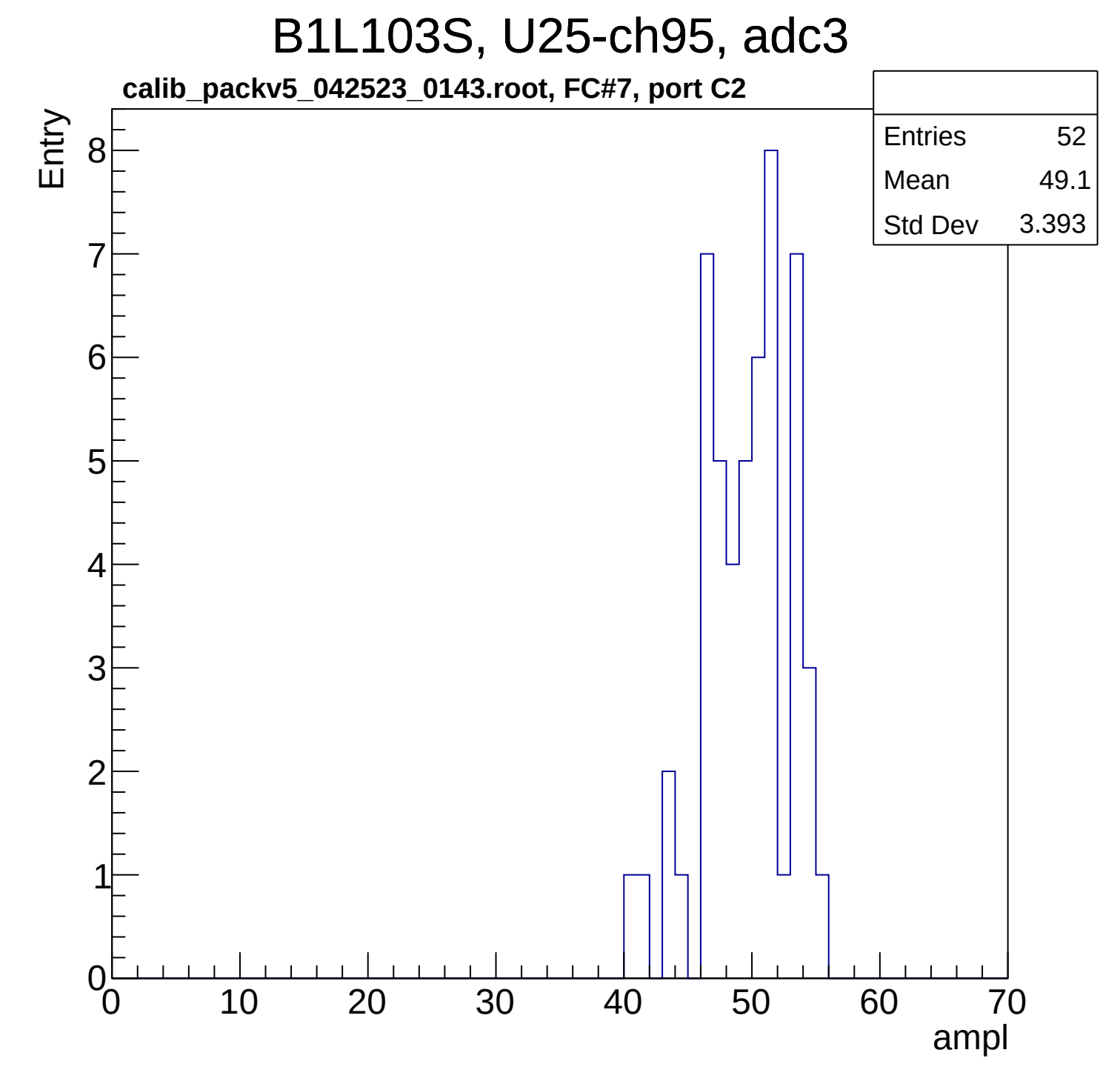
B1L103S, U25-ch95, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 49.1
Std Dev 3.393
Entry
ampl
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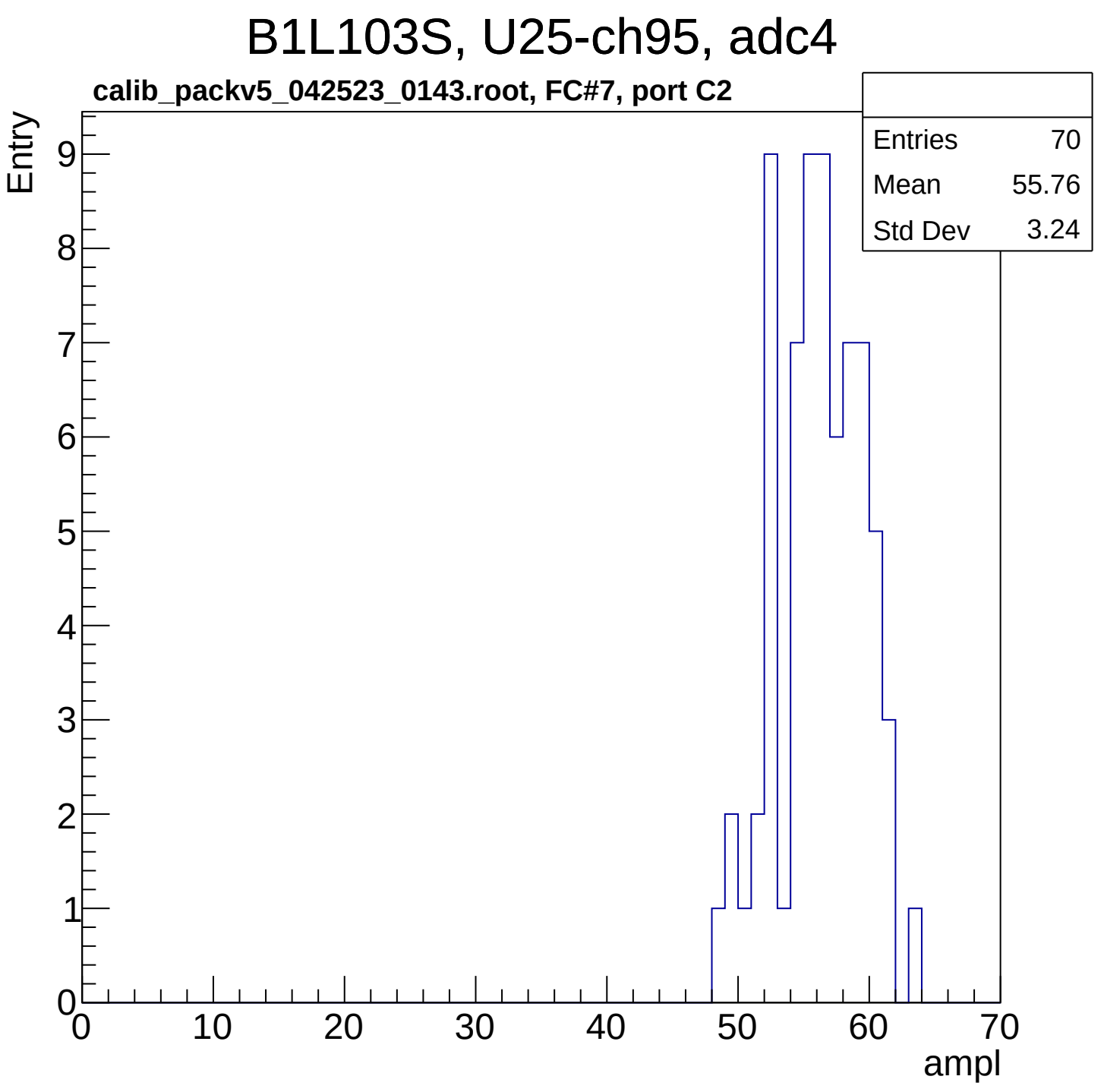
B1L103S, U25-ch95, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 55.76
Std Dev 3.24
Entry
0
1
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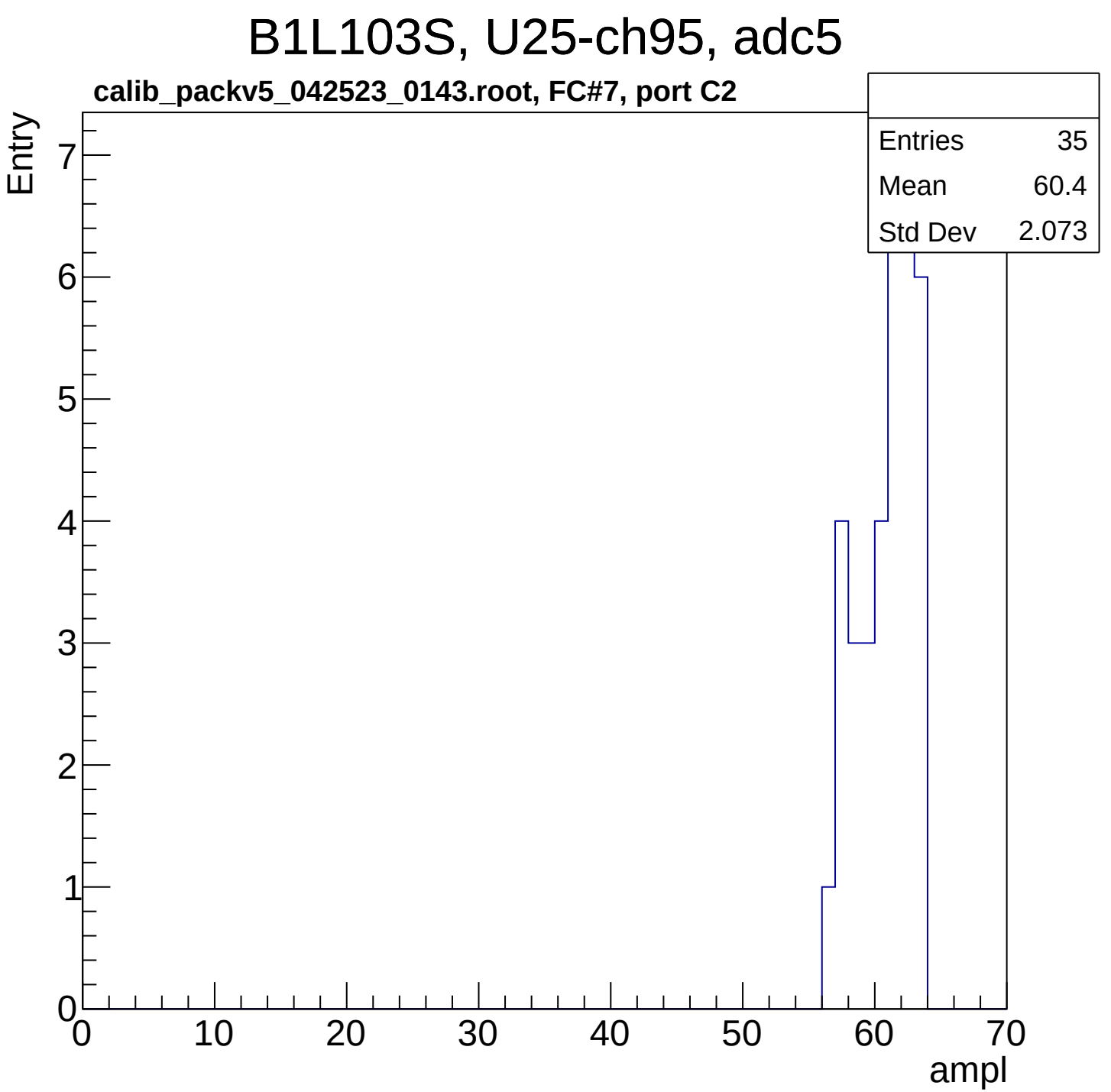
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch95, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 35
Mean 60.4
Std Dev 2.073
Entry
ampl
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3
4
5
6
7
0
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20
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60
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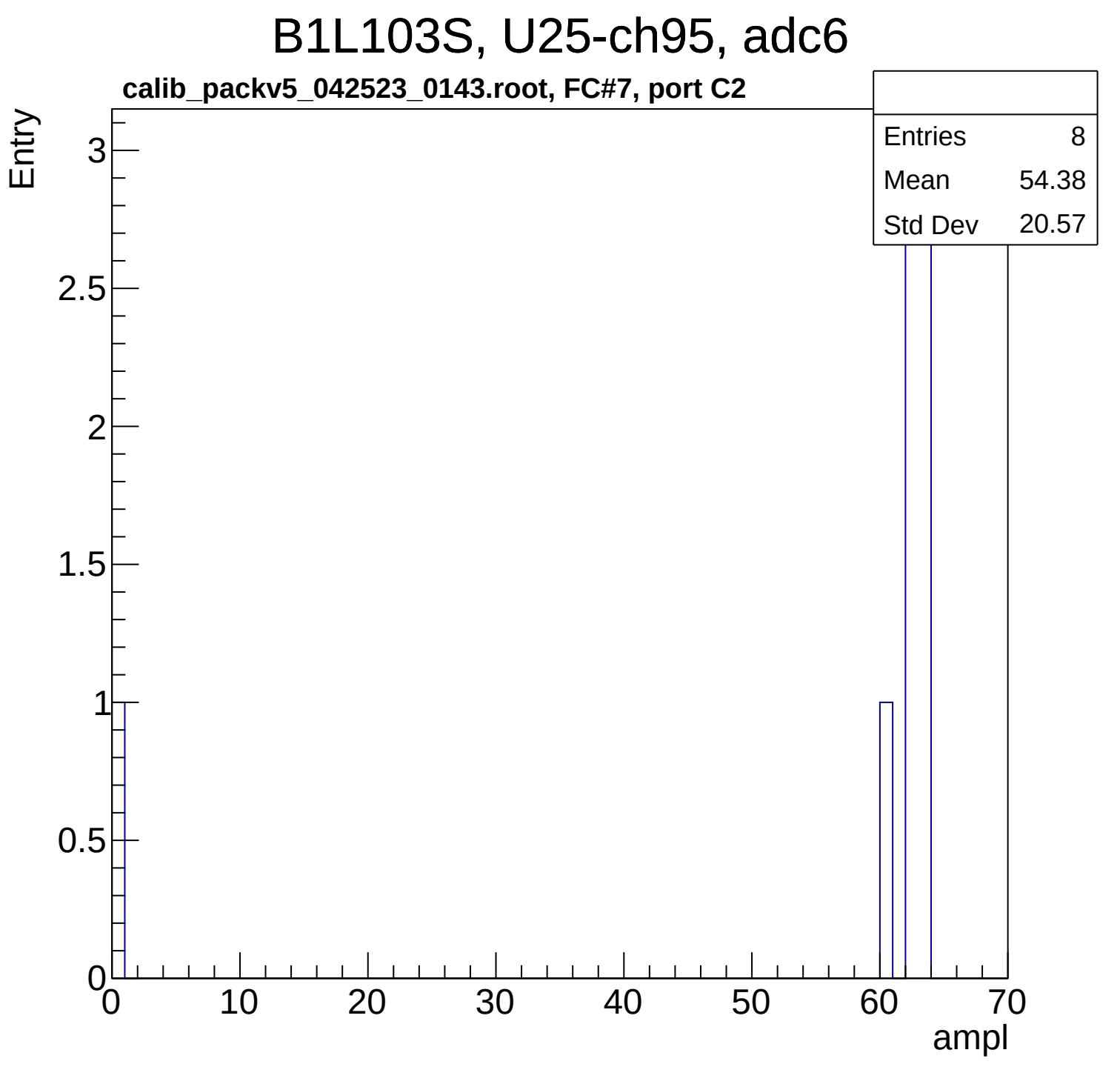
B1L103S, U25-ch95, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
ampl
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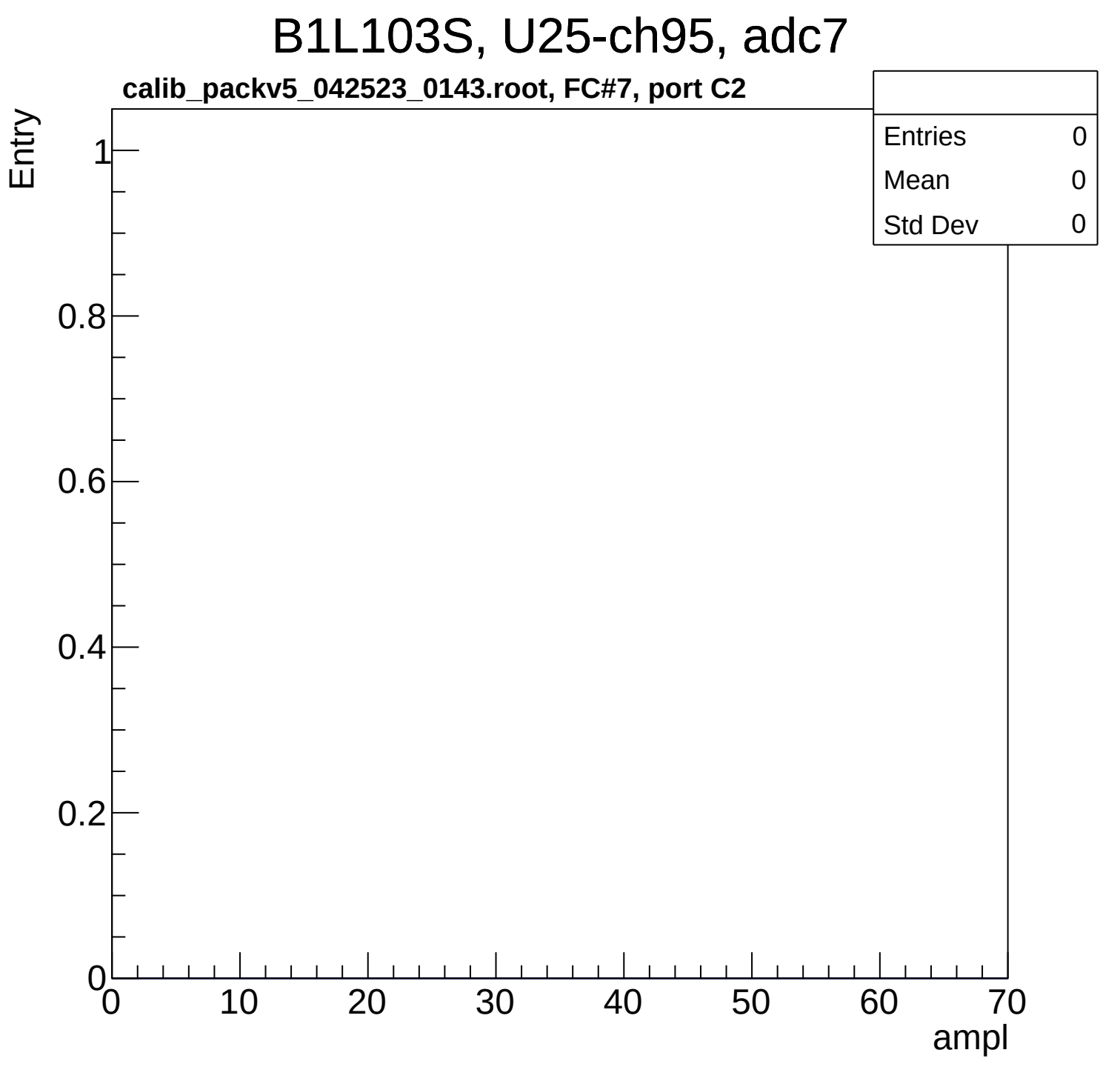
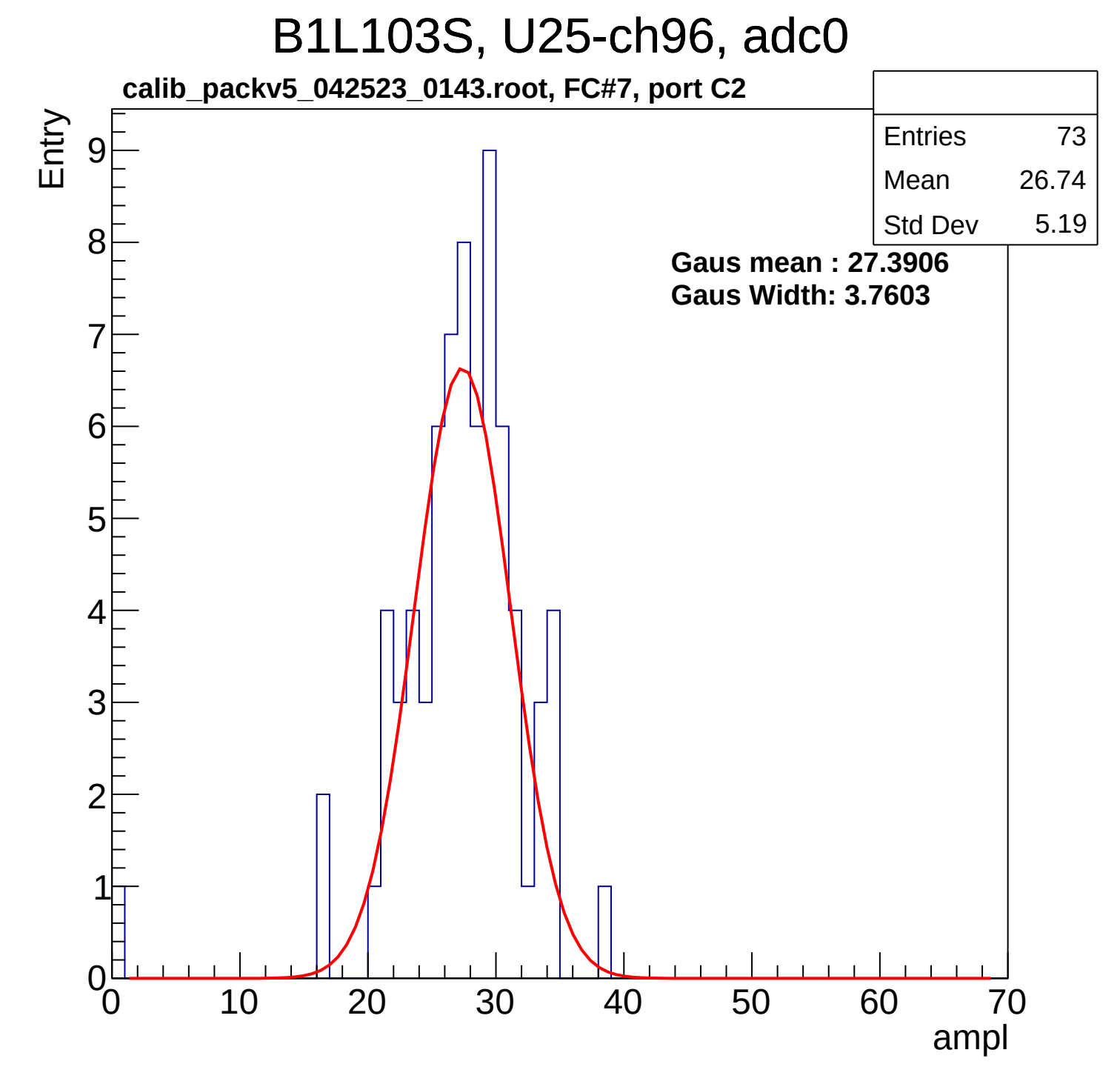
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch95, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch96, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 26.74
Std Dev 5.19
Gaus mean : 27.3906
Gaus Width: 3.7603
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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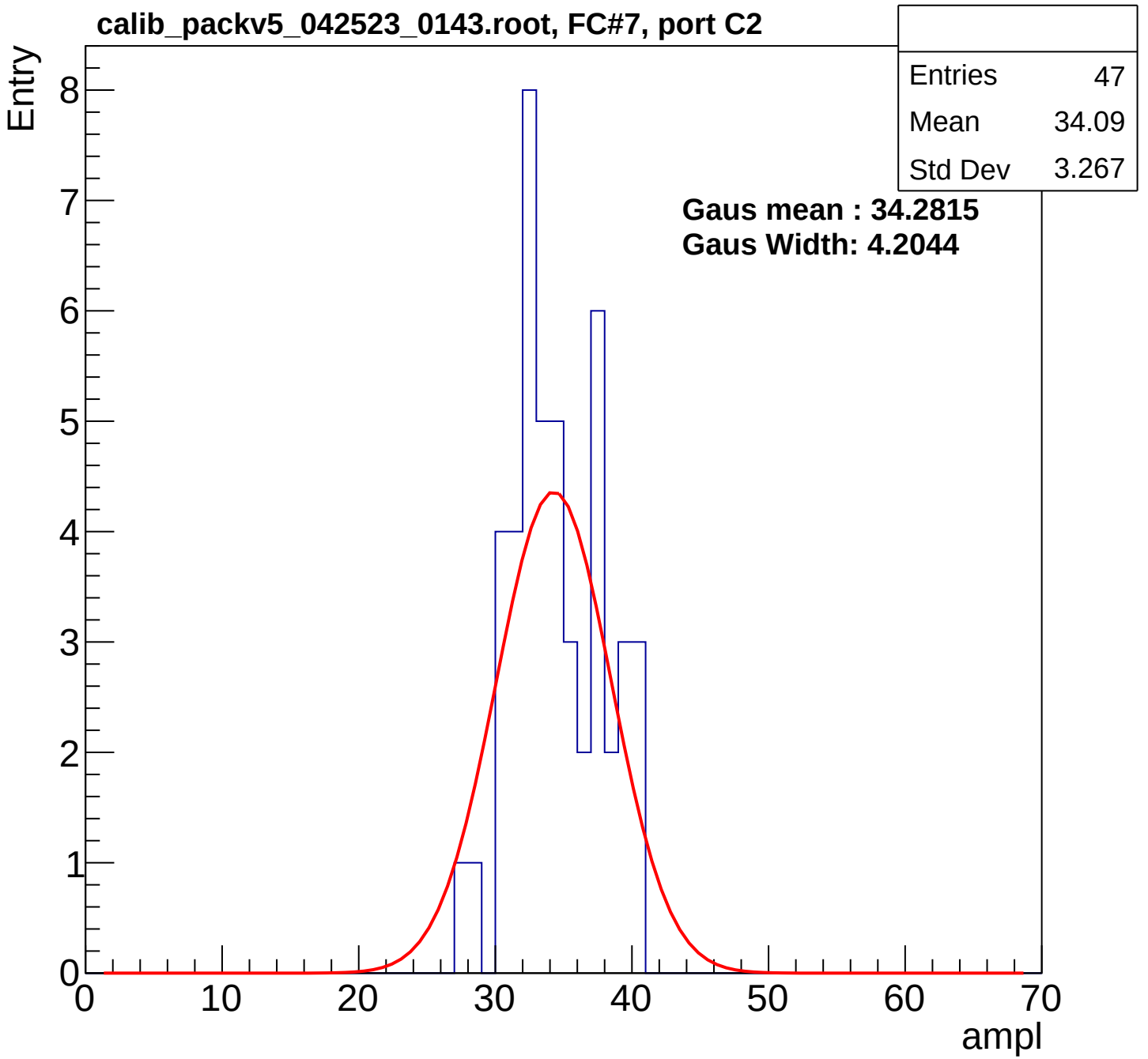
B1L103S, U25-ch96, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 34.09
Std Dev 3.267
Gaus mean : 34.2815
Gaus Width: 4.2044
Entry
ampl
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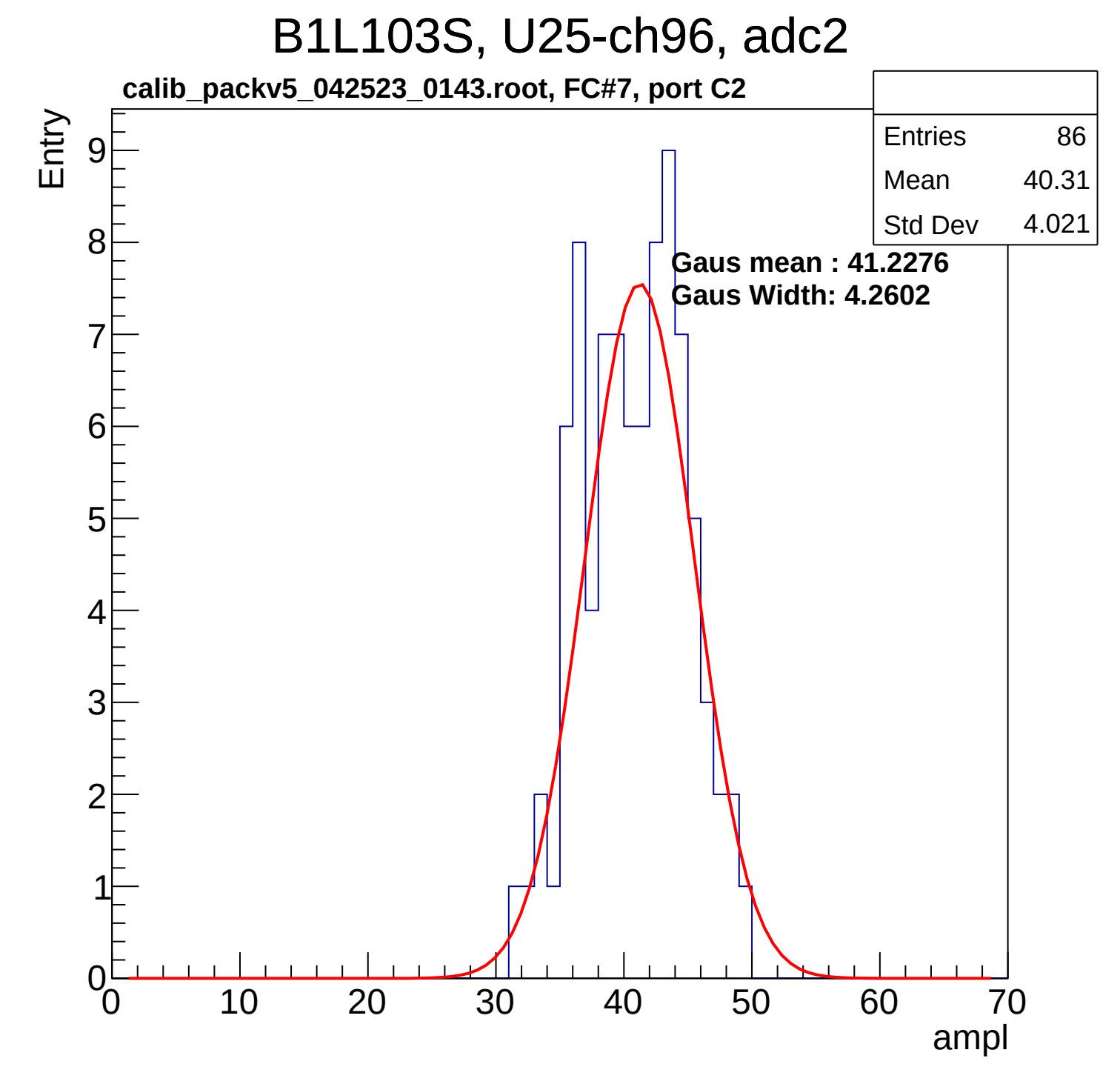
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch96, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 40.31
Std Dev 4.021
Gaus mean : 41.2276
Gaus Width: 4.2602
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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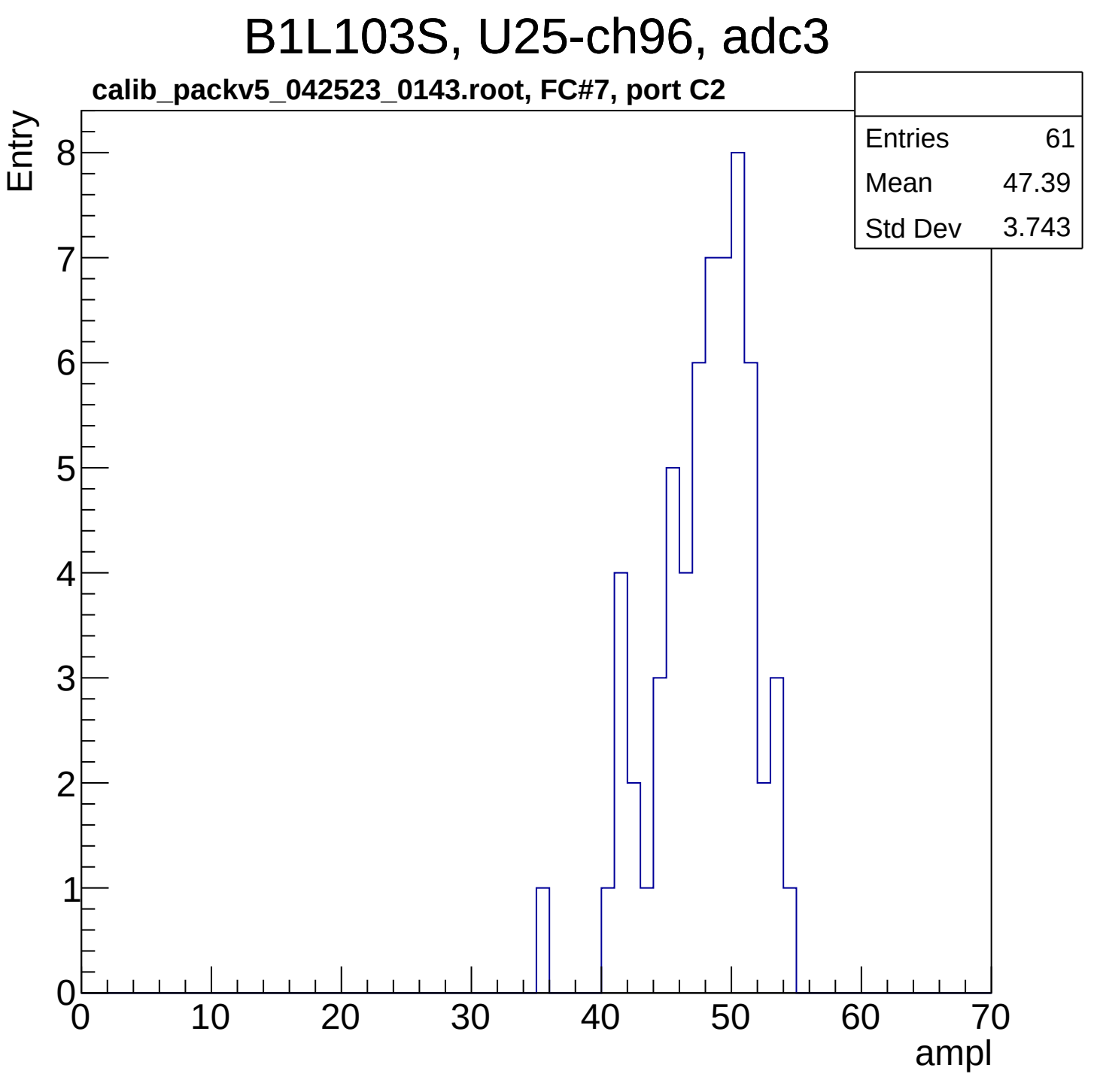
B1L103S, U25-ch96, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 47.39
Std Dev 3.743
Entry
ampl
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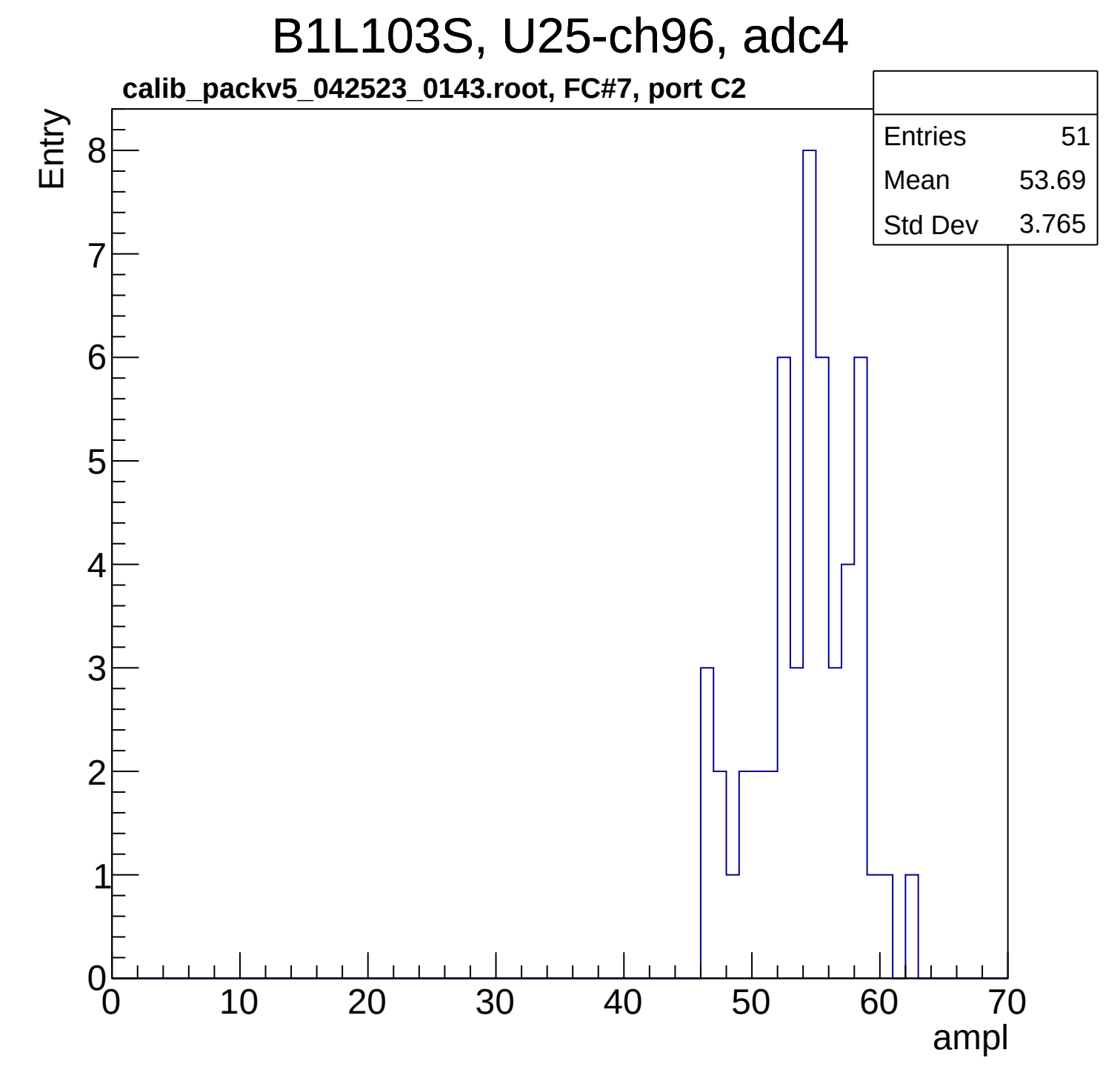
B1L103S, U25-ch96, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 53.69
Std Dev 3.765
Entry
ampl
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1
2
3
4
5
6
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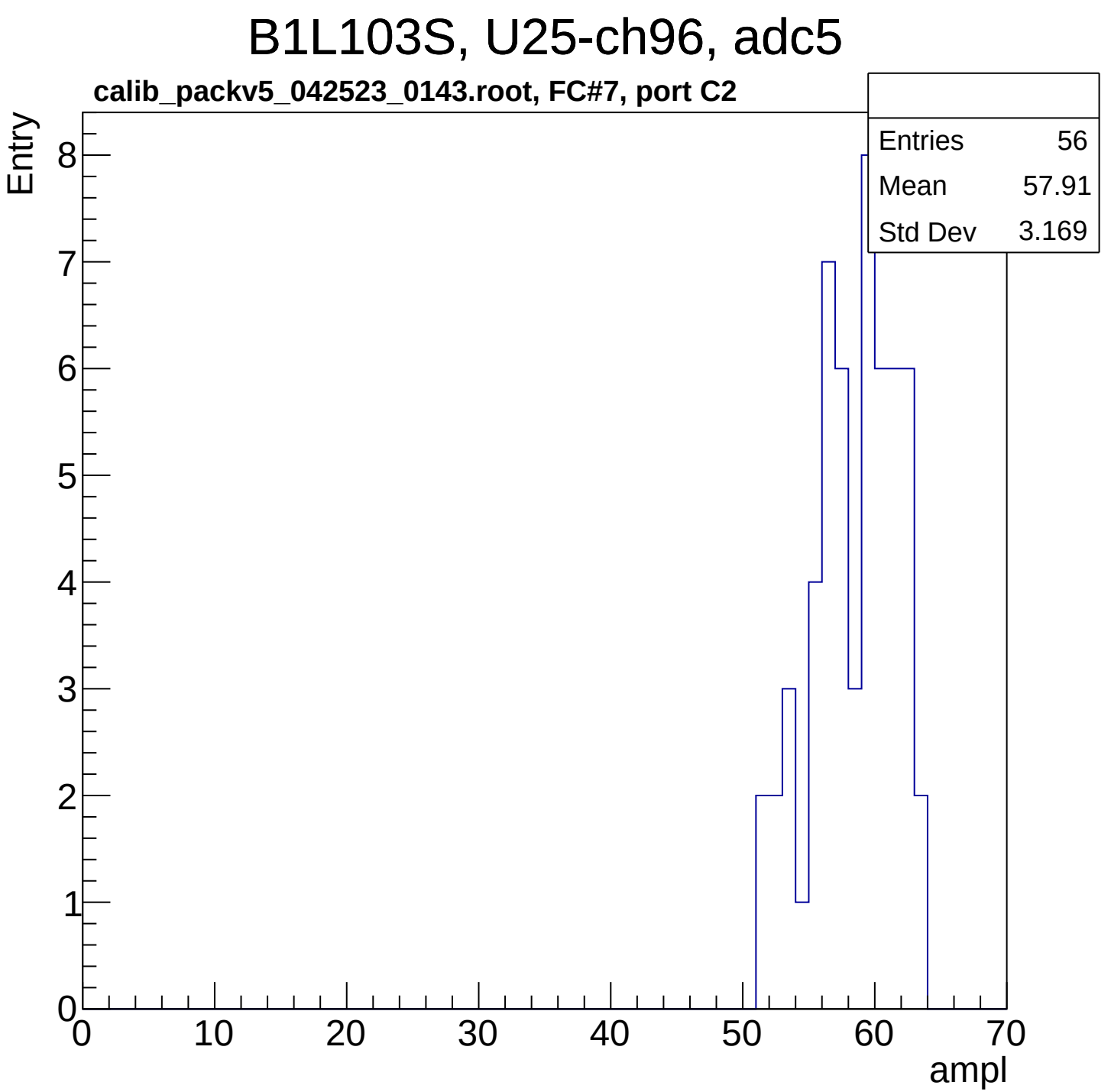
B1L103S, U25-ch96, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 57.91
Std Dev 3.169
Entry
ampl
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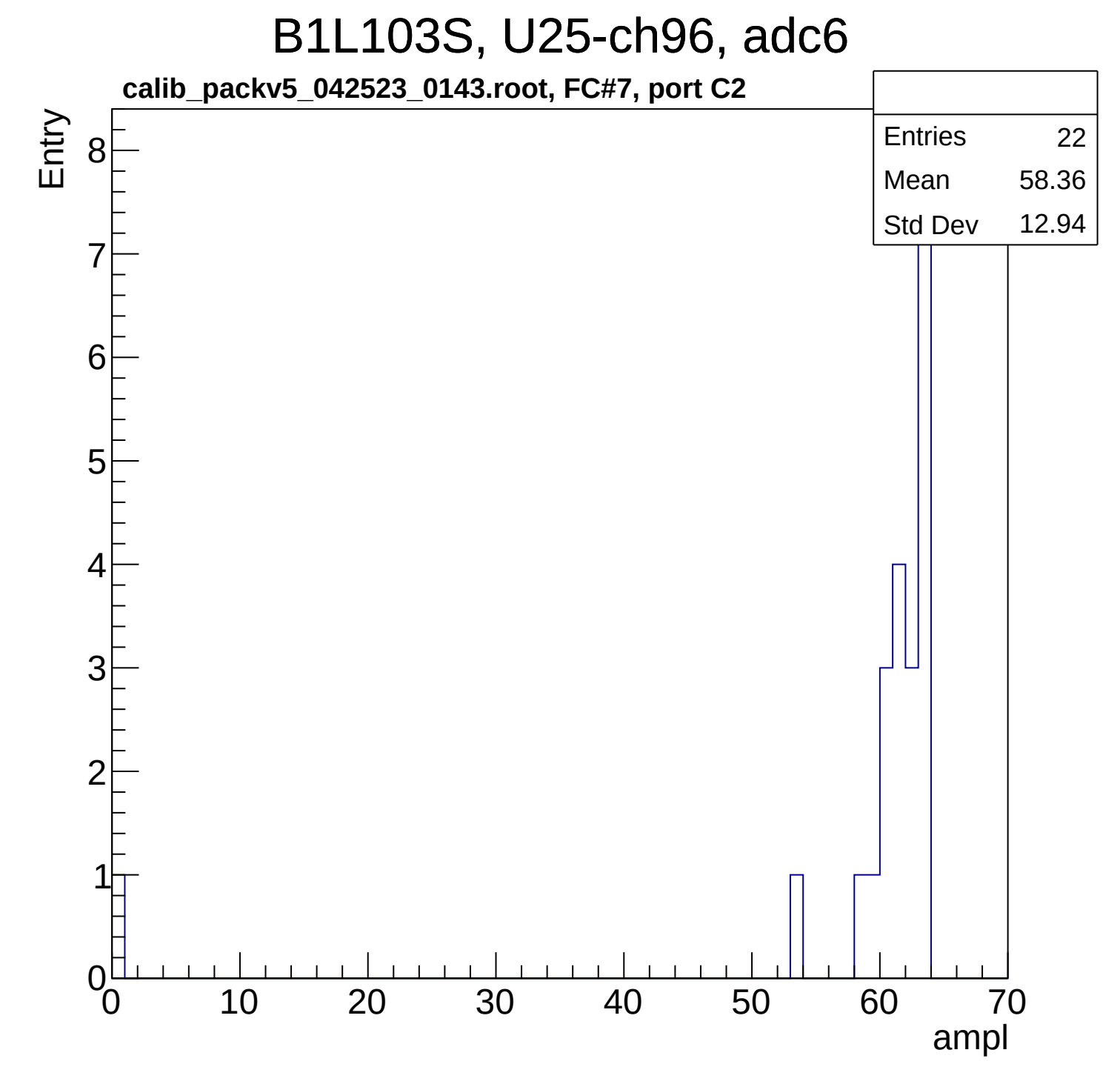
B1L103S, U25-ch96, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 22
Mean 58.36
Std Dev 12.94
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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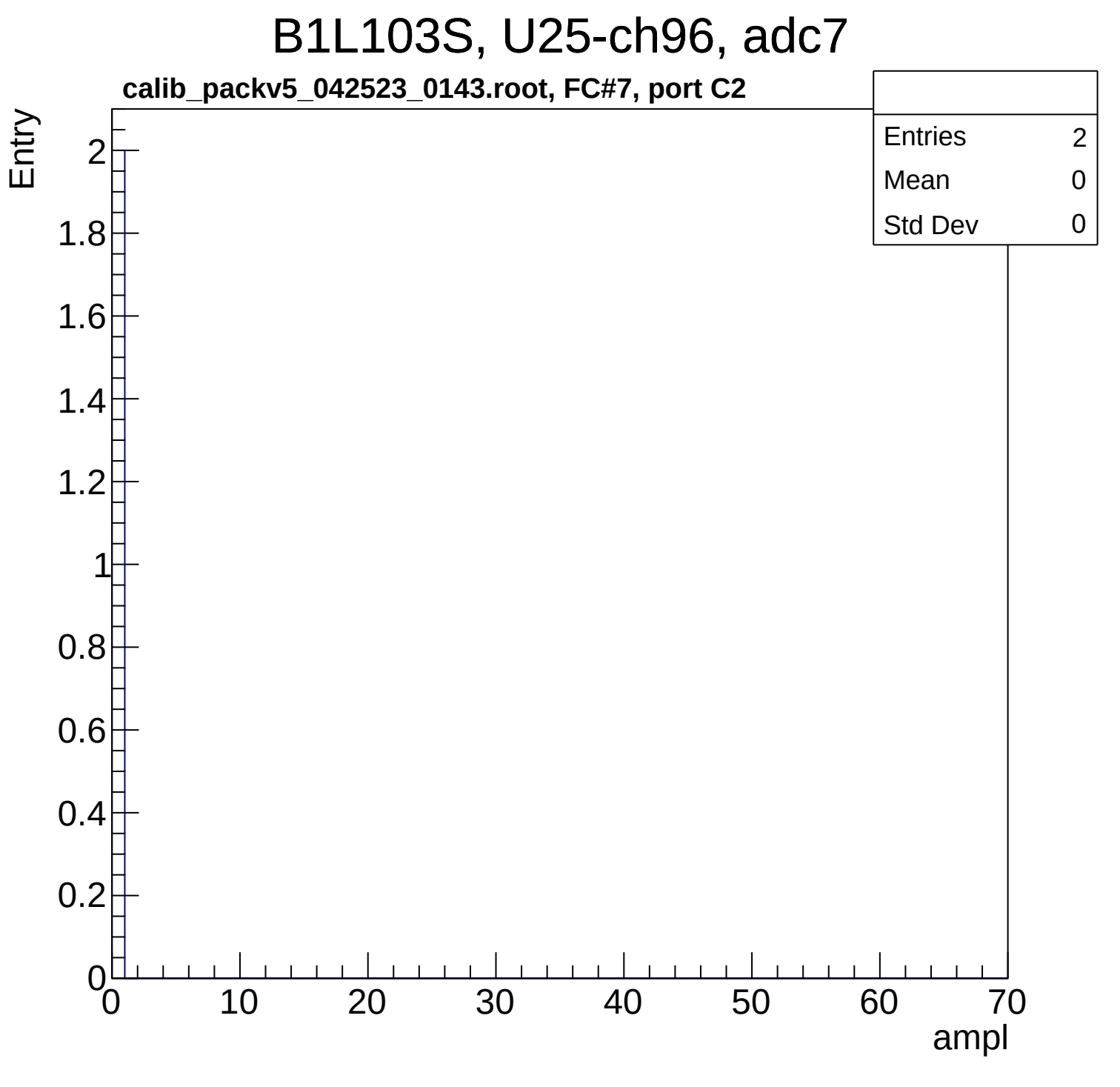
B1L103S, U25-ch96, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
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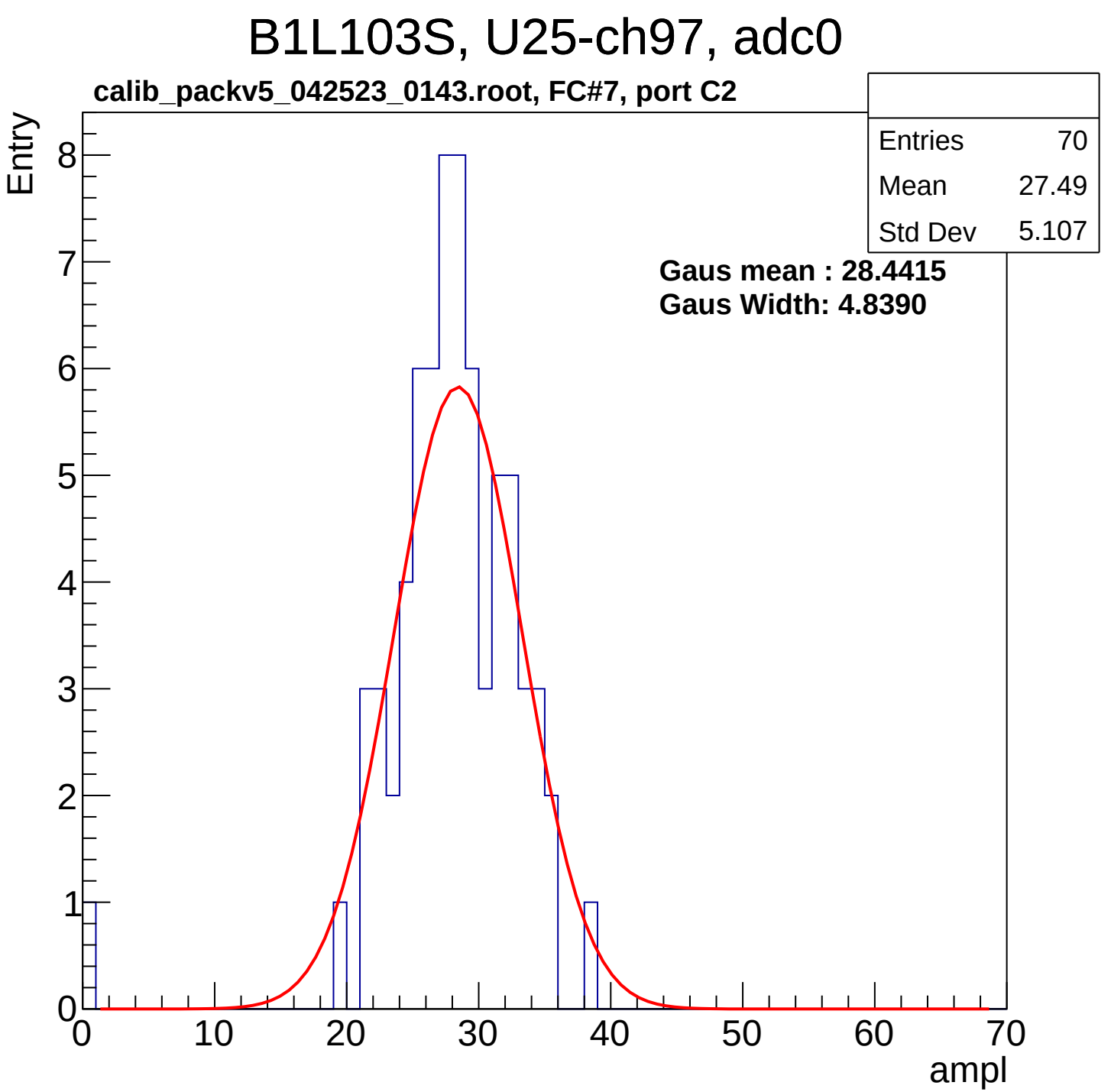
40
50
60
70
ampl


B1L103S, U25-ch97, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 27.49
Std Dev 5.107
Gaus mean : 28.4415
Gaus Width: 4.8390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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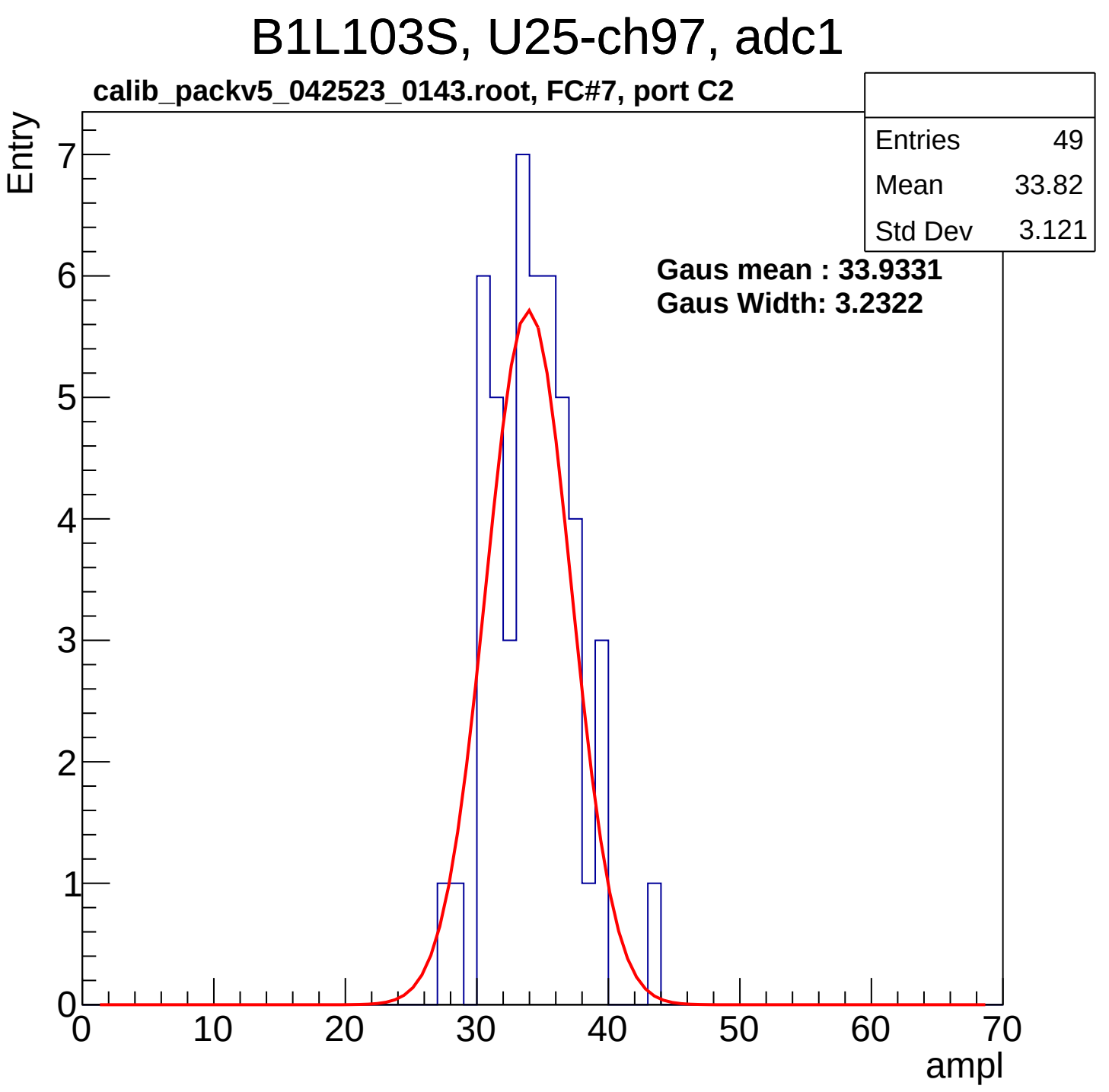
B1L103S, U25-ch97, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 33.82
Std Dev 3.121
Gaus mean : 33.9331
Gaus Width: 3.2322
Entry
ampl
0
1
2
3
4
5
6
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0
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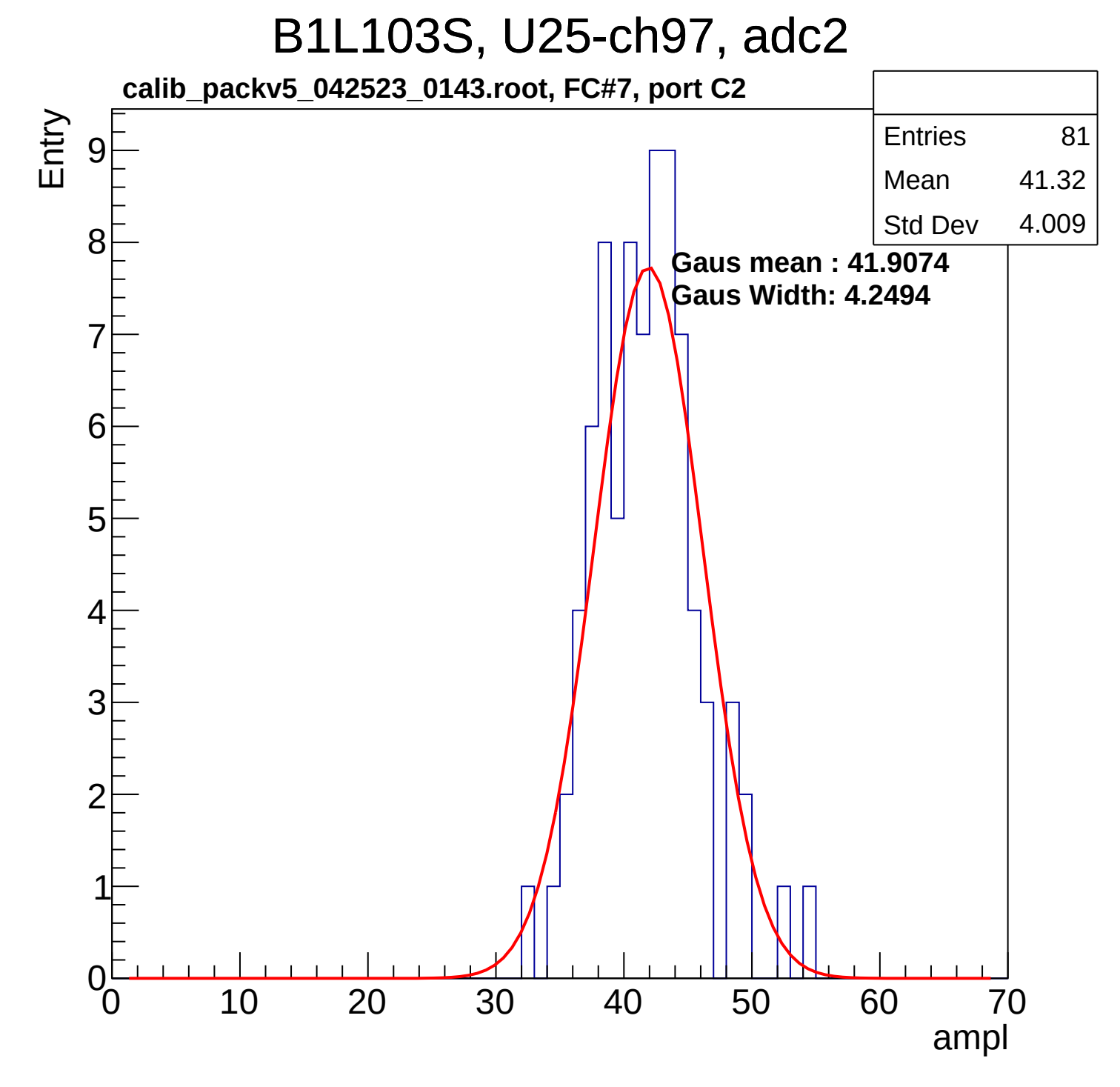
B1L103S, U25-ch97, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 81
Mean 41.32
Std Dev 4.009
Gaus mean : 41.9074
Gaus Width: 4.2494
Entry
ampl
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3
4
5
6
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9
0
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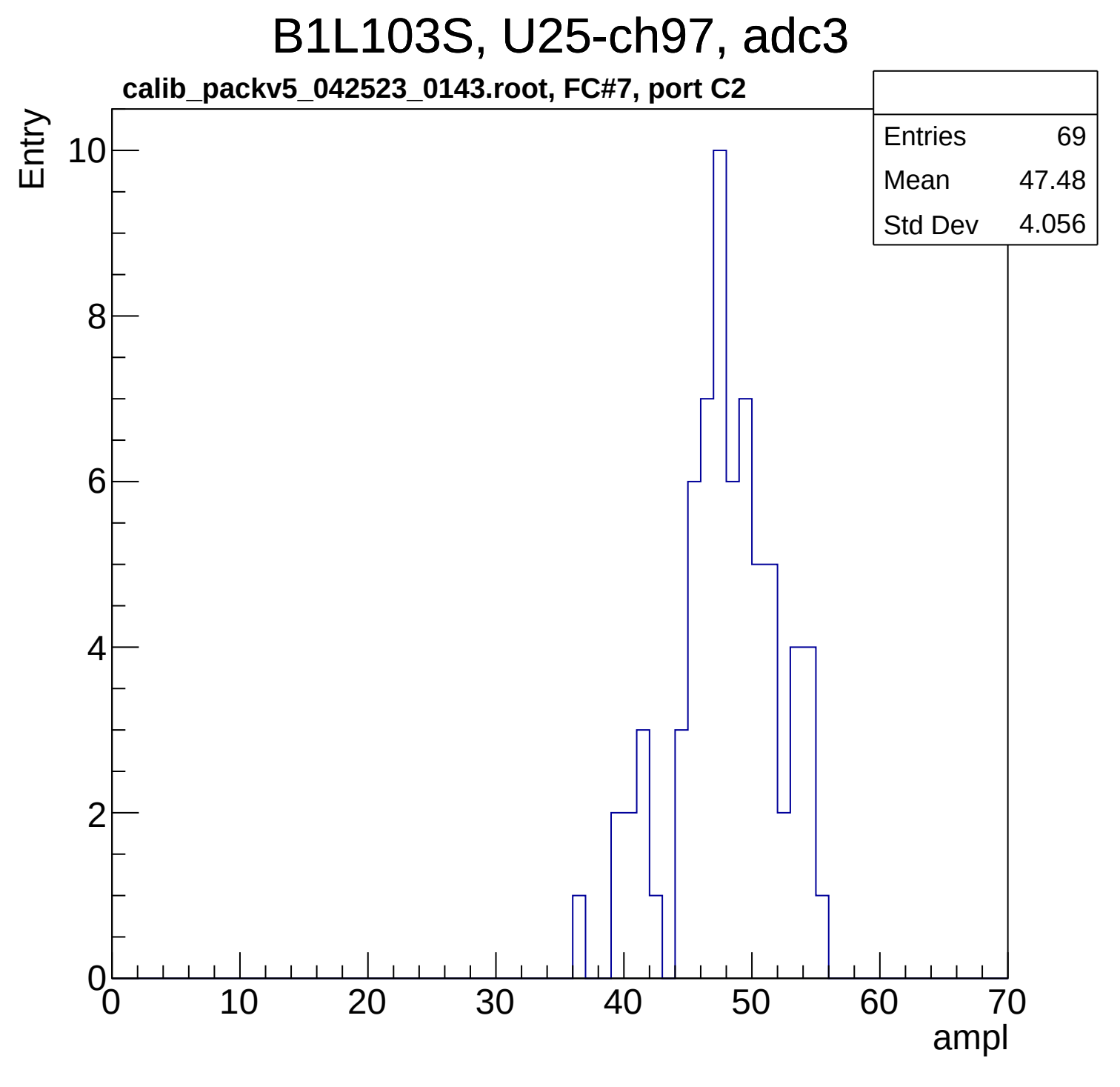
B1L103S, U25-ch97, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 47.48
Std Dev 4.056
Entry
ampl
0
2
4
6
8
10
0
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40
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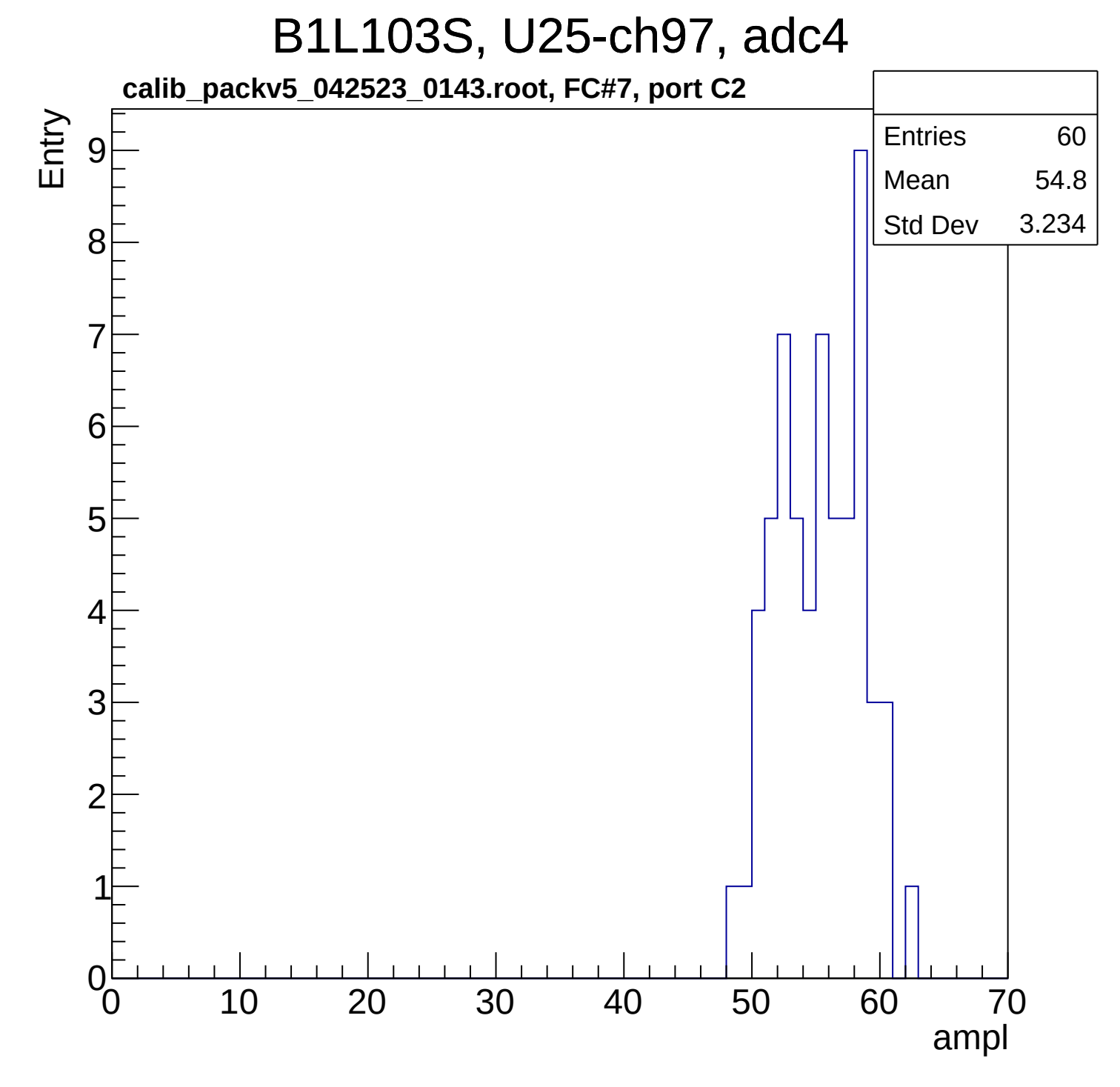
B1L103S, U25-ch97, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 54.8
Std Dev 3.234
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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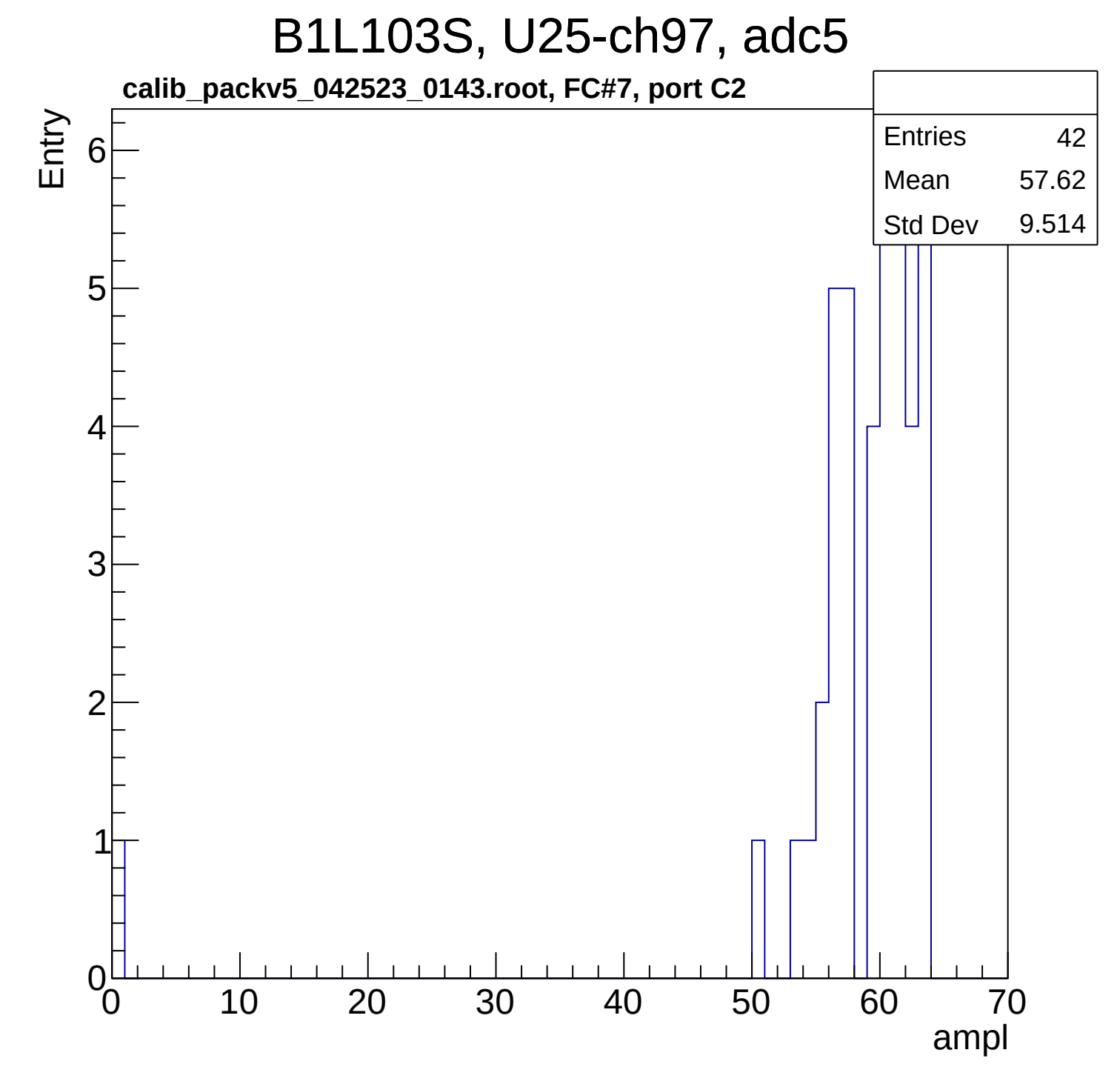
B1L103S, U25-ch97, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 57.62
Std Dev 9.514
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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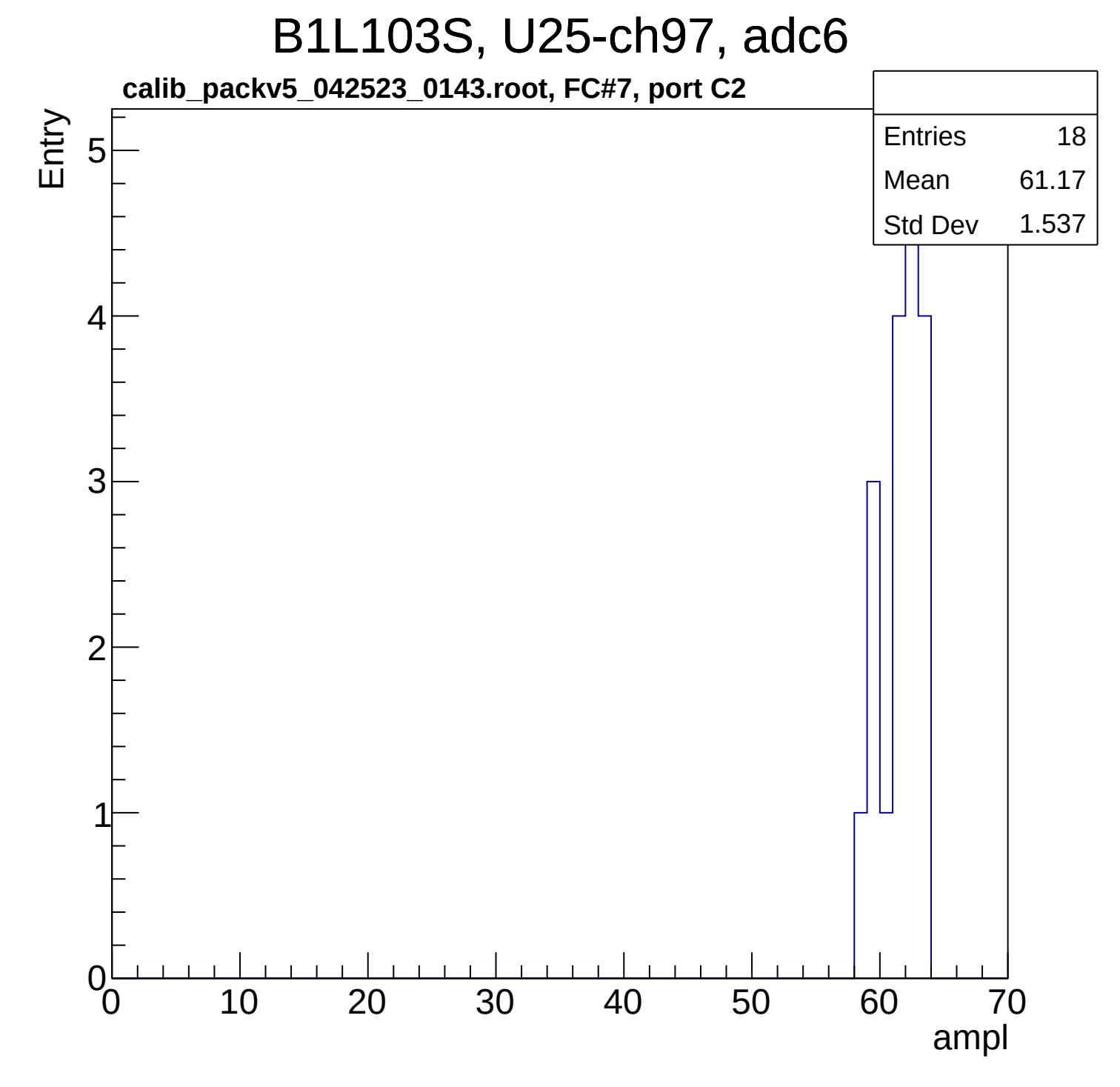
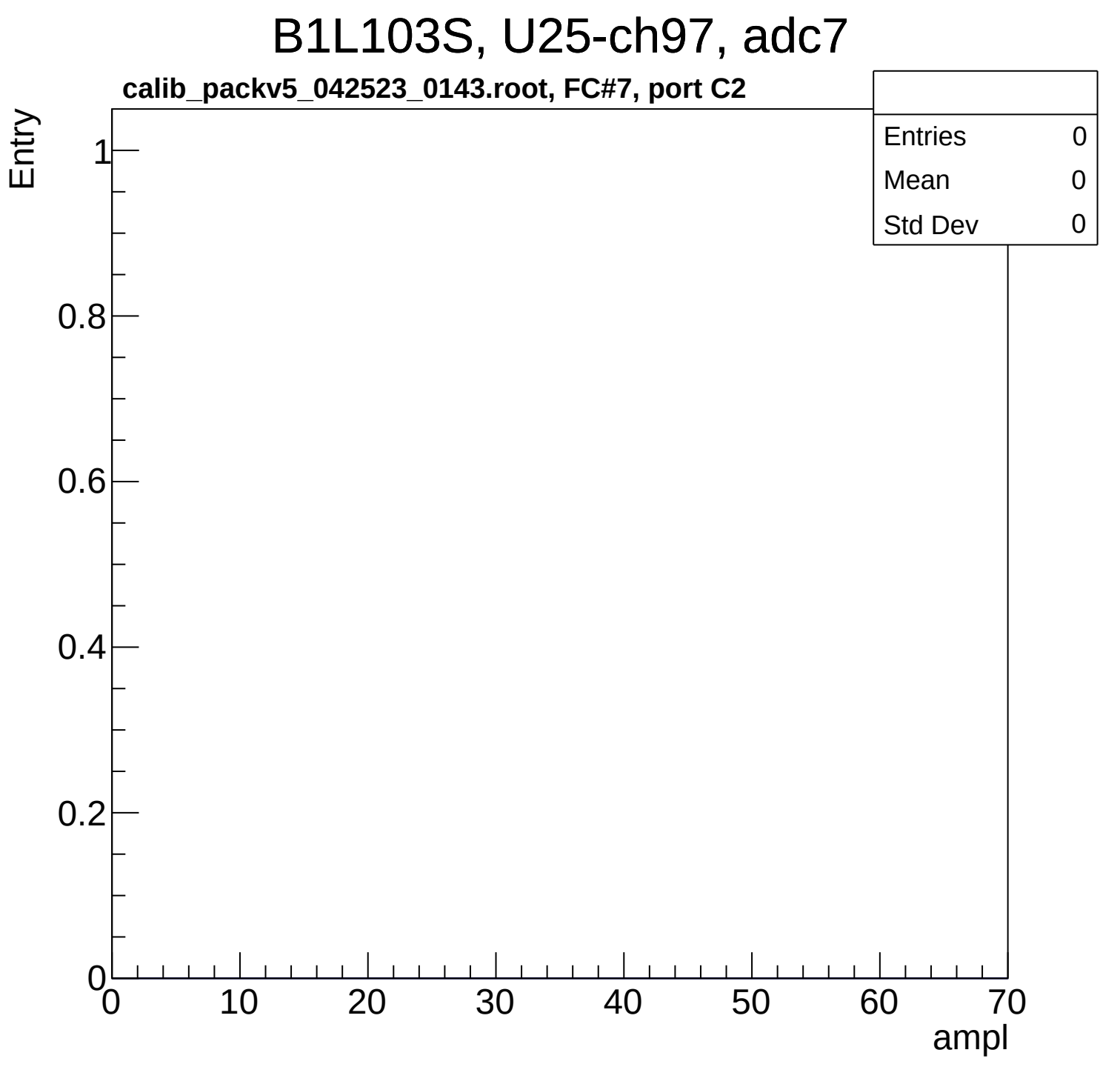
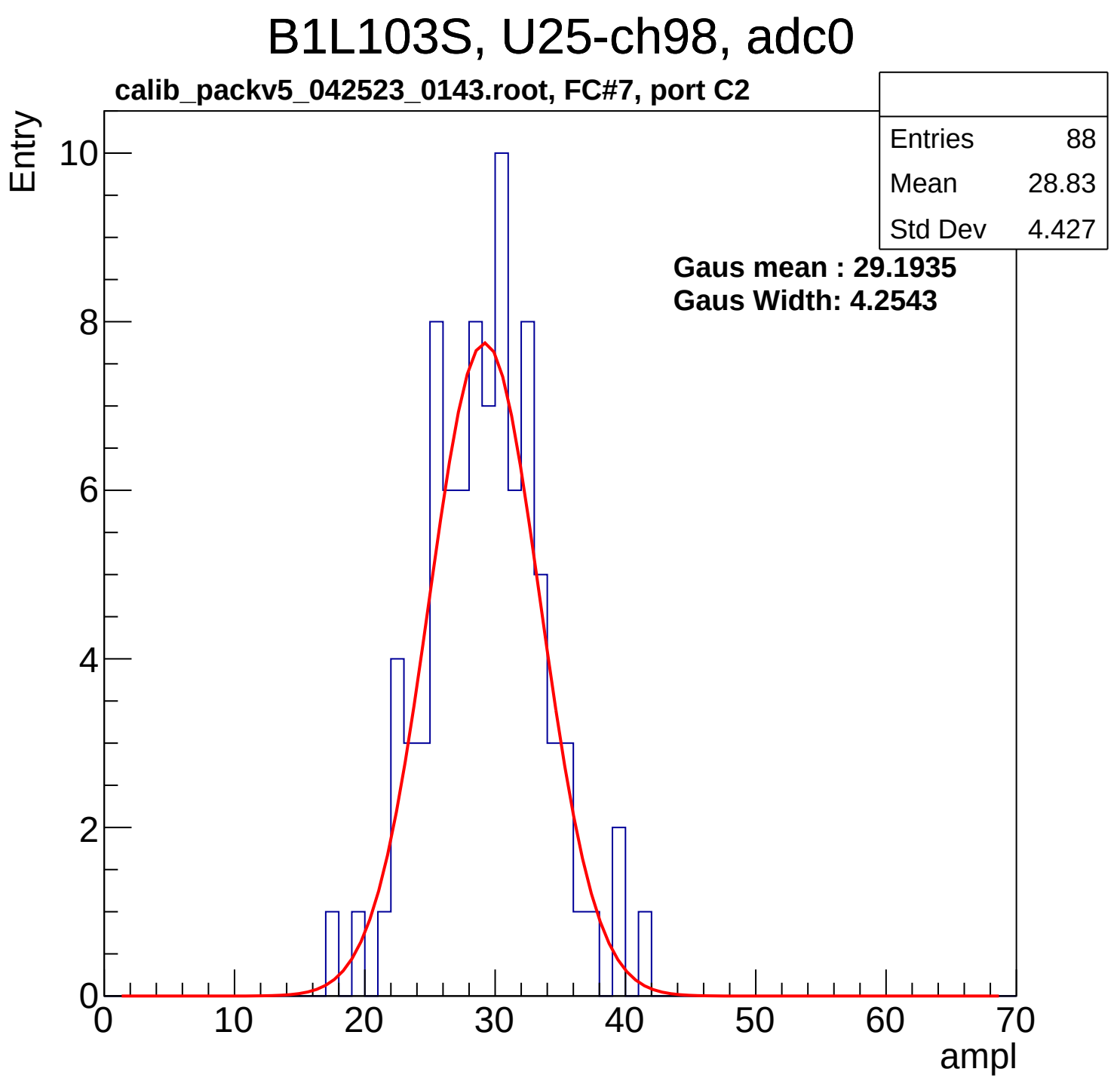

B1L103S, U25-ch97, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 18
Mean 61.17
Std Dev 1.537
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch97, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch98, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 28.83
Std Dev 4.427
Gaus mean : 29.1935
Gaus Width: 4.2543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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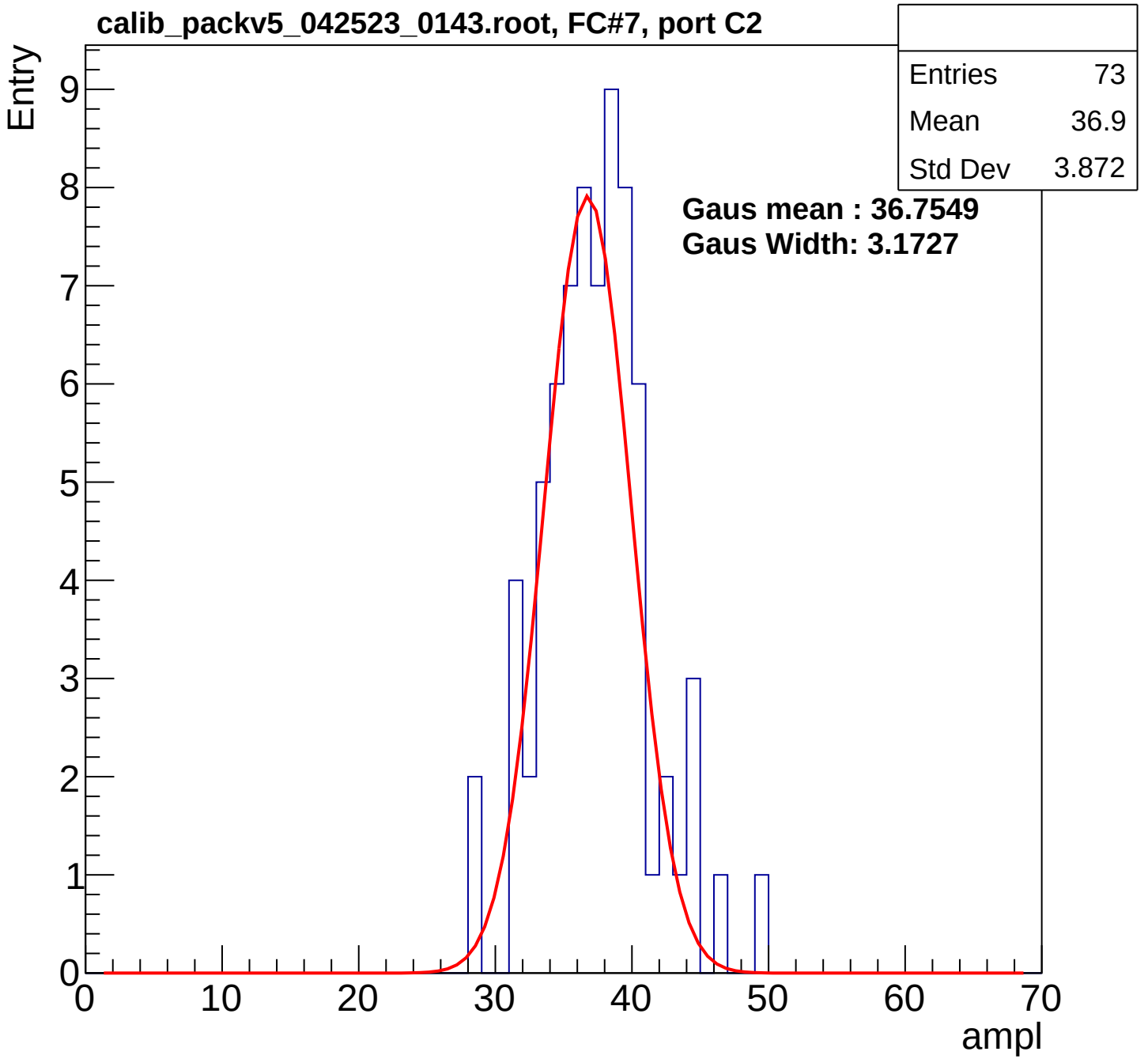
B1L103S, U25-ch98, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 36.9
Std Dev 3.872
Gaus mean : 36.7549
Gaus Width: 3.1727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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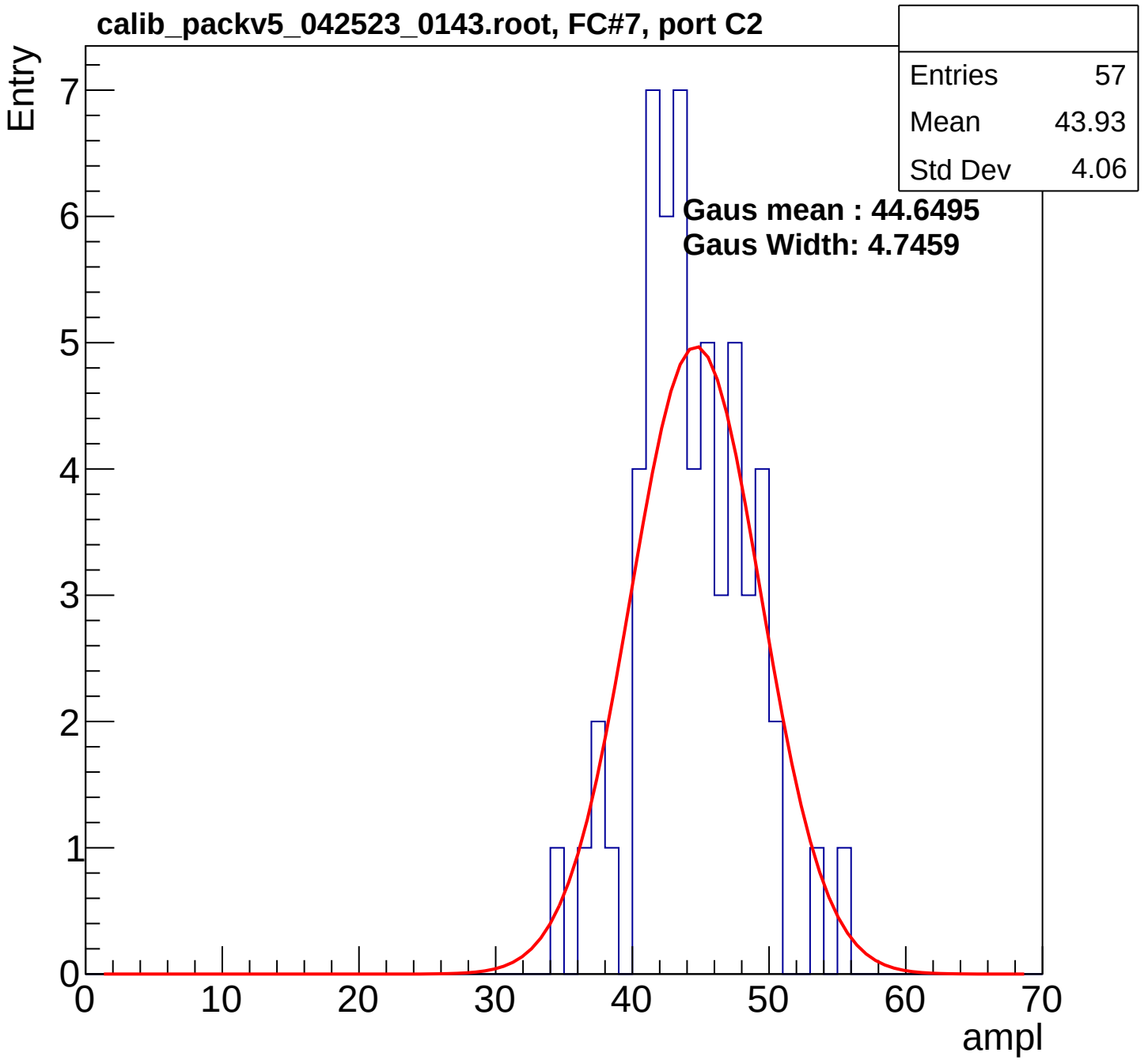
B1L103S, U25-ch98, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 43.93
Std Dev 4.06
Gaus mean : 44.6495
Gaus Width: 4.7459
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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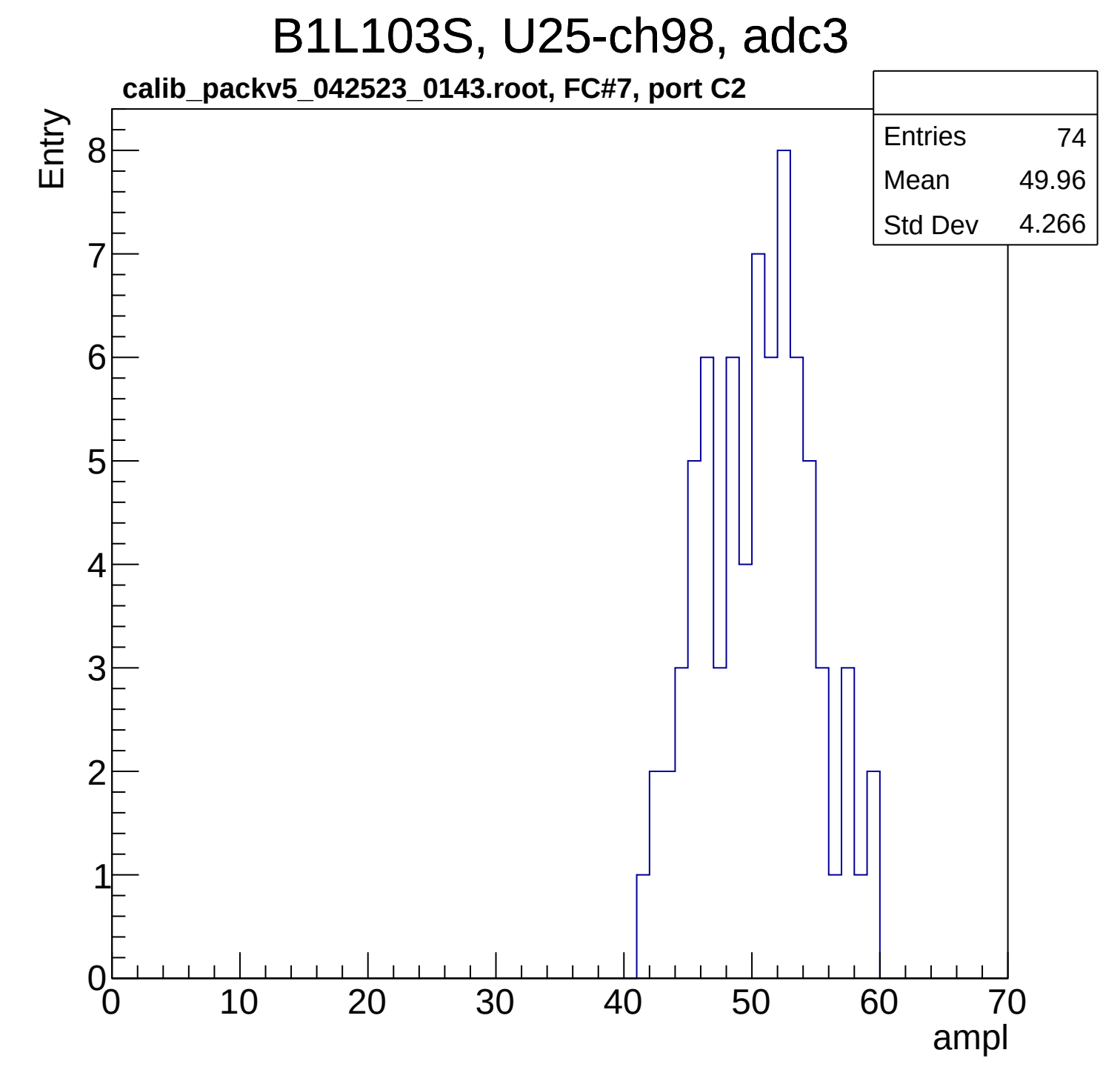
B1L103S, U25-ch98, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 49.96
Std Dev 4.266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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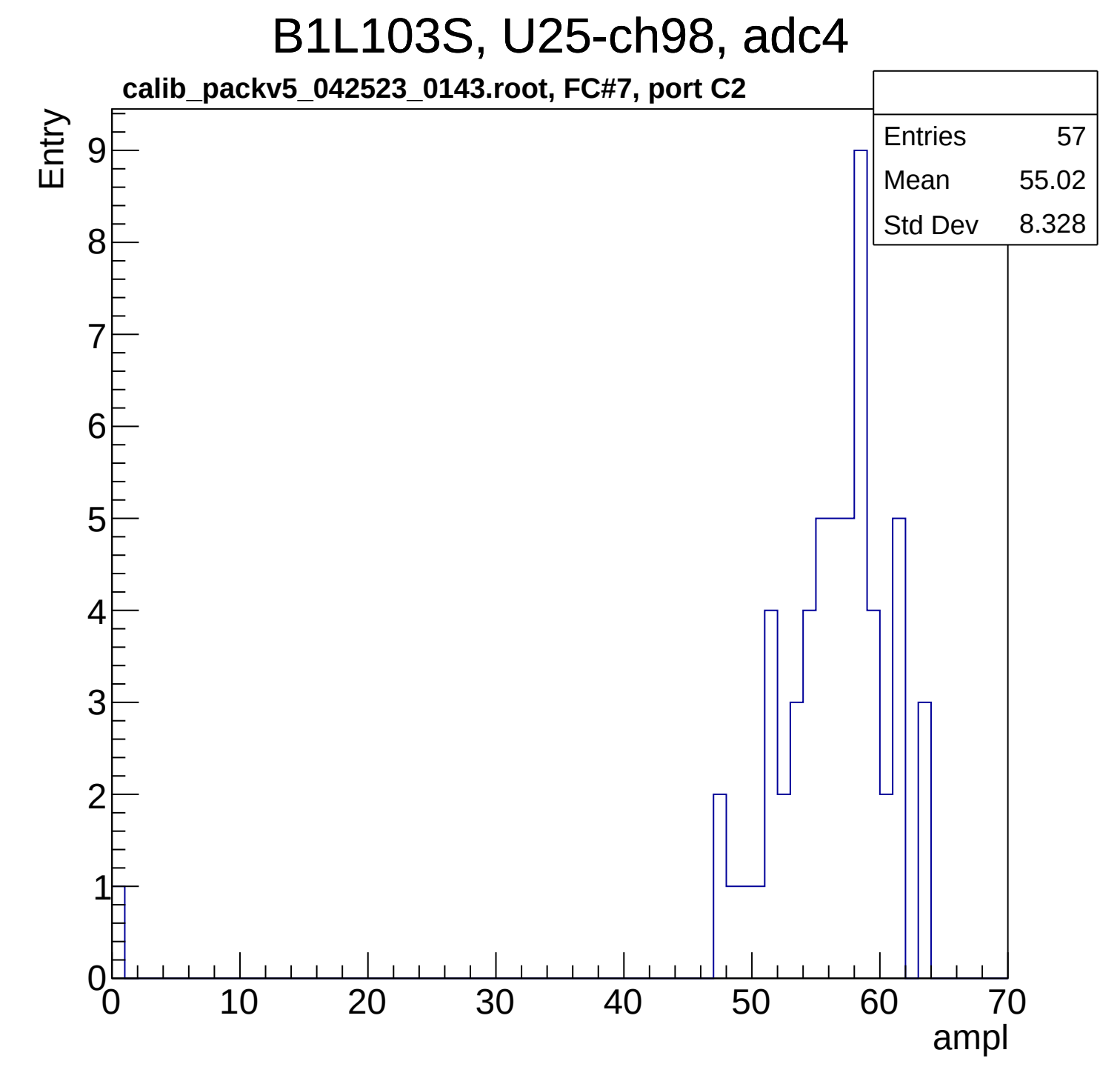
B1L103S, U25-ch98, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 55.02
Std Dev 8.328
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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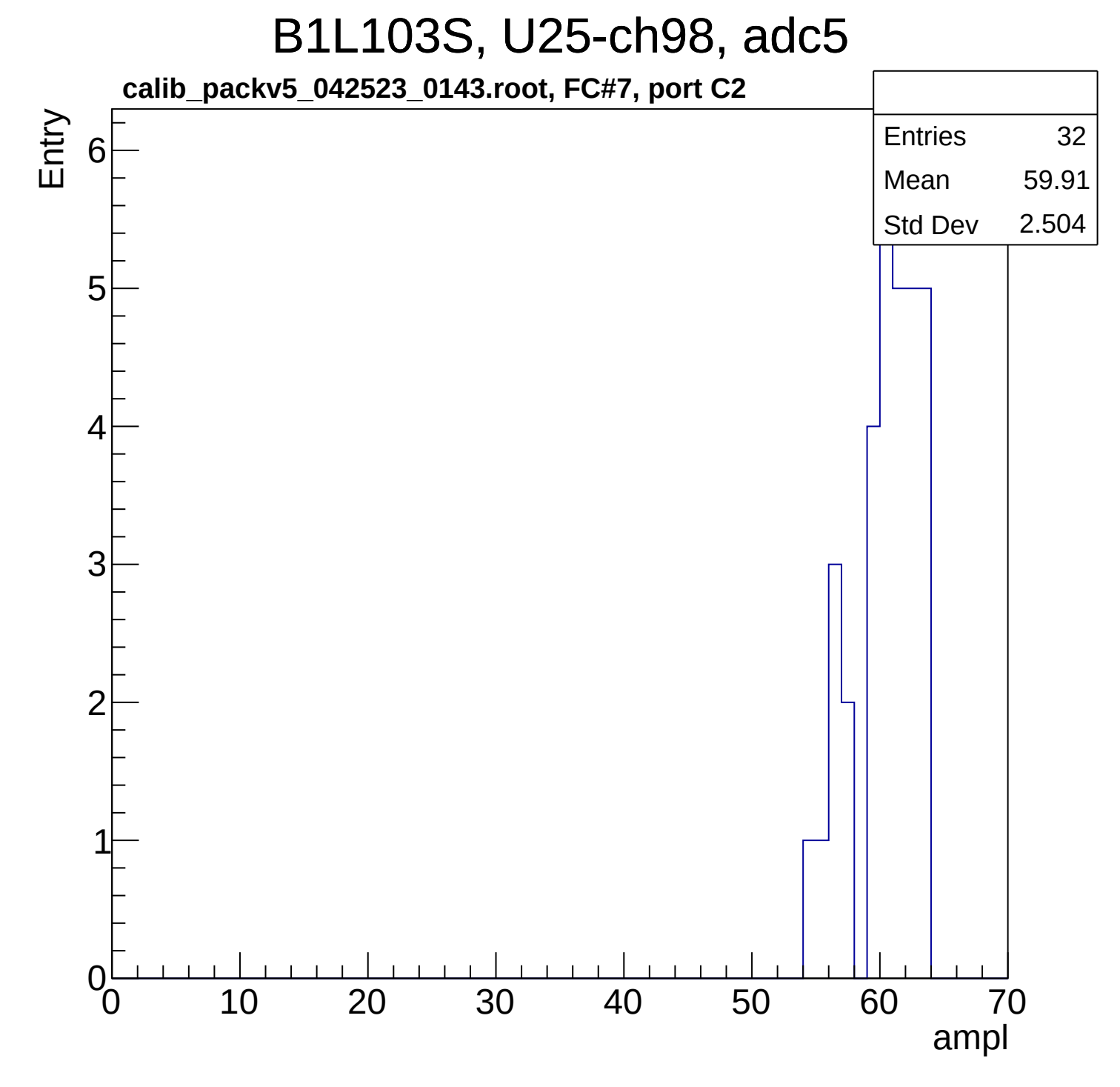
B1L103S, U25-ch98, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 59.91
Std Dev 2.504
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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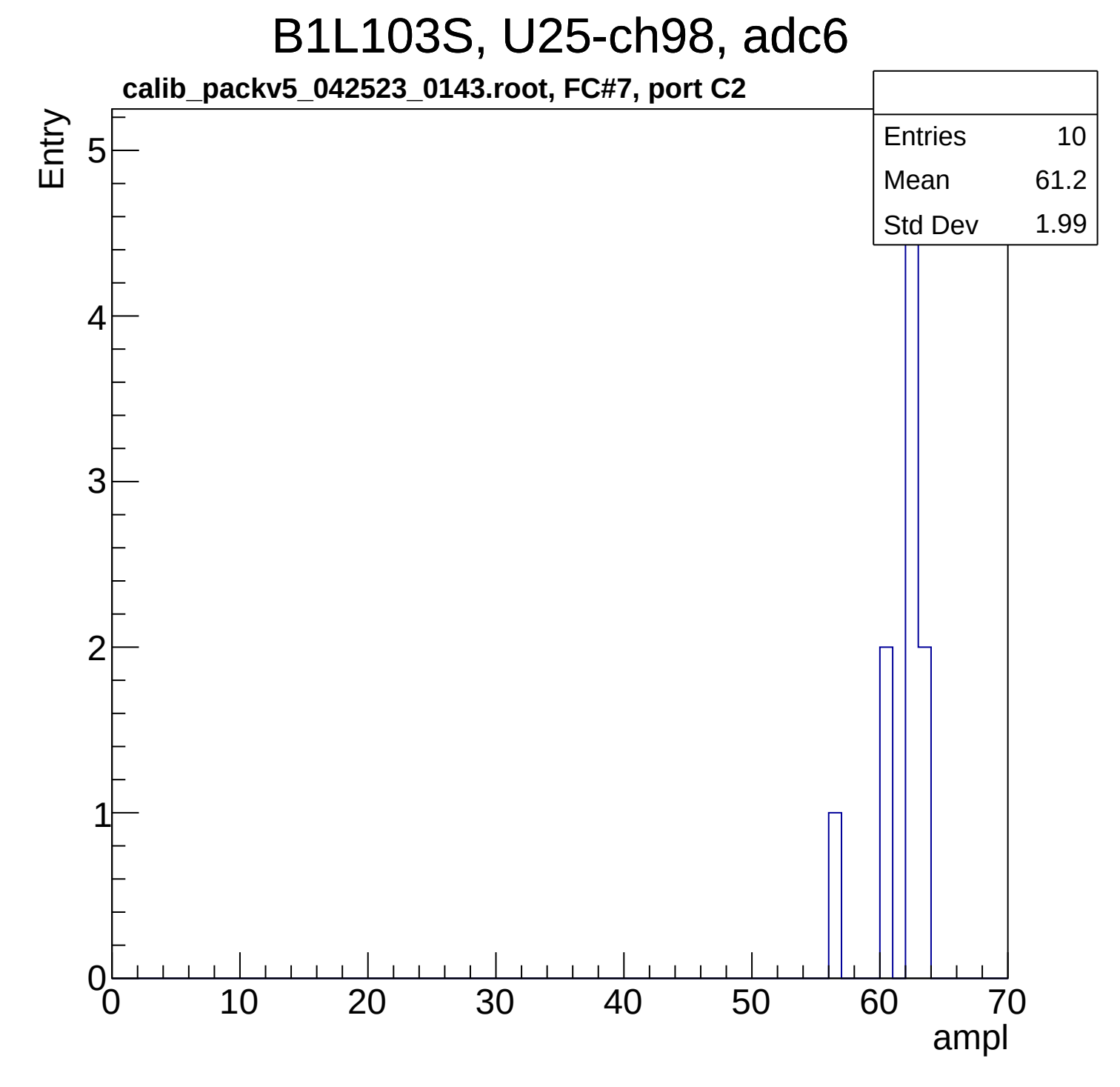
B1L103S, U25-ch98, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 61.2
Std Dev 1.99
Entry
ampl
0
1
2
3
4
5
0
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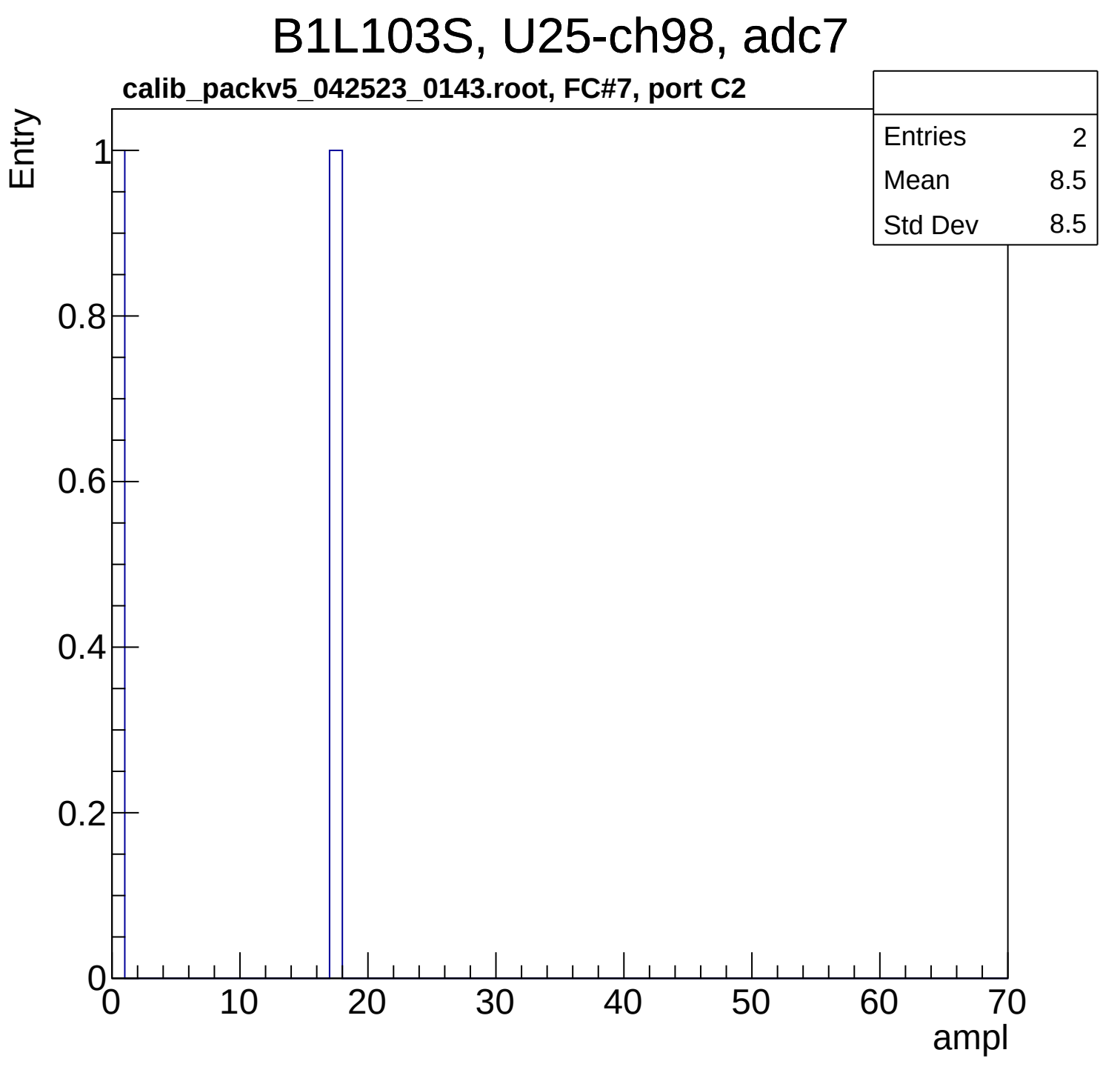
20
30
40
50
60
70


B1L103S, U25-ch98, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 8.5
Std Dev 8.5
Entry
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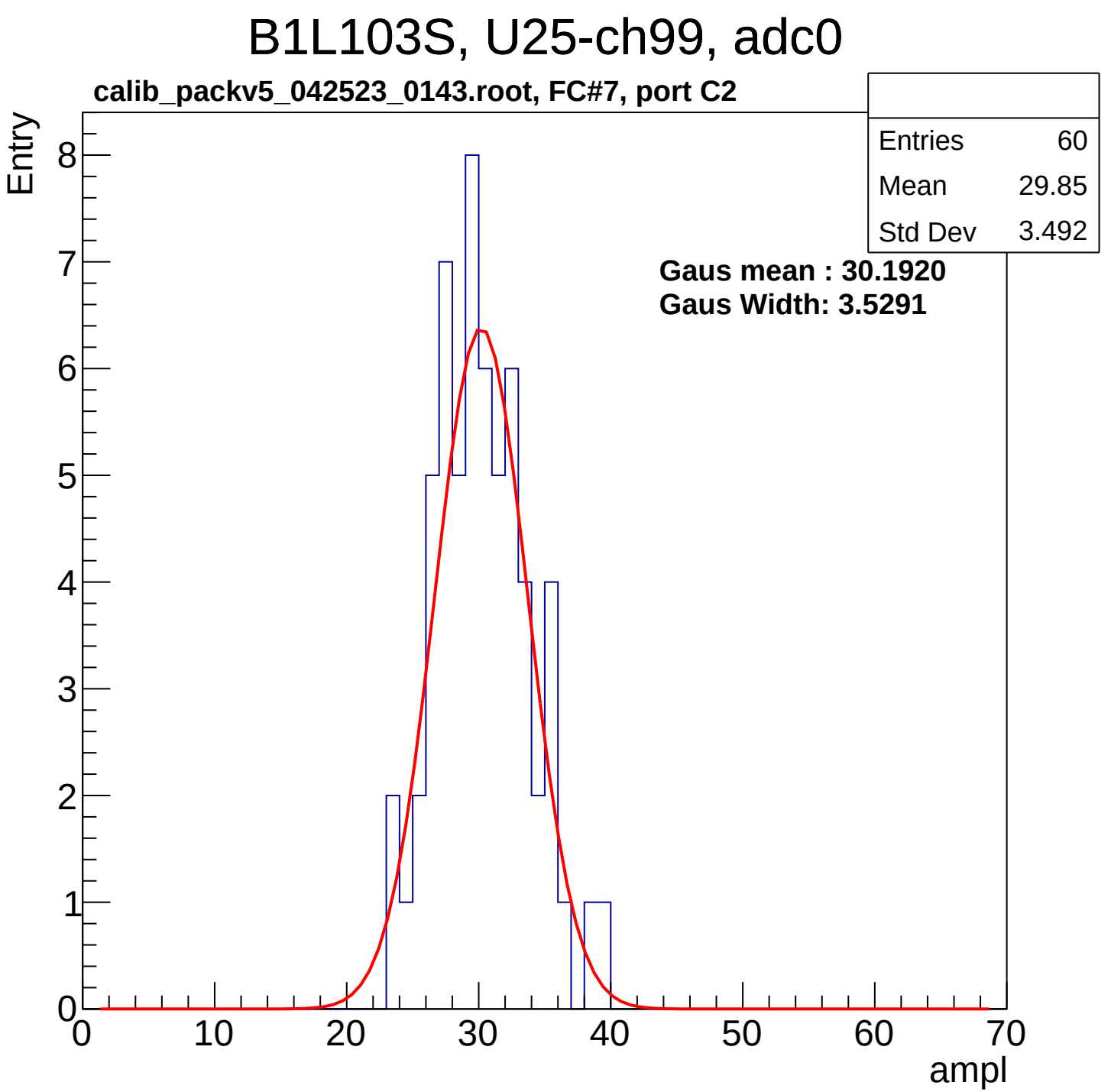
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch99, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 29.85
Std Dev 3.492
Gaus mean : 30.1920
Gaus Width: 3.5291
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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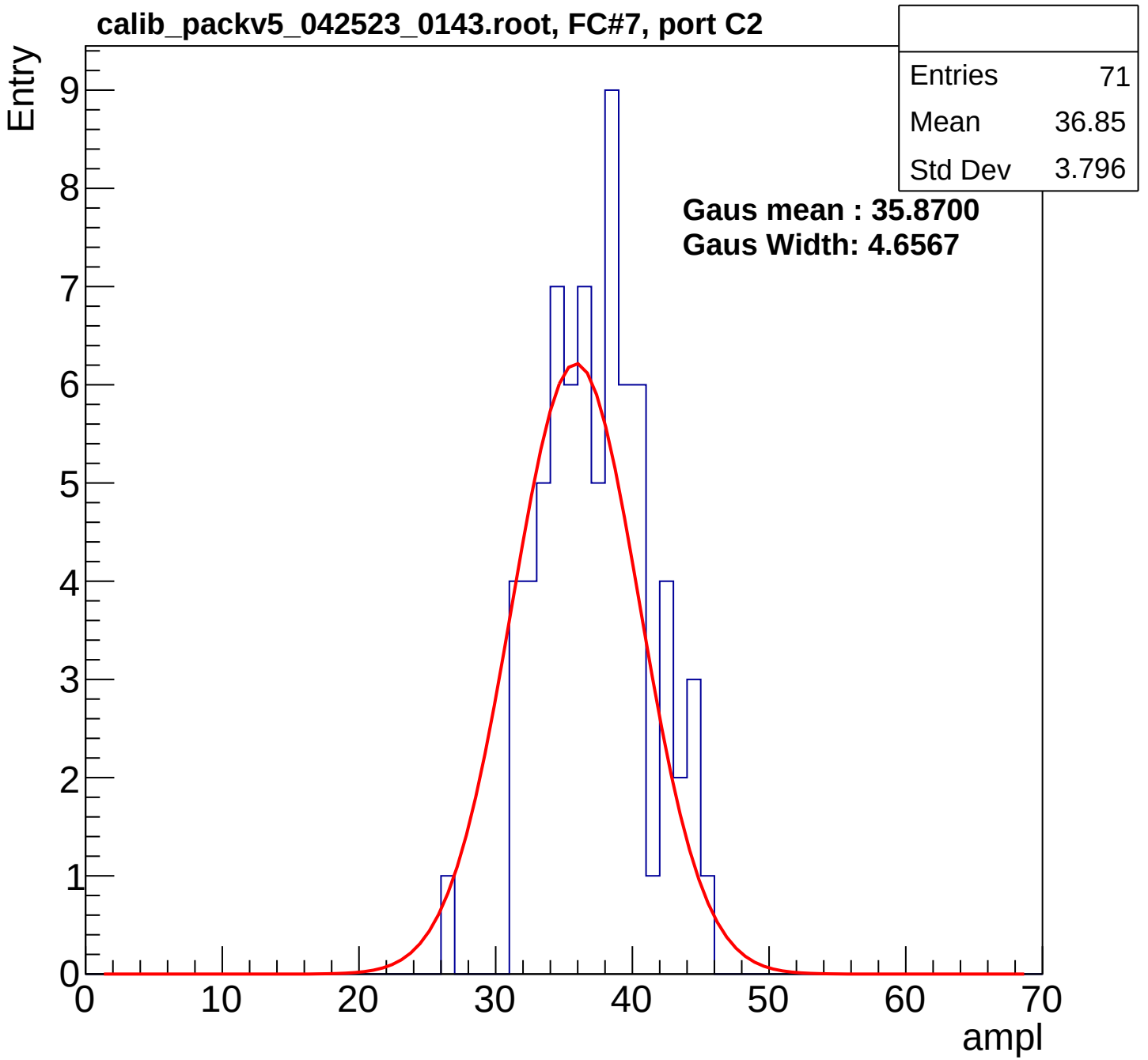
B1L103S, U25-ch99, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 36.85
Std Dev 3.796
Gaus mean : 35.8700
Gaus Width: 4.6567
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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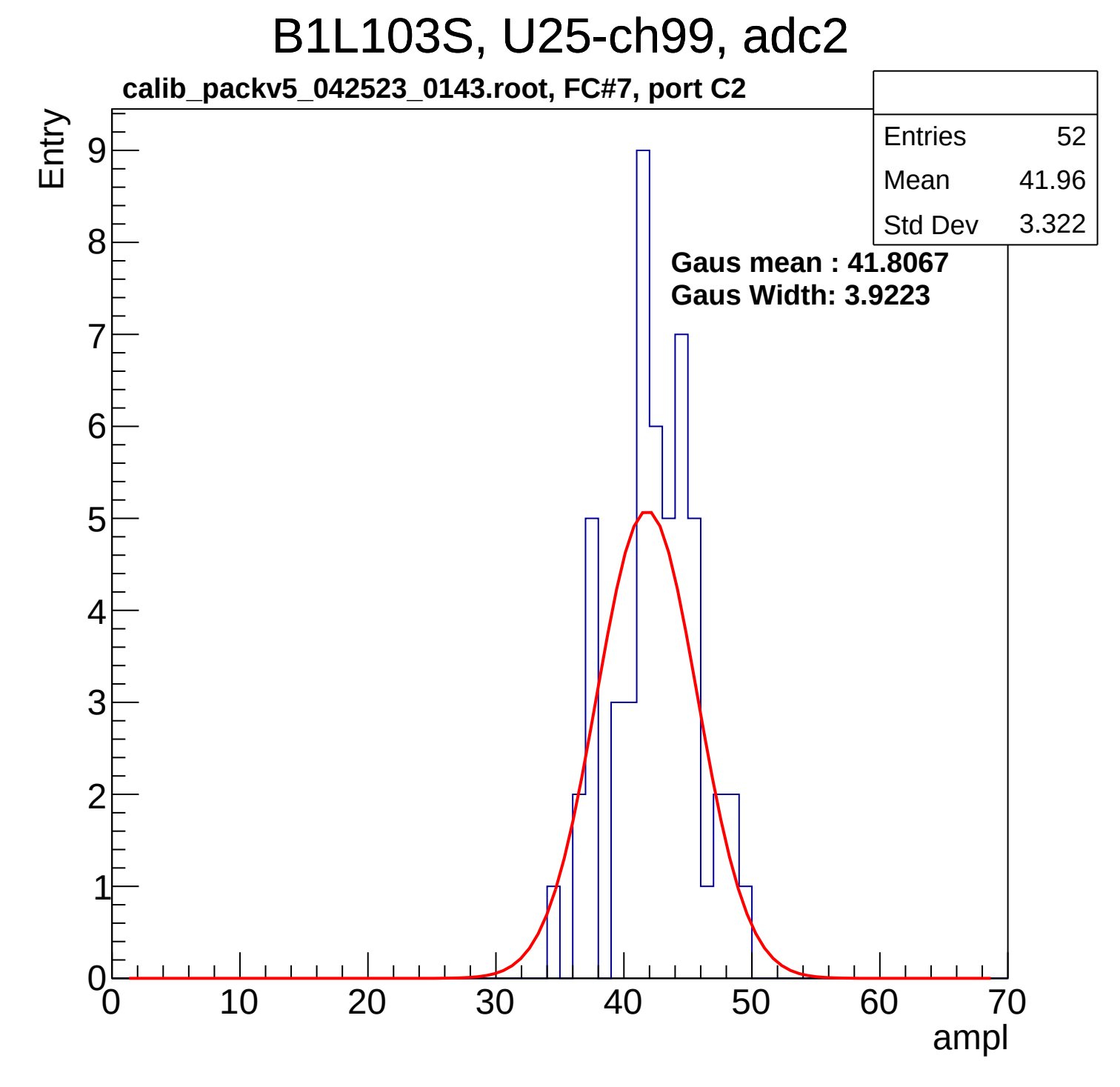
B1L103S, U25-ch99, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 41.96
Std Dev 3.322
Gaus mean : 41.8067
Gaus Width: 3.9223
Entry
ampl
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3
4
5
6
7
8
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0
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60
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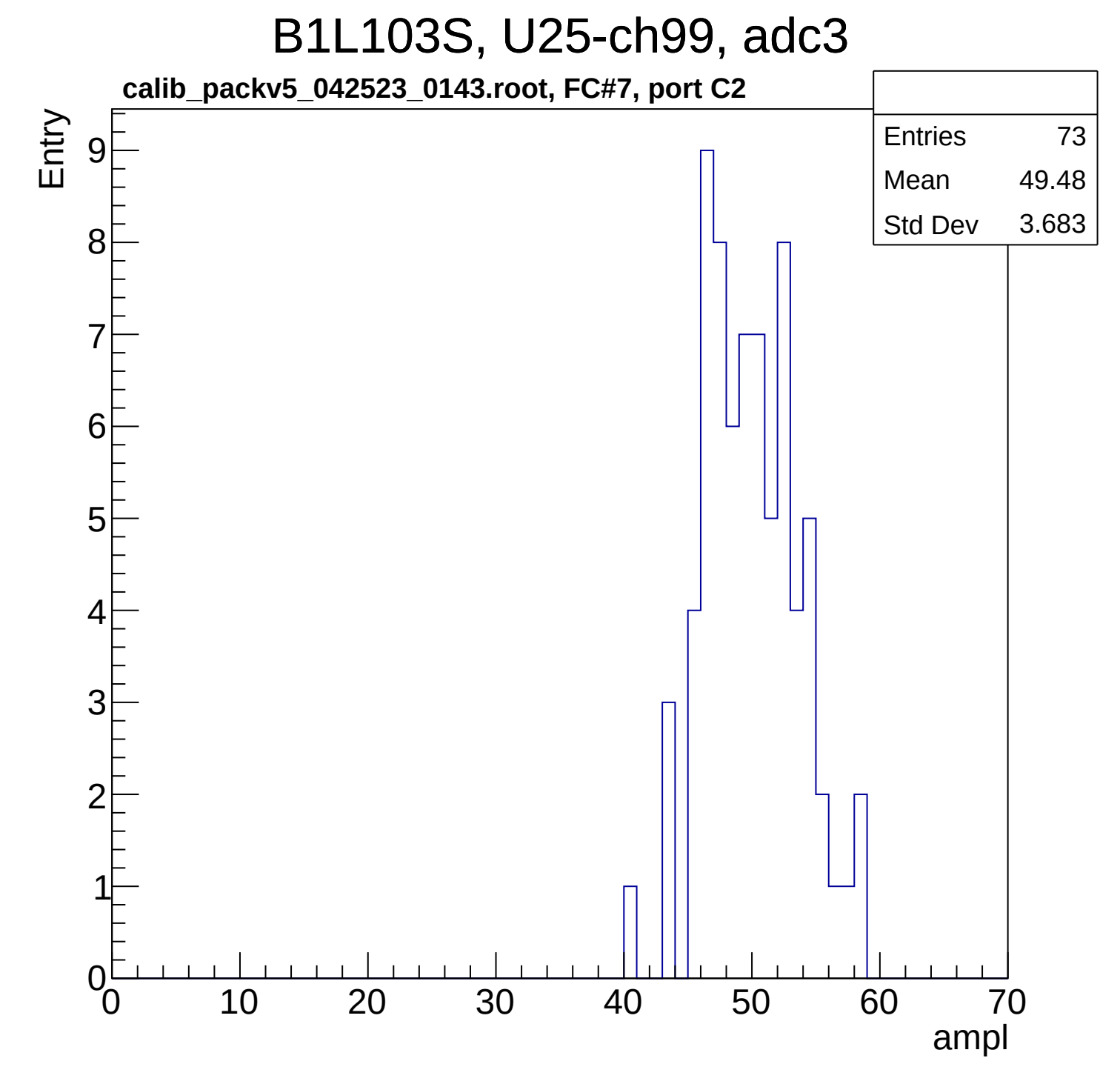
B1L103S, U25-ch99, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 49.48
Std Dev 3.683
Entry
ampl
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3
4
5
6
7
8
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0
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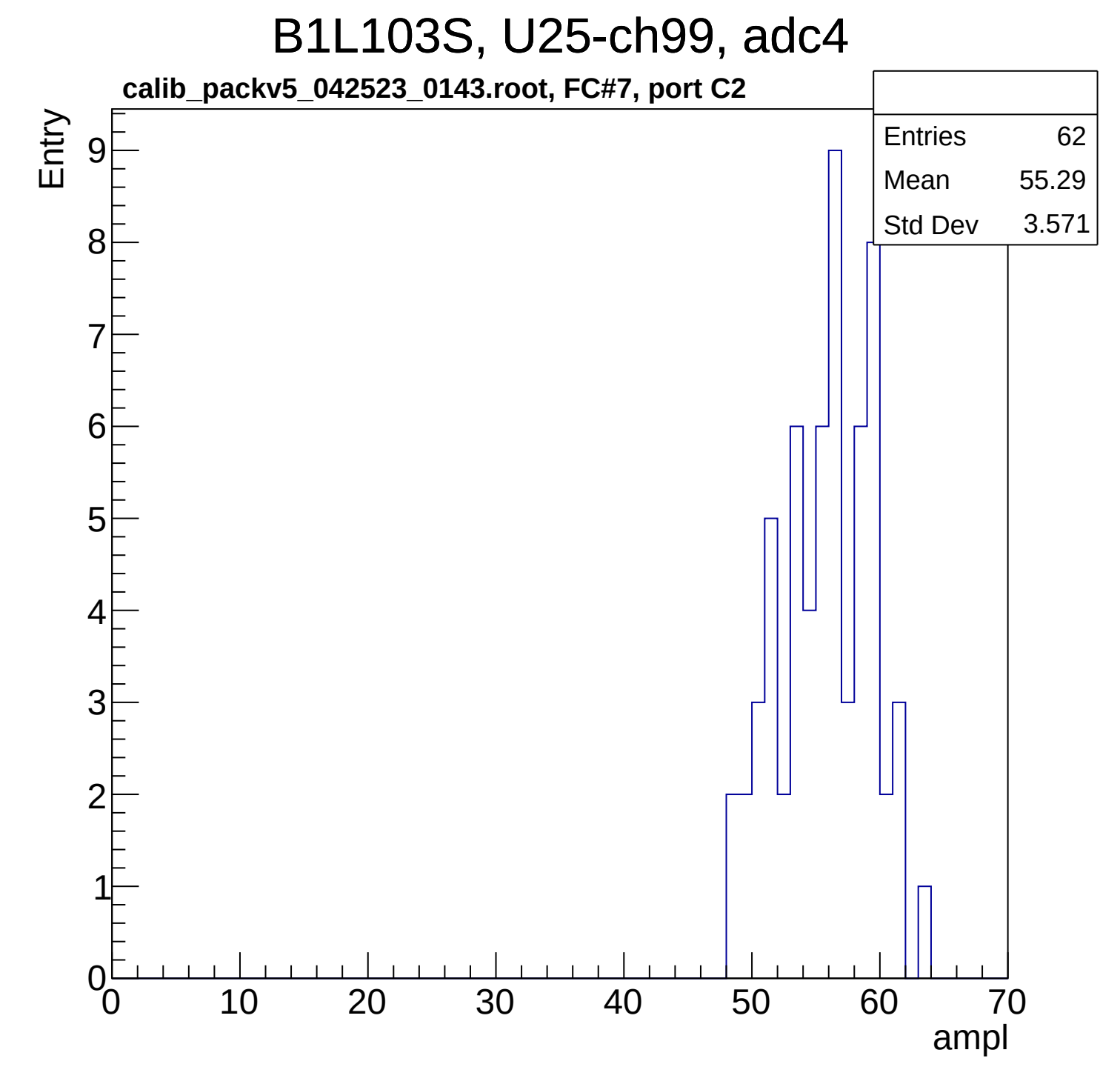
B1L103S, U25-ch99, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.29
Std Dev 3.571
Entry
ampl
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3
4
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6
7
8
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0
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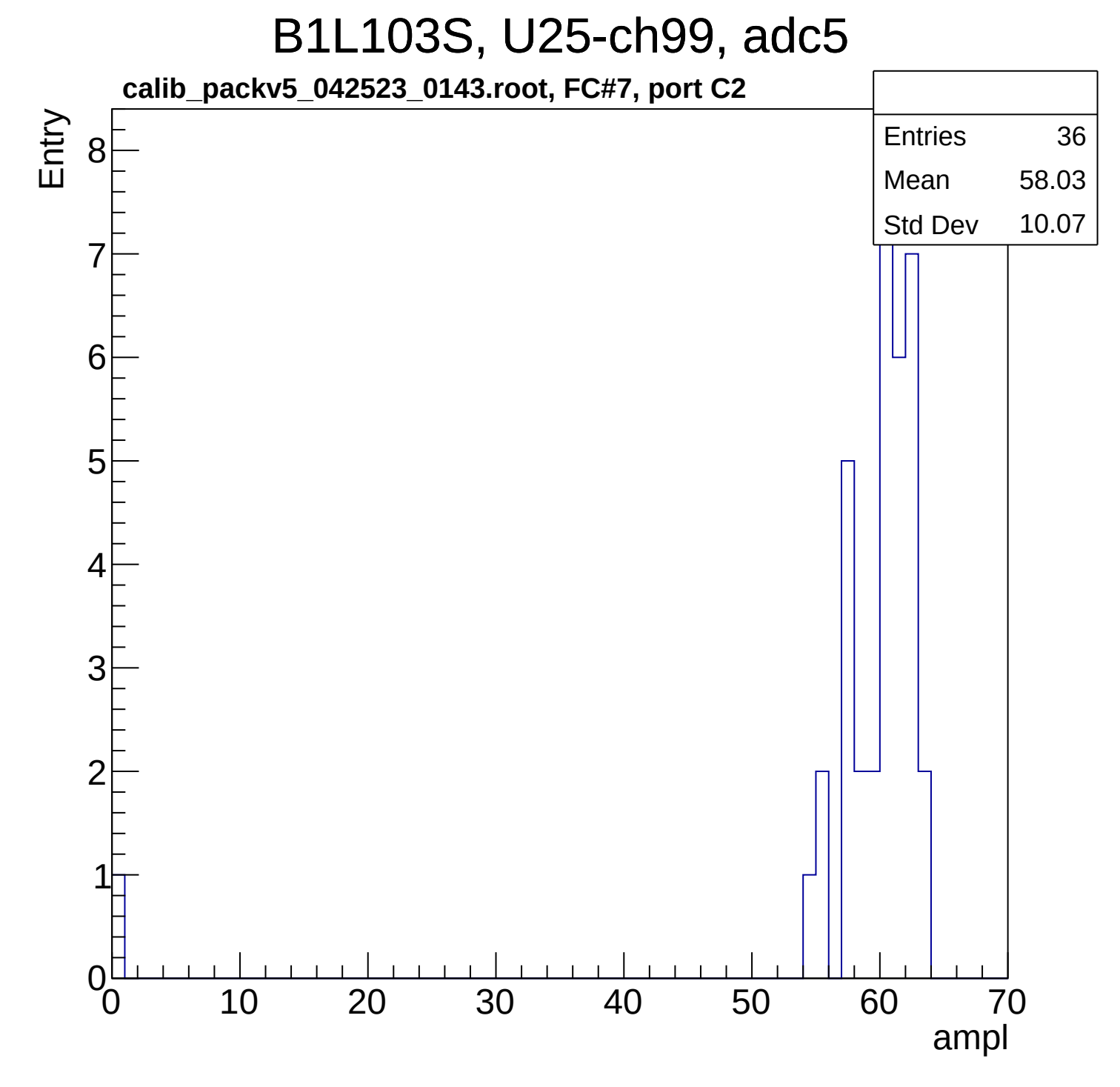
B1L103S, U25-ch99, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 36
Mean 58.03
Std Dev 10.07
Entry
ampl
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4
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7
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0
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20
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60
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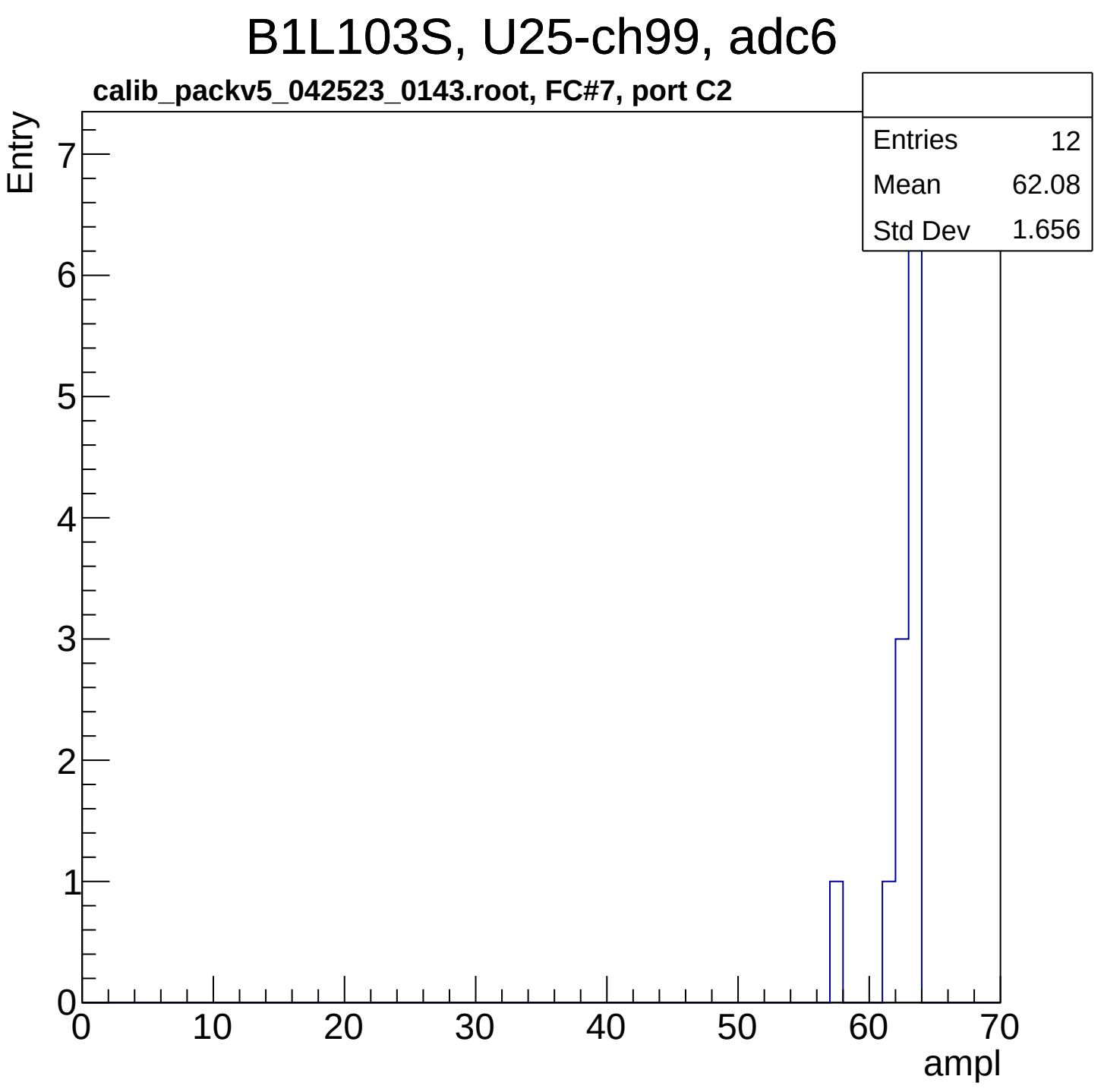
B1L103S, U25-ch99, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 62.08
Std Dev 1.656
Entry
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2
3
4
5
6
7
0
10
20
30
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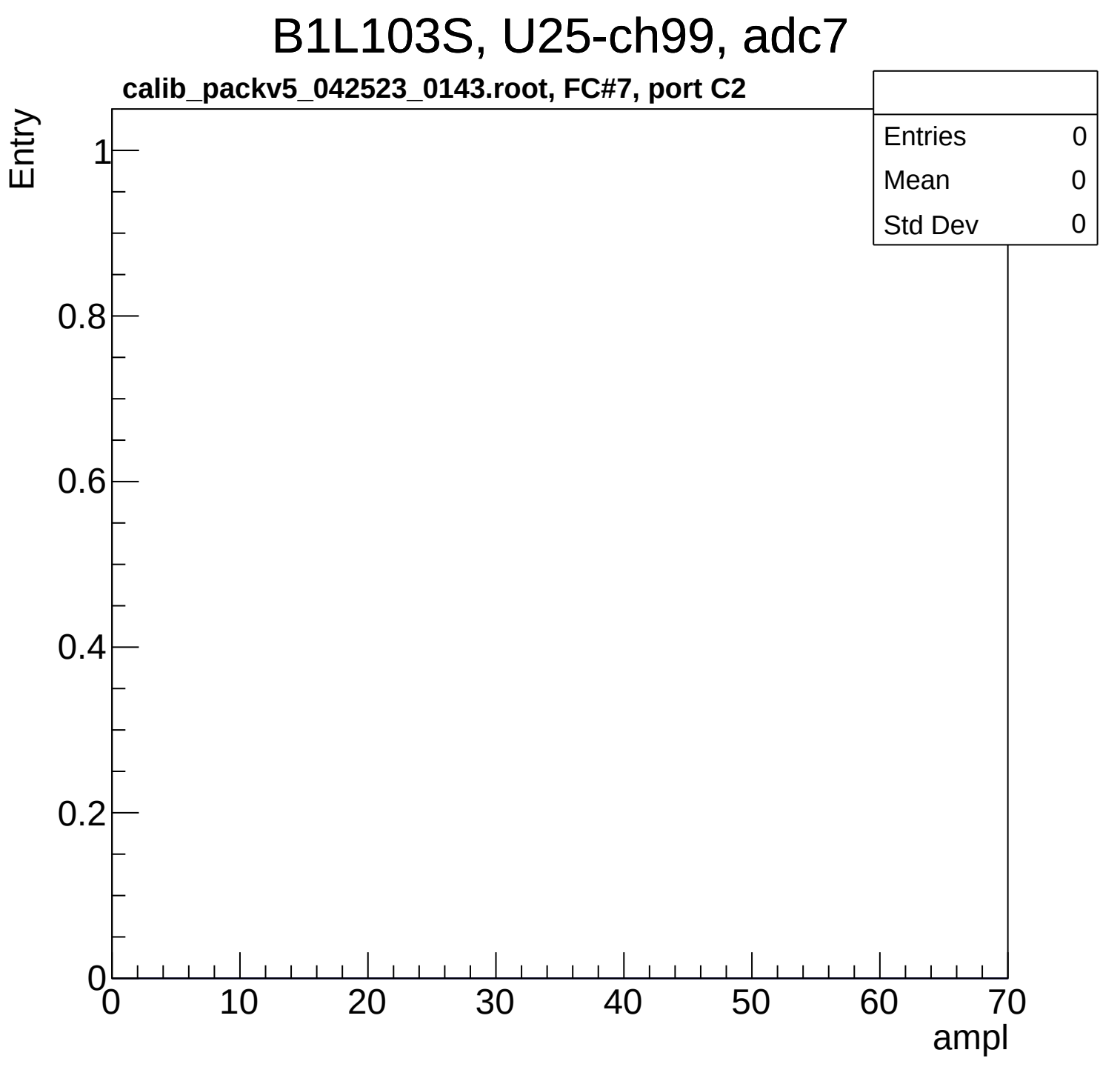
50
60
70
ampl


B1L103S, U25-ch99, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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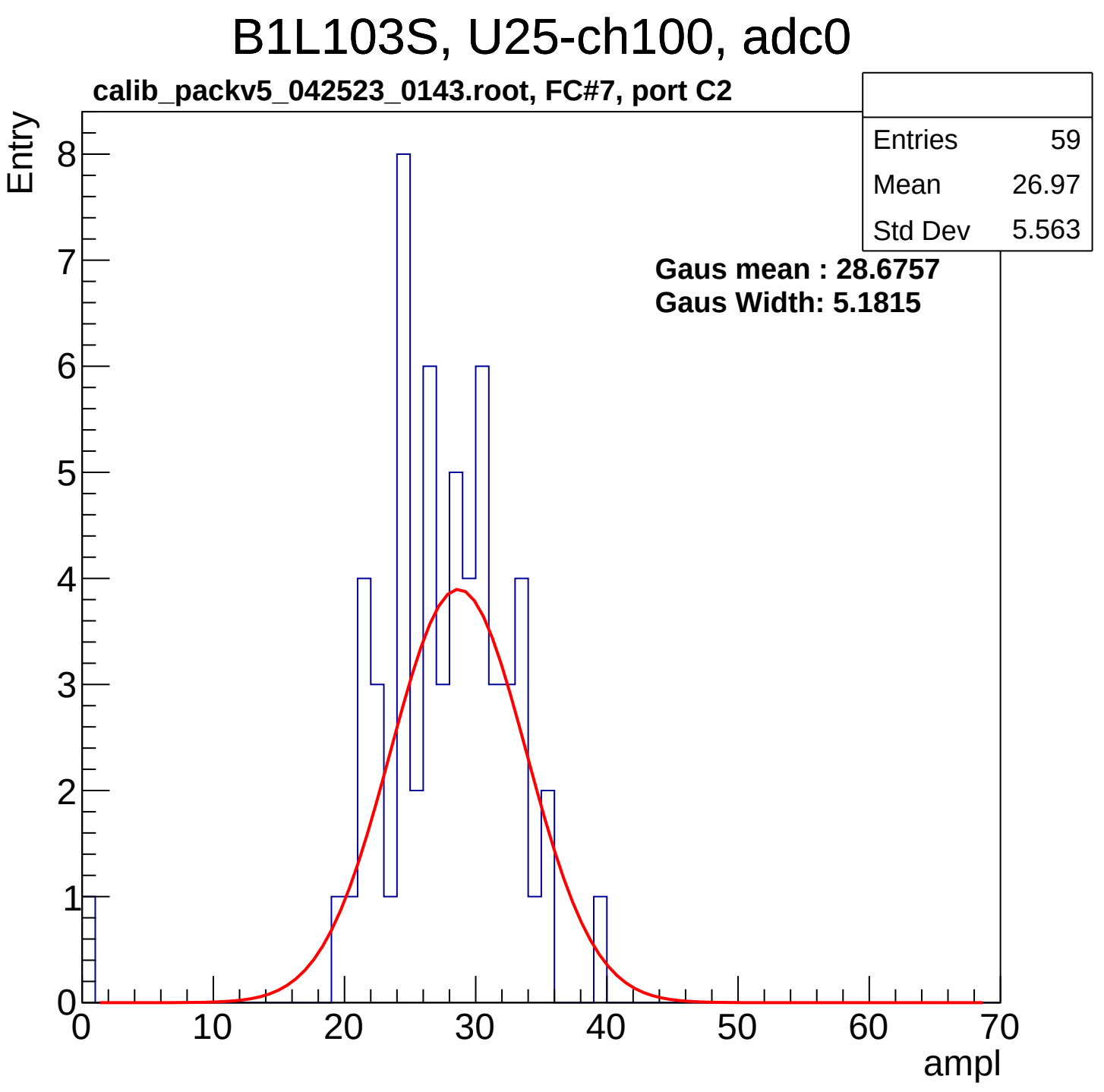
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch100, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 26.97
Std Dev 5.563
Gaus mean : 28.6757
Gaus Width: 5.1815
Entry
ampl
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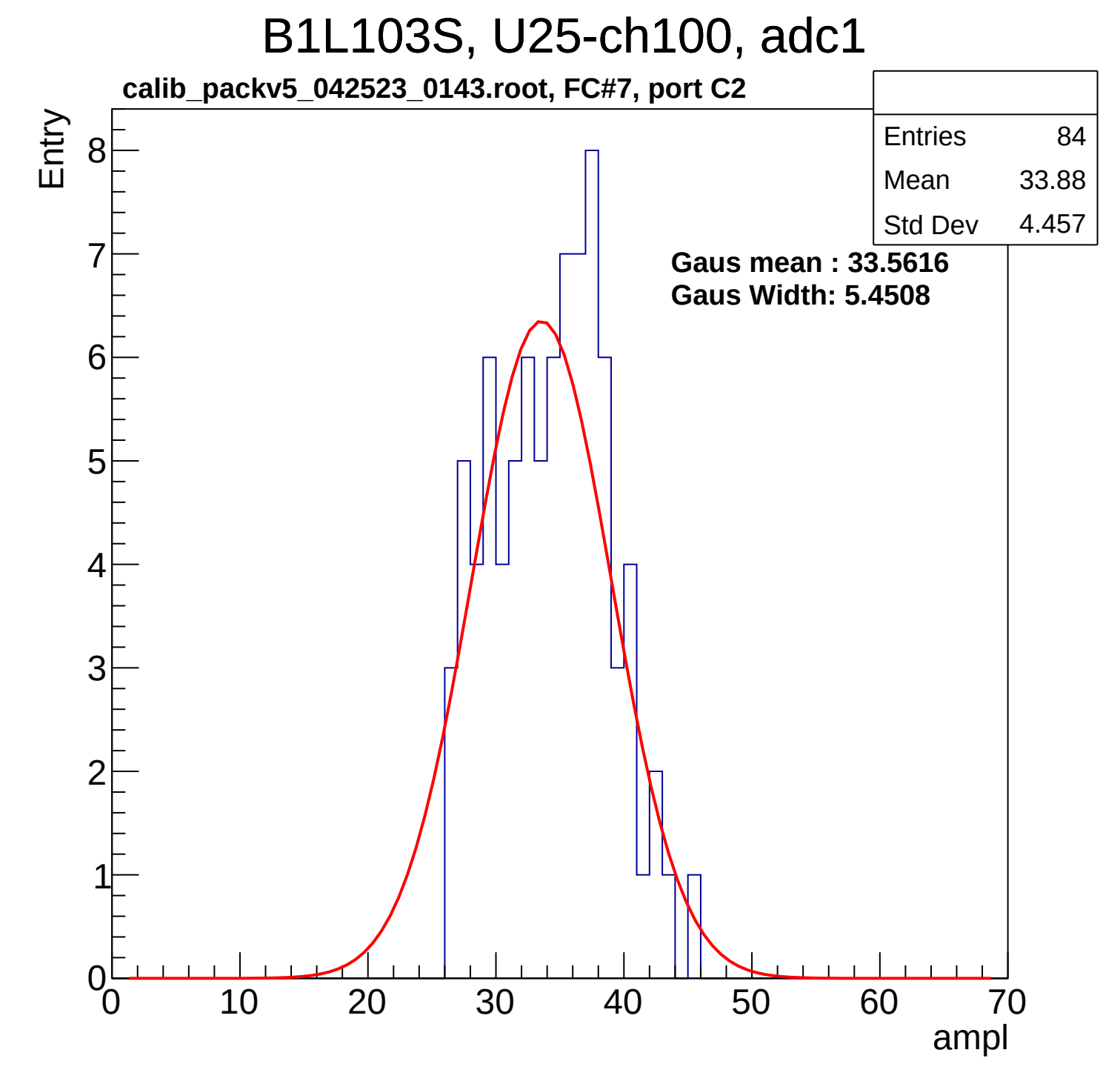
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch100, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 33.88
Std Dev 4.457
Gaus mean : 33.5616
Gaus Width: 5.4508
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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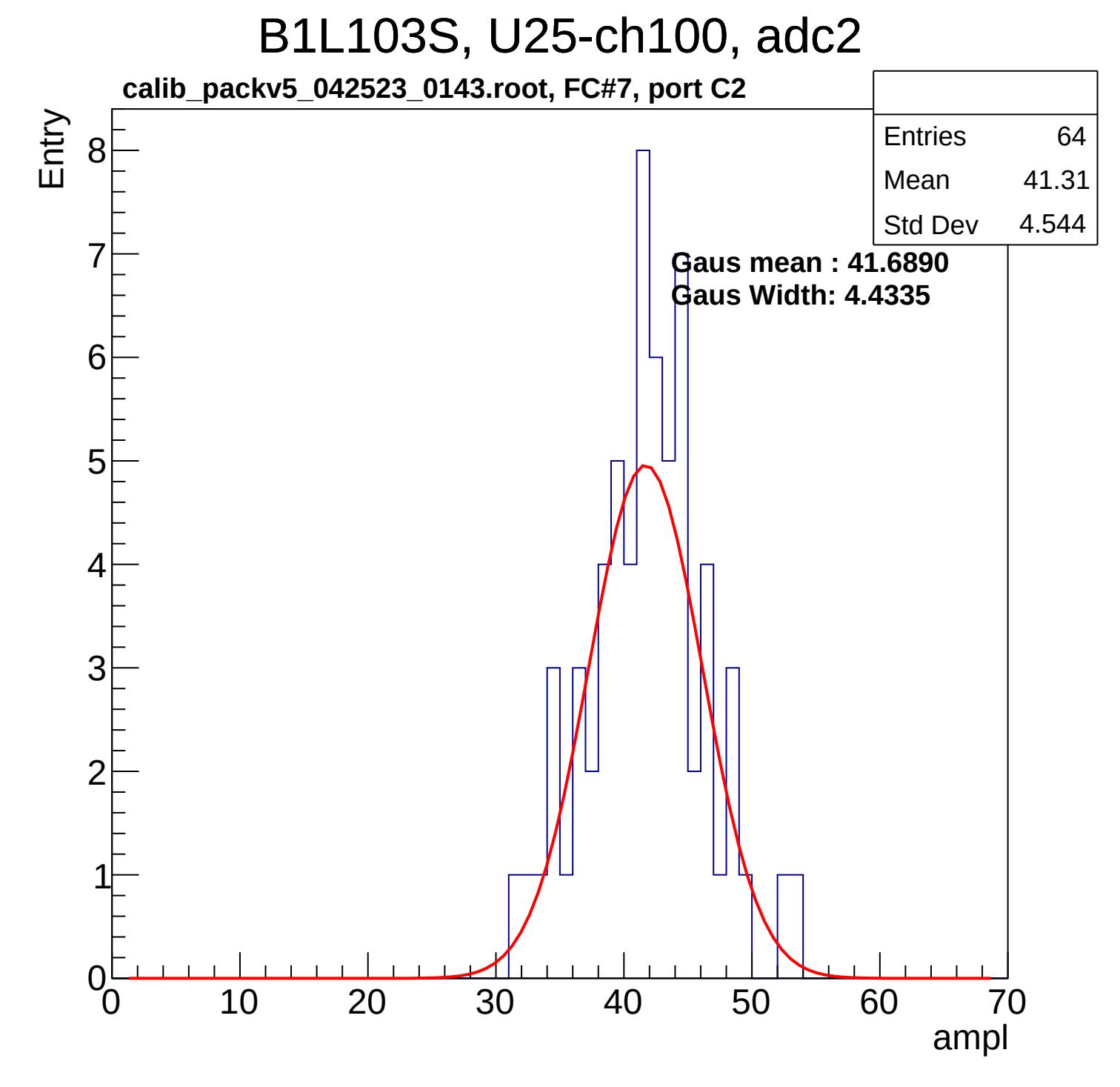
B1L103S, U25-ch100, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 41.31
Std Dev 4.544
Gaus mean : 41.6890
Gaus Width: 4.4335
Entry
ampl
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1
2
3
4
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6
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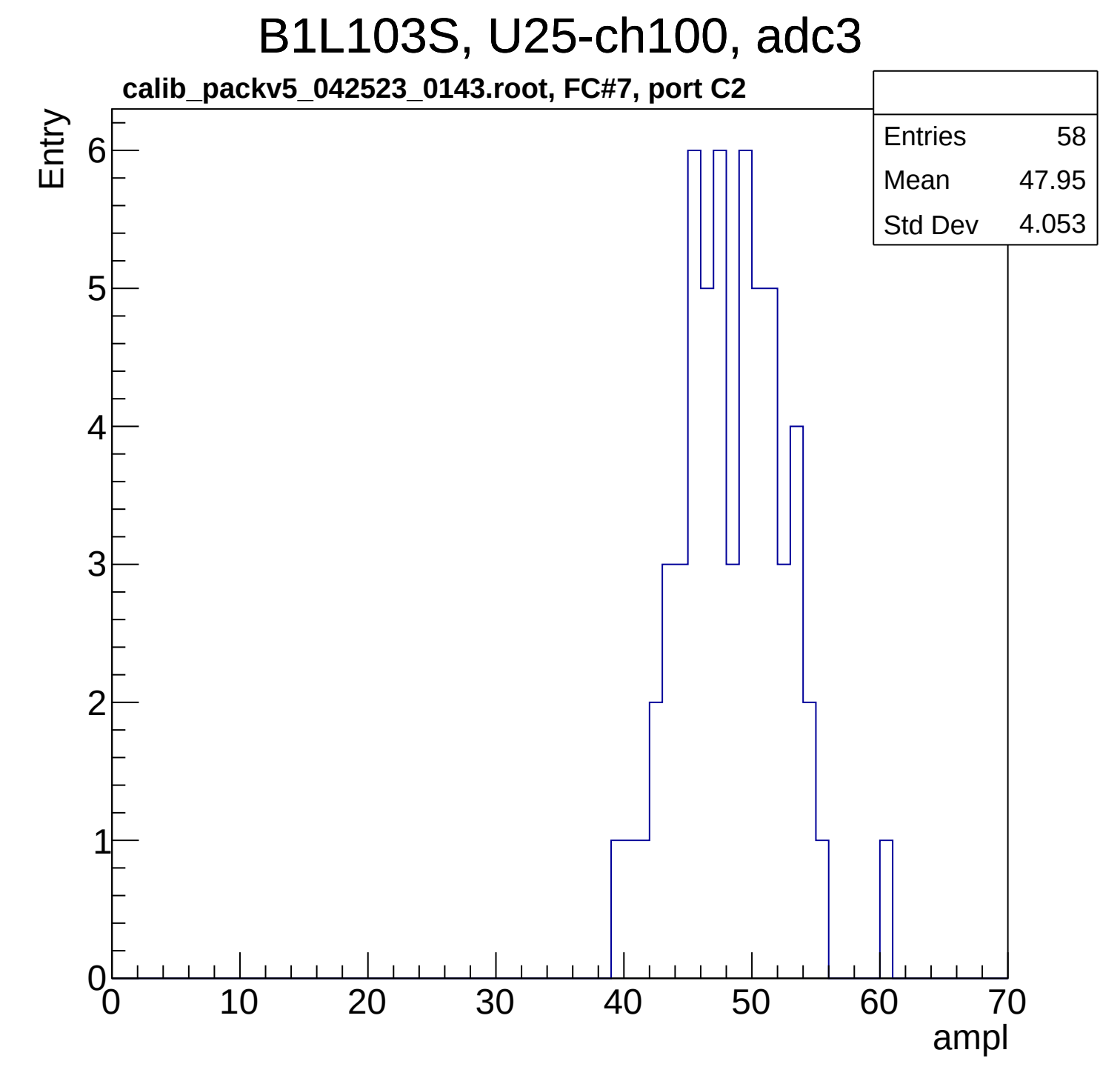
B1L103S, U25-ch100, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 47.95
Std Dev 4.053
Entry
ampl
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2
3
4
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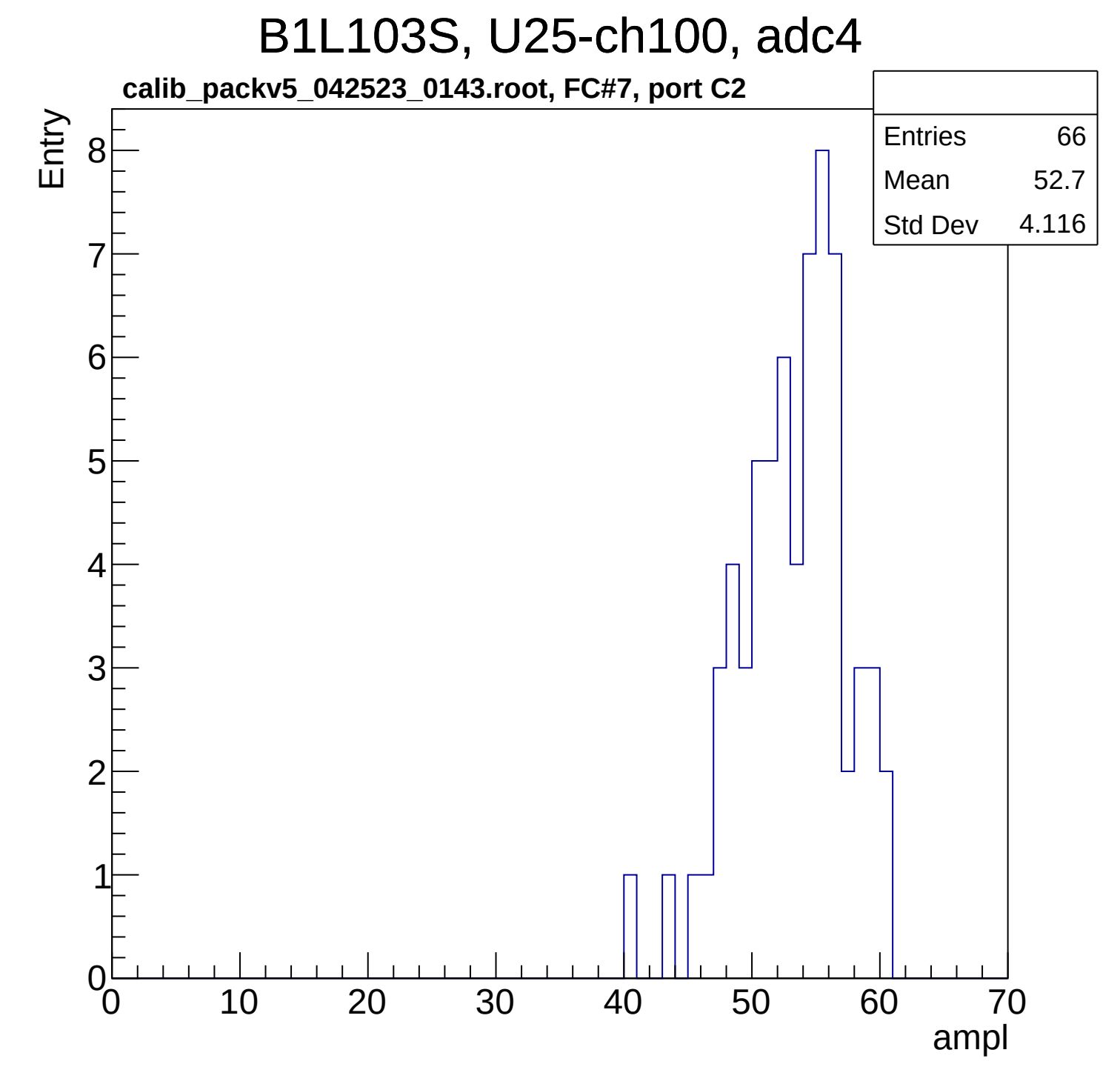
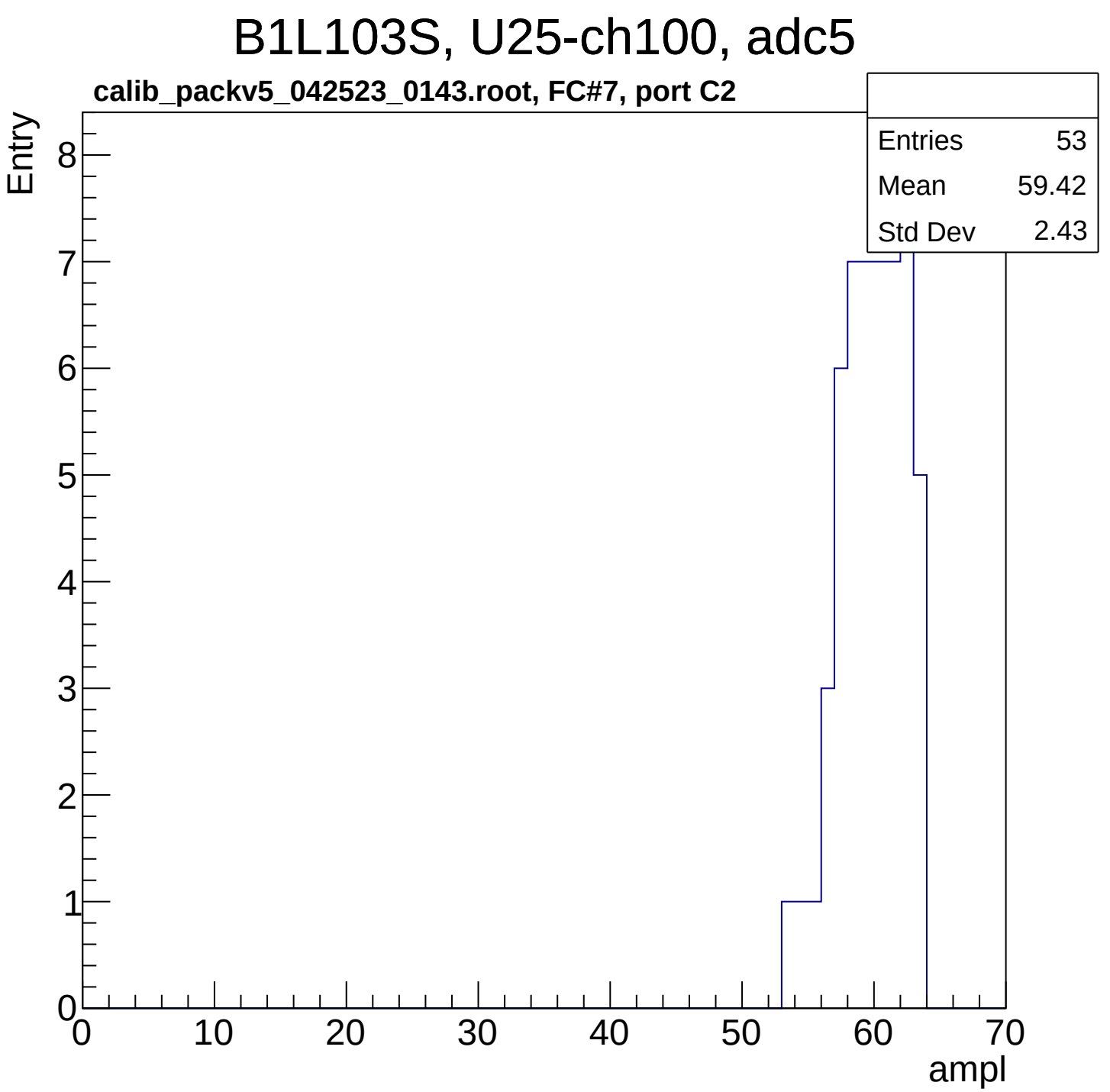

B1L103S, U25-ch100, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 52.7
Std Dev 4.116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch100, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 59.42
Std Dev 2.43
Entry
ampl
0
1
2
3
4
5
6
7
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40
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60
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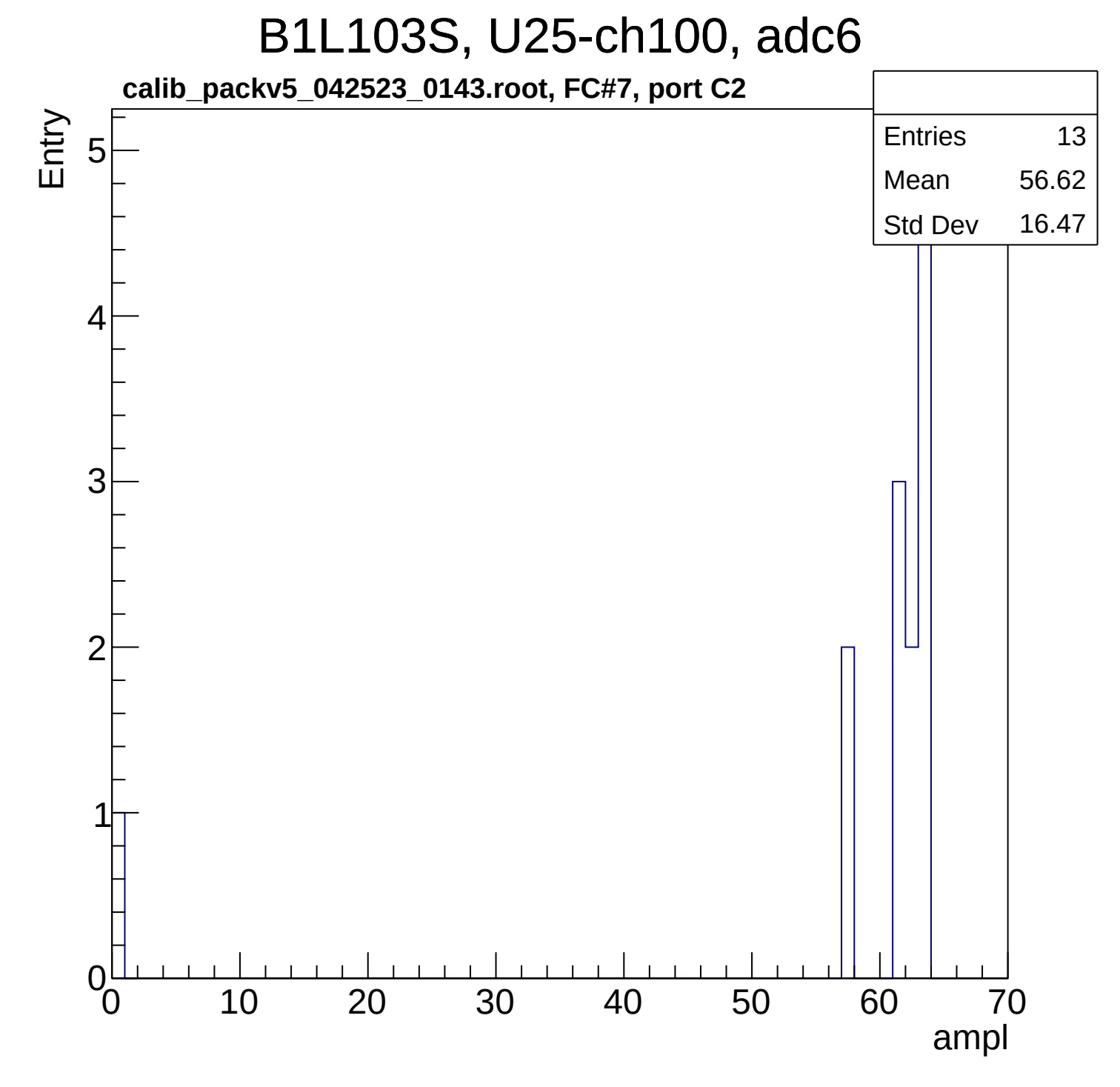
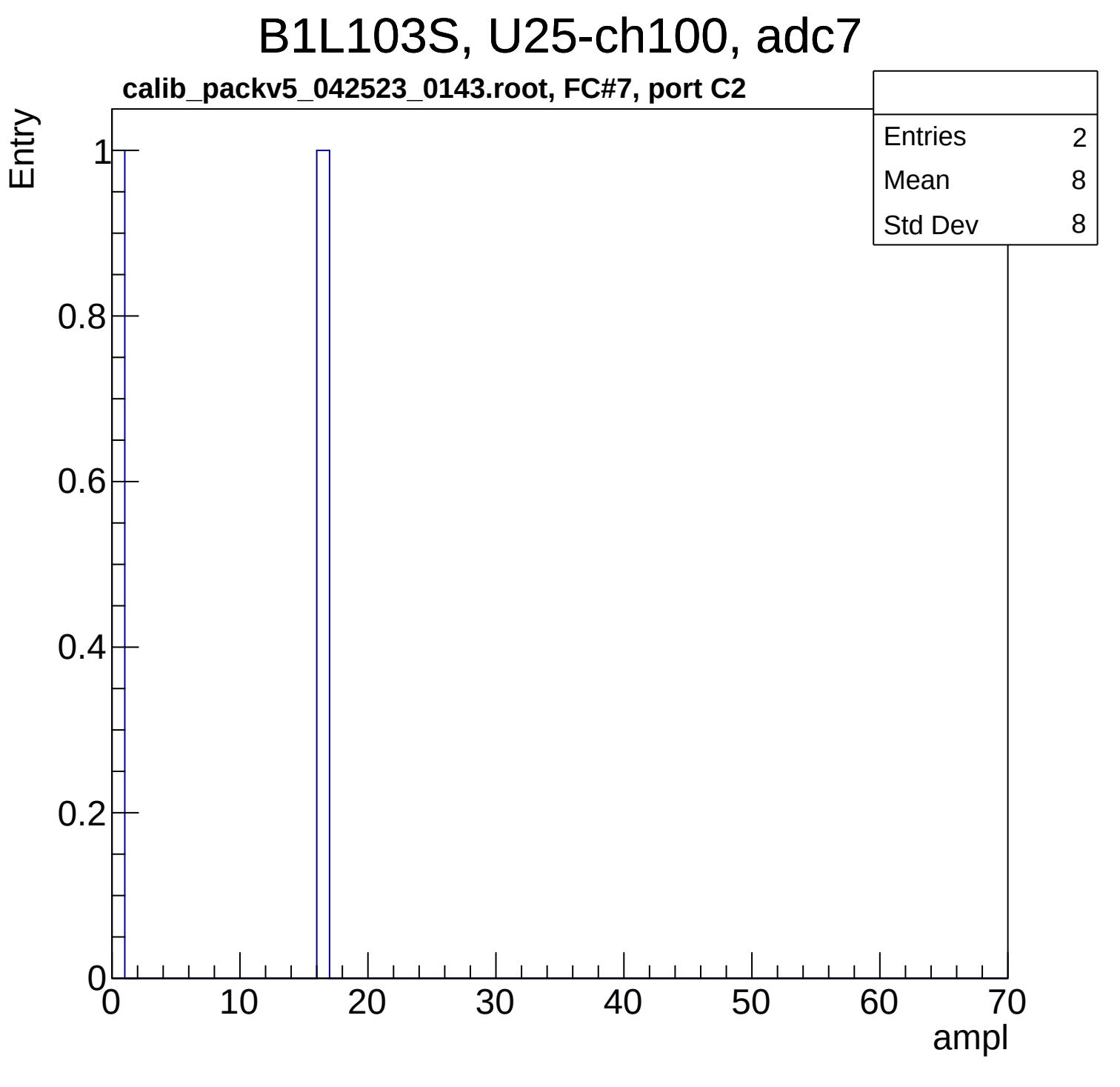

B1L103S, U25-ch100, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.62
Std Dev 16.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch100, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 8
Std Dev 8
Entry
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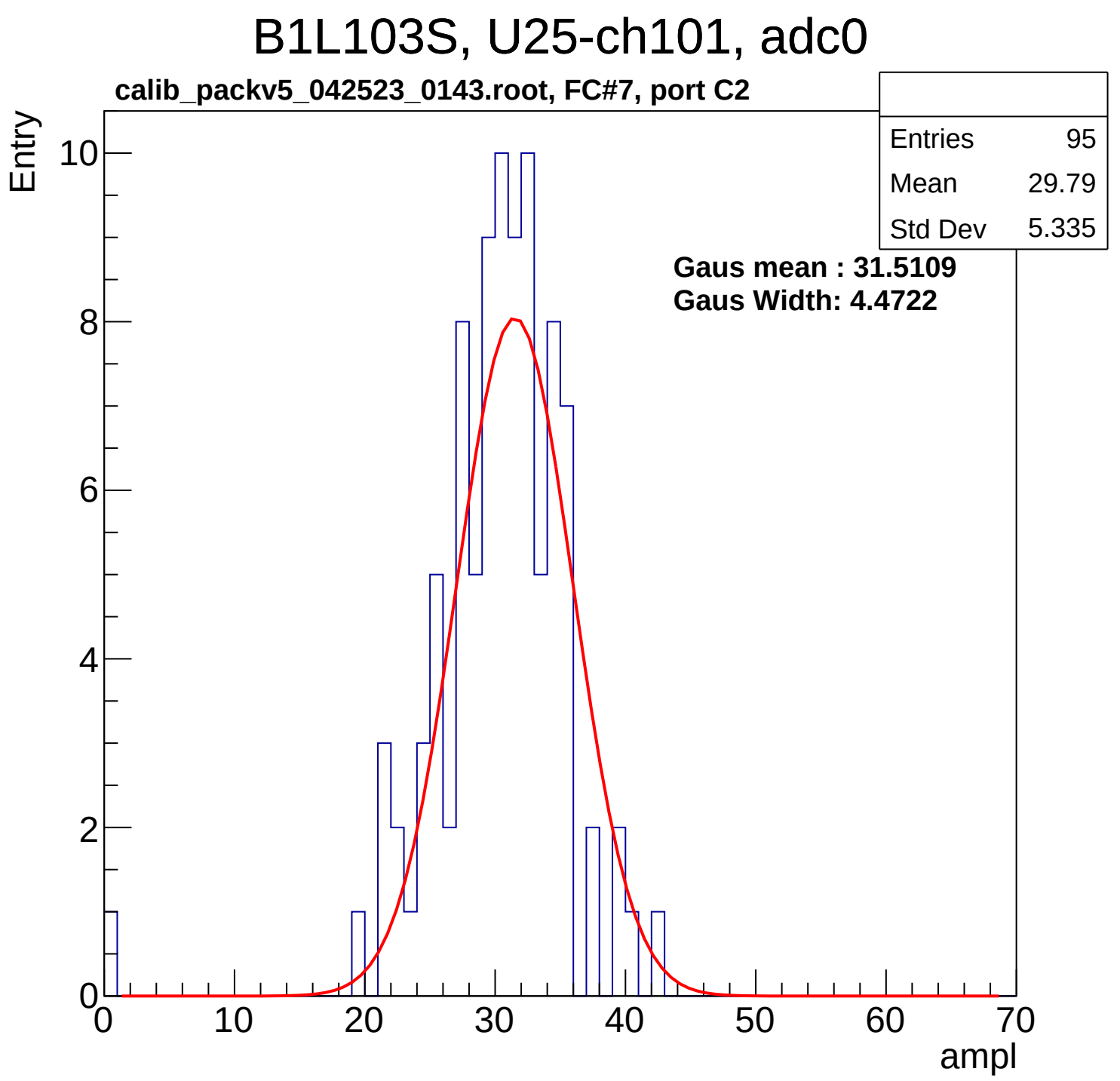
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch101, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 95
Mean 29.79
Std Dev 5.335
Gaus mean : 31.5109
Gaus Width: 4.4722
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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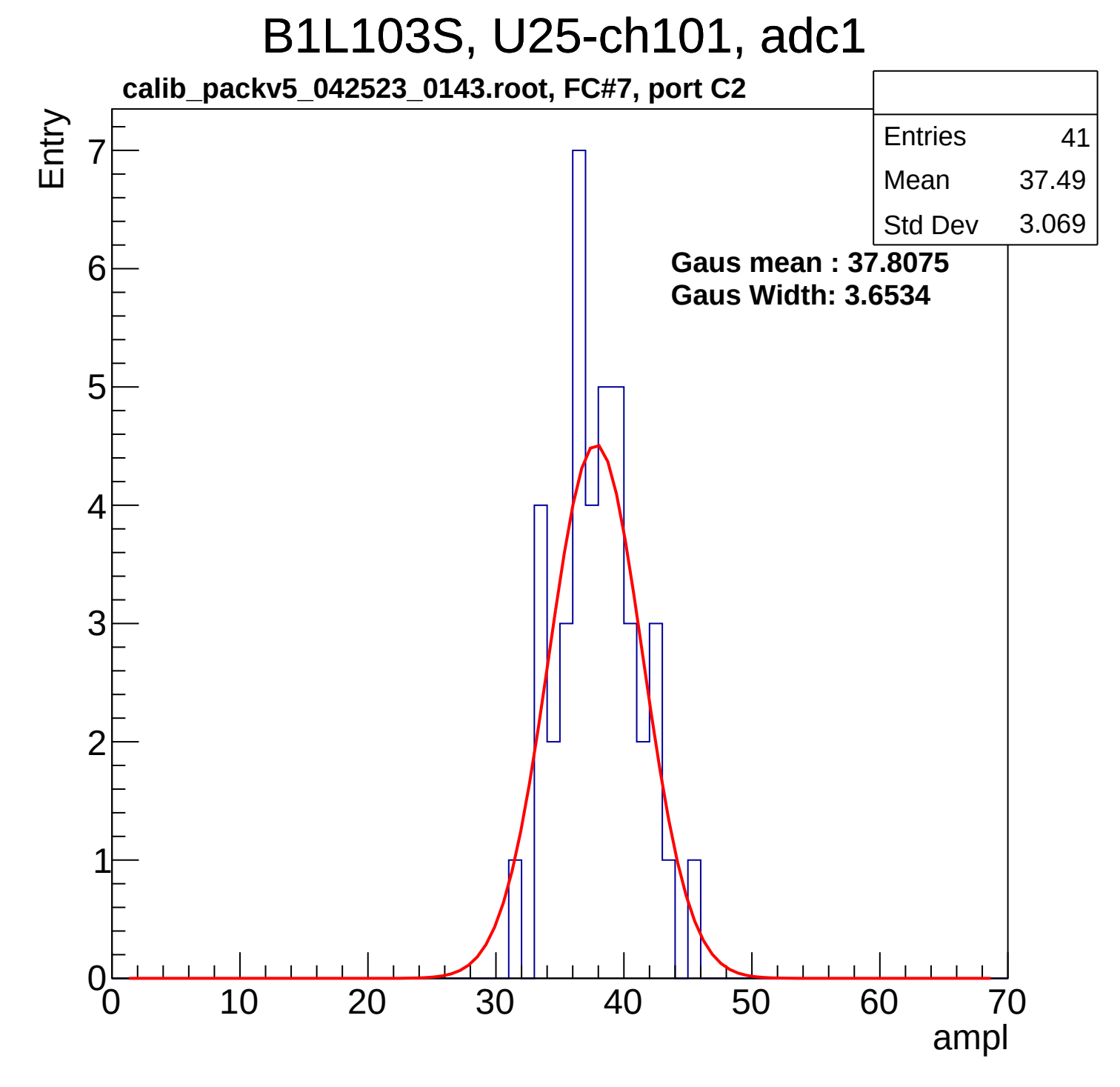
B1L103S, U25-ch101, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 37.49
Std Dev 3.069
Gaus mean : 37.8075
Gaus Width: 3.6534
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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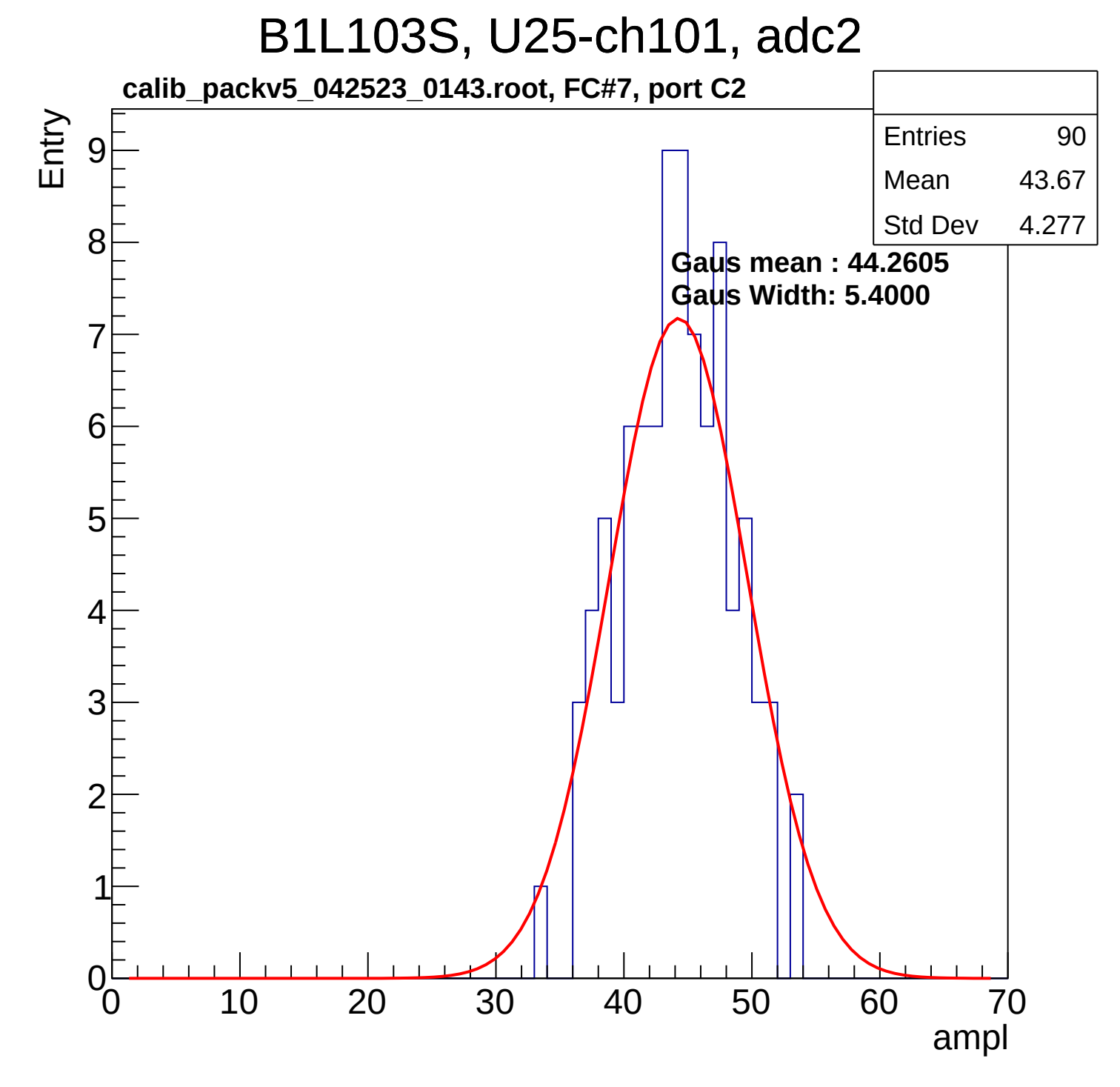
B1L103S, U25-ch101, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 43.67
Std Dev 4.277
Gaus mean : 44.2605
Gaus Width: 5.4000
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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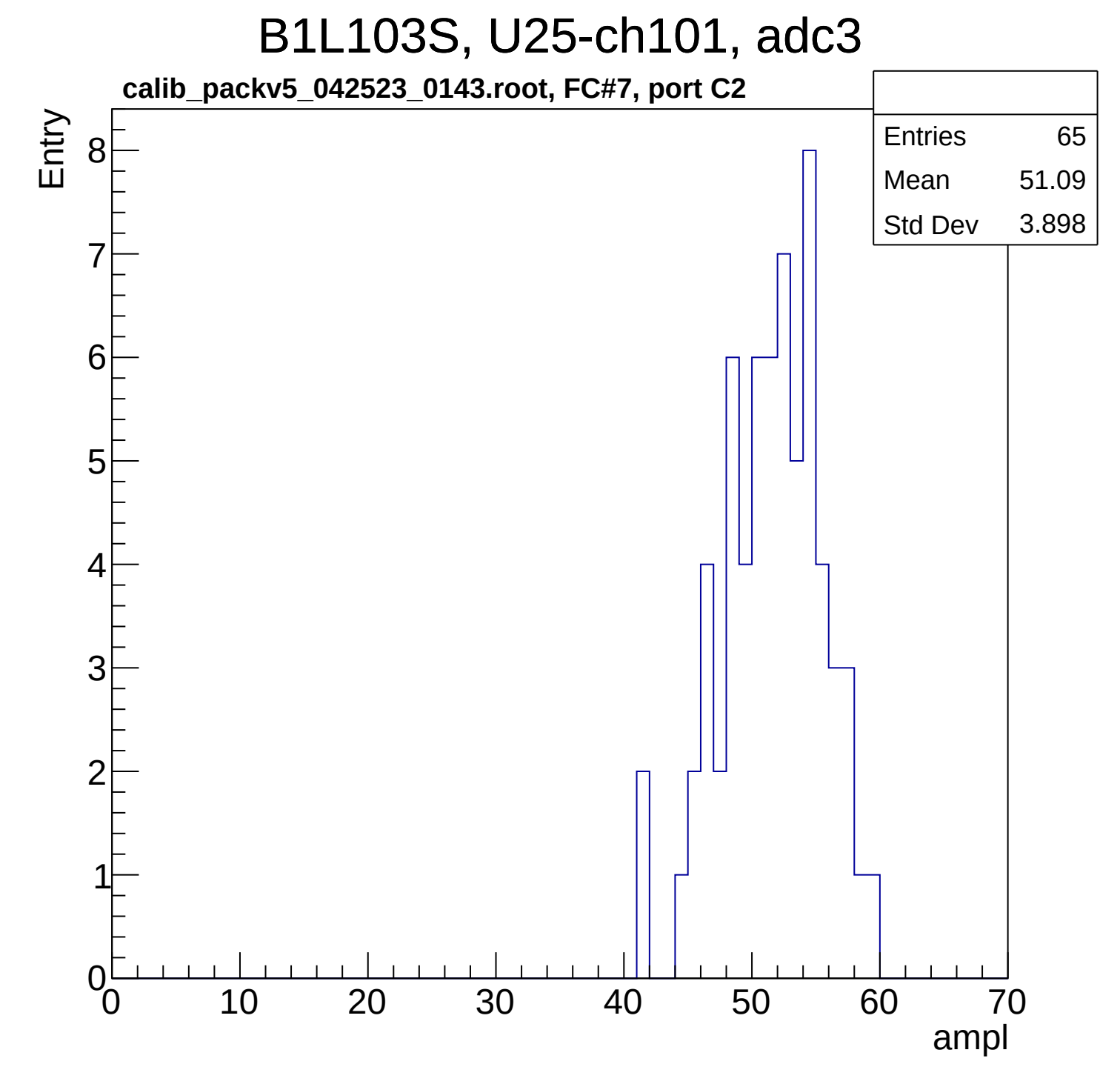
B1L103S, U25-ch101, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 51.09
Std Dev 3.898
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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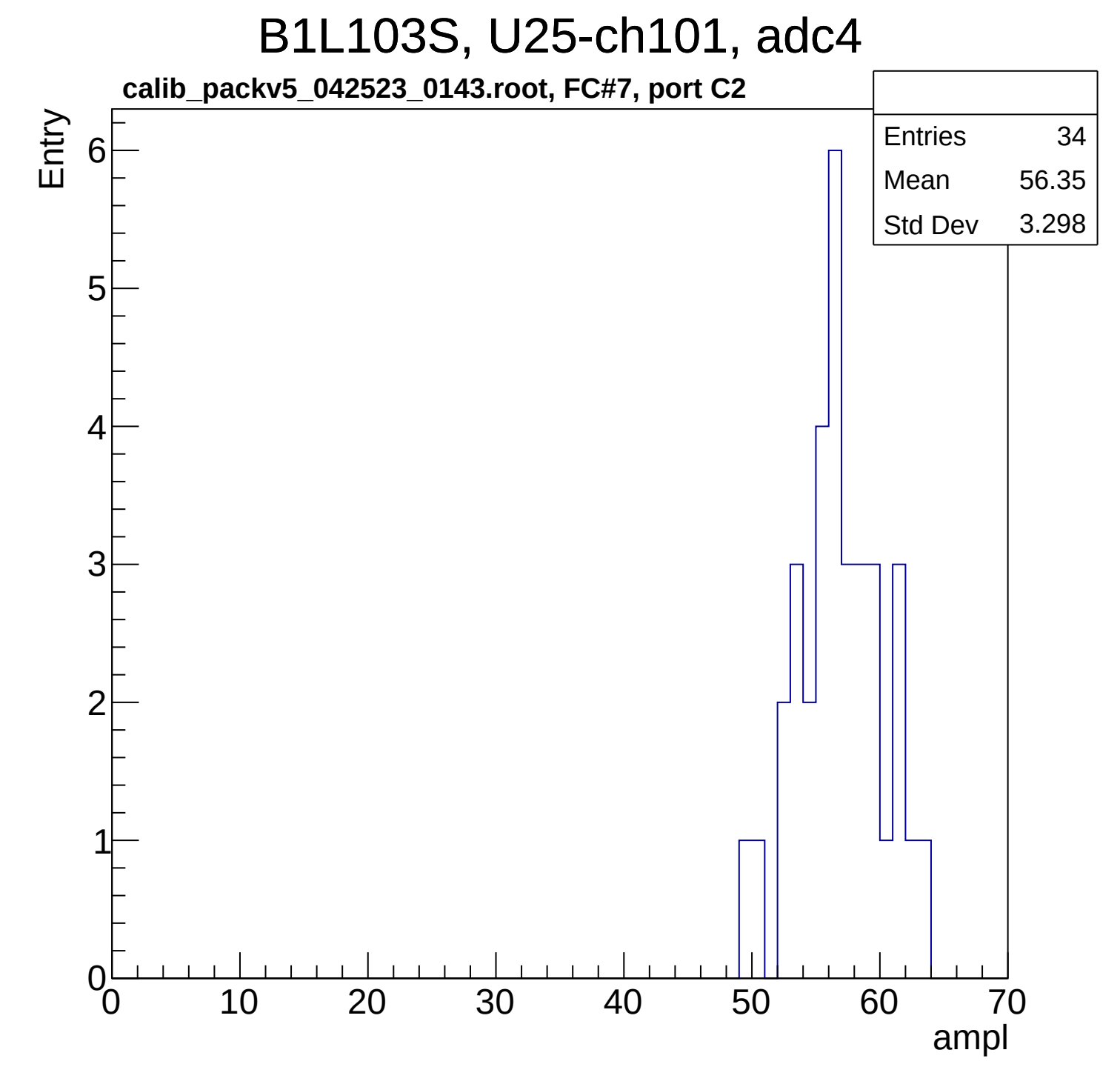
B1L103S, U25-ch101, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 56.35
Std Dev 3.298
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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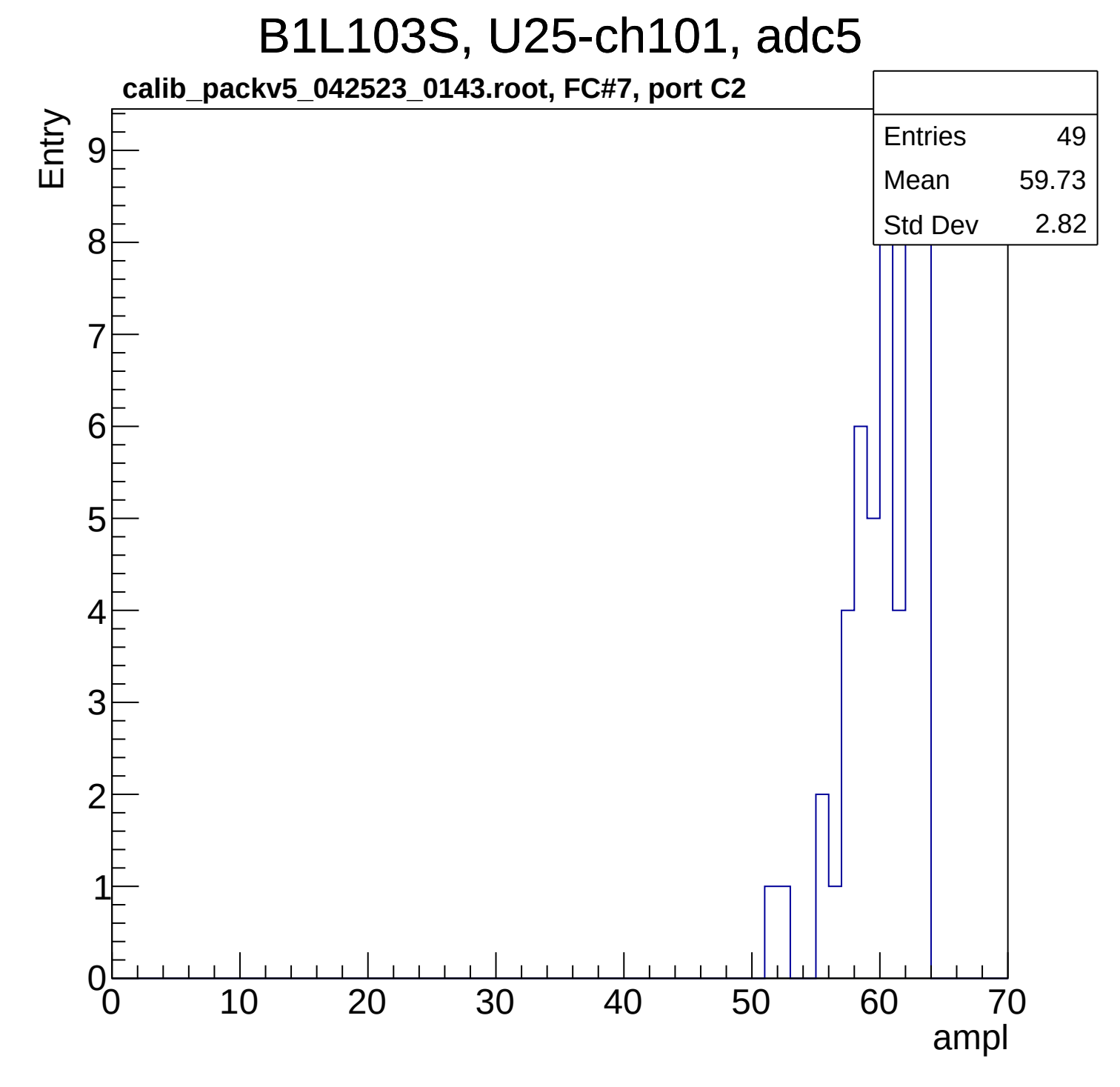
B1L103S, U25-ch101, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 59.73
Std Dev 2.82
Entry
ampl
0
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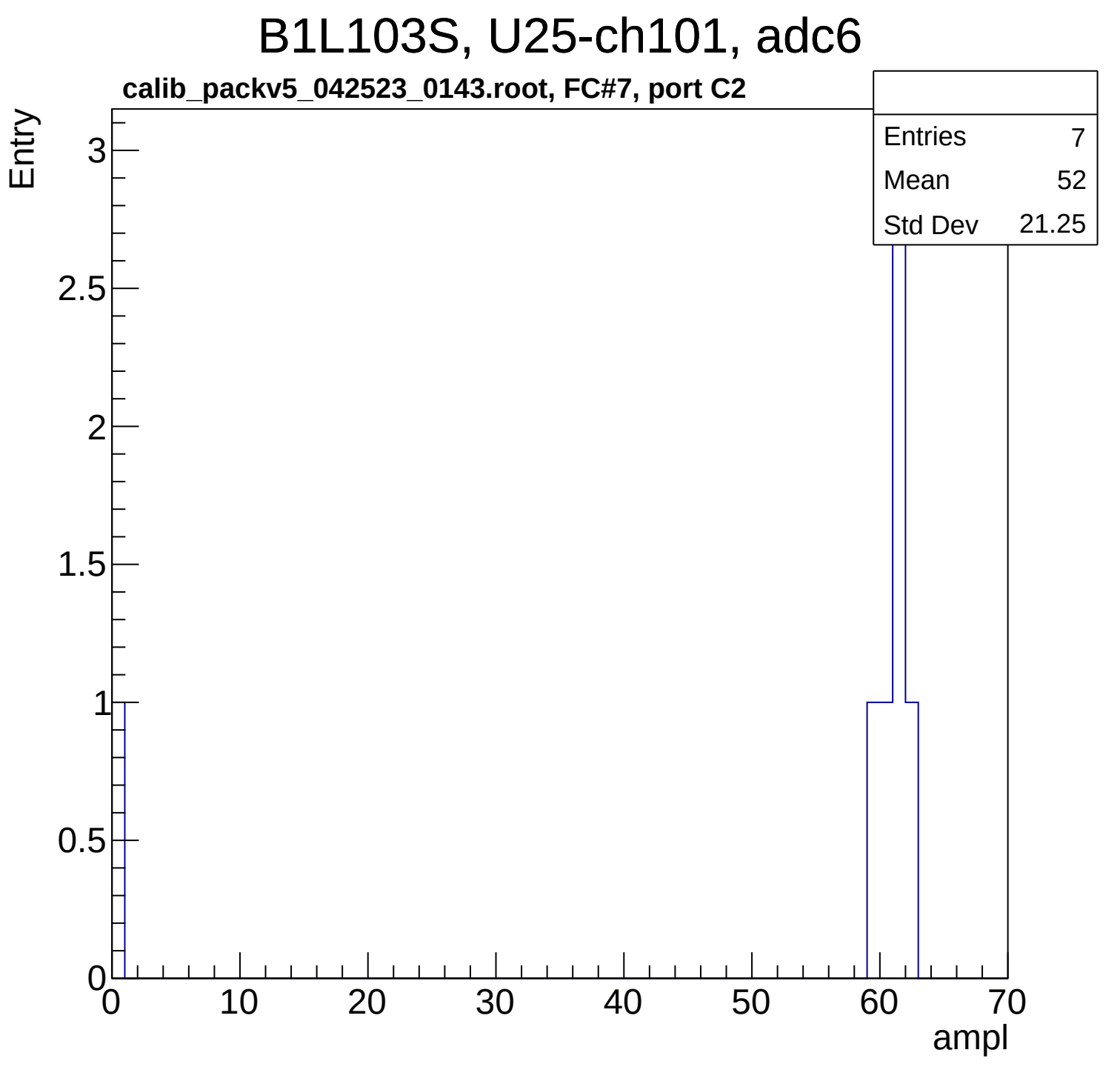
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch101, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 7
Mean 52
Std Dev 21.25
Entry
ampl
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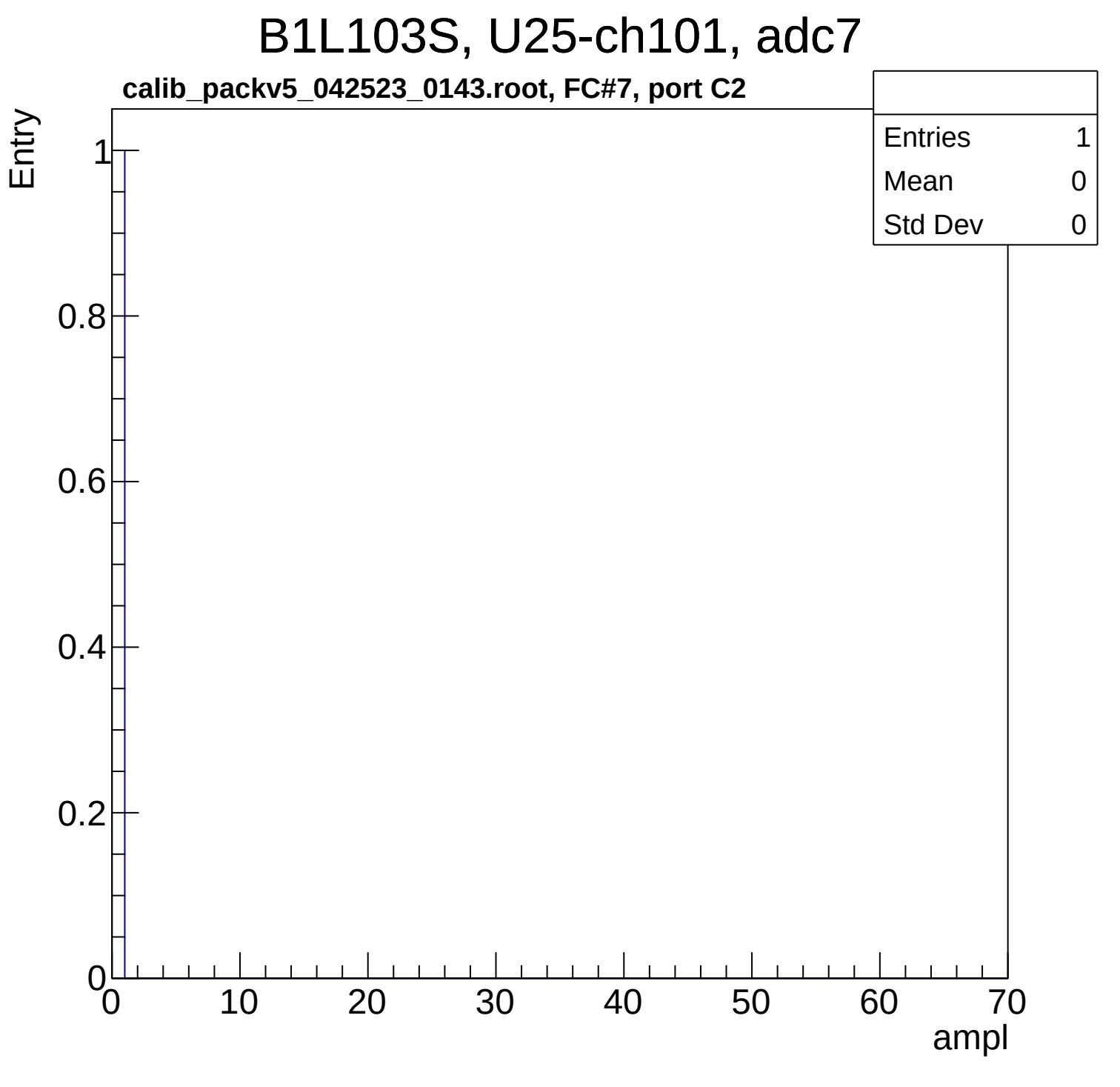
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch101, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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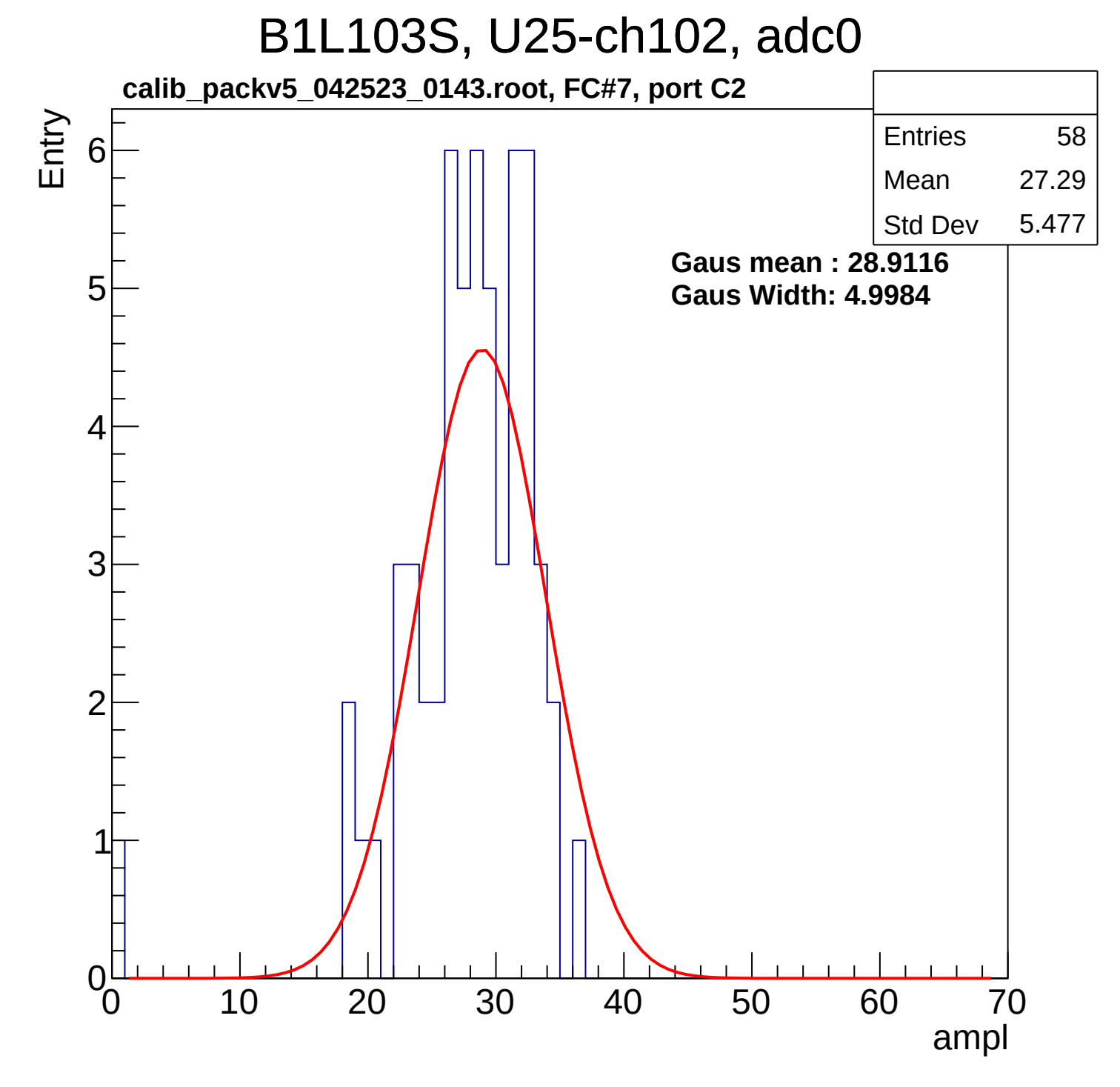
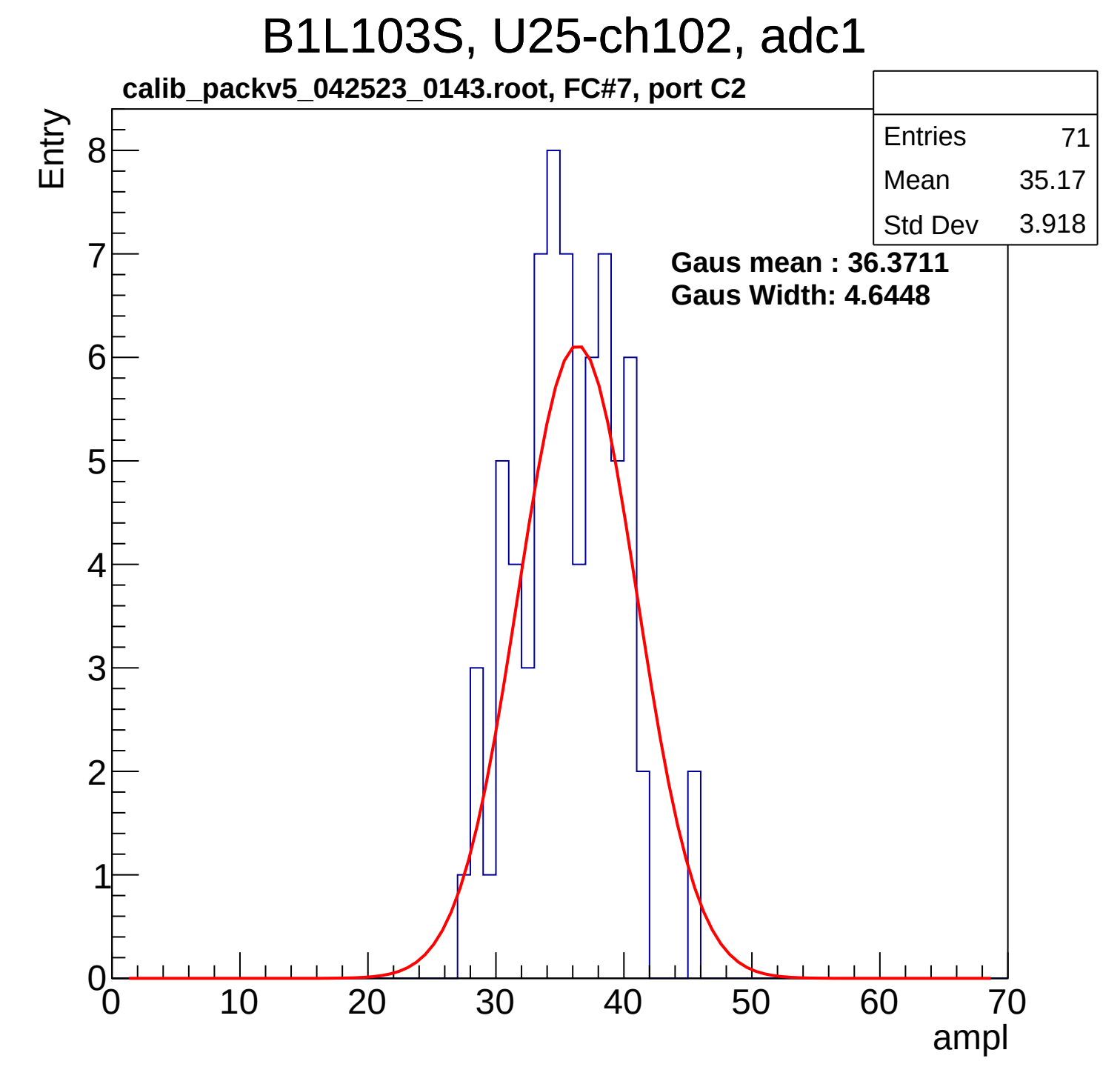
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch102, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 27.29
Std Dev 5.477
Gaus mean : 28.9116
Gaus Width: 4.9984
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 35.17
Std Dev 3.918
Gaus mean : 36.3711
Gaus Width: 4.6448
Entry
ampl
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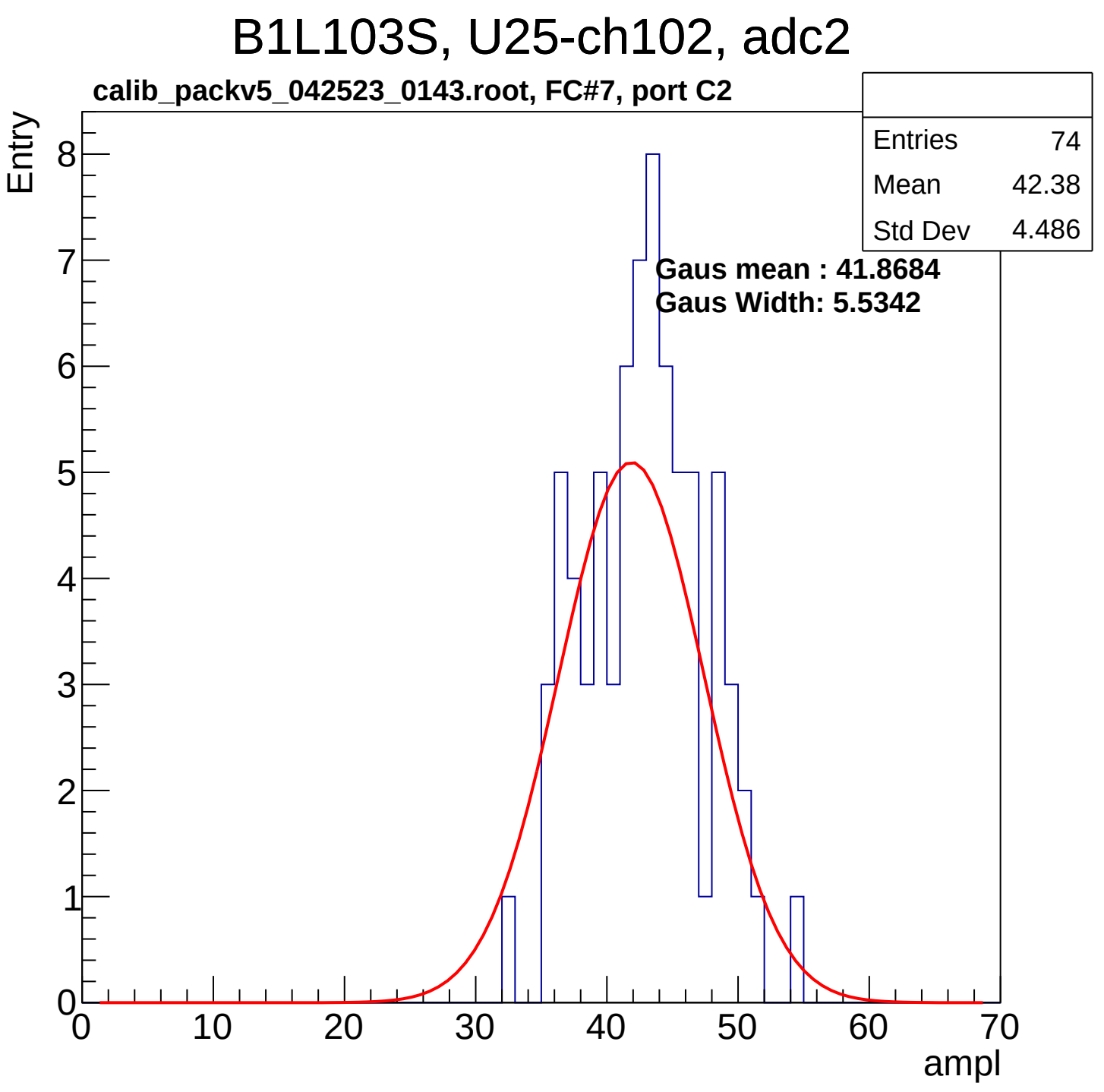
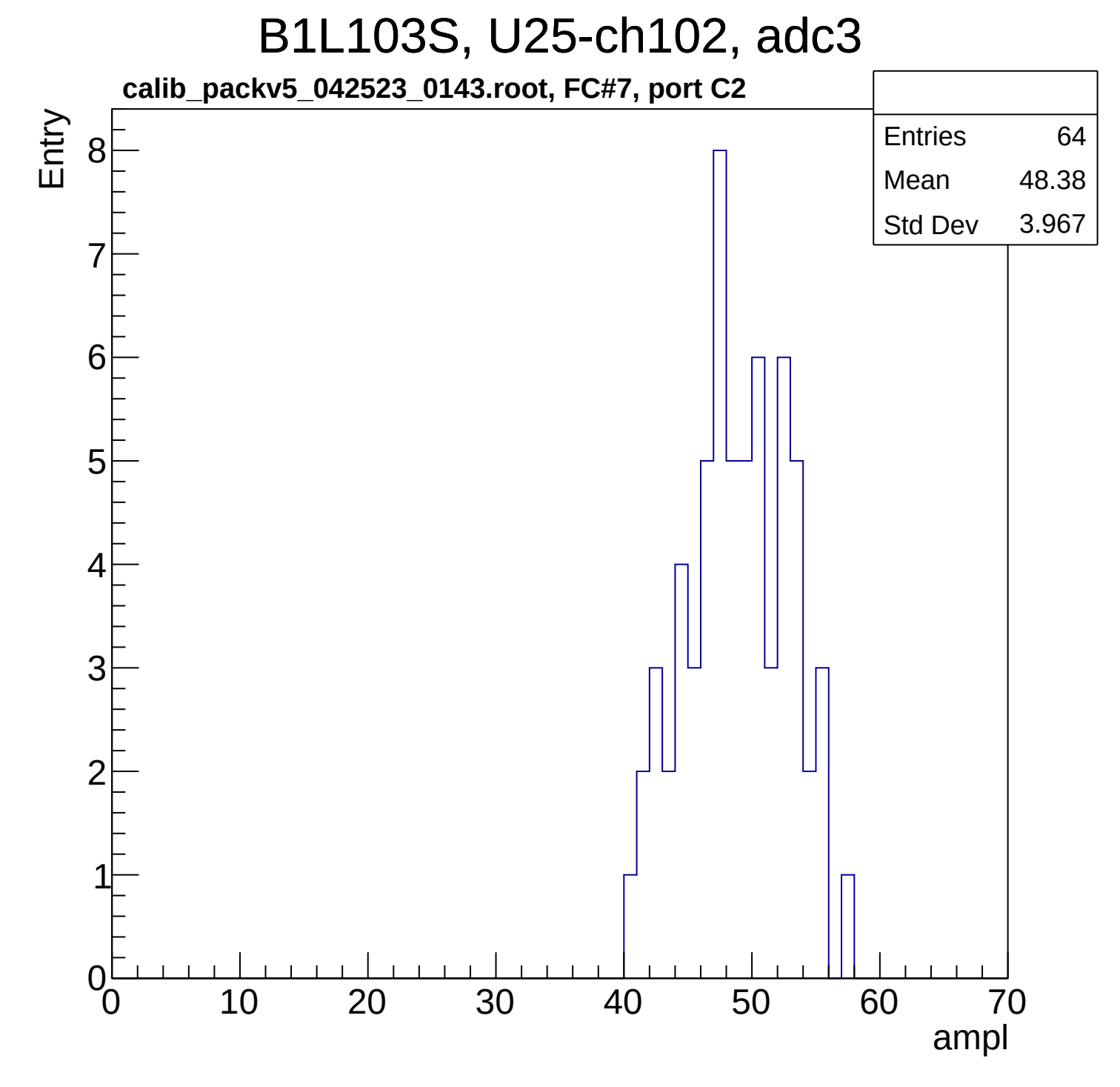
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 42.38
Std Dev 4.486
Gaus mean : 41.8684
Gaus Width: 5.5342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 48.38
Std Dev 3.967
Entry
ampl
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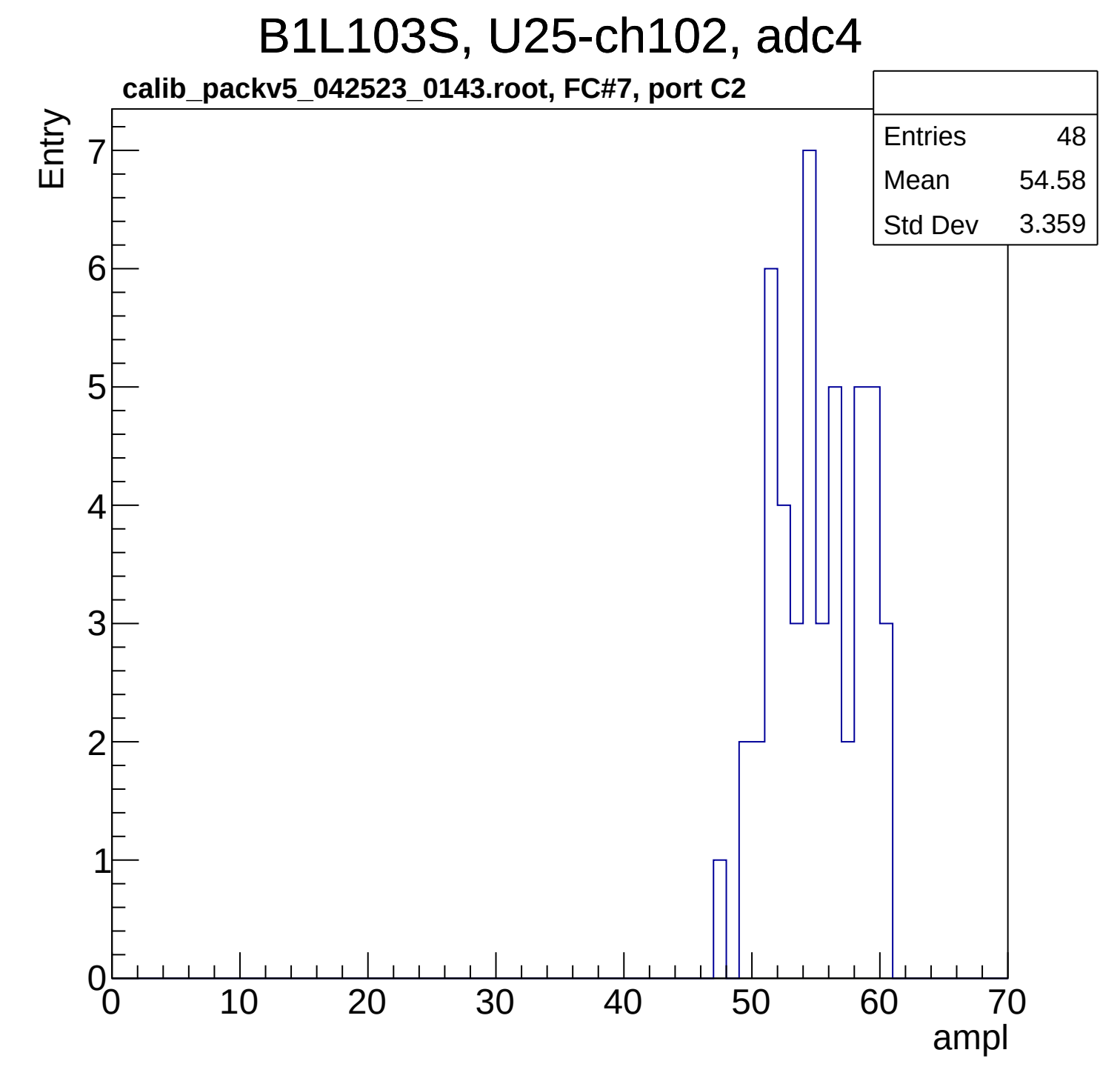
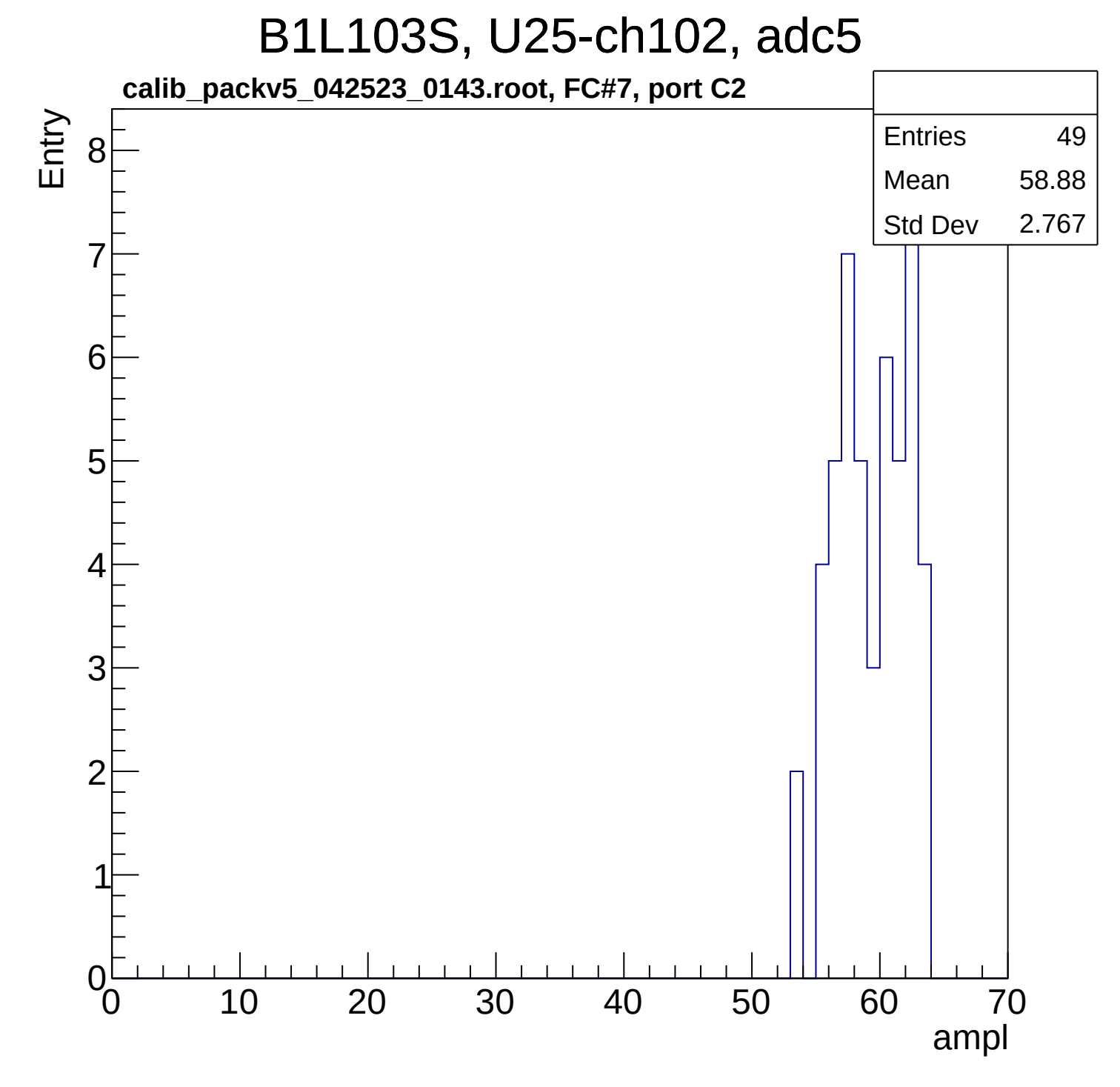
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 54.58
Std Dev 3.359
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.88
Std Dev 2.767
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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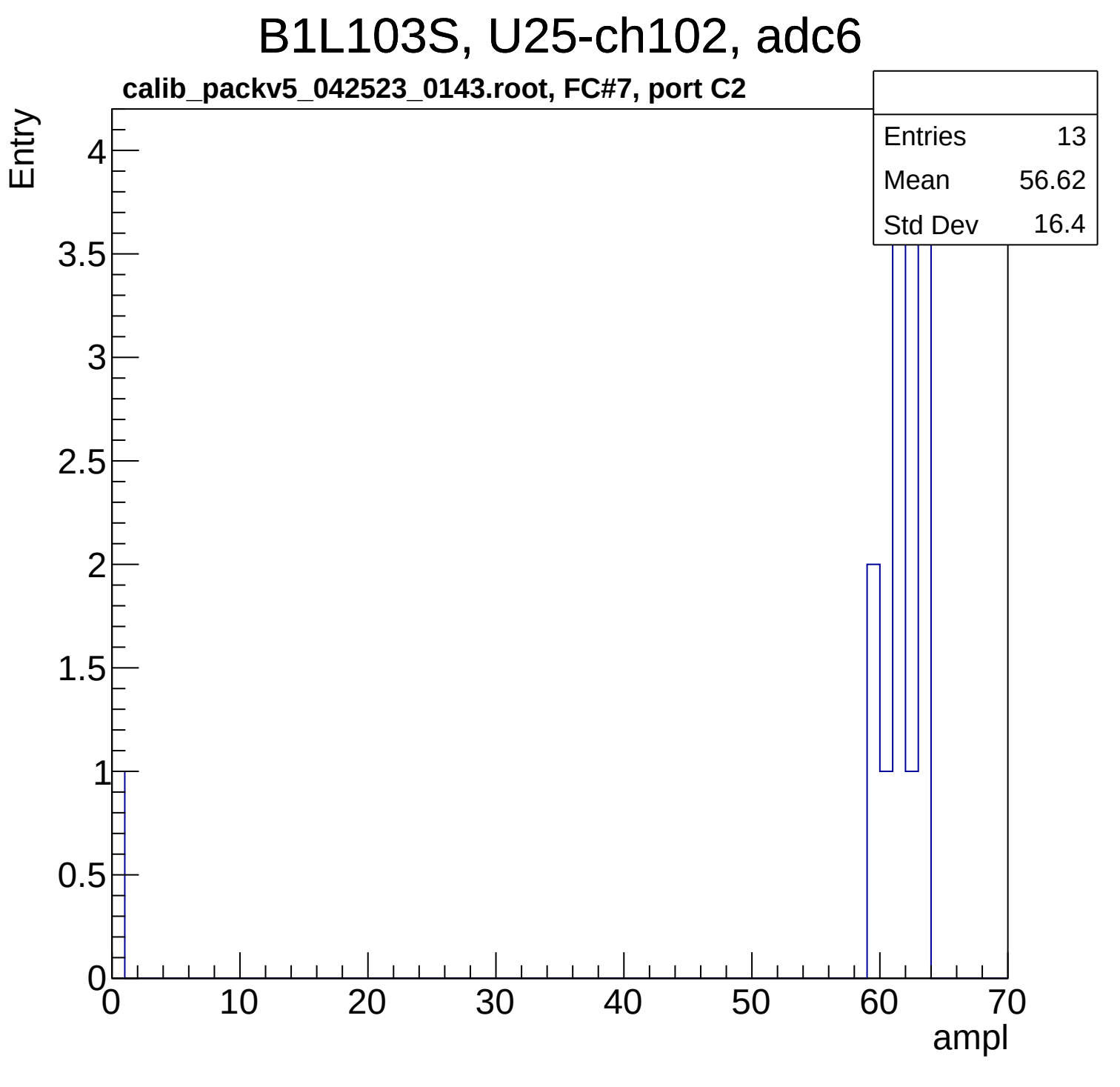
B1L103S, U25-ch102, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 56.62
Std Dev 16.4
Entry
ampl
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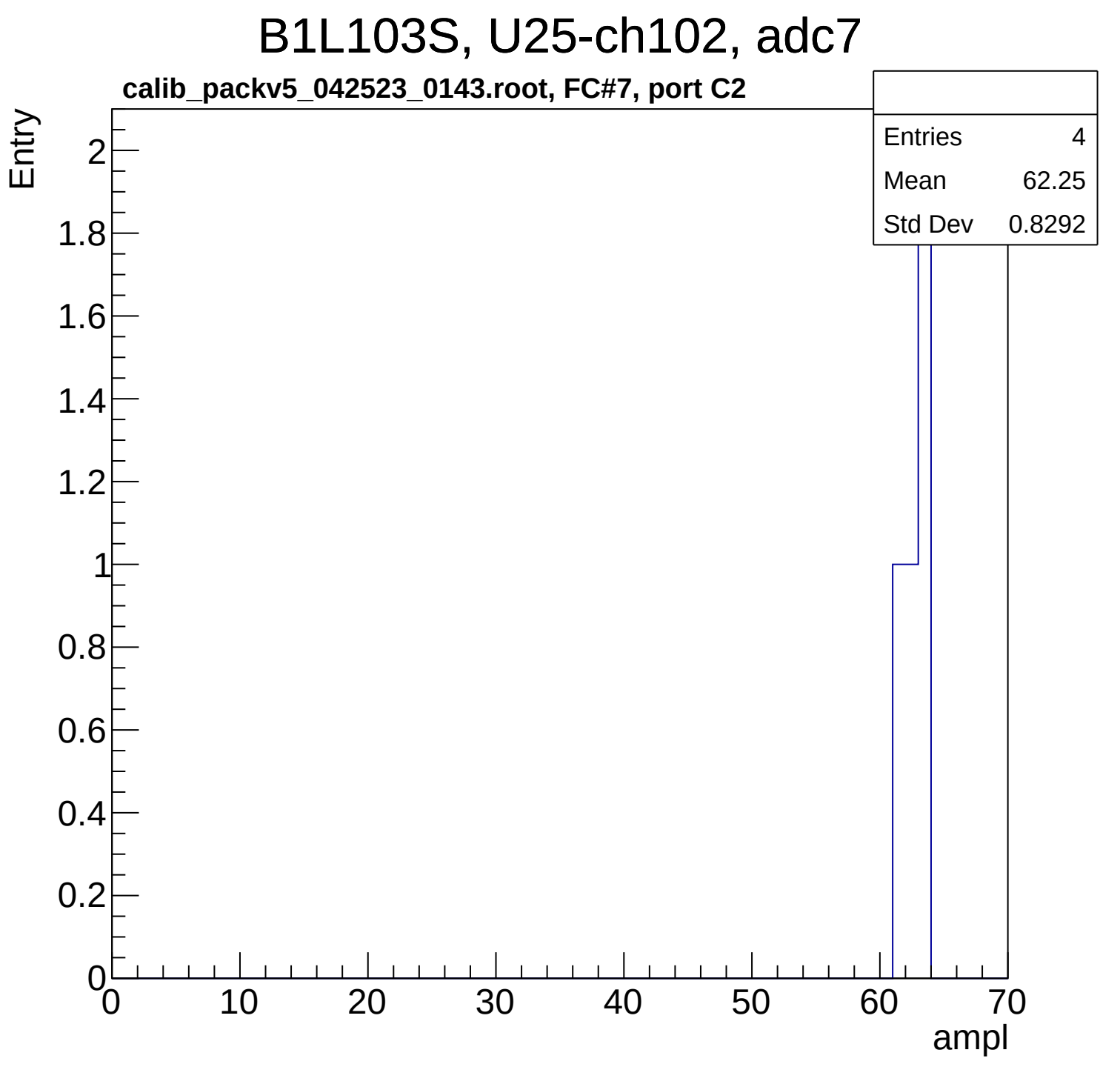
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U25-ch102, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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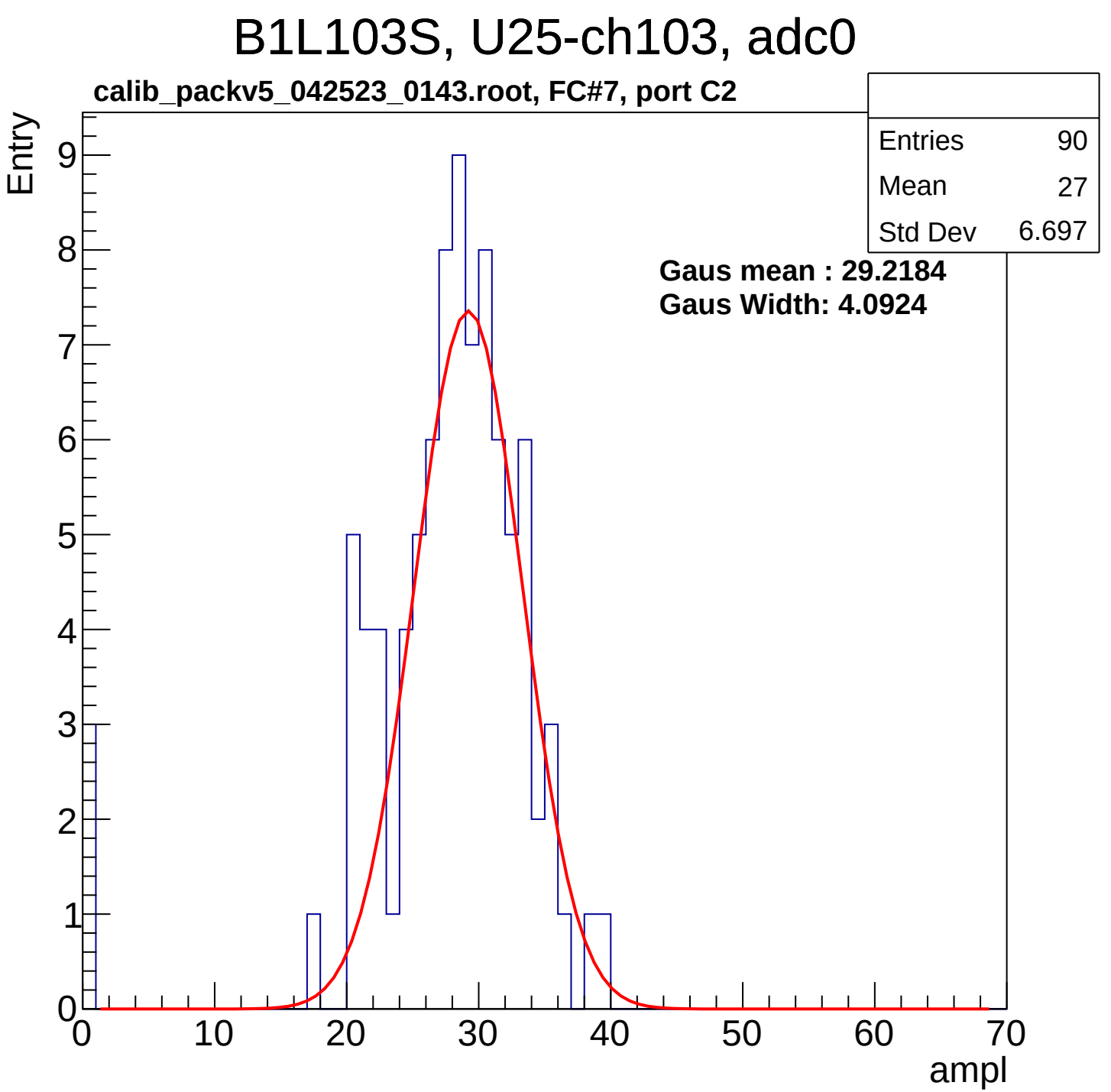
B1L103S, U25-ch103, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 27
Std Dev 6.697
Gaus mean : 29.2184
Gaus Width: 4.0924
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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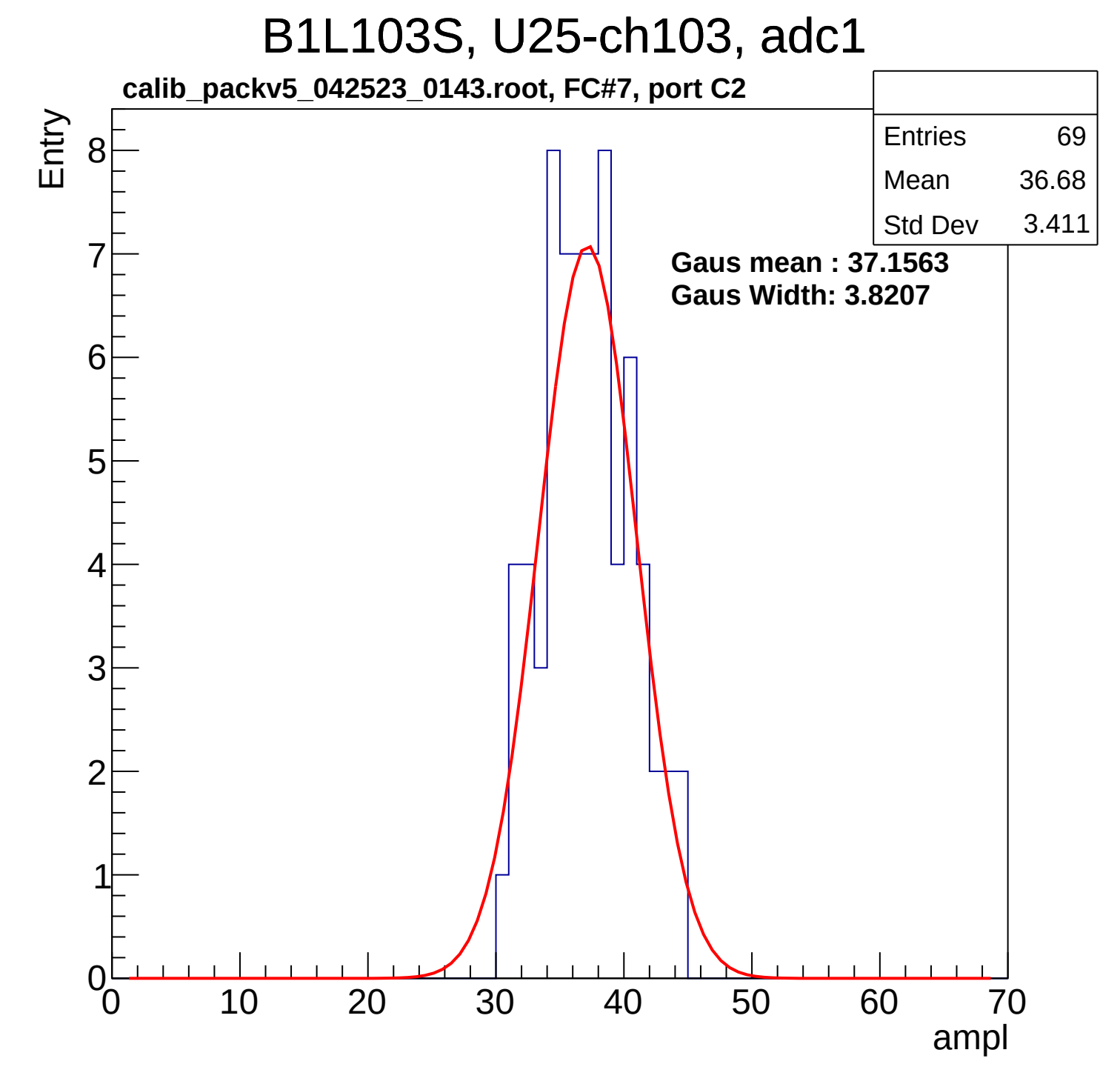
B1L103S, U25-ch103, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 36.68
Std Dev 3.411
Gaus mean : 37.1563
Gaus Width: 3.8207
Entry
ampl
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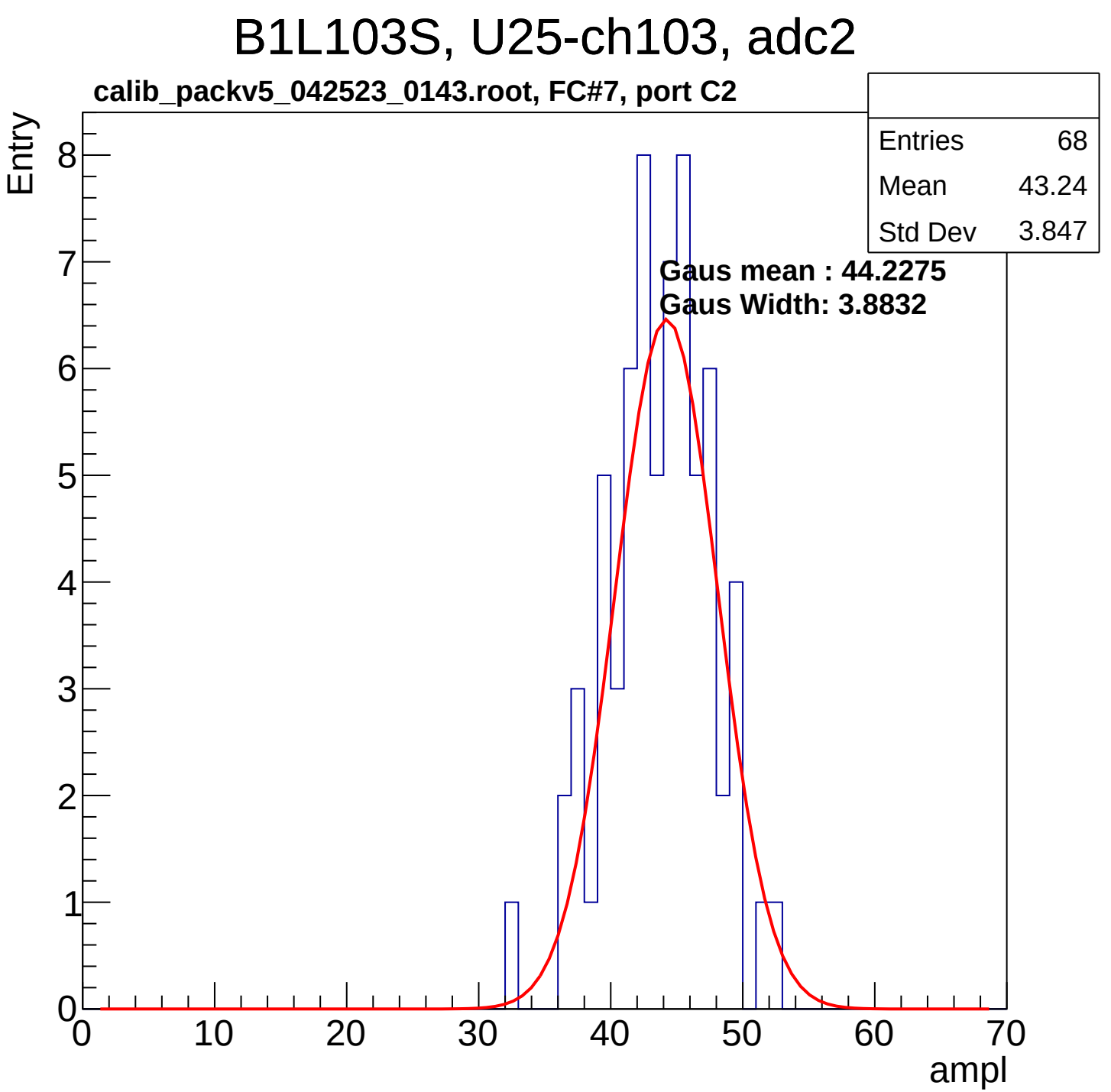
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch103, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 43.24
Std Dev 3.847
Gaus mean : 44.2275
Gaus Width: 3.8832
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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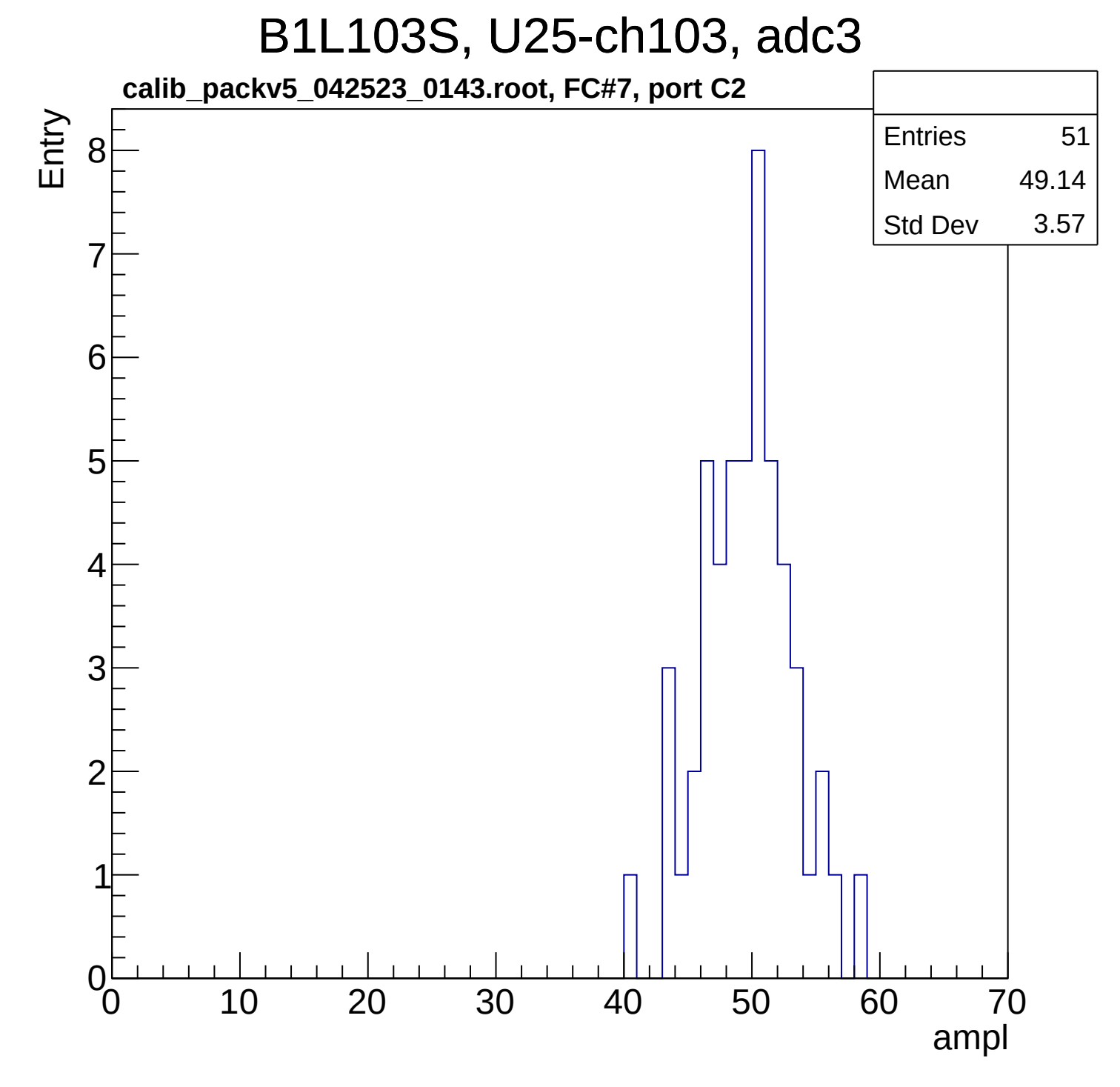
B1L103S, U25-ch103, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 49.14
Std Dev 3.57
Entry
ampl
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2
3
4
5
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7
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0
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40
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60
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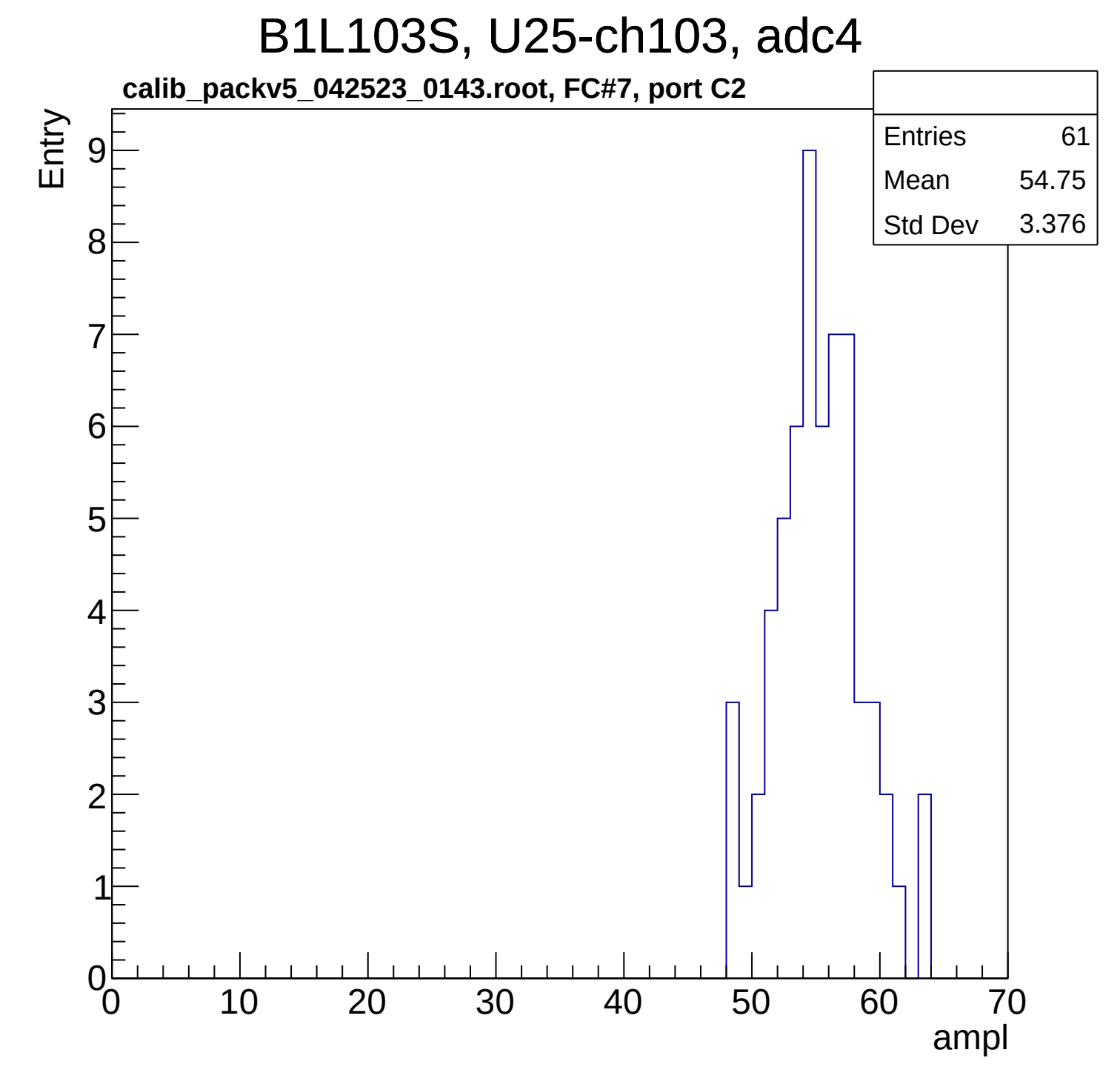
B1L103S, U25-ch103, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 54.75
Std Dev 3.376
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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40
50
60
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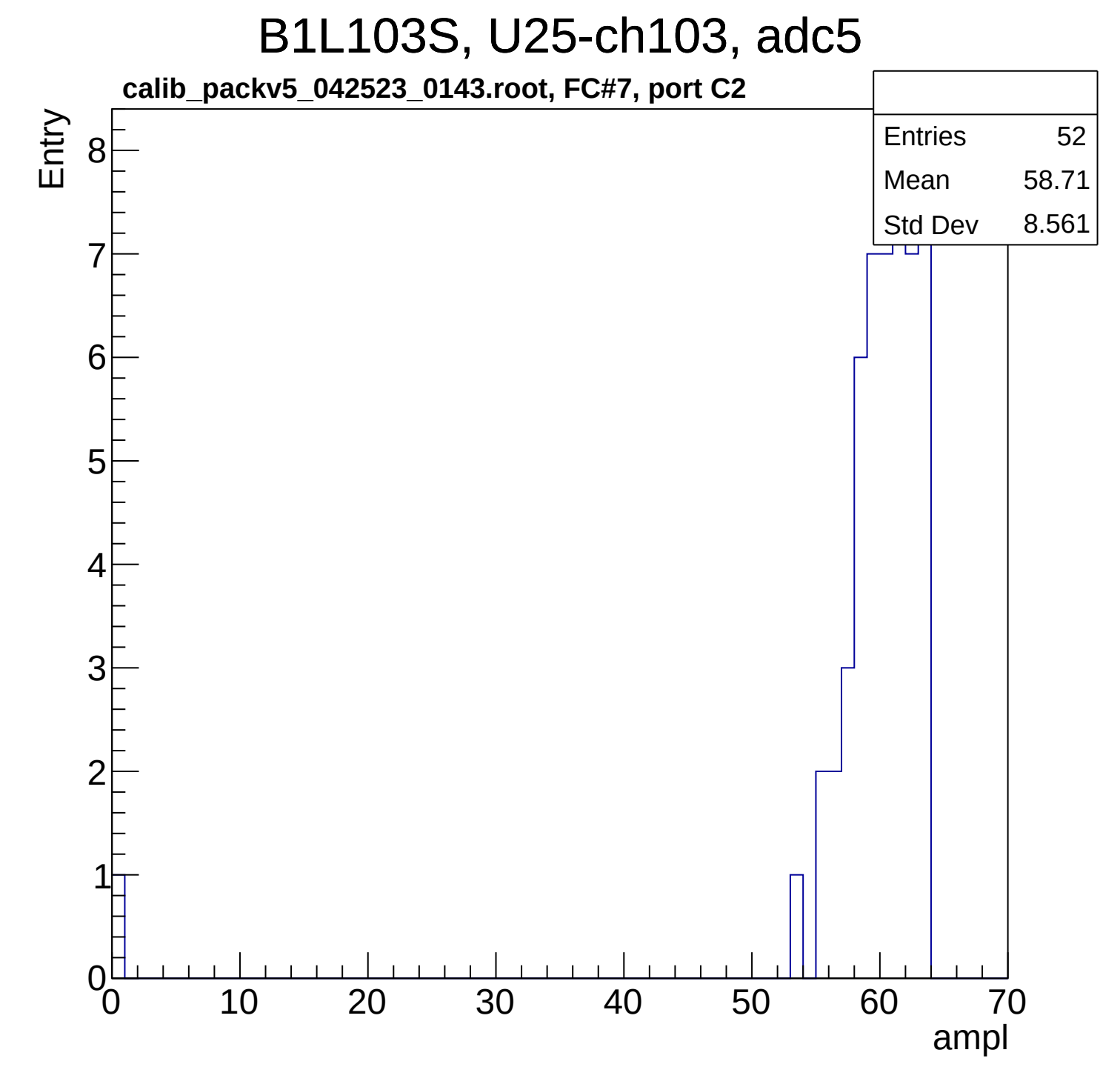
B1L103S, U25-ch103, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 58.71
Std Dev 8.561
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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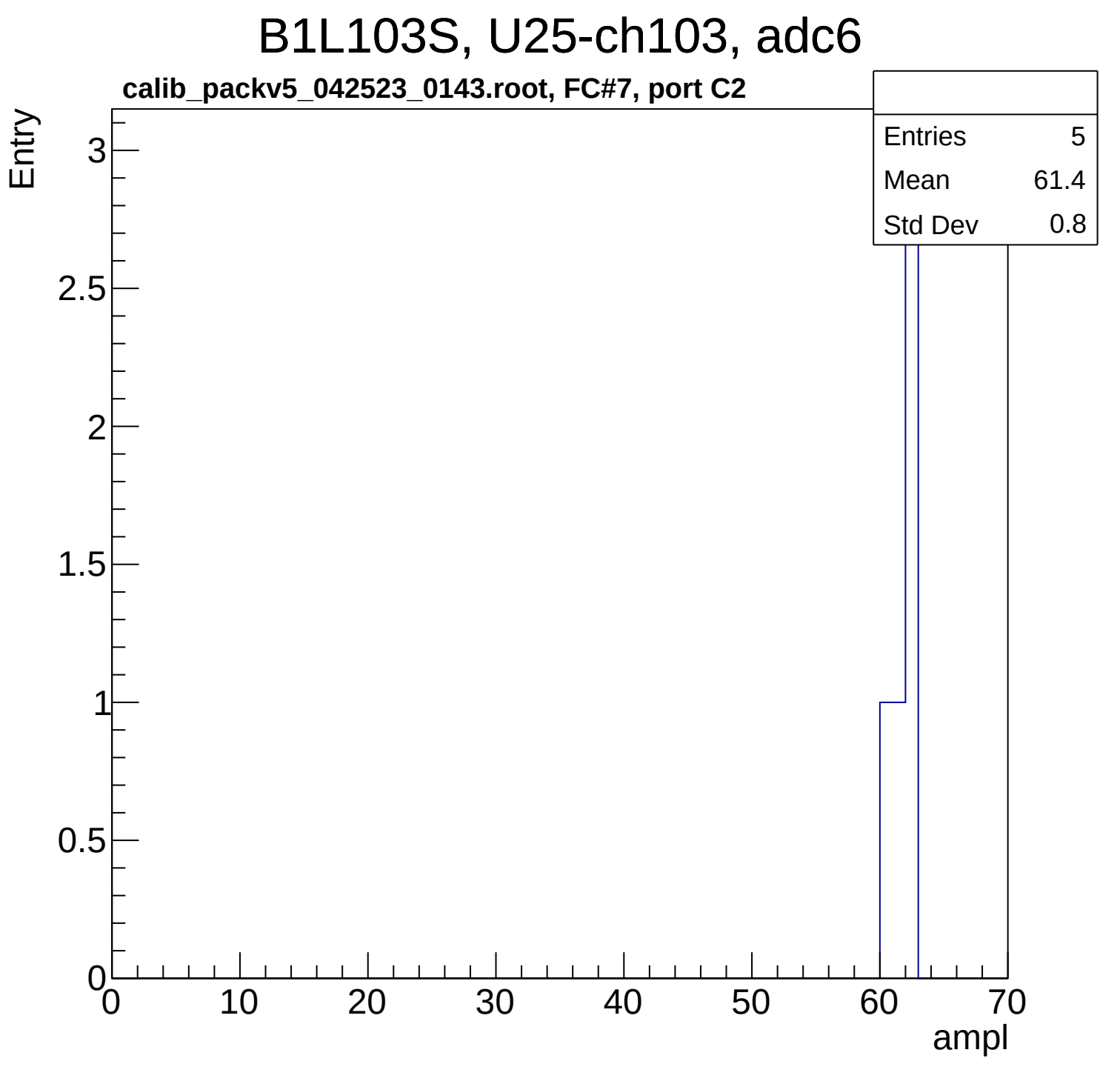
B1L103S, U25-ch103, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.4
Std Dev 0.8
Entry
ampl
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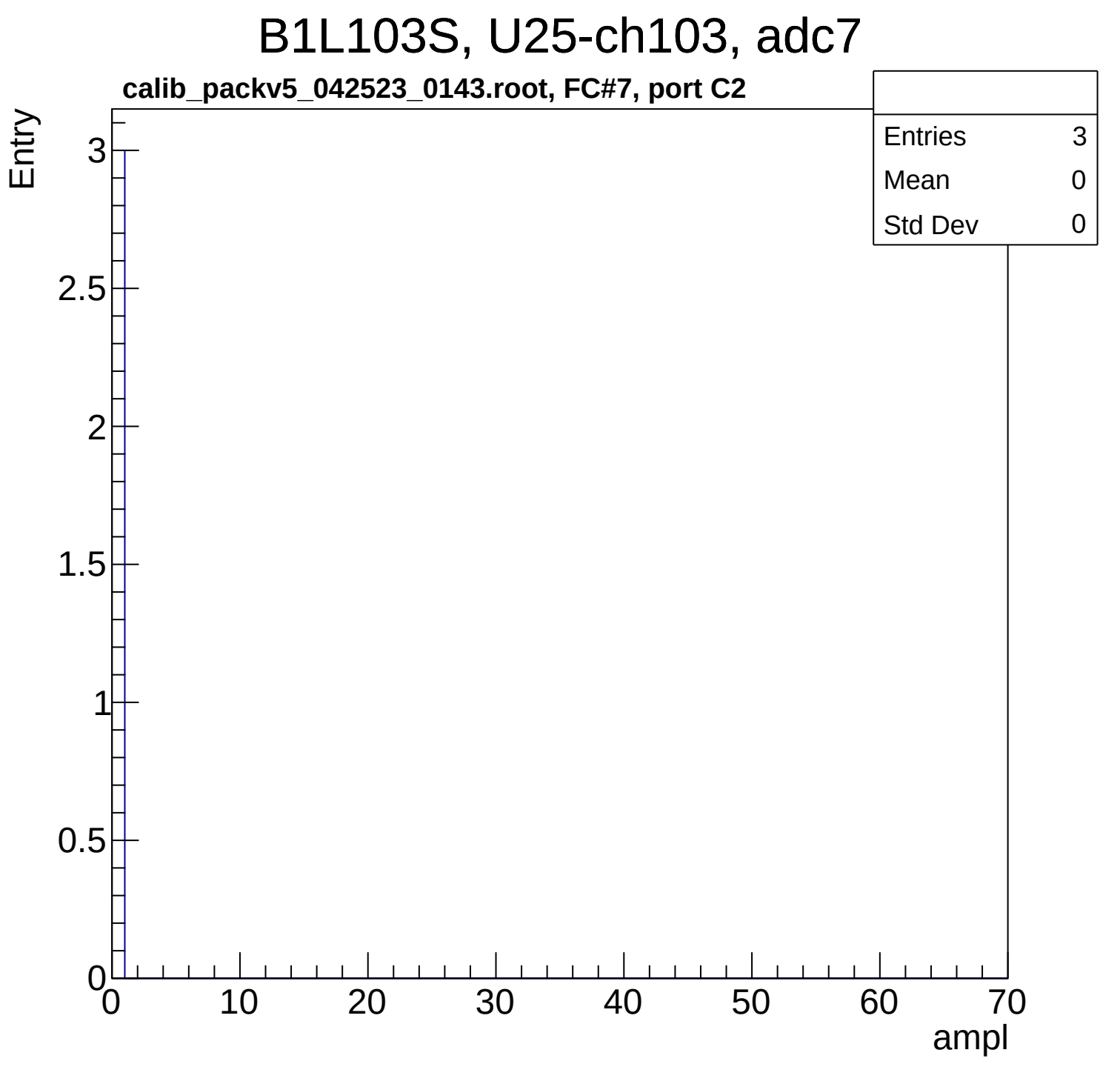
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch103, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 0
Std Dev 0
Entry
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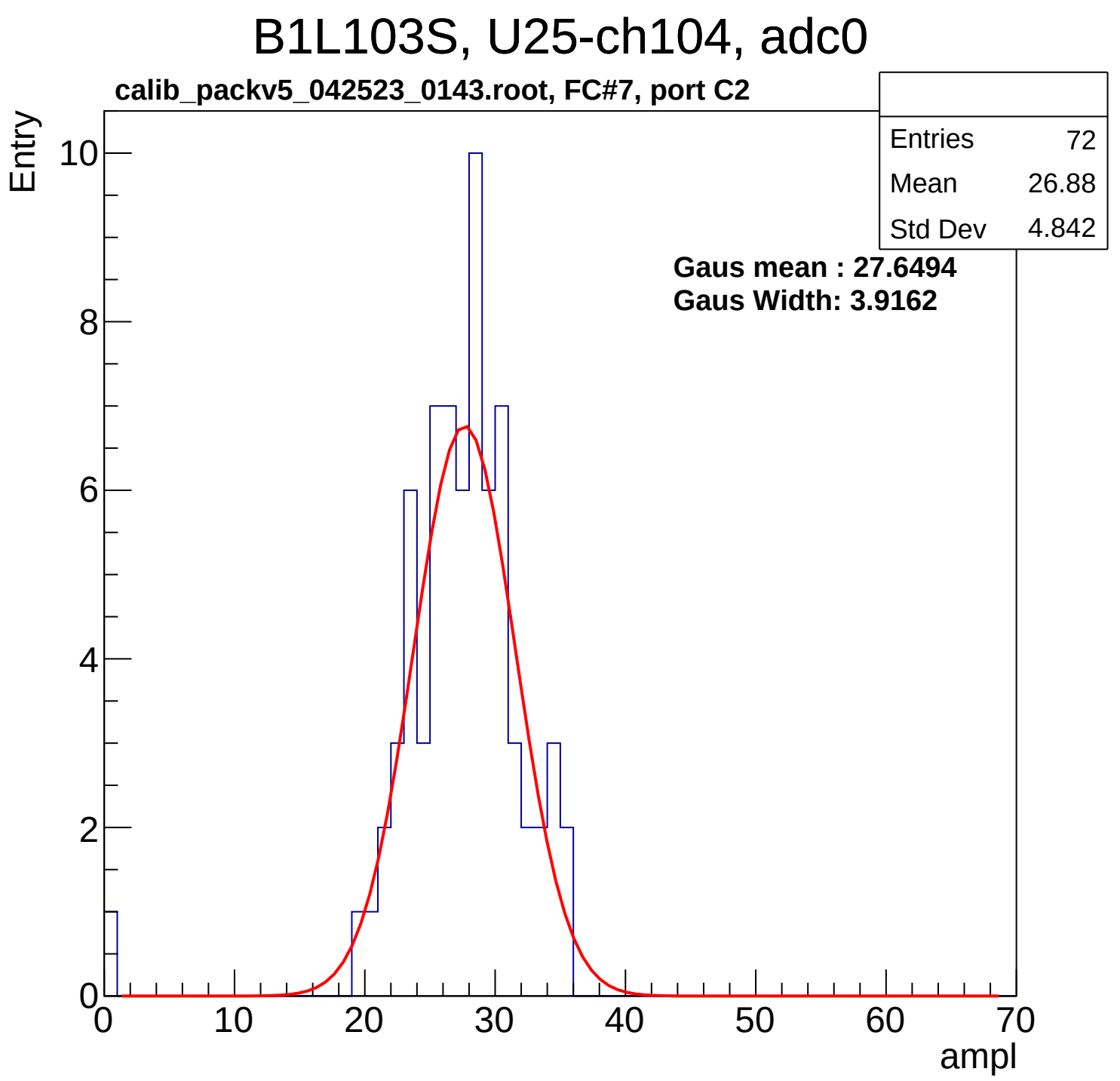
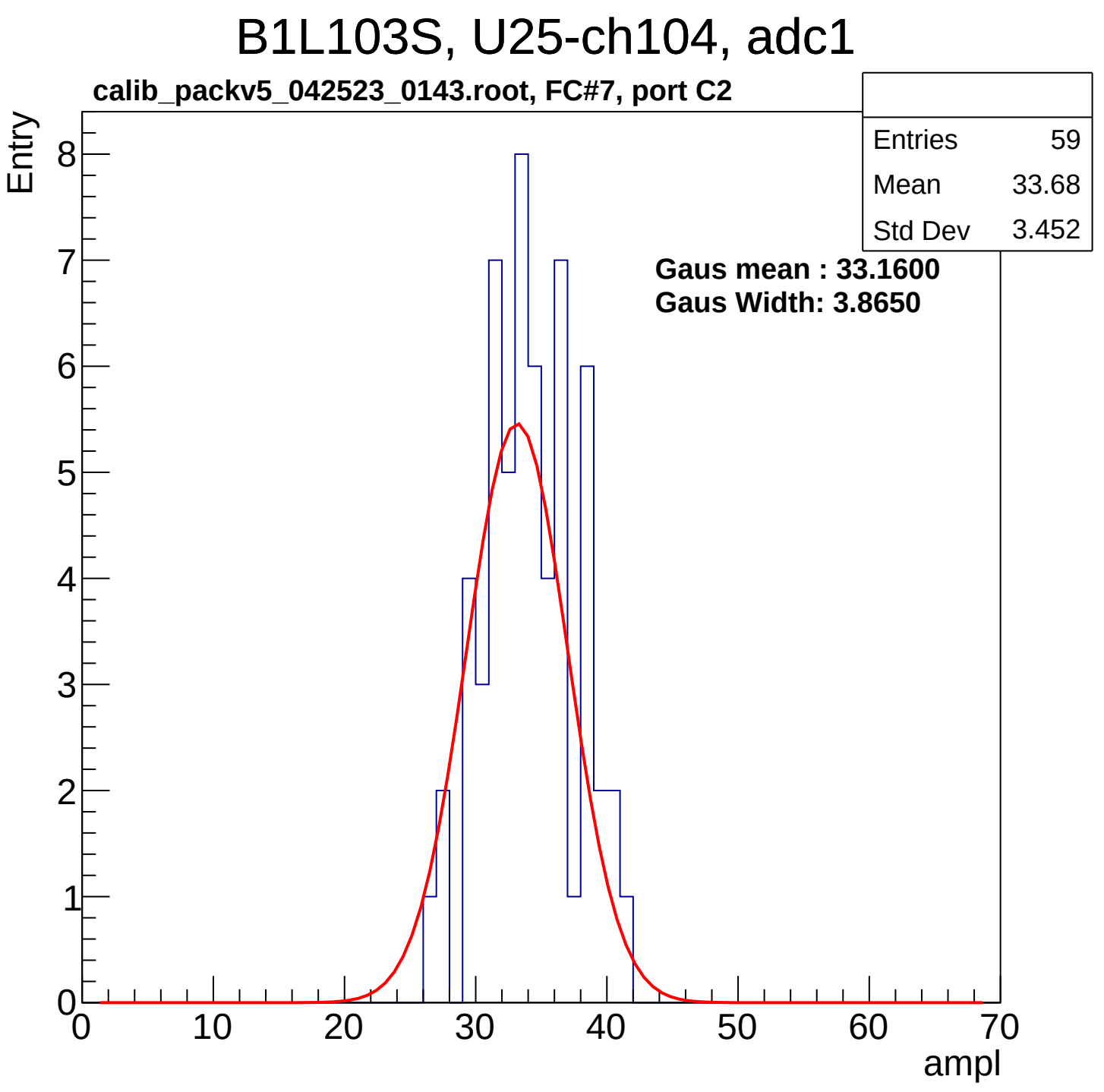
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch104, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 26.88
Std Dev 4.842
Gaus mean : 27.6494
Gaus Width: 3.9162
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch104, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 33.68
Std Dev 3.452
Gaus mean : 33.1600
Gaus Width: 3.8650
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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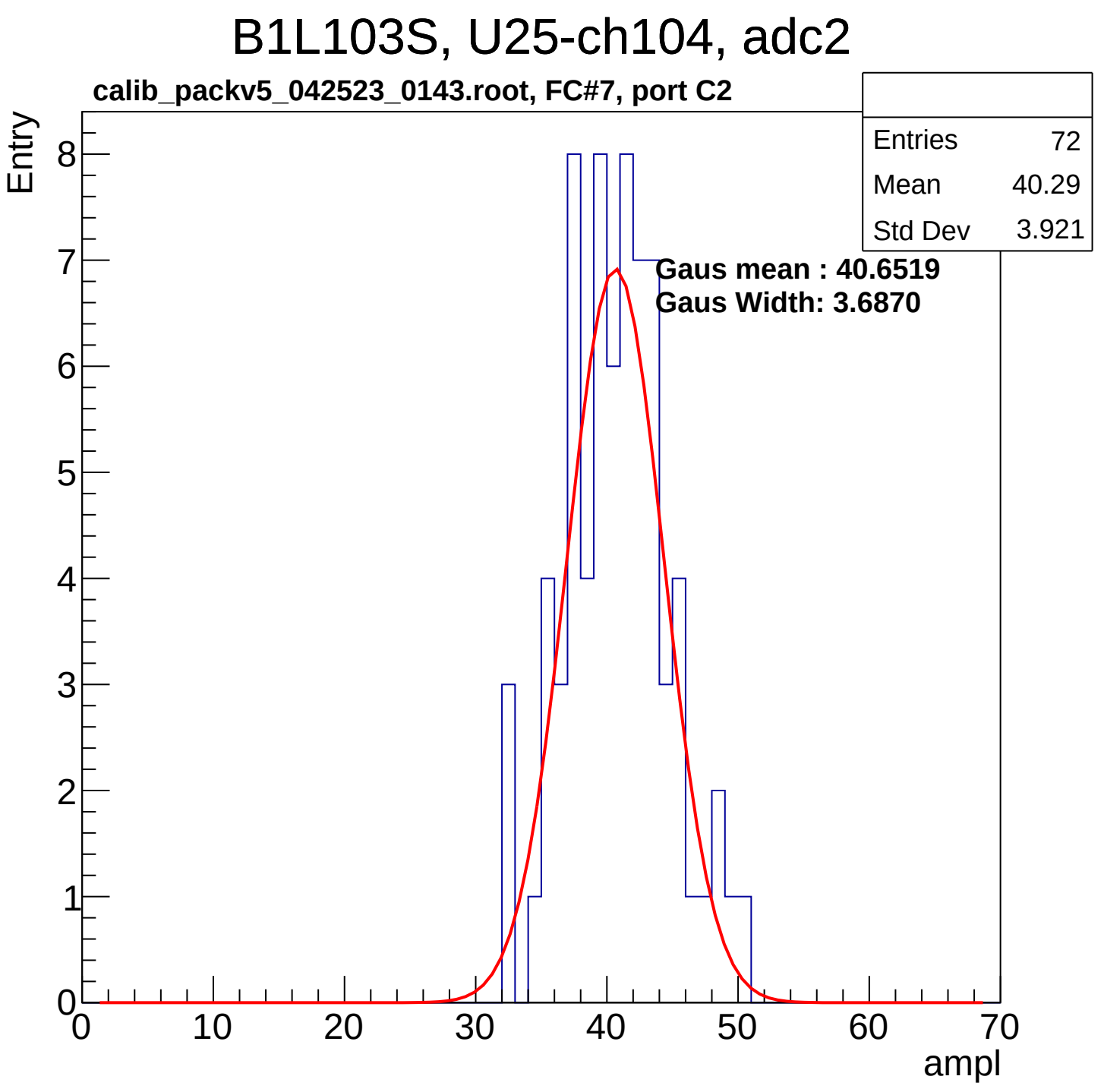
40
50
60
70


B1L103S, U25-ch104, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 40.29
Std Dev 3.921
Gaus mean : 40.6519
Gaus Width: 3.6870
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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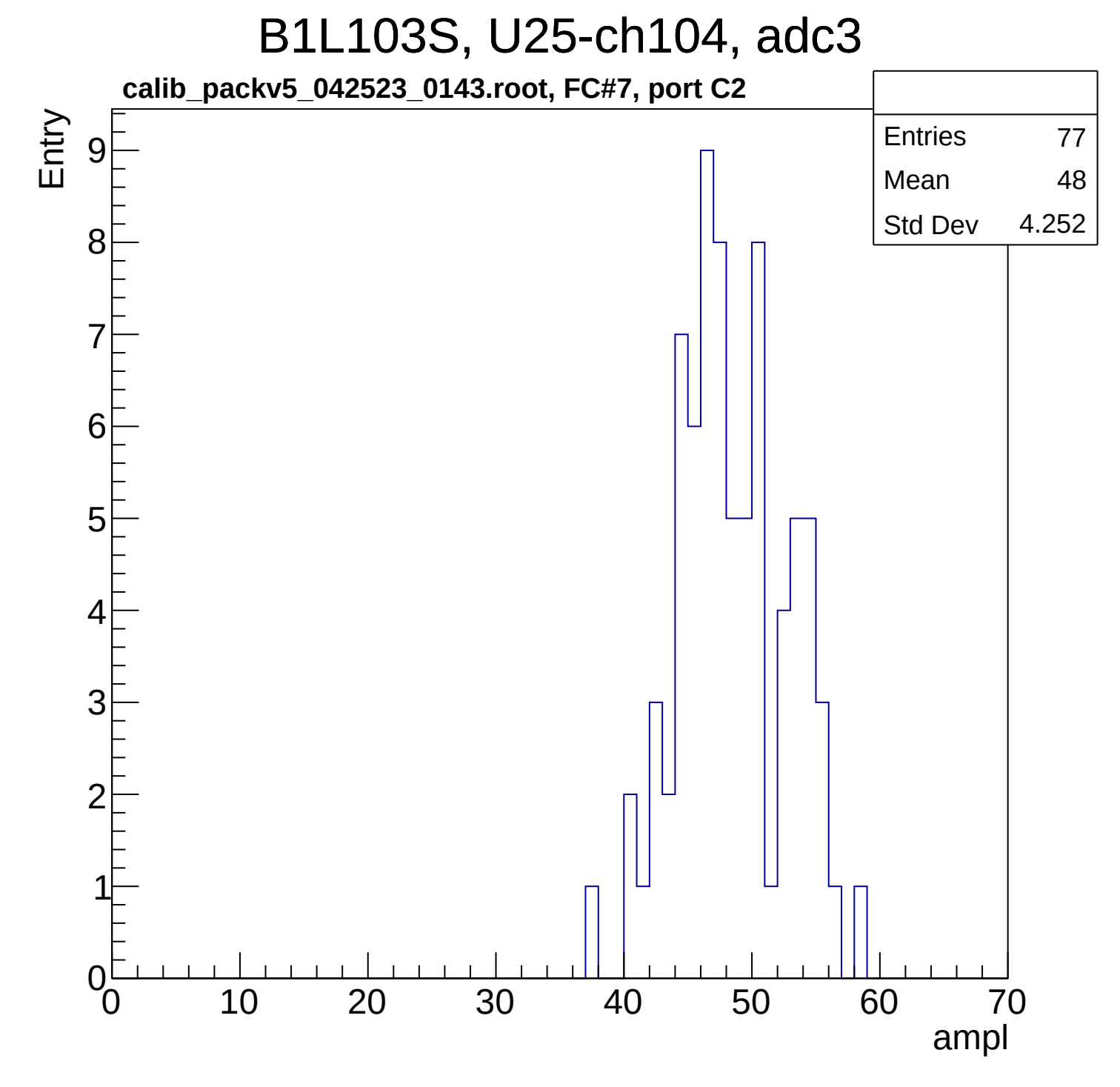
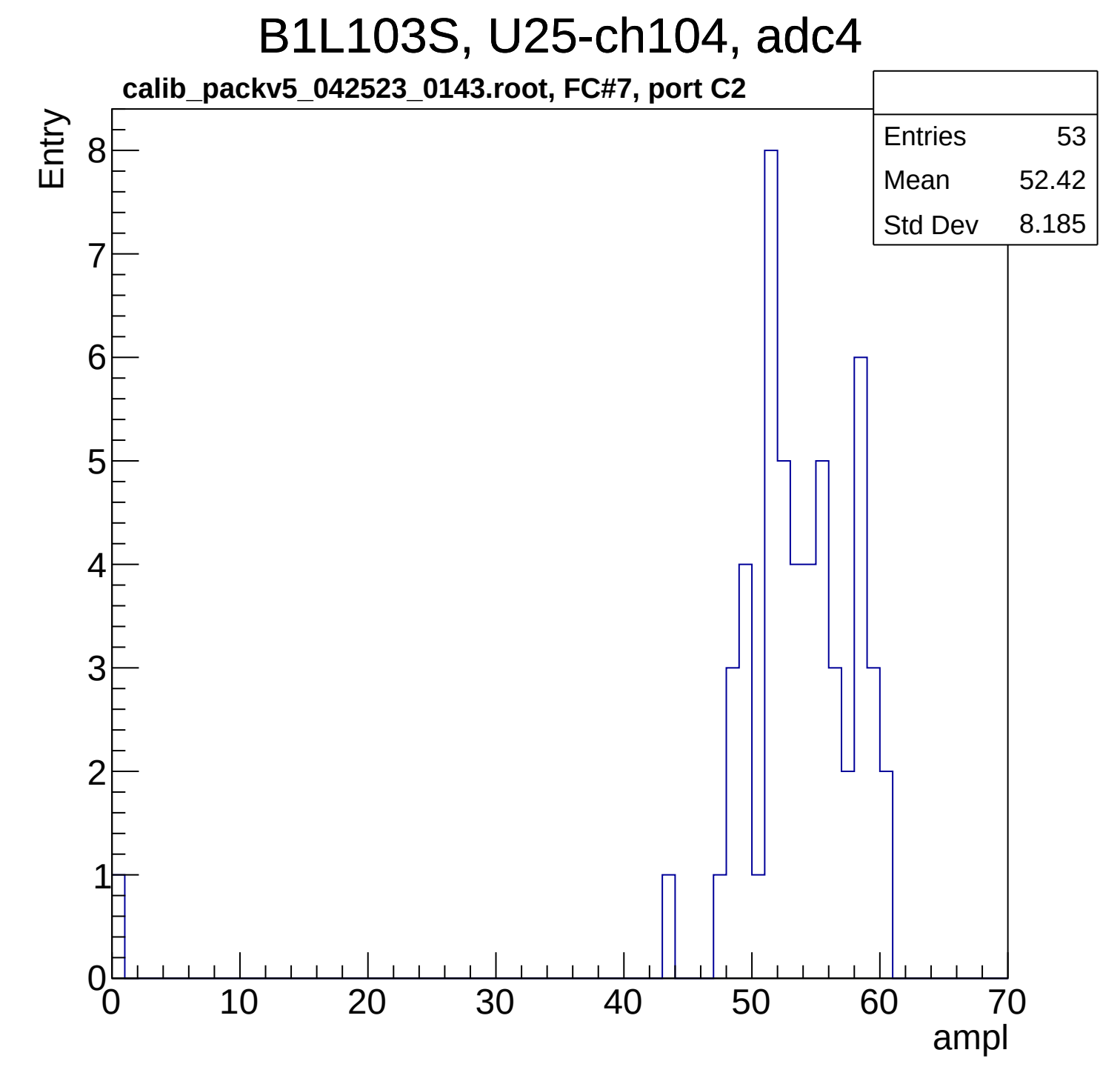

B1L103S, U25-ch104, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 48
Std Dev 4.252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch104, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 52.42
Std Dev 8.185
Entry
ampl
0
1
2
3
4
5
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7
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40
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60
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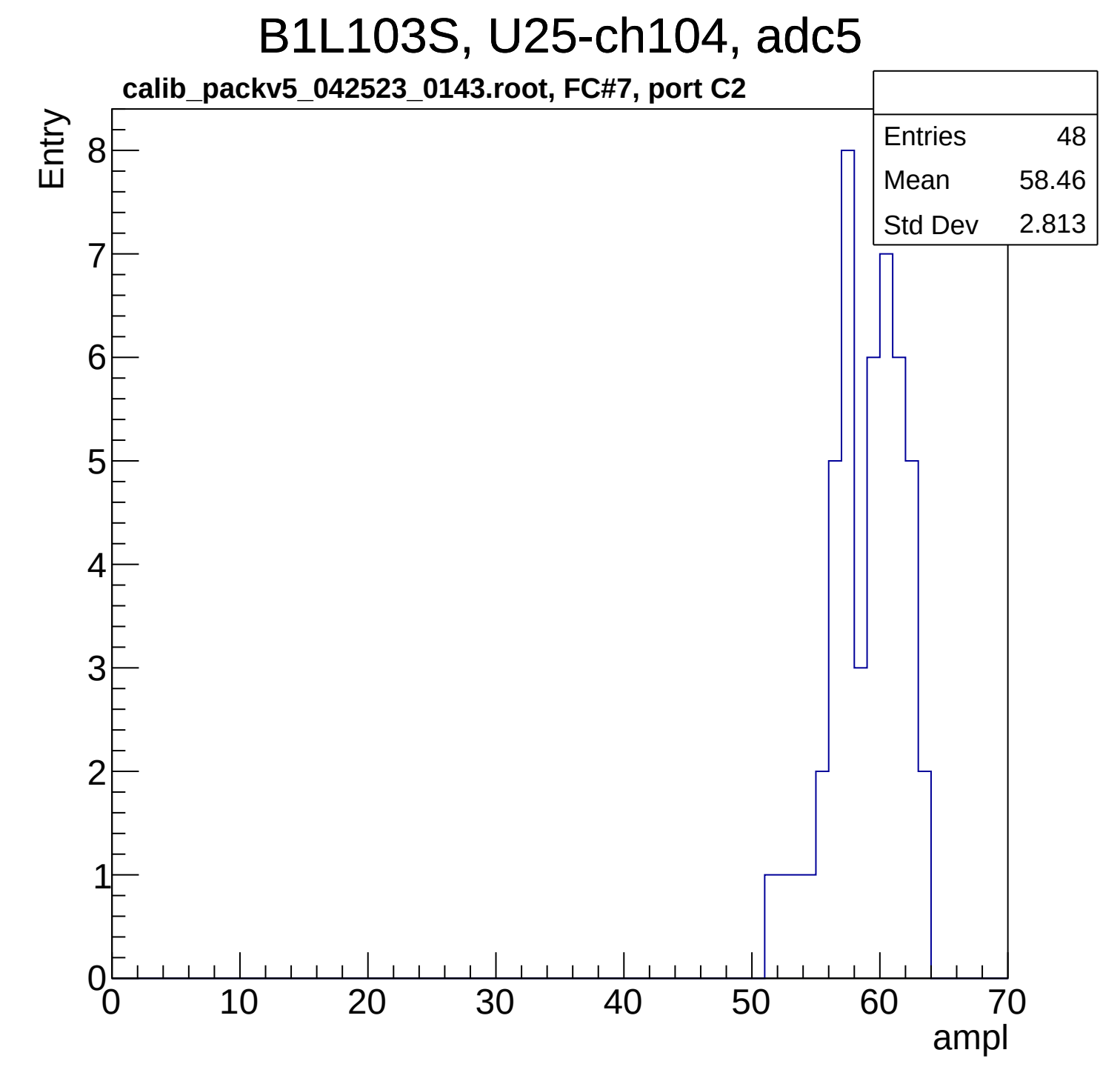
B1L103S, U25-ch104, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.46
Std Dev 2.813
Entry
ampl
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2
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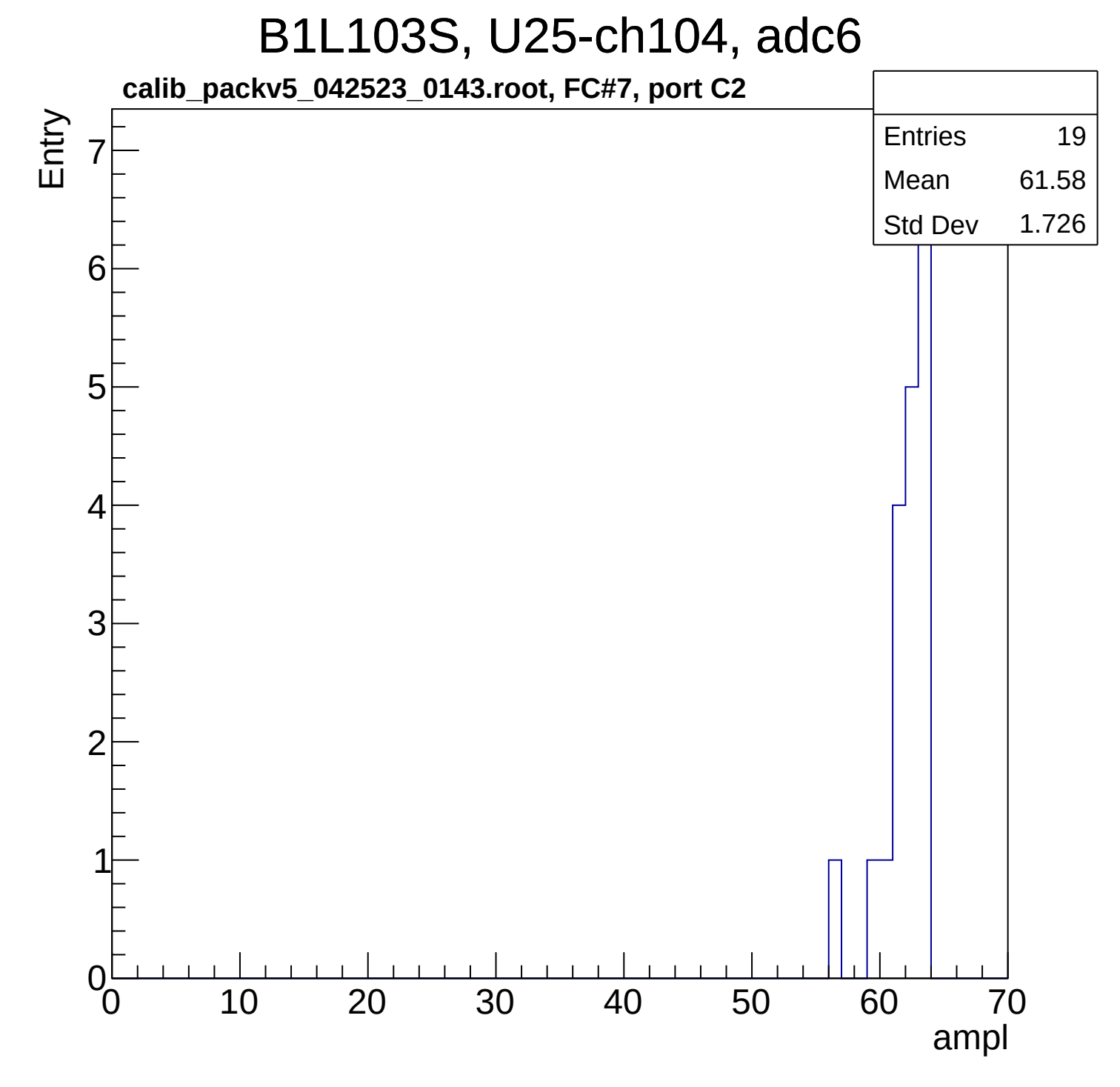
B1L103S, U25-ch104, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 19
Mean 61.58
Std Dev 1.726
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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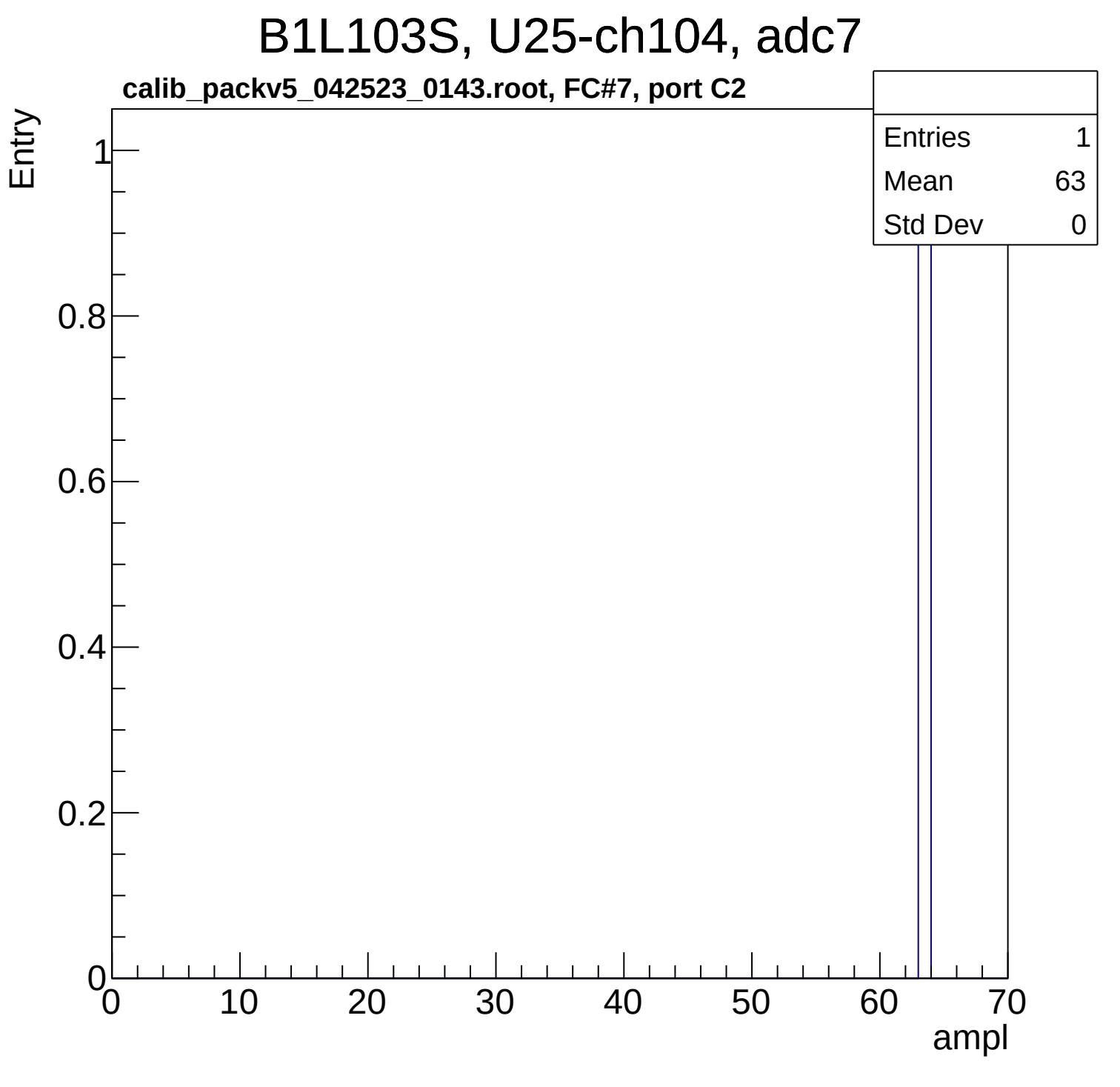
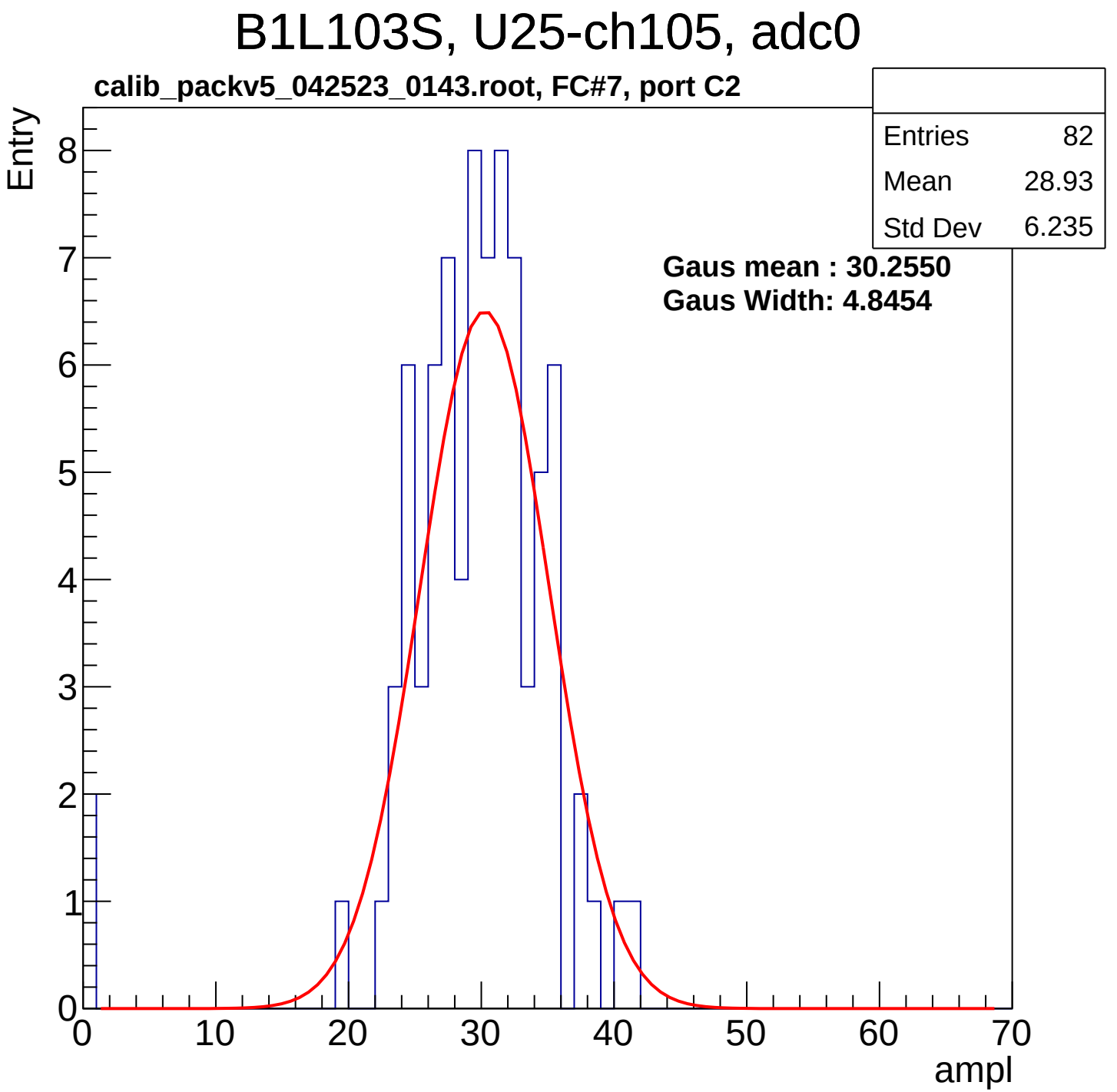

B1L103S, U25-ch104, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch105, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 28.93
Std Dev 6.235
Gaus mean : 30.2550
Gaus Width: 4.8454
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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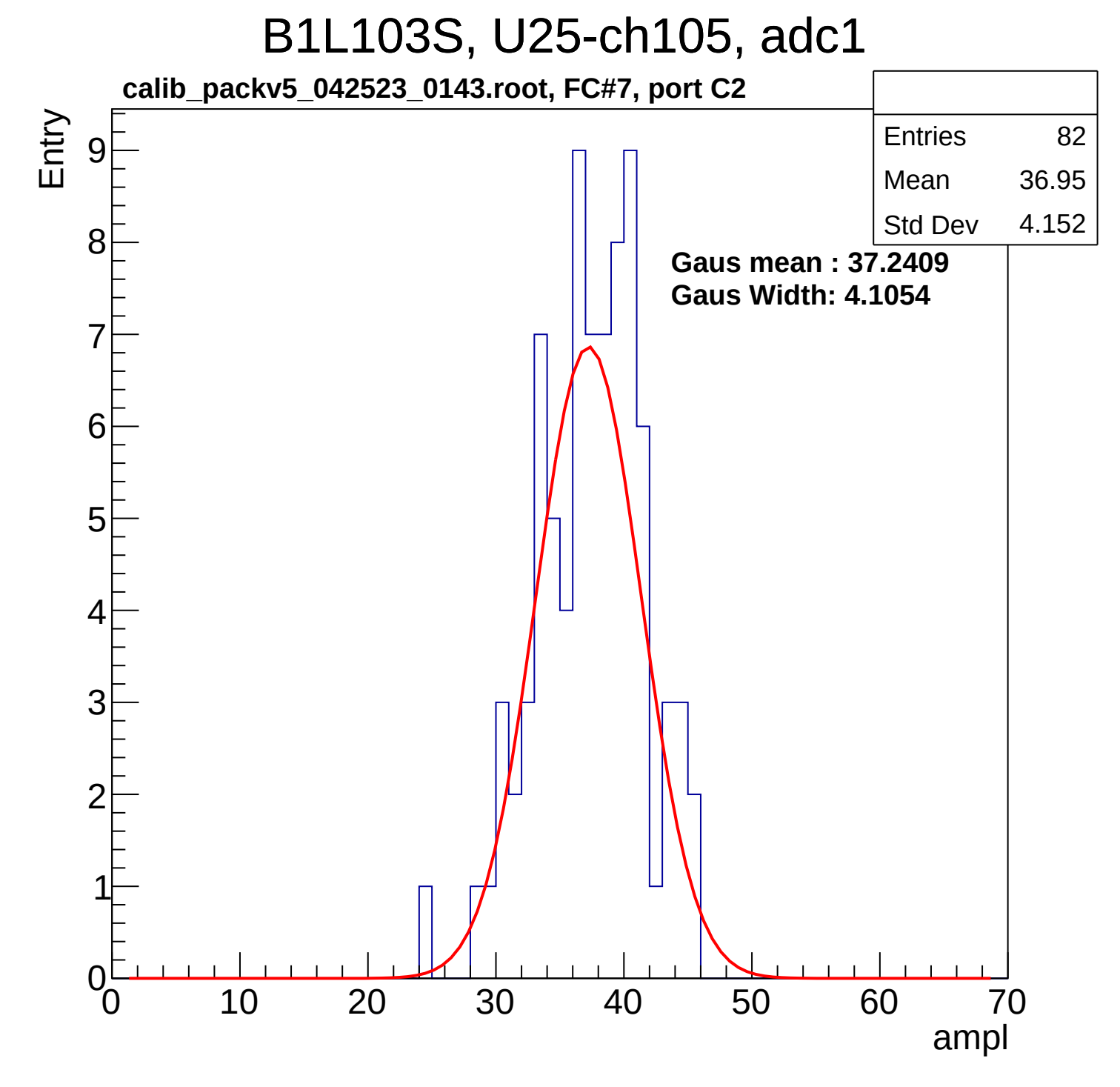
B1L103S, U25-ch105, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 36.95
Std Dev 4.152
Gaus mean : 37.2409
Gaus Width: 4.1054
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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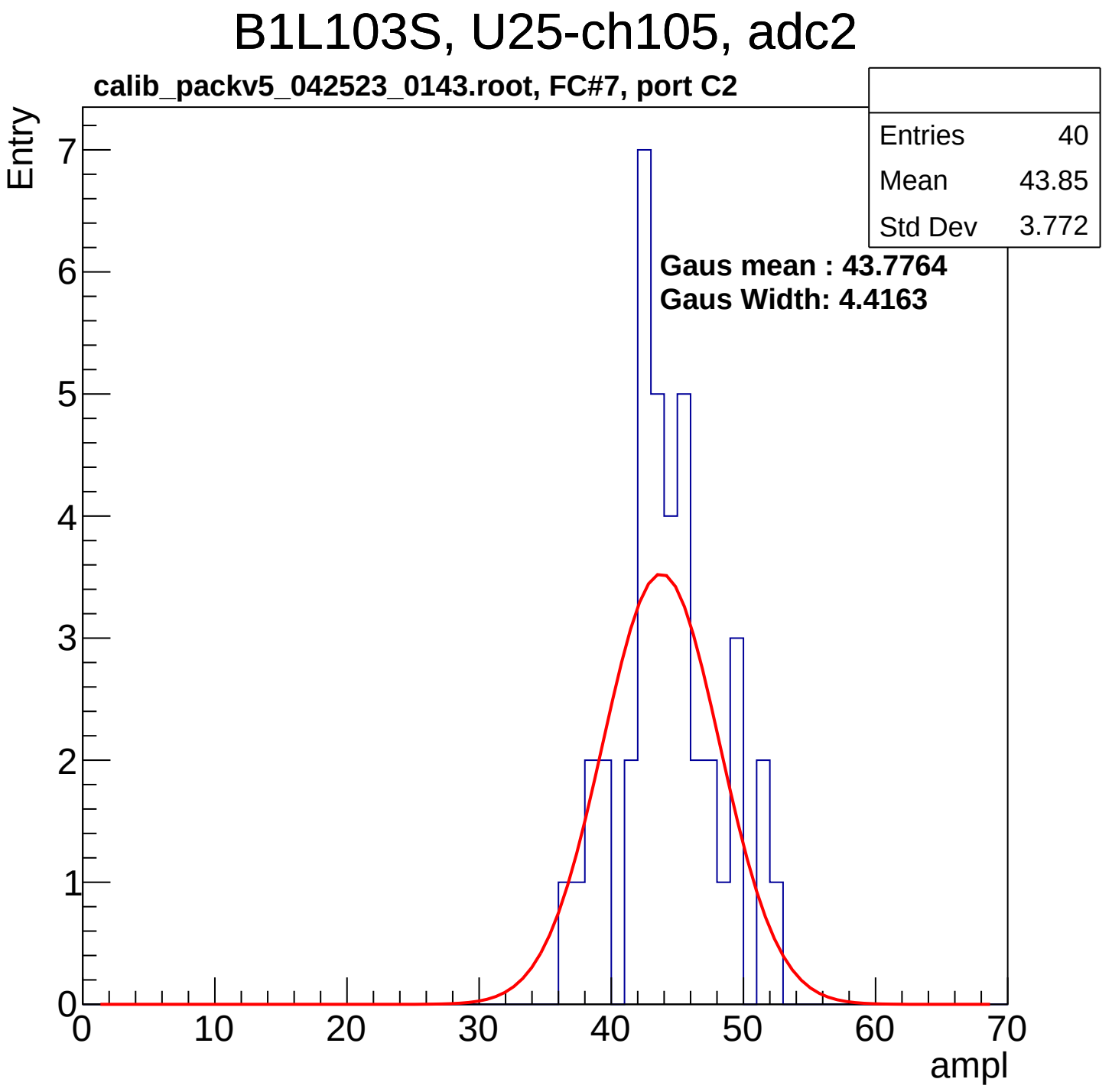
B1L103S, U25-ch105, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 40
Mean 43.85
Std Dev 3.772
Gaus mean : 43.7764
Gaus Width: 4.4163
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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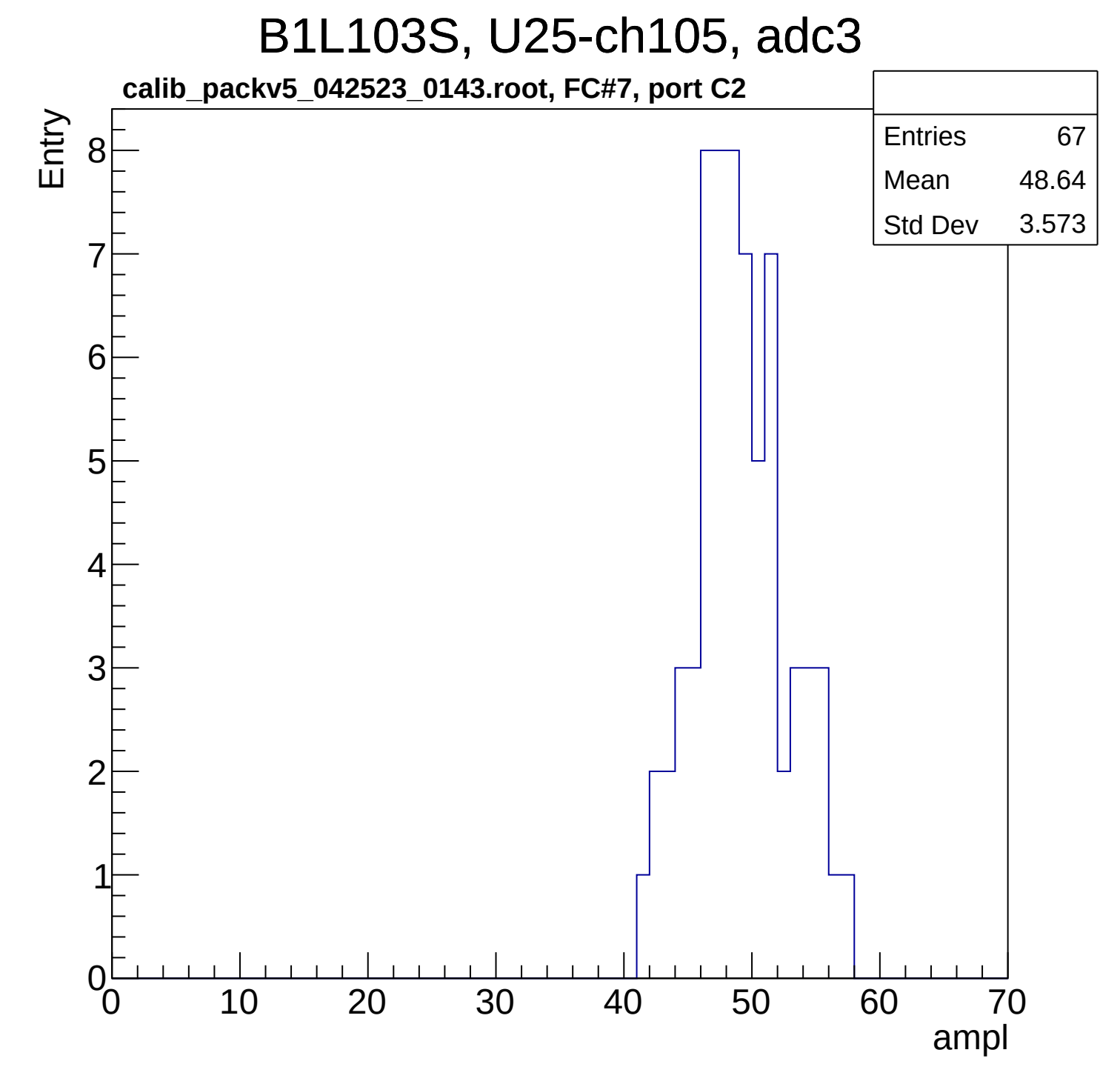
B1L103S, U25-ch105, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 48.64
Std Dev 3.573
Entry
ampl
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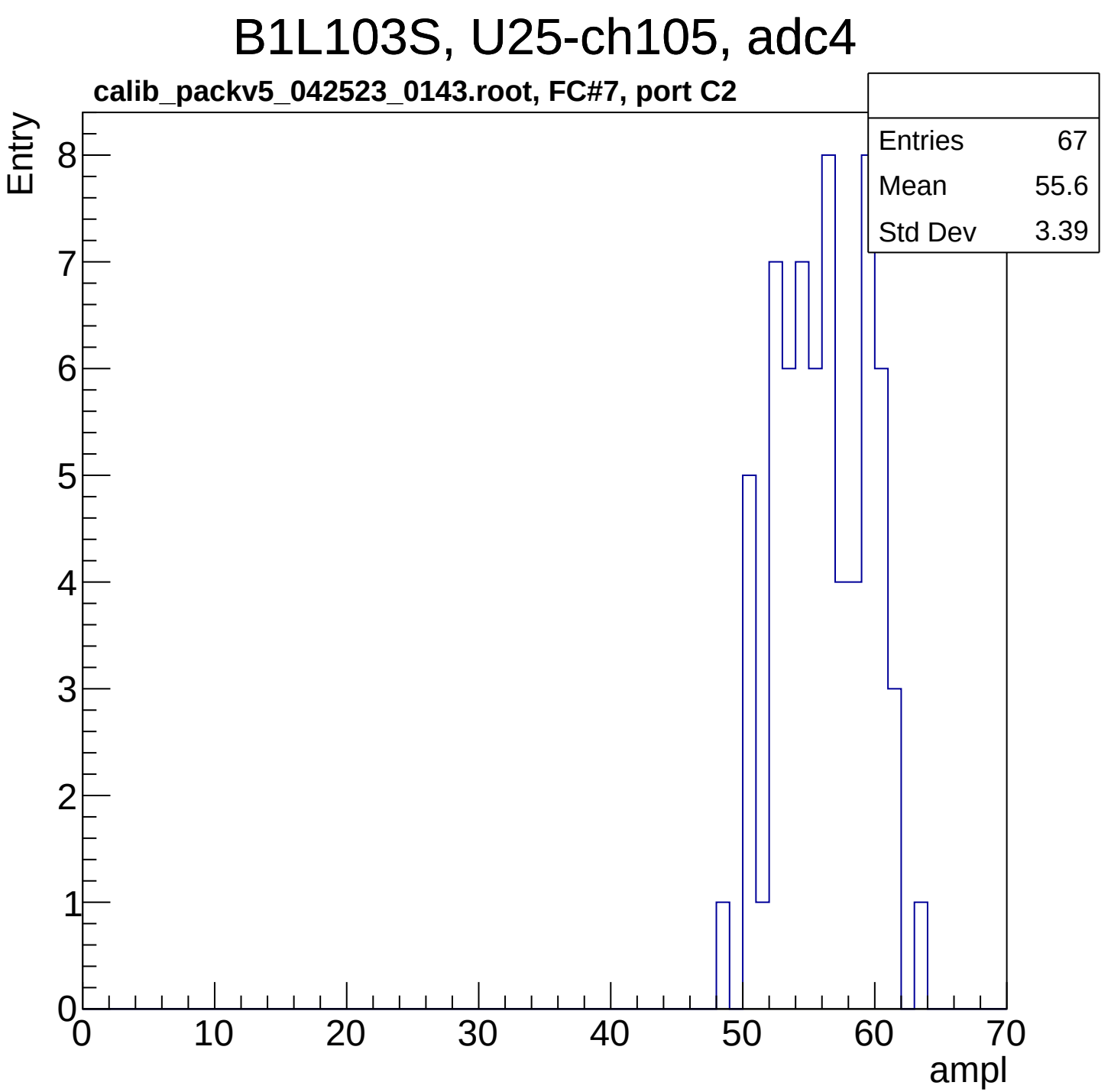
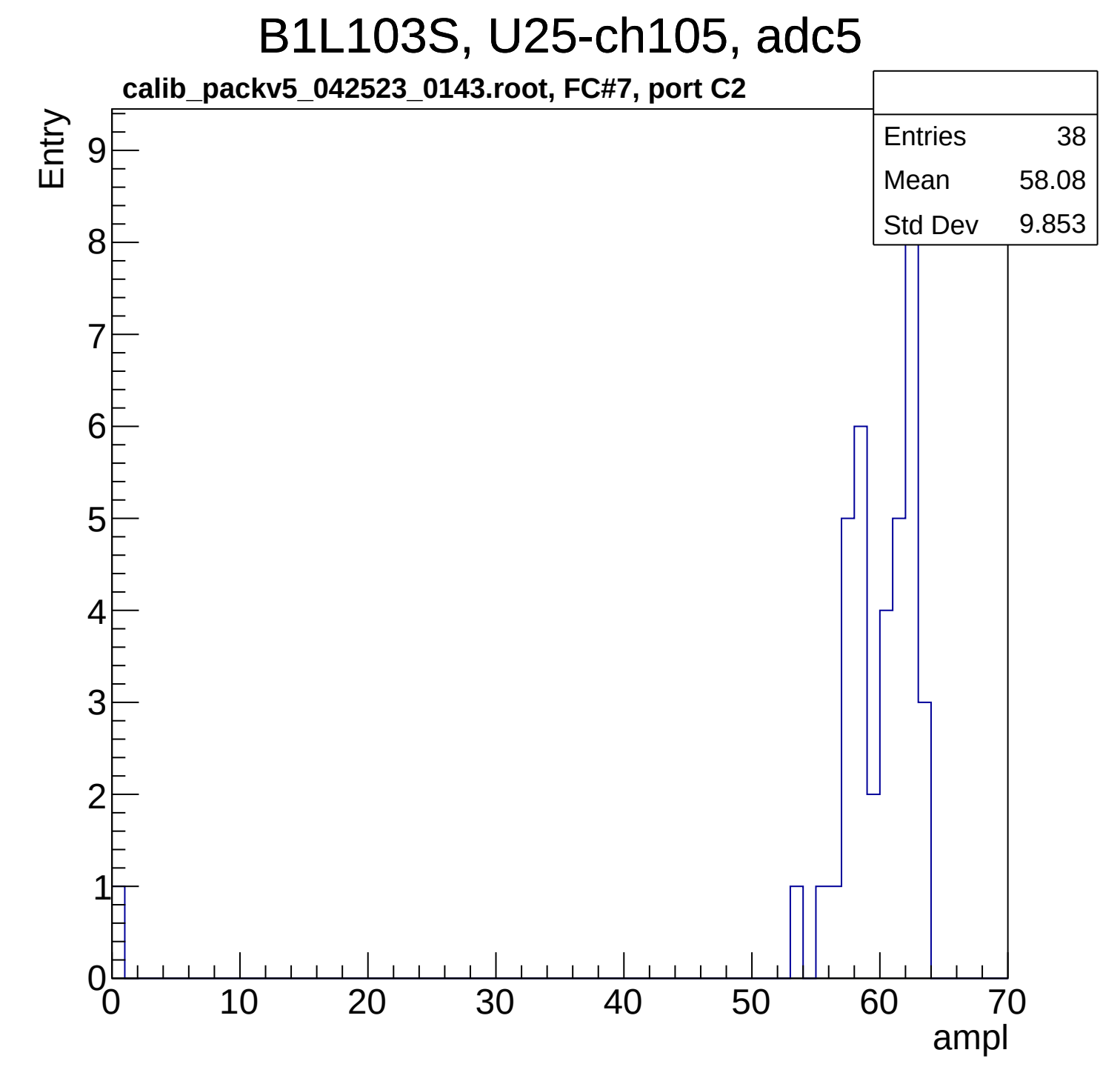
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch105, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 55.6
Std Dev 3.39
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch105, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 38
Mean 58.08
Std Dev 9.853
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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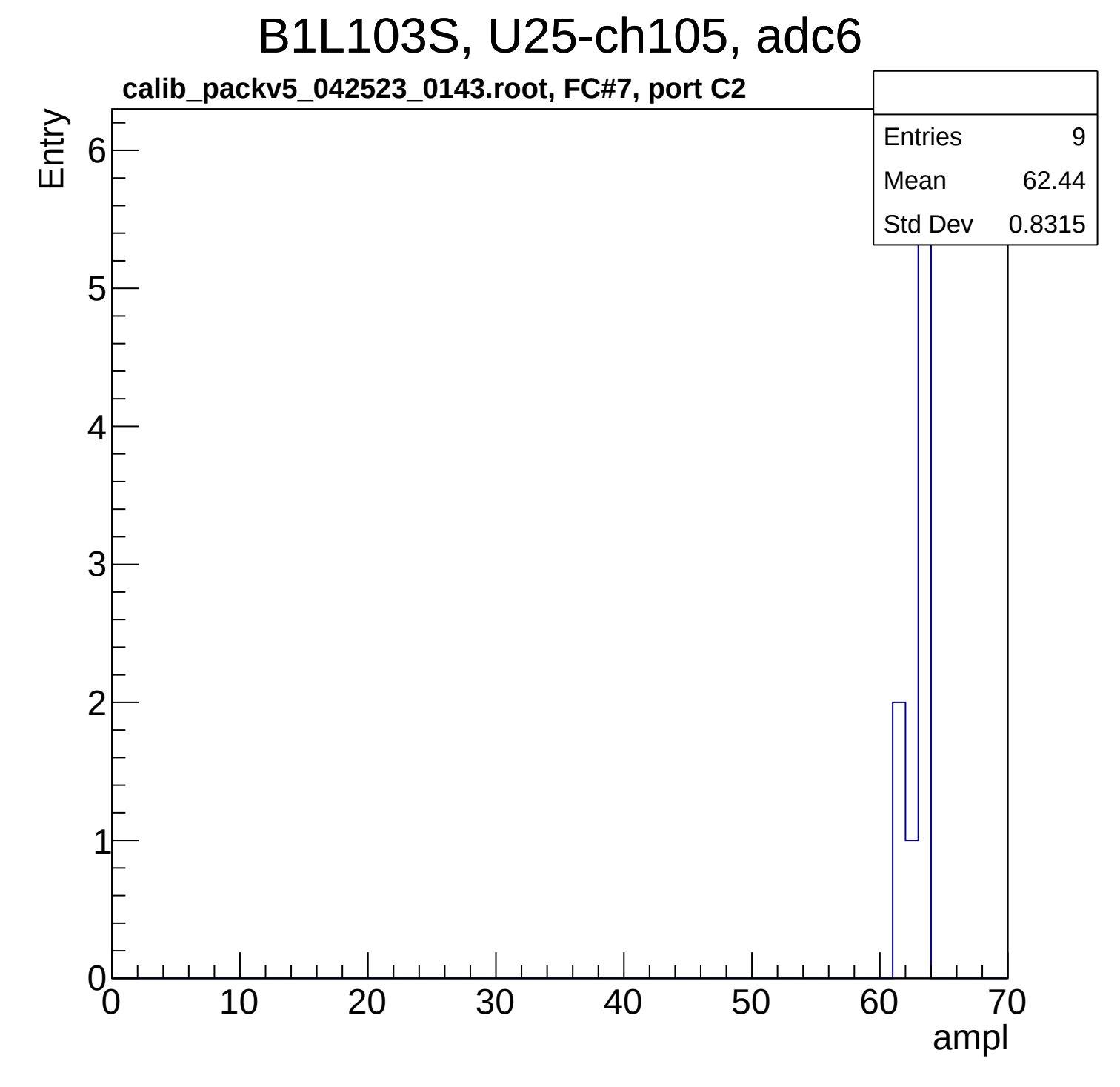
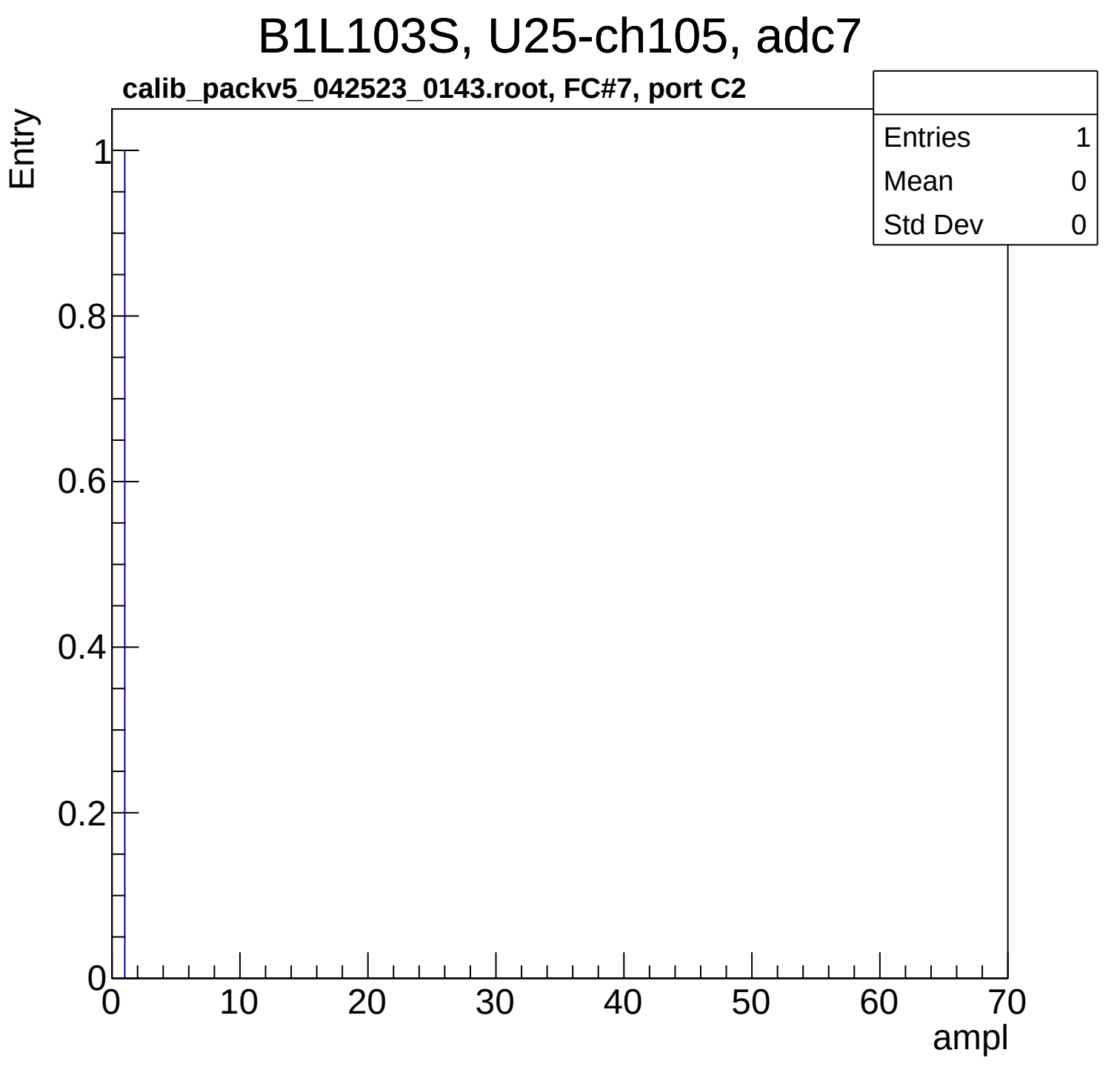

B1L103S, U25-ch105, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 62.44
Std Dev 0.8315
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch105, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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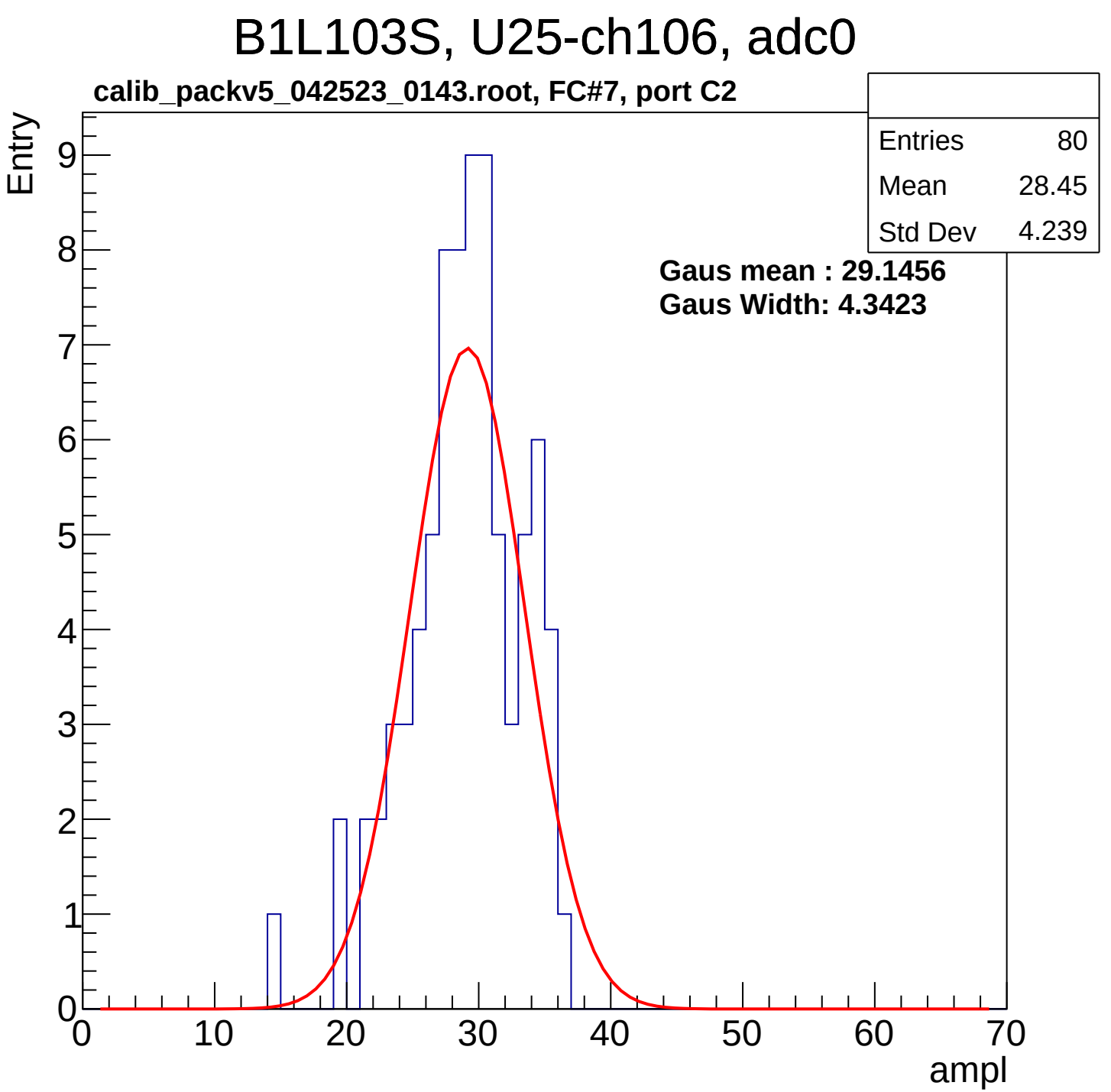
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch106, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 28.45
Std Dev 4.239
Gaus mean : 29.1456
Gaus Width: 4.3423
Entry
ampl
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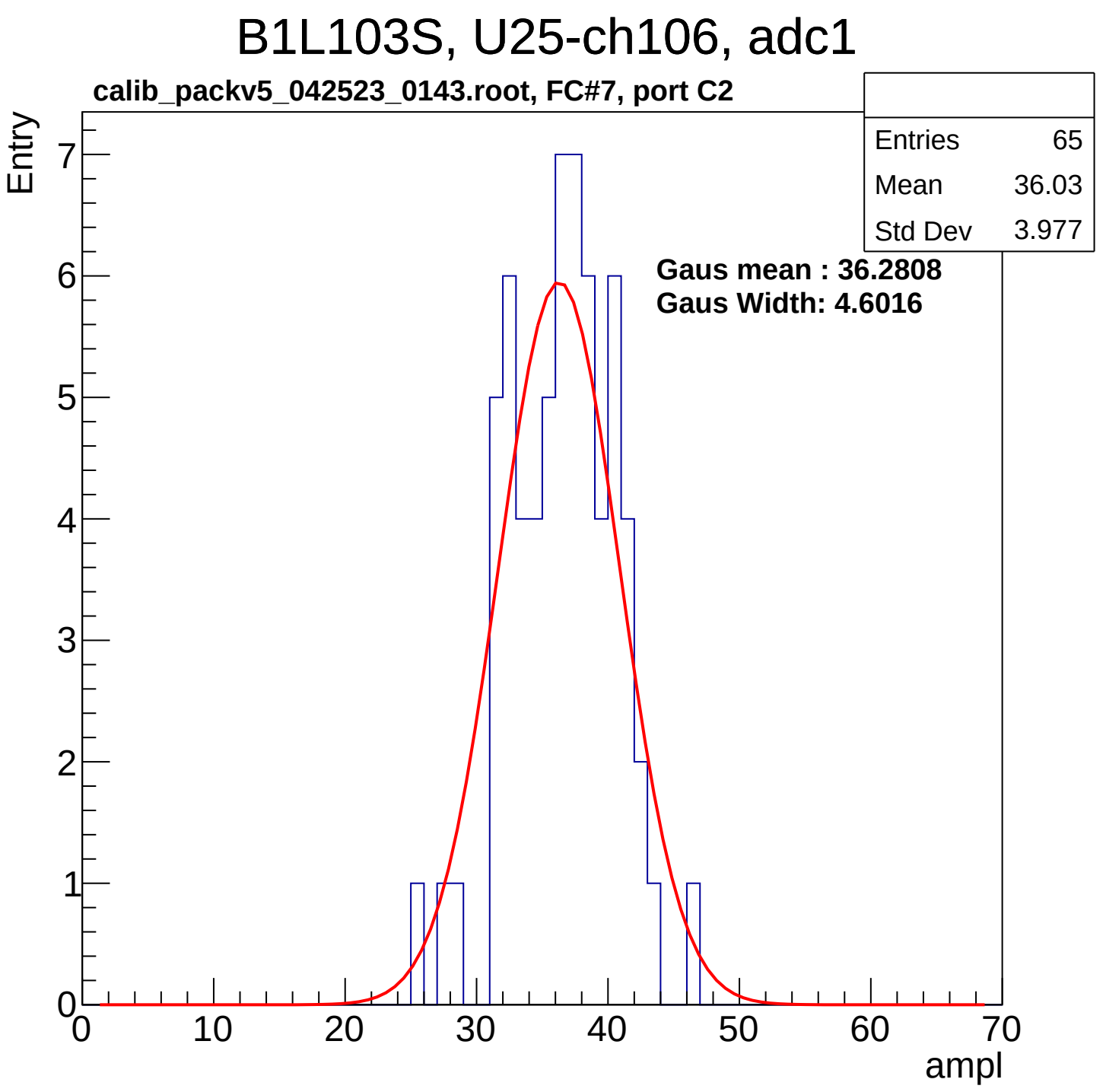
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch106, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 36.03
Std Dev 3.977
Gaus mean : 36.2808
Gaus Width: 4.6016
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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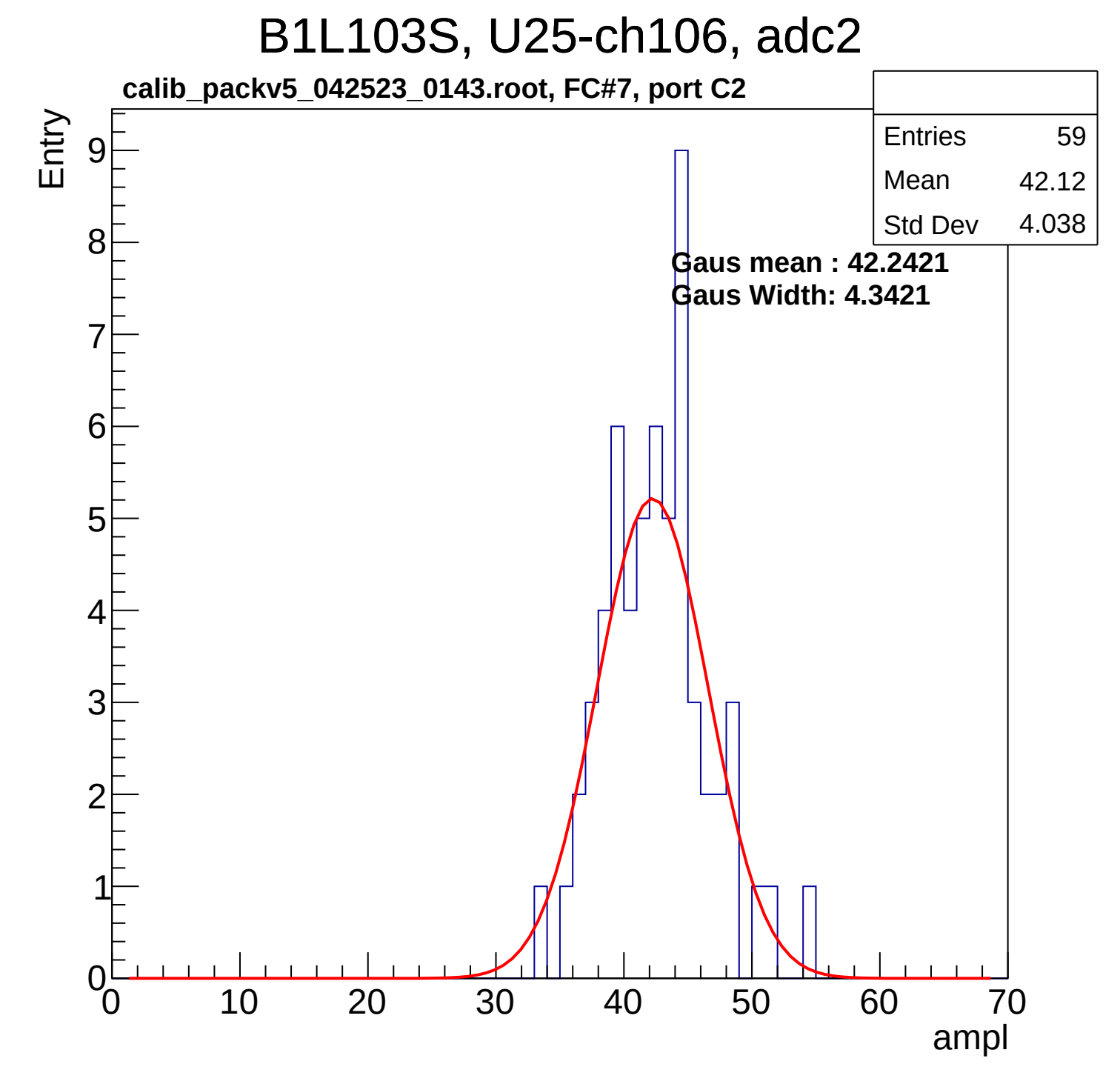
B1L103S, U25-ch106, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 42.12
Std Dev 4.038
Gaus mean : 42.2421
Gaus Width: 4.3421
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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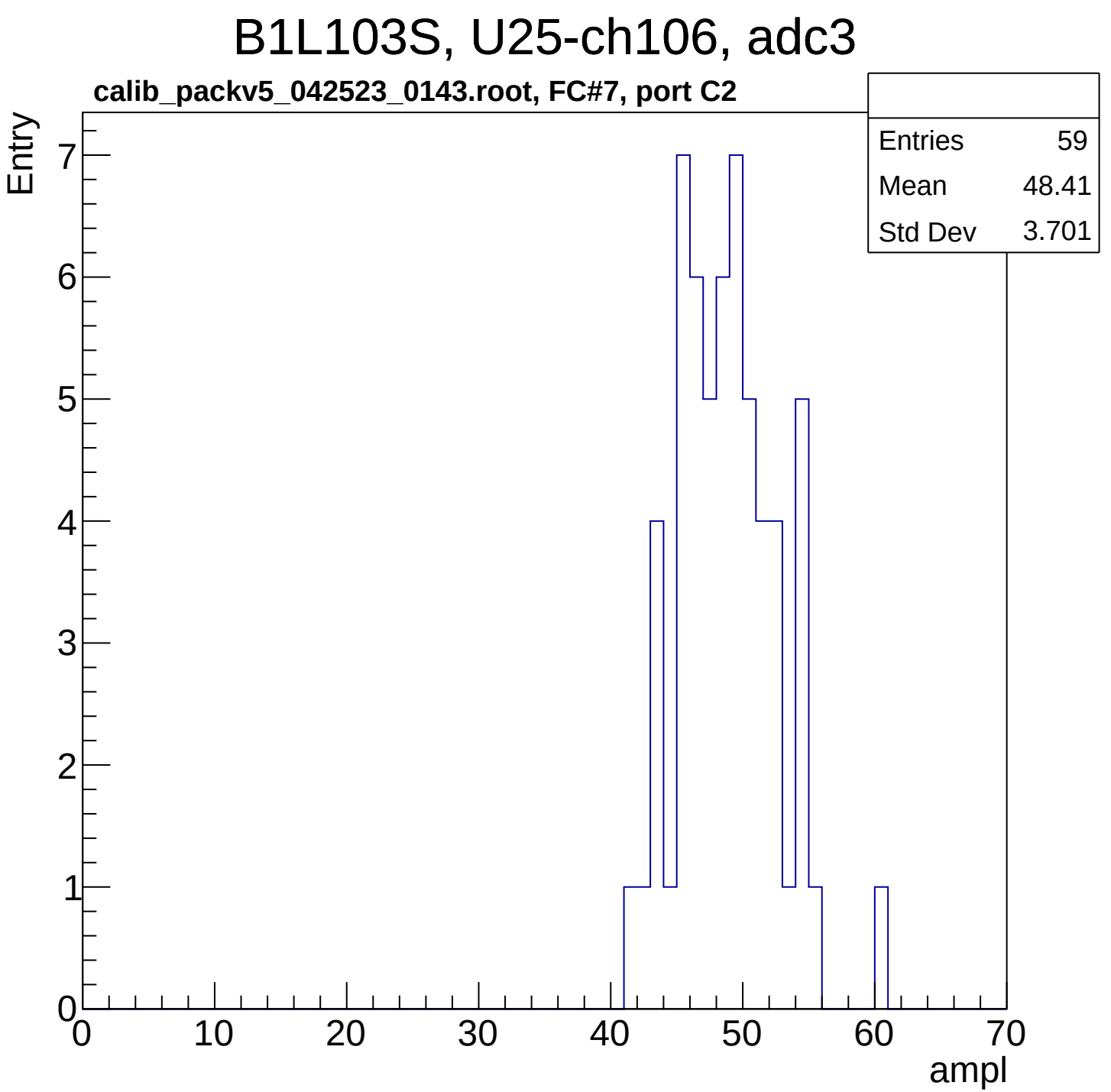
B1L103S, U25-ch106, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 48.41
Std Dev 3.701
Entry
ampl
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1
2
3
4
5
6
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0
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60
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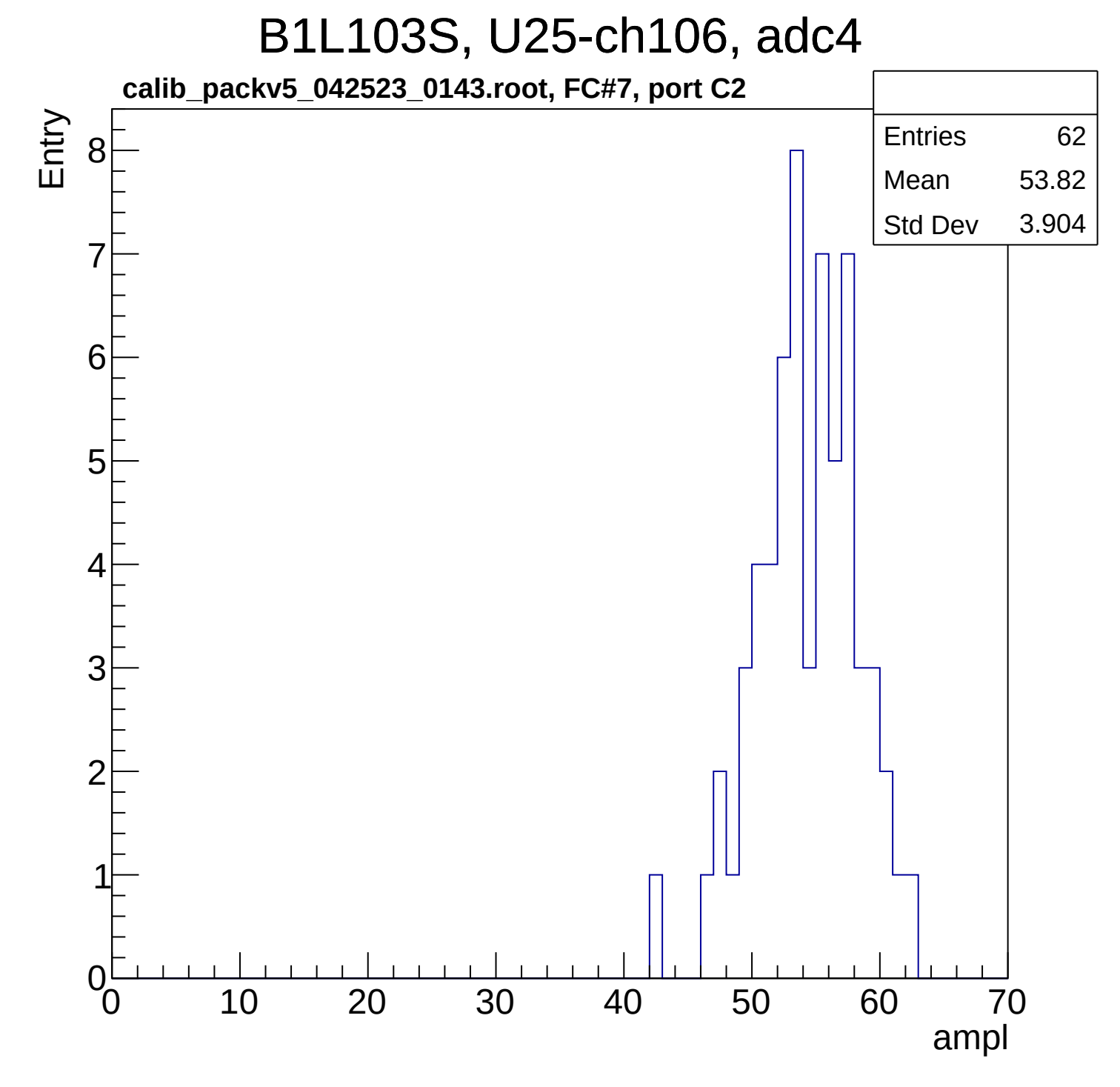
B1L103S, U25-ch106, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 53.82
Std Dev 3.904
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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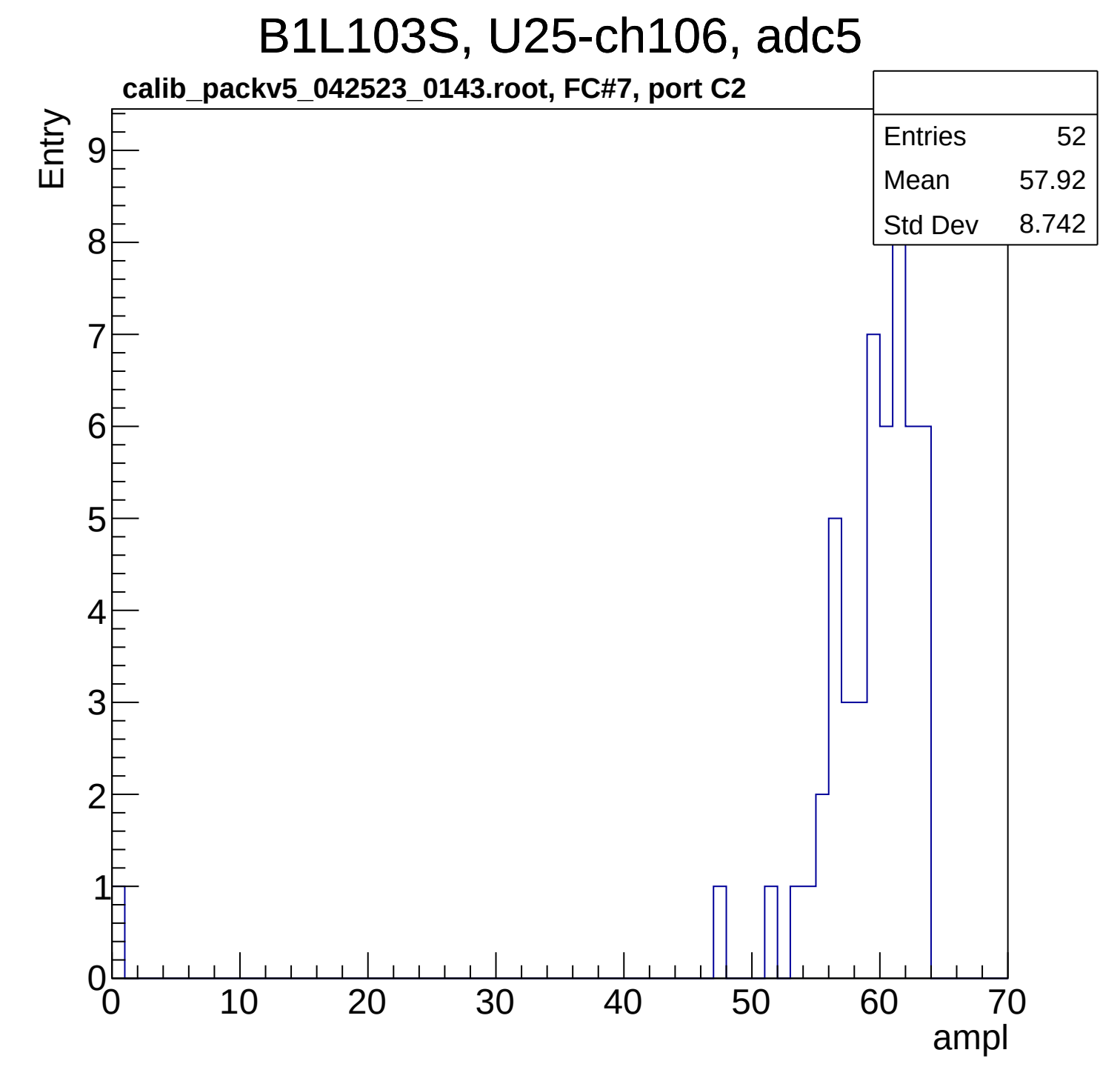
40
50
60
70


B1L103S, U25-ch106, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 57.92
Std Dev 8.742
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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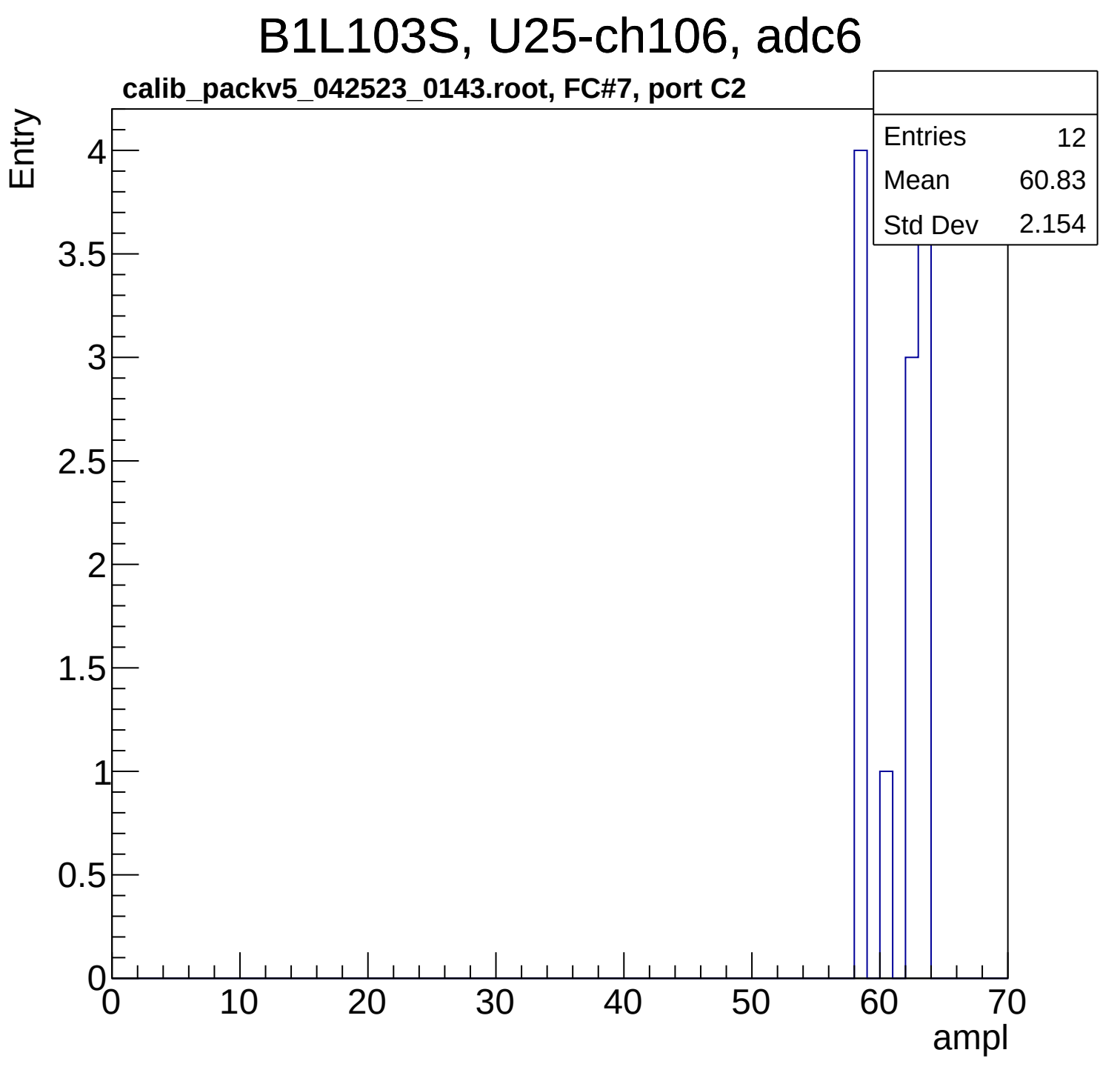
B1L103S, U25-ch106, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 12
Mean 60.83
Std Dev 2.154
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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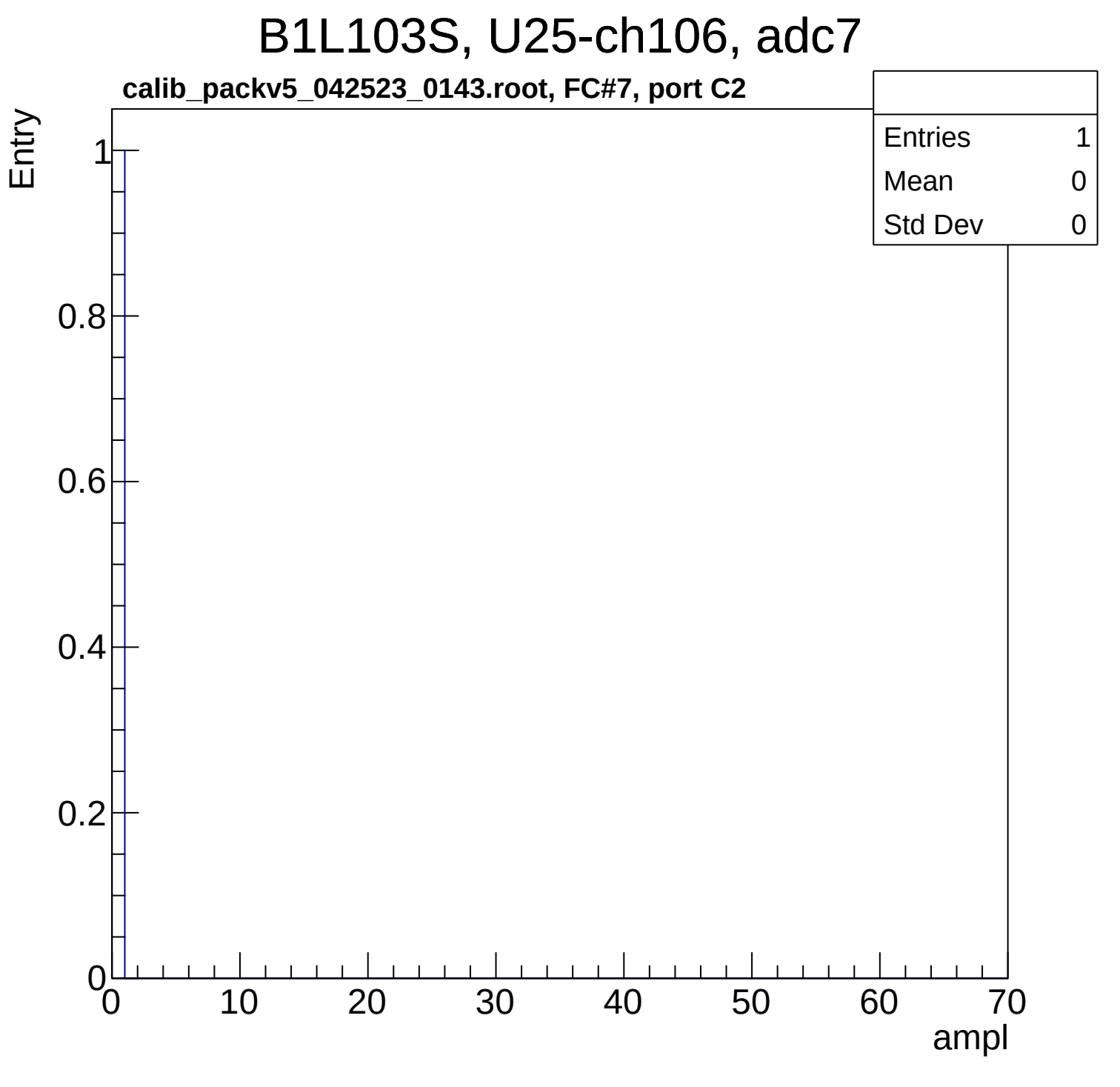
50
60
70


B1L103S, U25-ch106, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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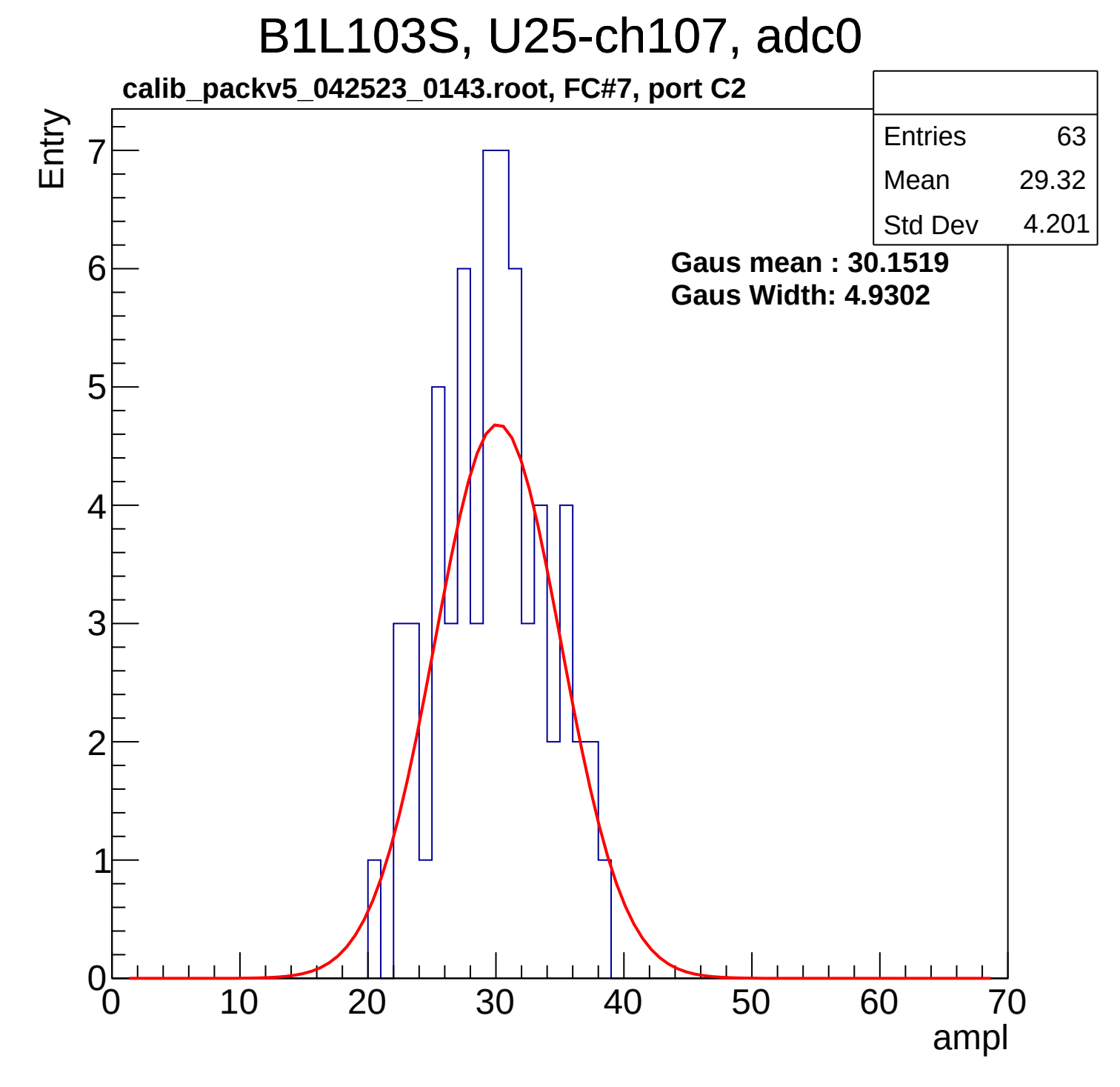
50
60
70
ampl


B1L103S, U25-ch107, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 29.32
Std Dev 4.201
Gaus mean : 30.1519
Gaus Width: 4.9302
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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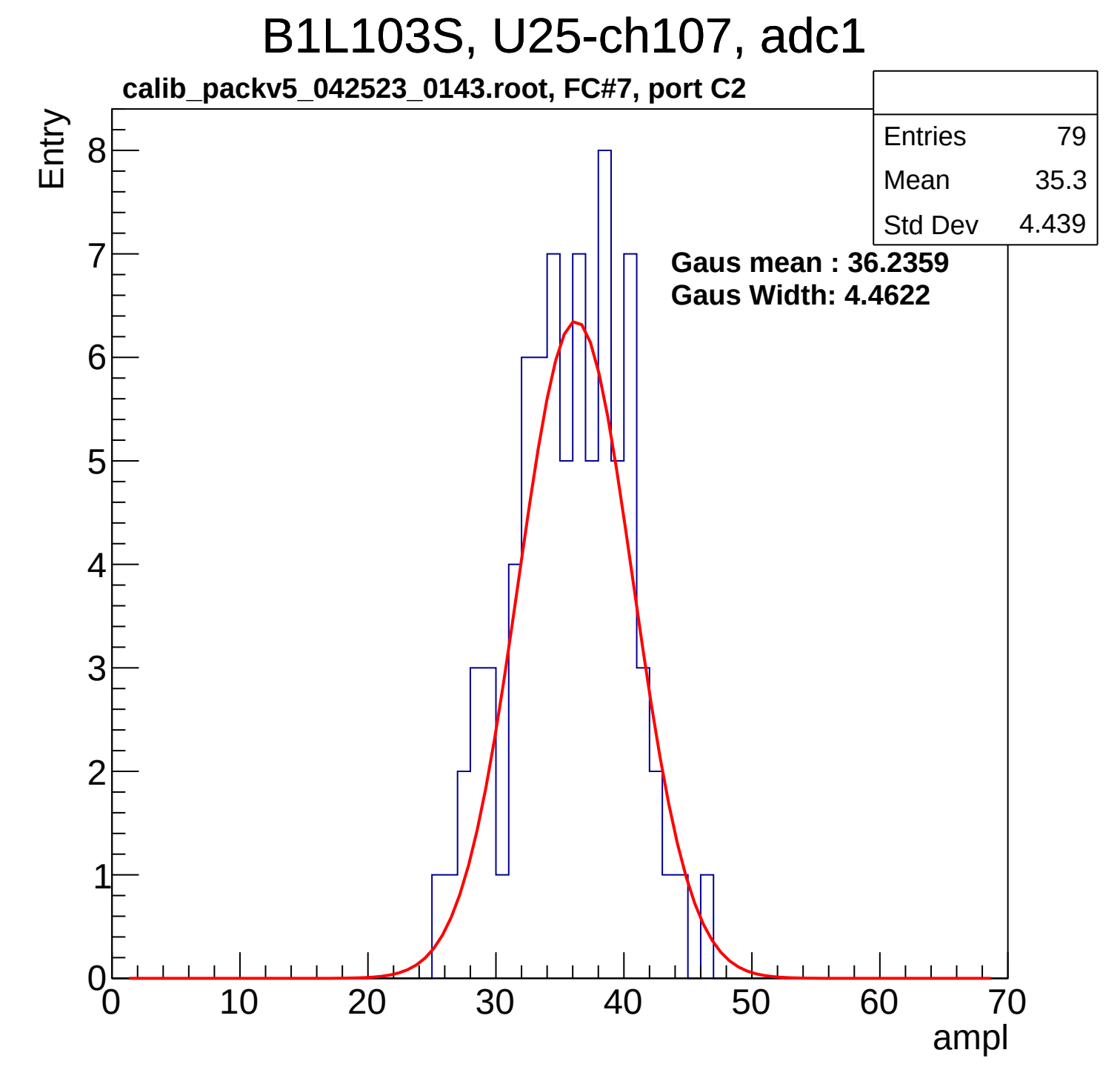
B1L103S, U25-ch107, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 79
Mean 35.3
Std Dev 4.439
Gaus mean : 36.2359
Gaus Width: 4.4622
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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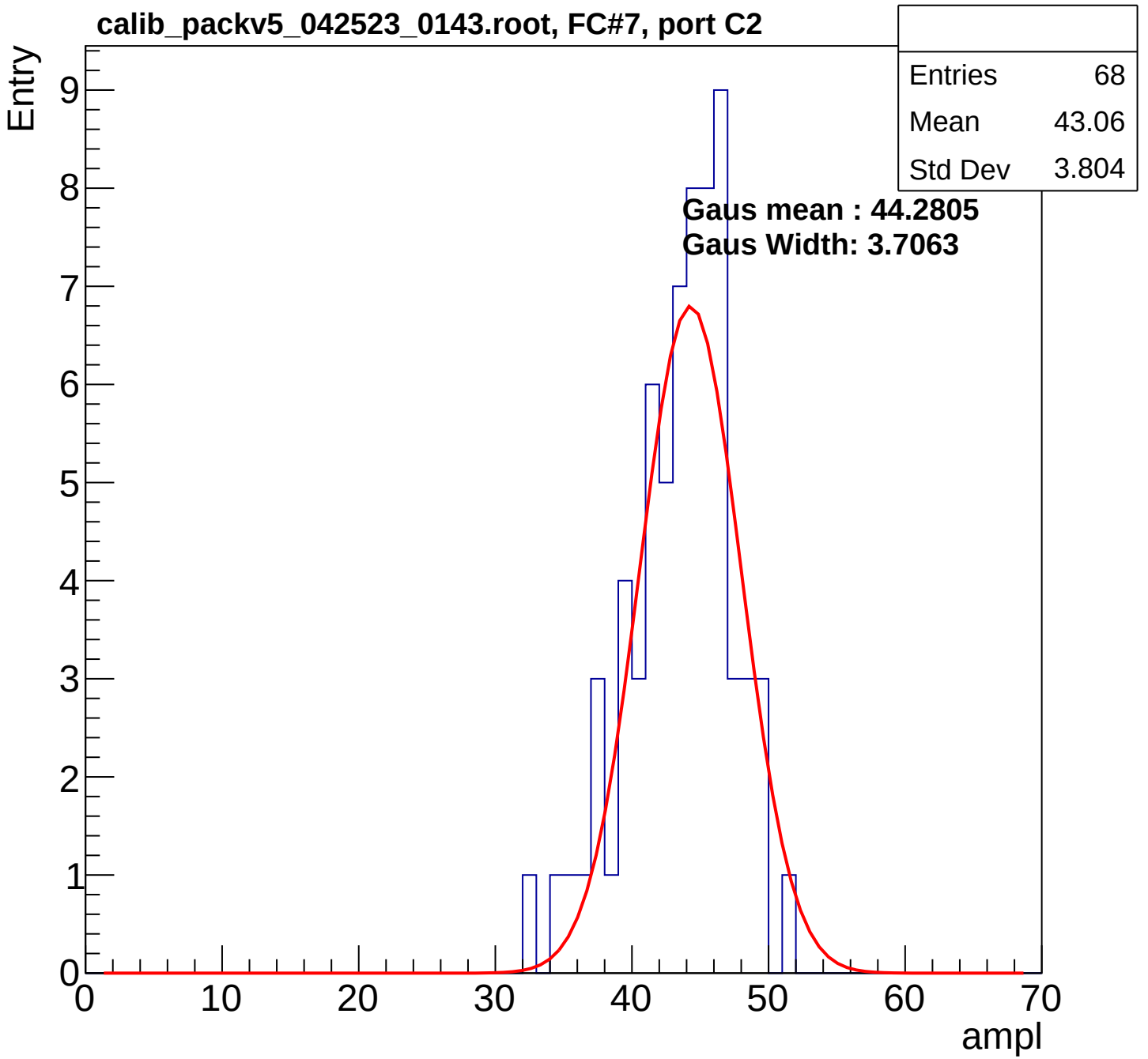
B1L103S, U25-ch107, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 43.06
Std Dev 3.804
Gaus mean : 44.2805
Gaus Width: 3.7063
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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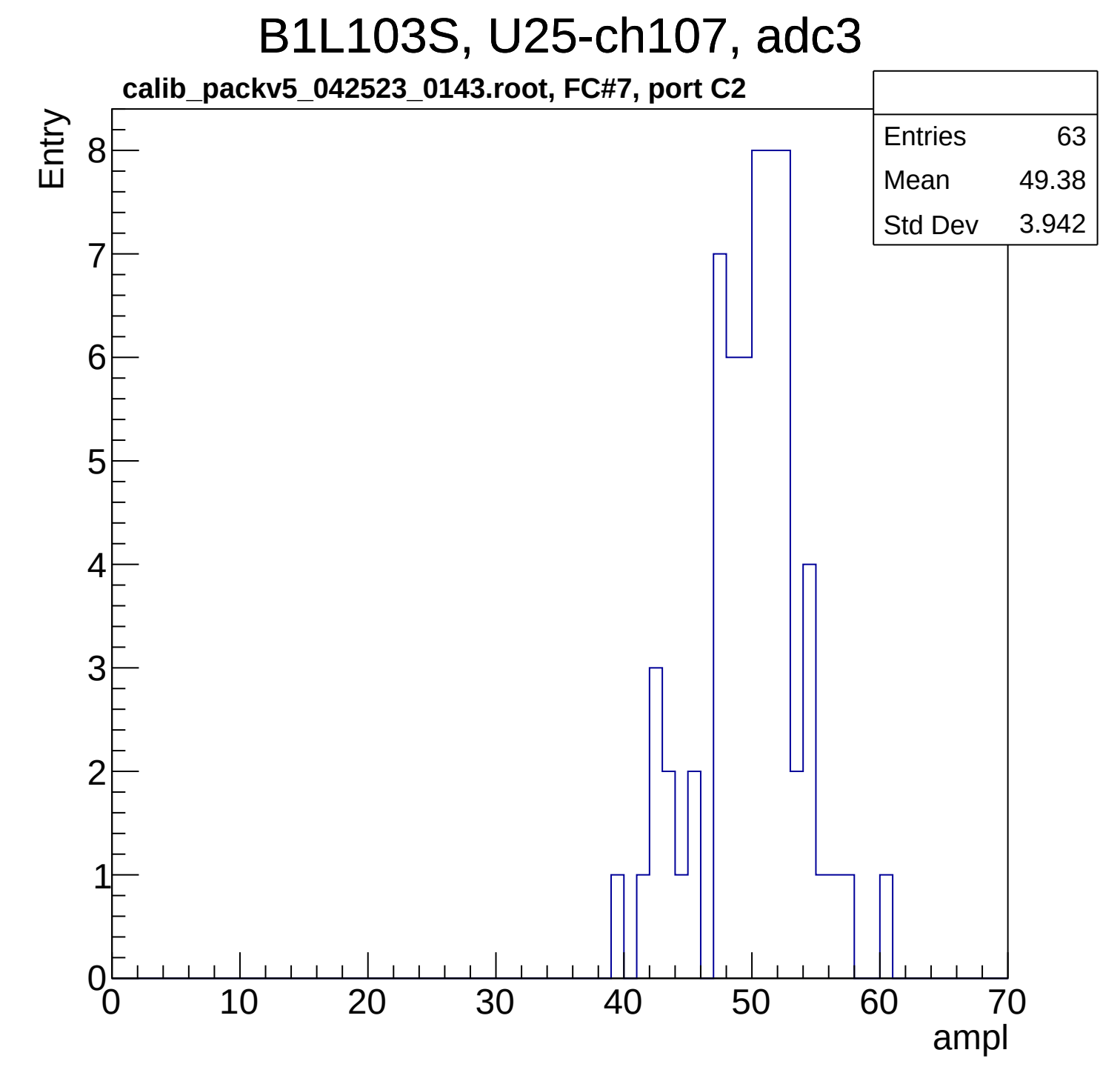
B1L103S, U25-ch107, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 49.38
Std Dev 3.942
Entry
ampl
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6
7
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60
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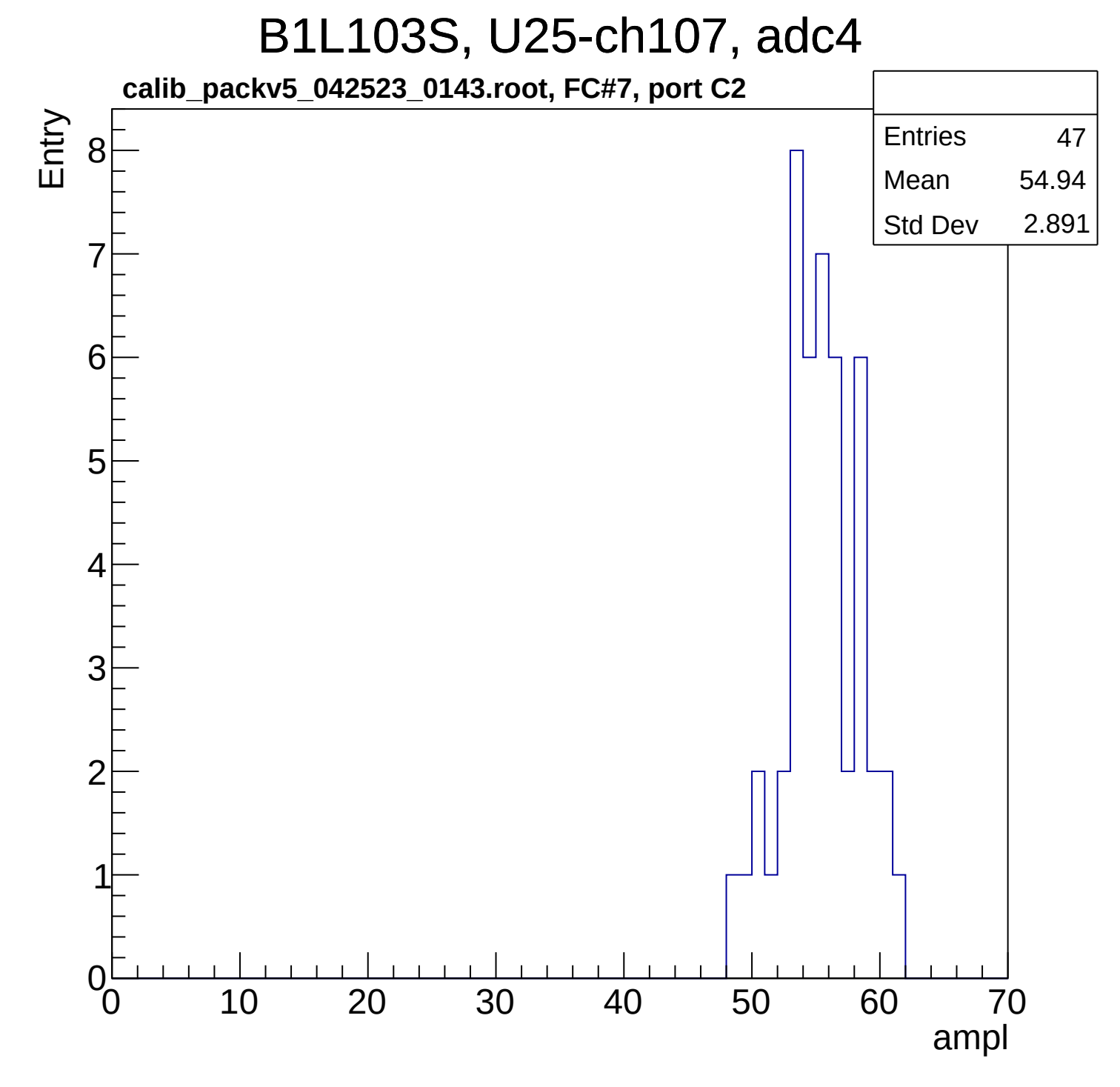
B1L103S, U25-ch107, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 54.94
Std Dev 2.891
Entry
ampl
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2
3
4
5
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0
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40
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60
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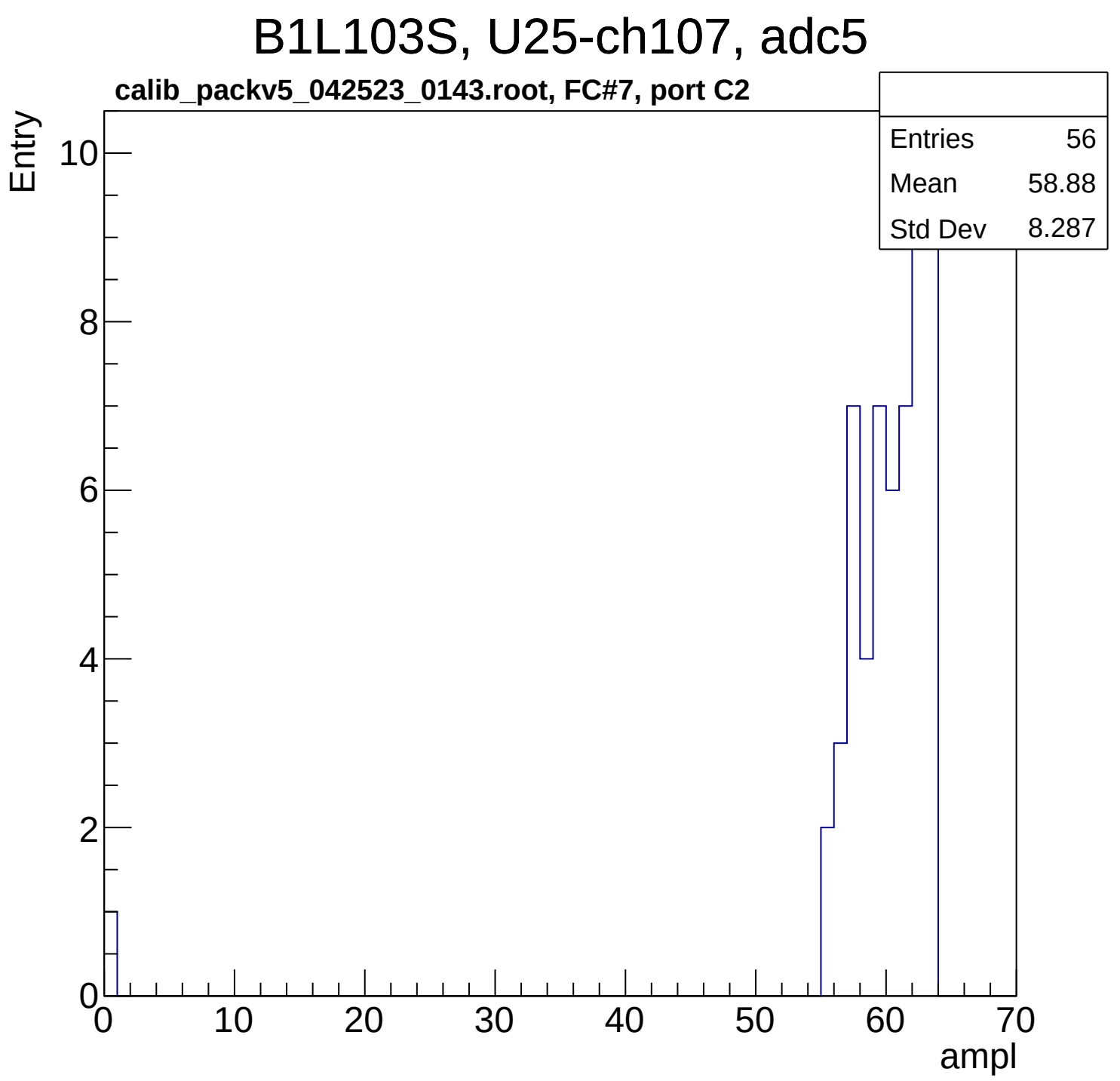
B1L103S, U25-ch107, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 58.88
Std Dev 8.287
Entry
ampl
0
2
4
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0
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50
60
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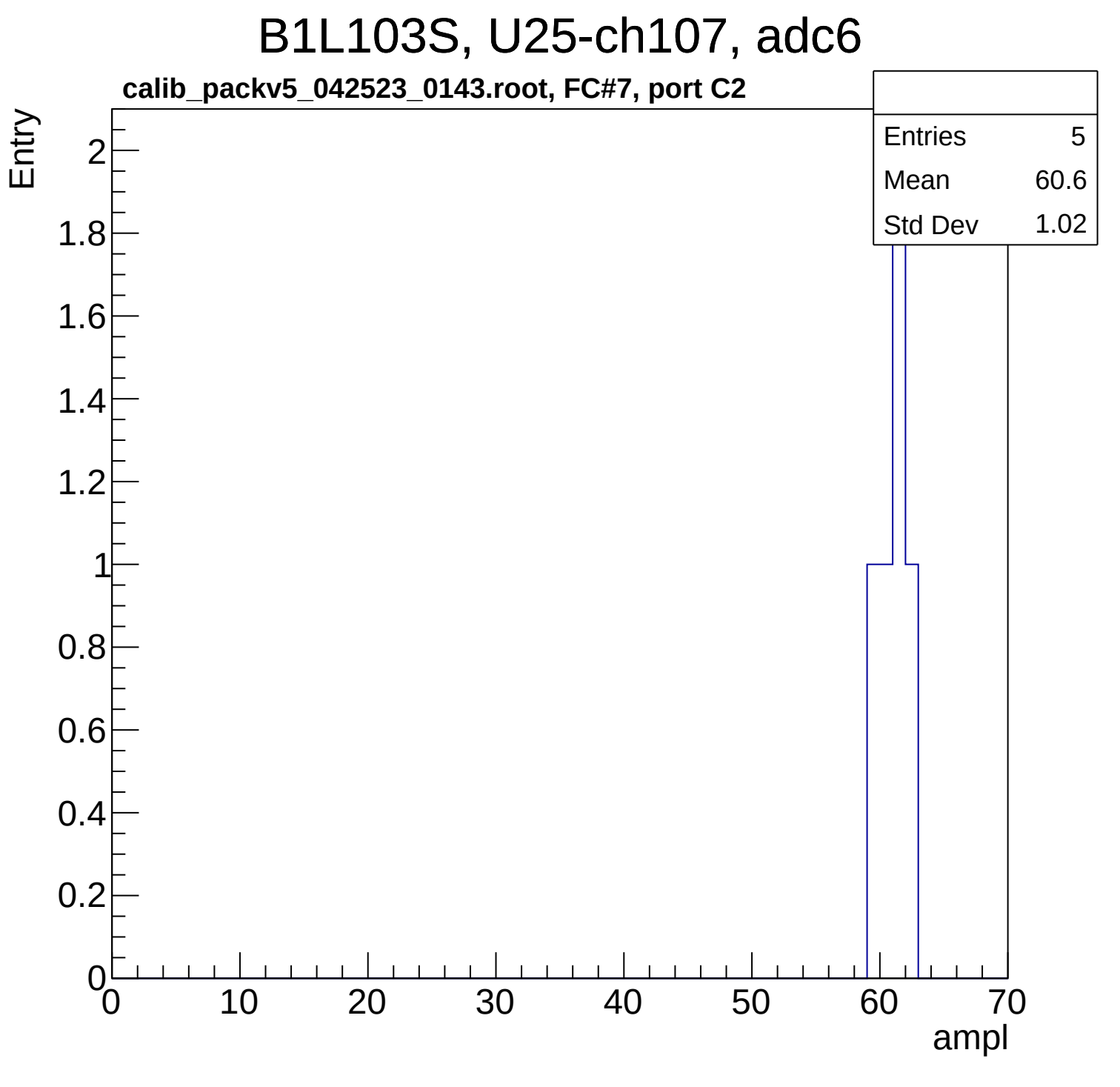
B1L103S, U25-ch107, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 60.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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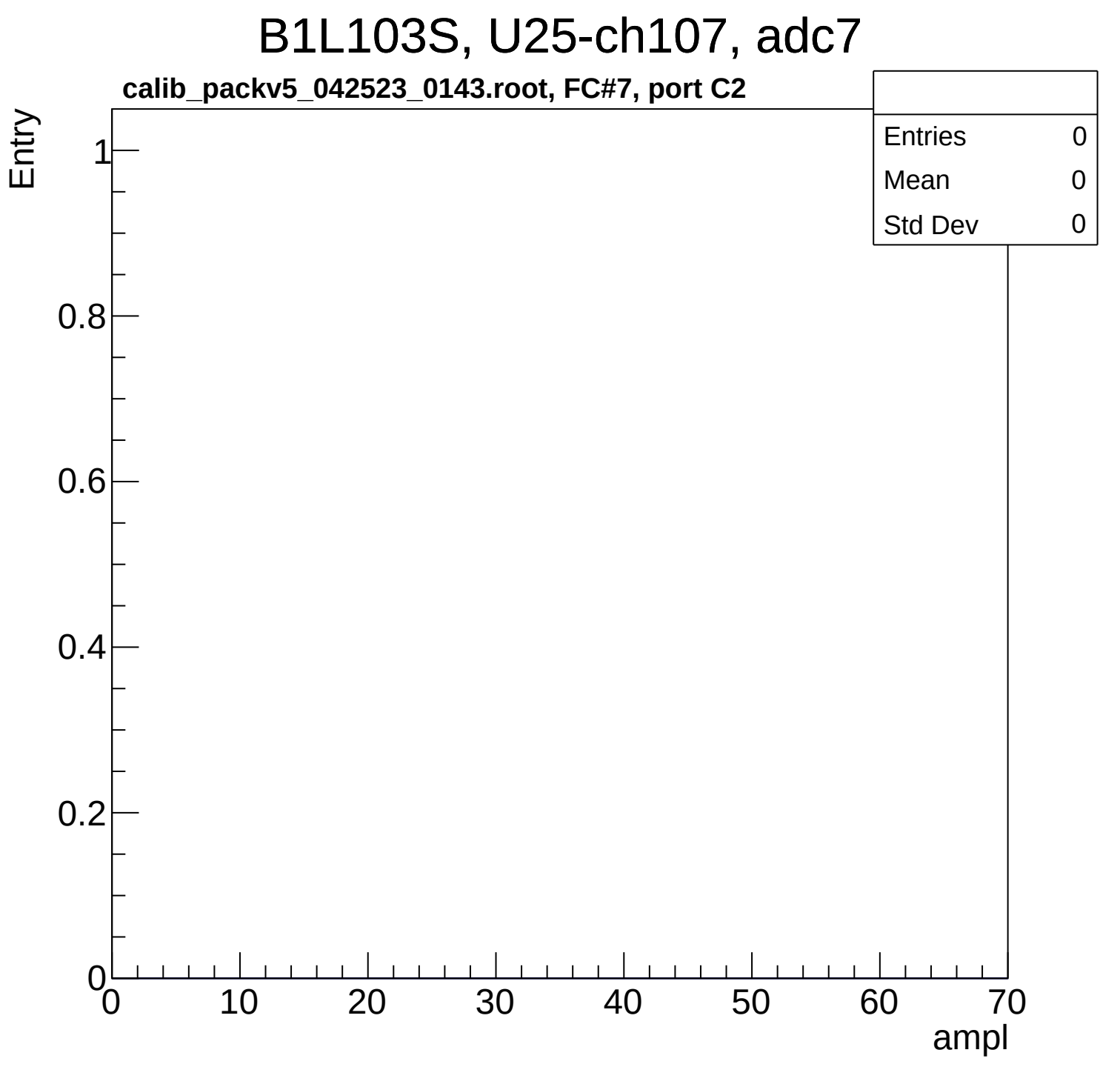
B1L103S, U25-ch107, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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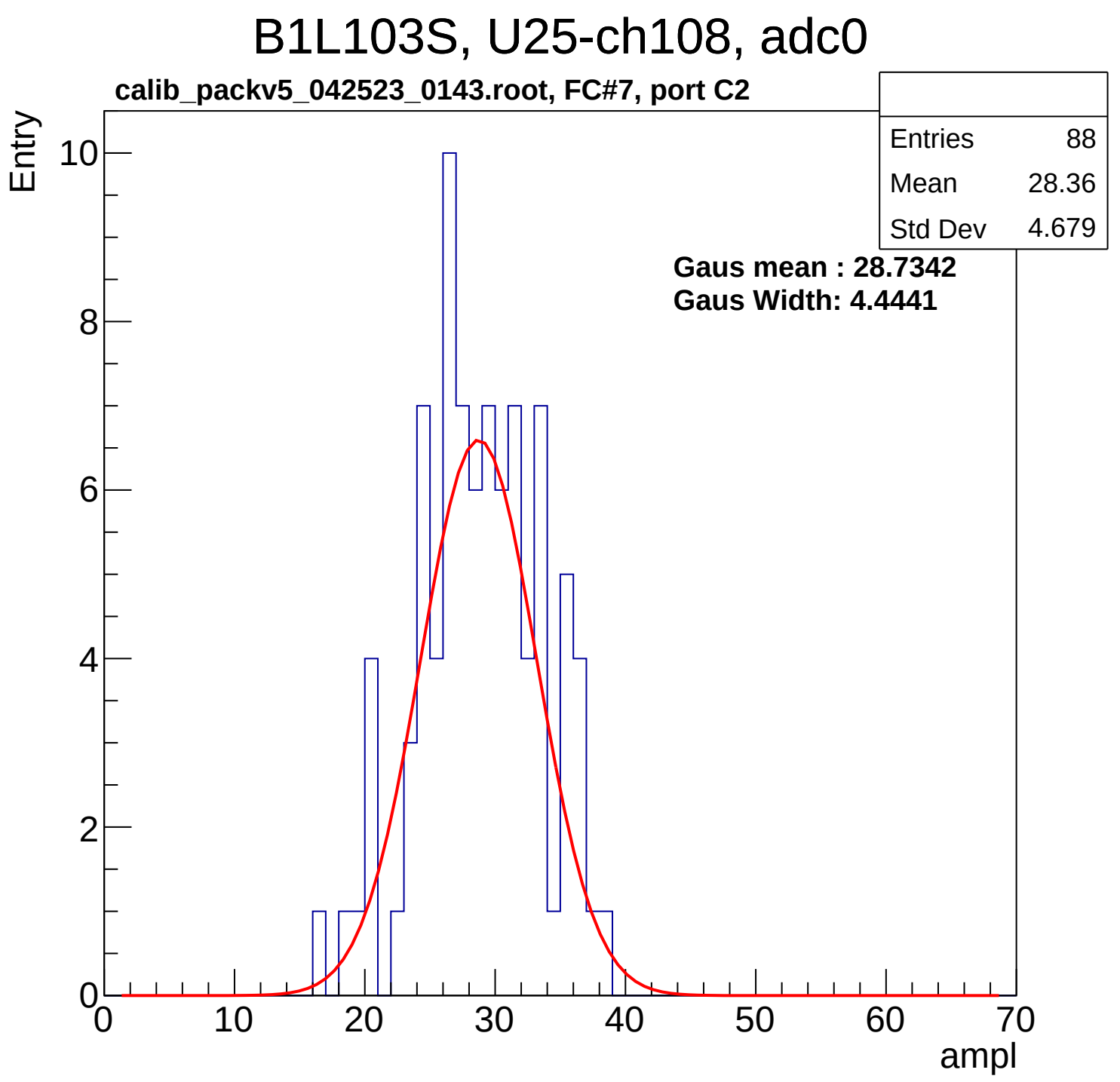
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch108, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 88
Mean 28.36
Std Dev 4.679
Gaus mean : 28.7342
Gaus Width: 4.4441
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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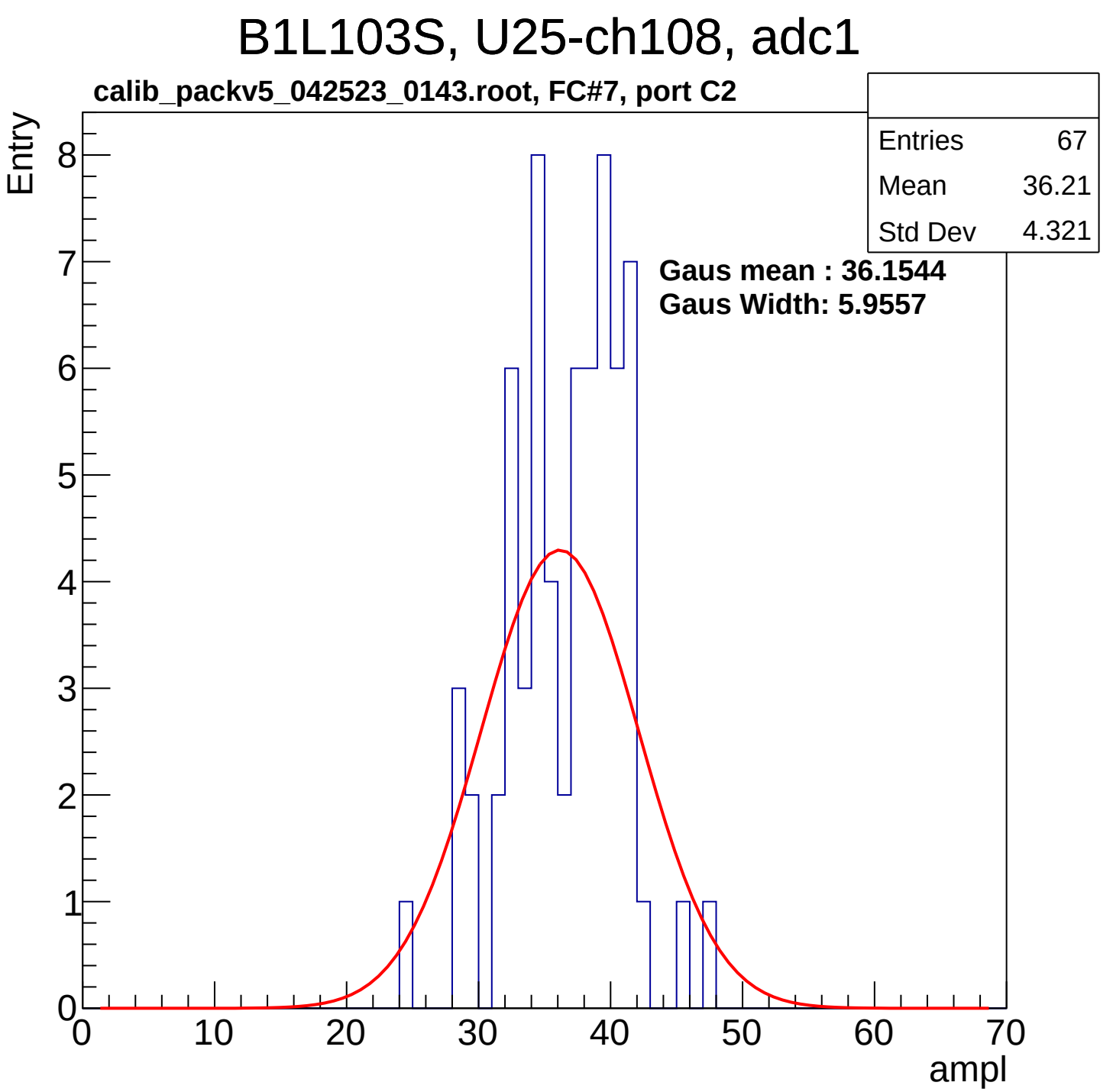
B1L103S, U25-ch108, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.21
Std Dev 4.321
Gaus mean : 36.1544
Gaus Width: 5.9557
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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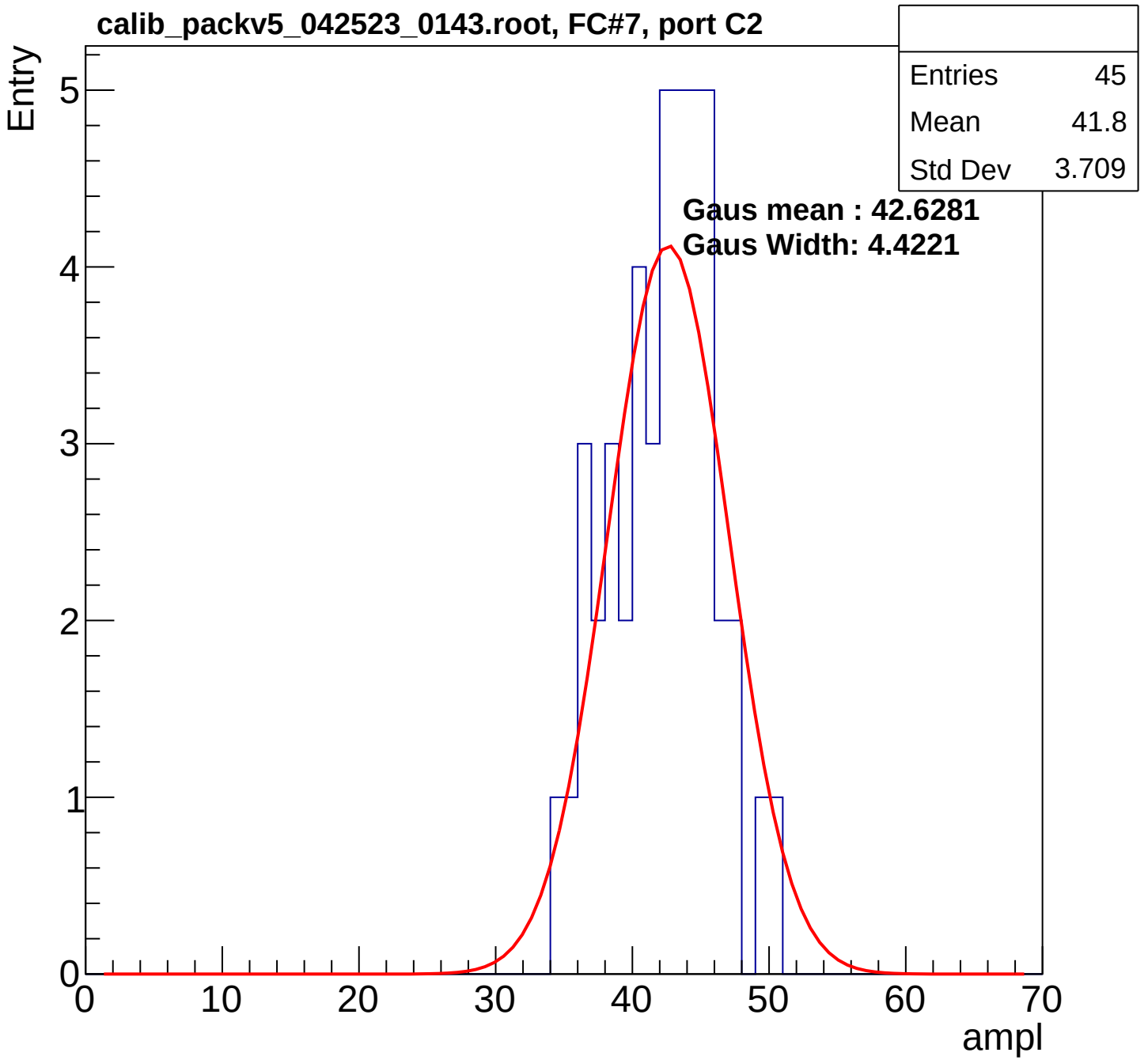
B1L103S, U25-ch108, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 41.8
Std Dev 3.709
Gaus mean : 42.6281
Gaus Width: 4.4221
Entry
ampl
0
1
2
3
4
5
0
10
20
30
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60
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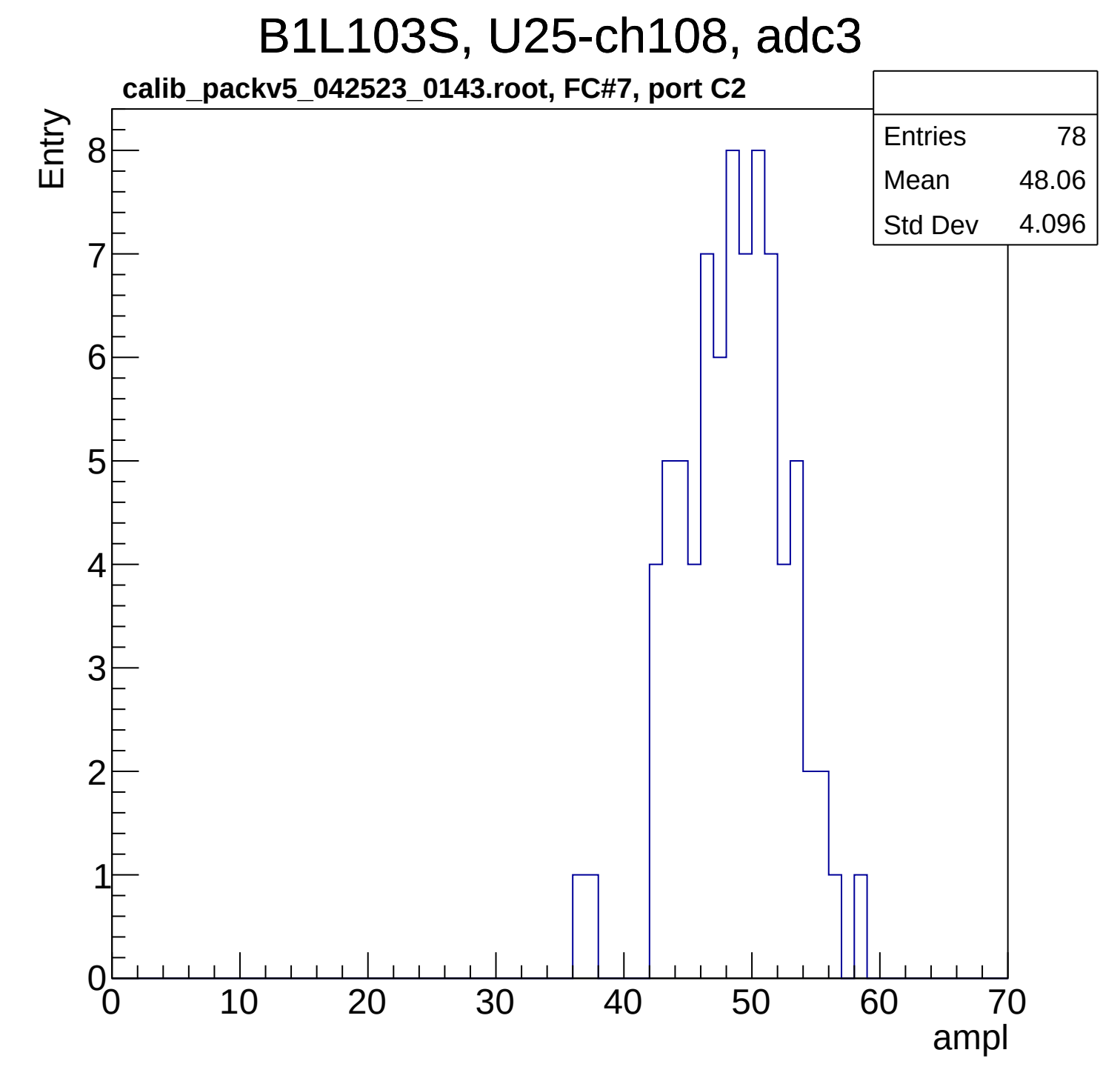
B1L103S, U25-ch108, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 48.06
Std Dev 4.096
Entry
ampl
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1
2
3
4
5
6
7
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60
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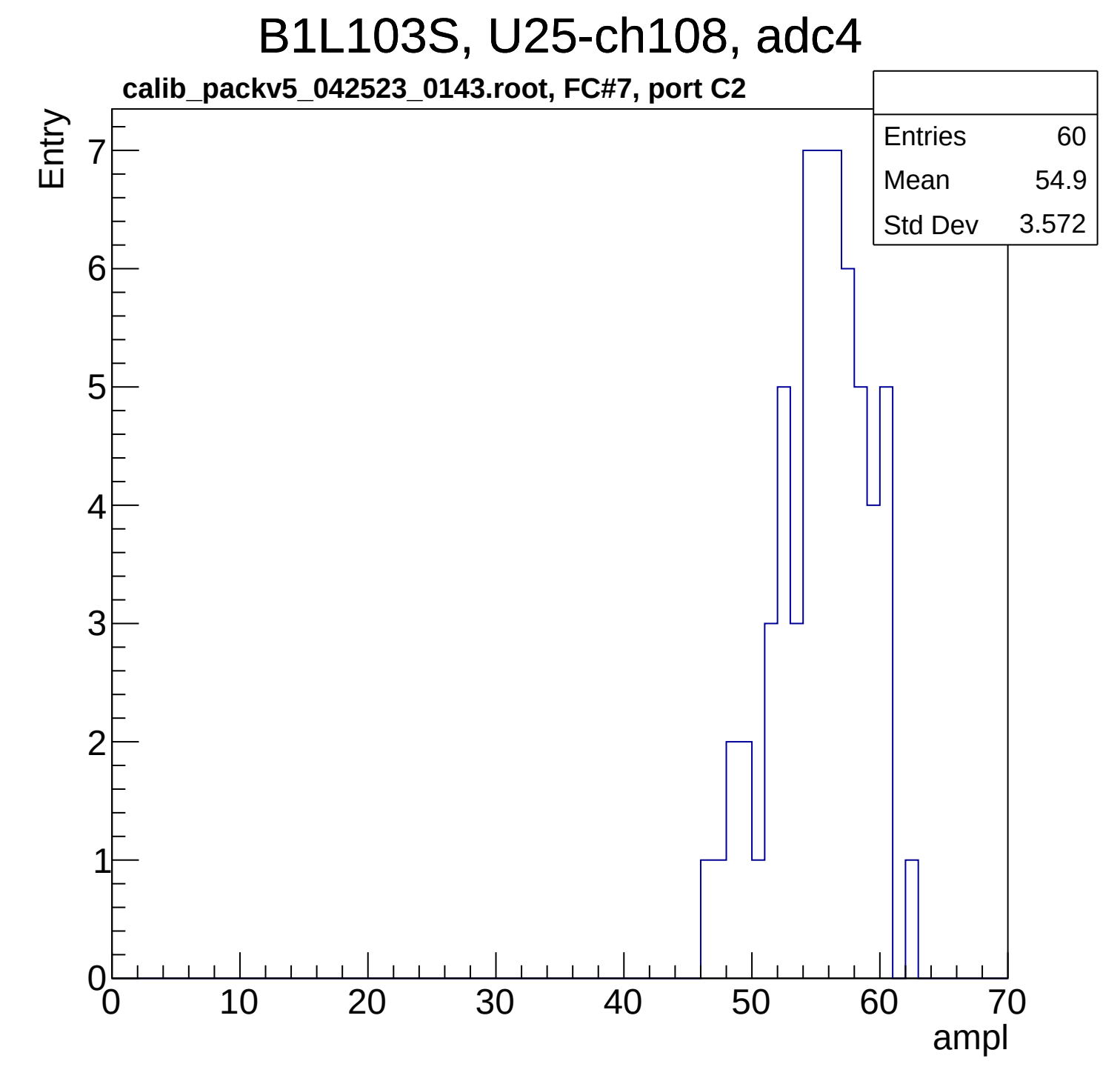
B1L103S, U25-ch108, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 54.9
Std Dev 3.572
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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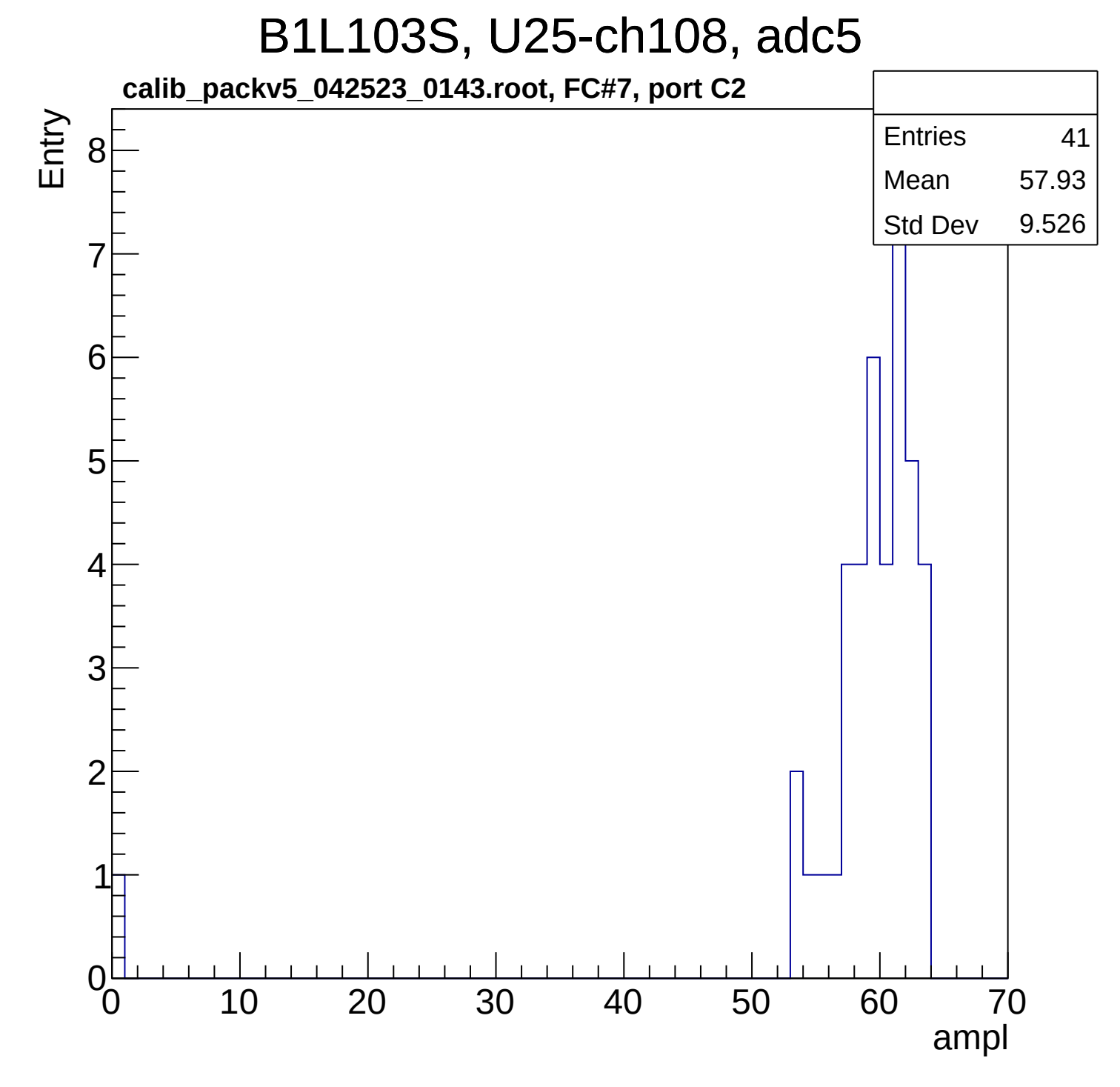
60
70


B1L103S, U25-ch108, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 41
Mean 57.93
Std Dev 9.526
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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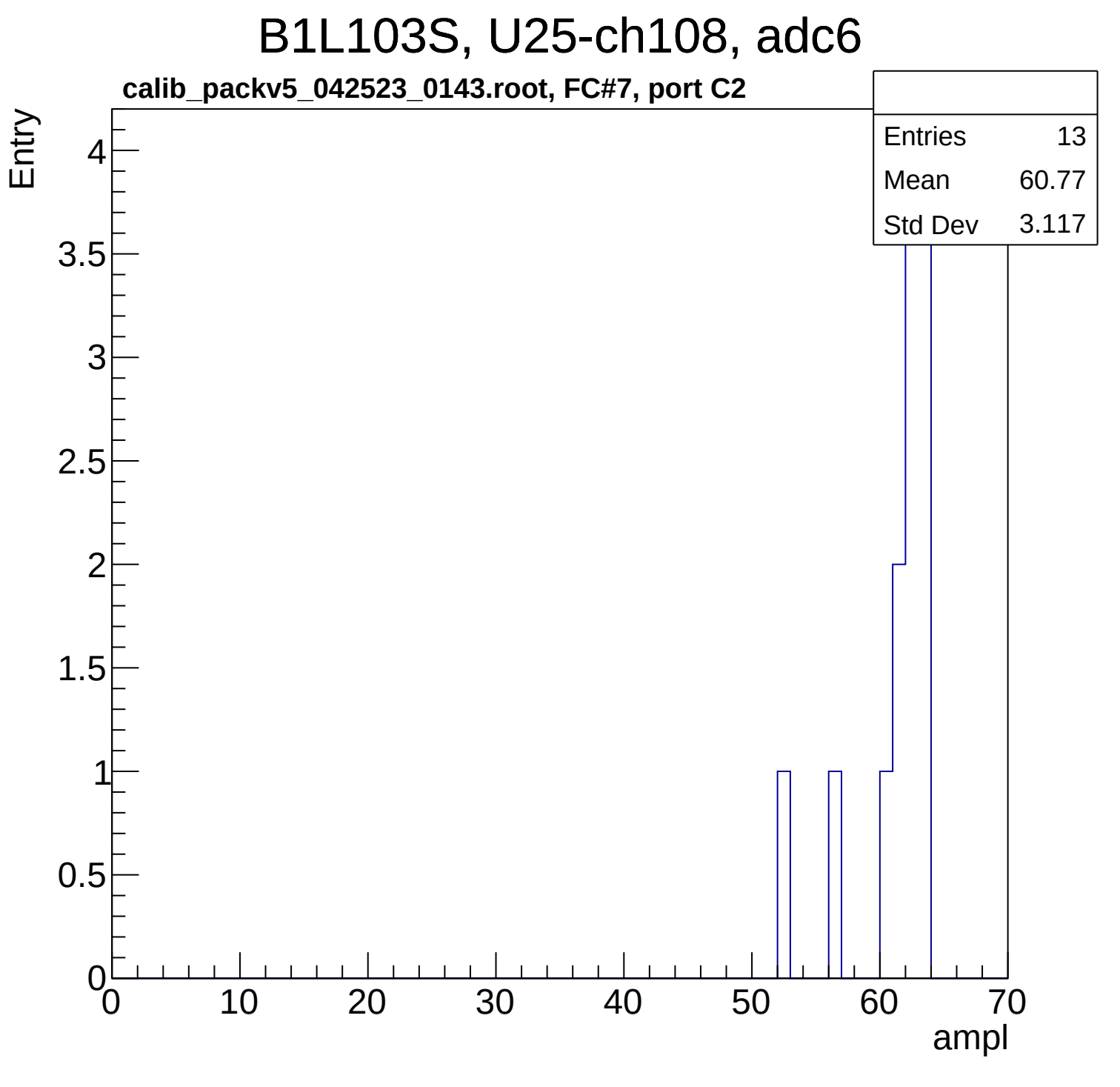
B1L103S, U25-ch108, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 60.77
Std Dev 3.117
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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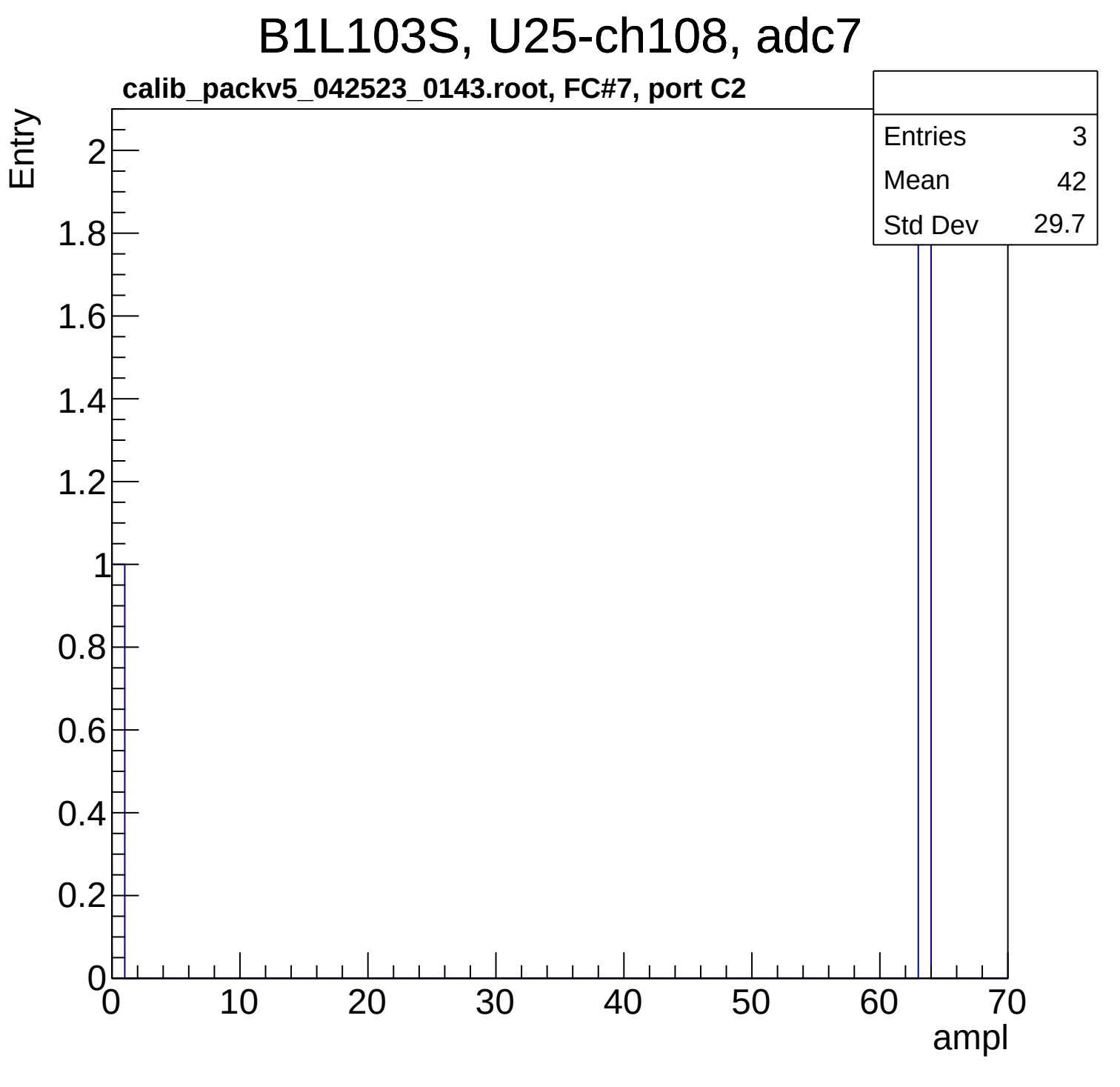
B1L103S, U25-ch108, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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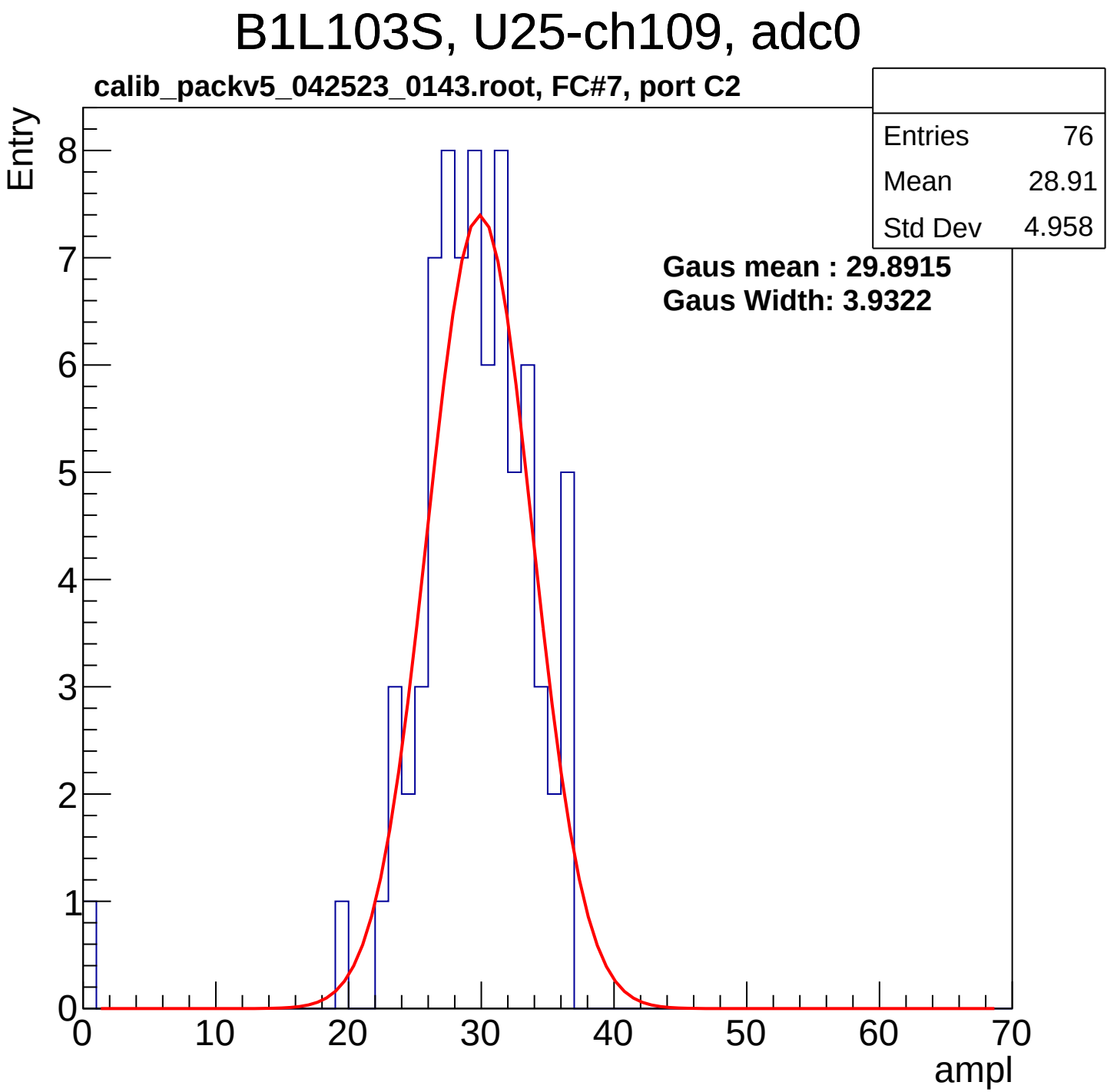
B1L103S, U25-ch109, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 28.91
Std Dev 4.958
Gaus mean : 29.8915
Gaus Width: 3.9322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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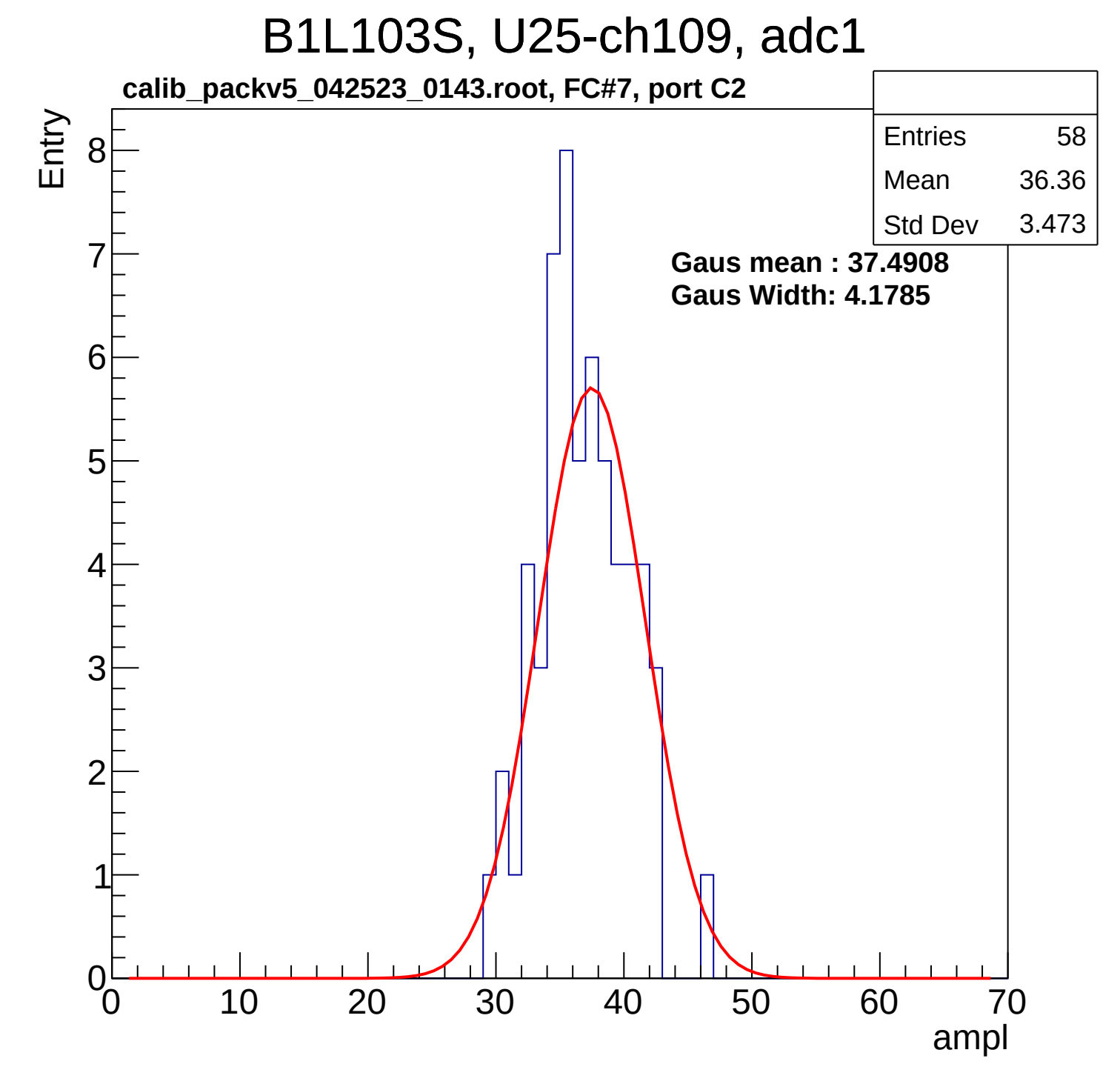
B1L103S, U25-ch109, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 36.36
Std Dev 3.473
Gaus mean : 37.4908
Gaus Width: 4.1785
Entry
ampl
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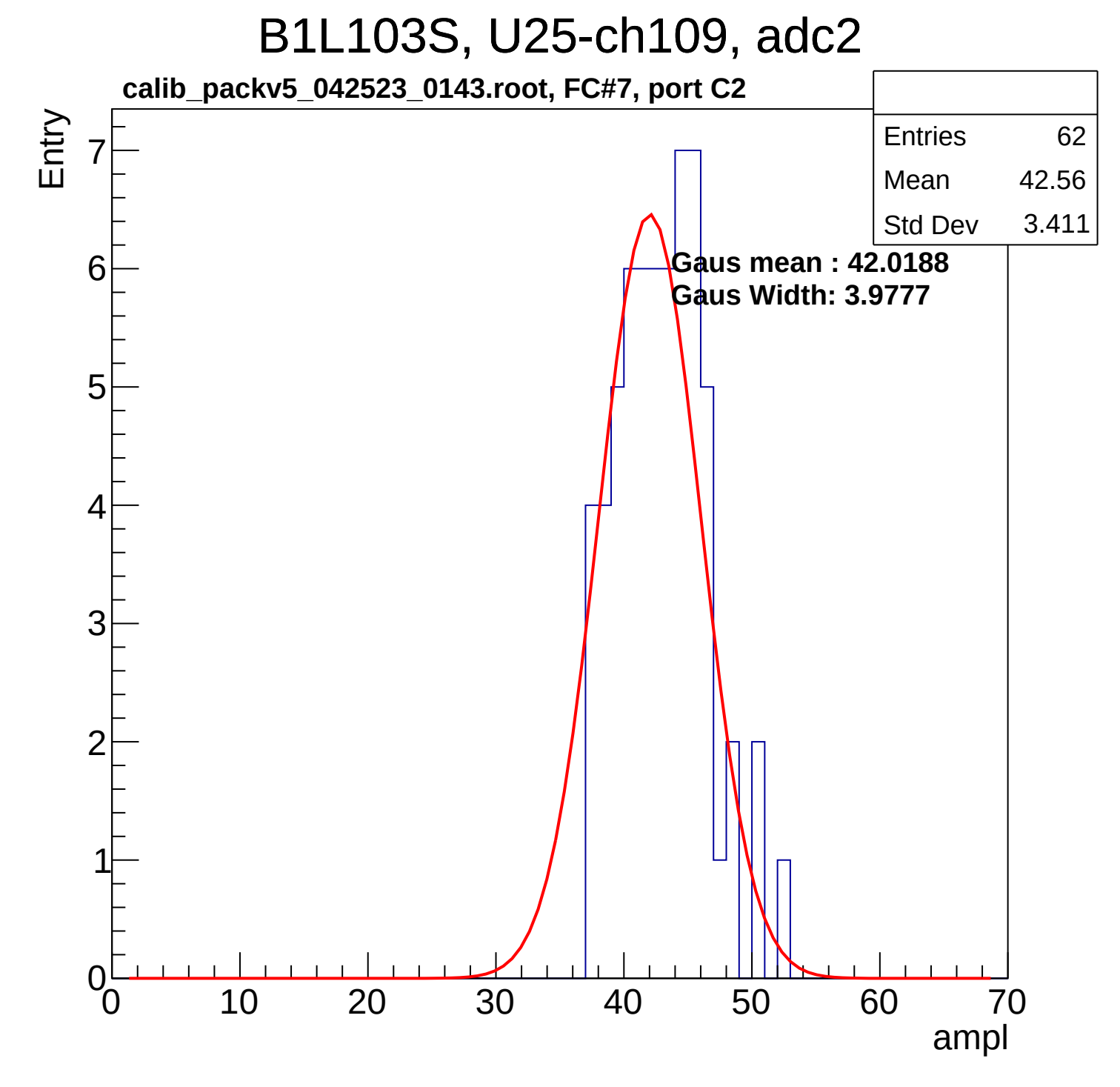
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch109, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 42.56
Std Dev 3.411
Gaus mean : 42.0188
Gaus Width: 3.9777
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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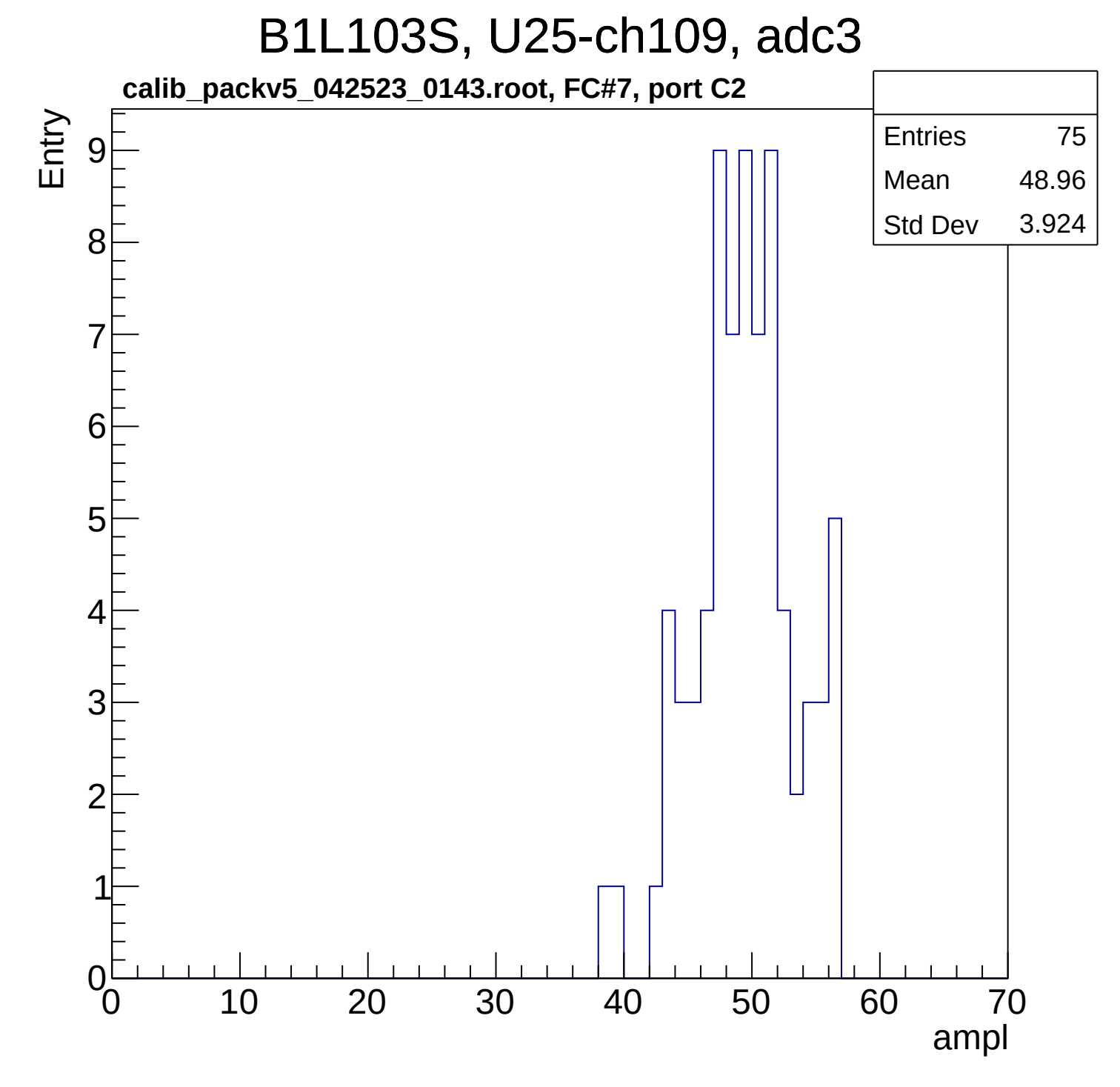
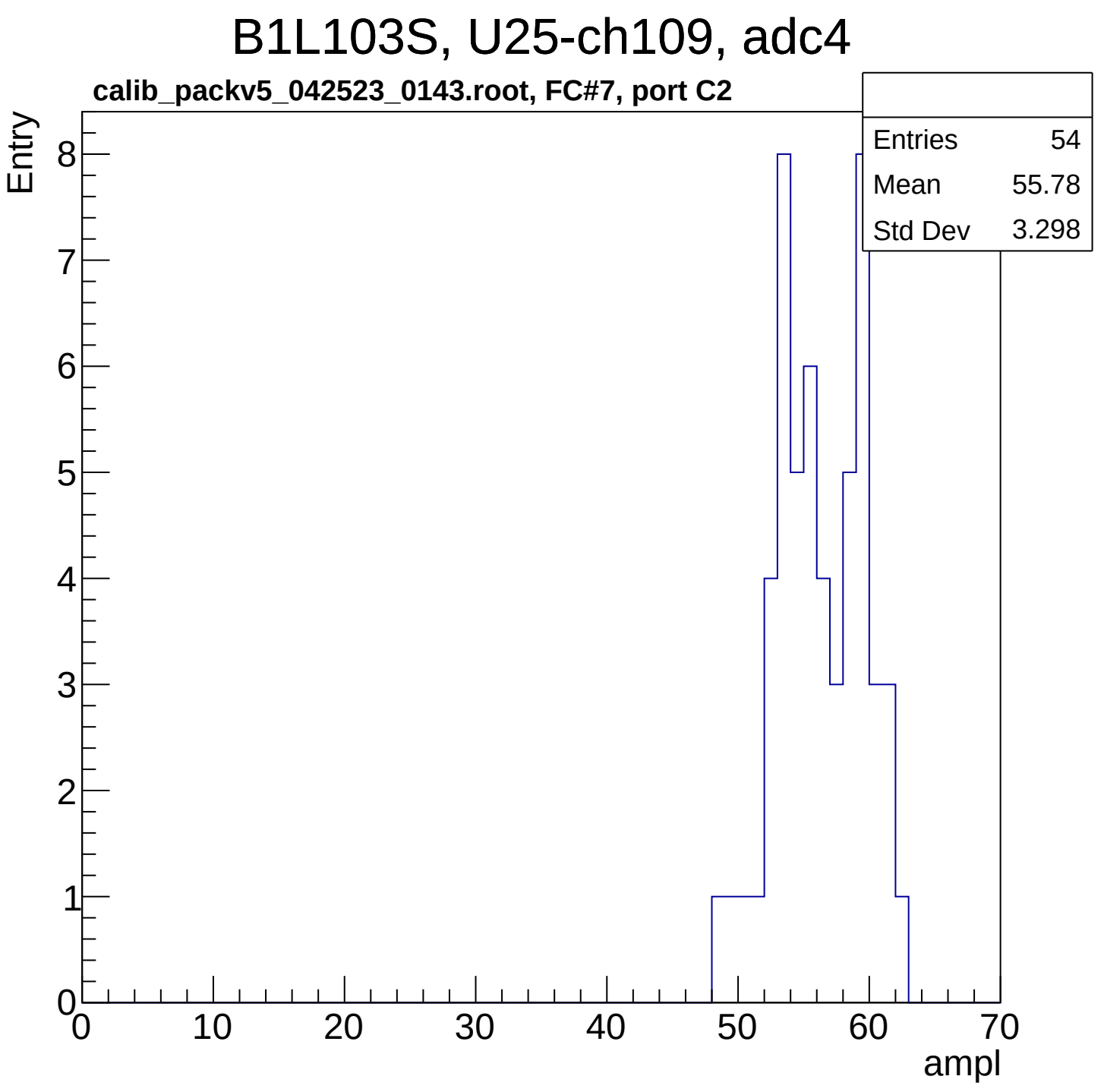

B1L103S, U25-ch109, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 48.96
Std Dev 3.924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch109, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 55.78
Std Dev 3.298
Entry
ampl
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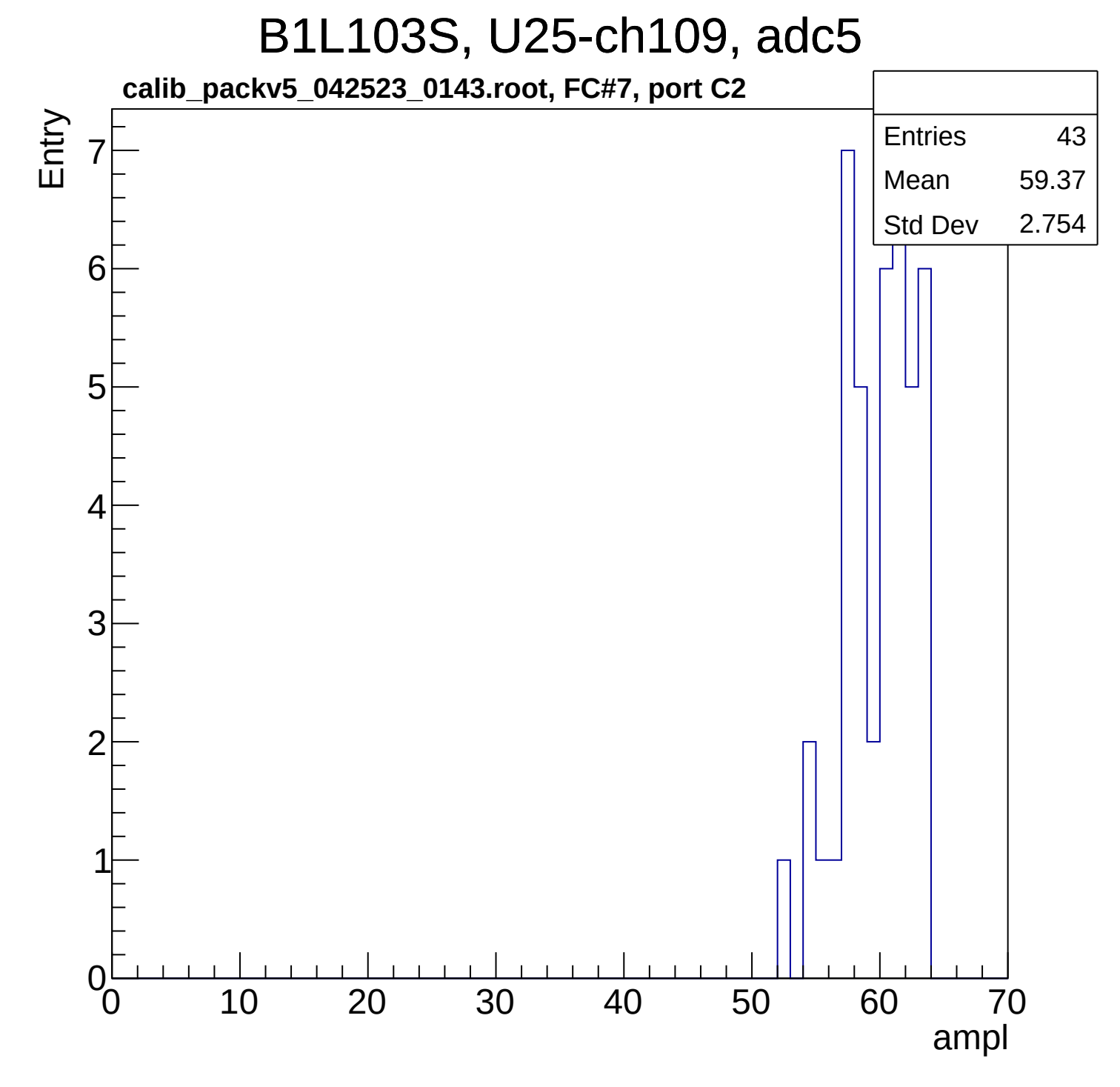
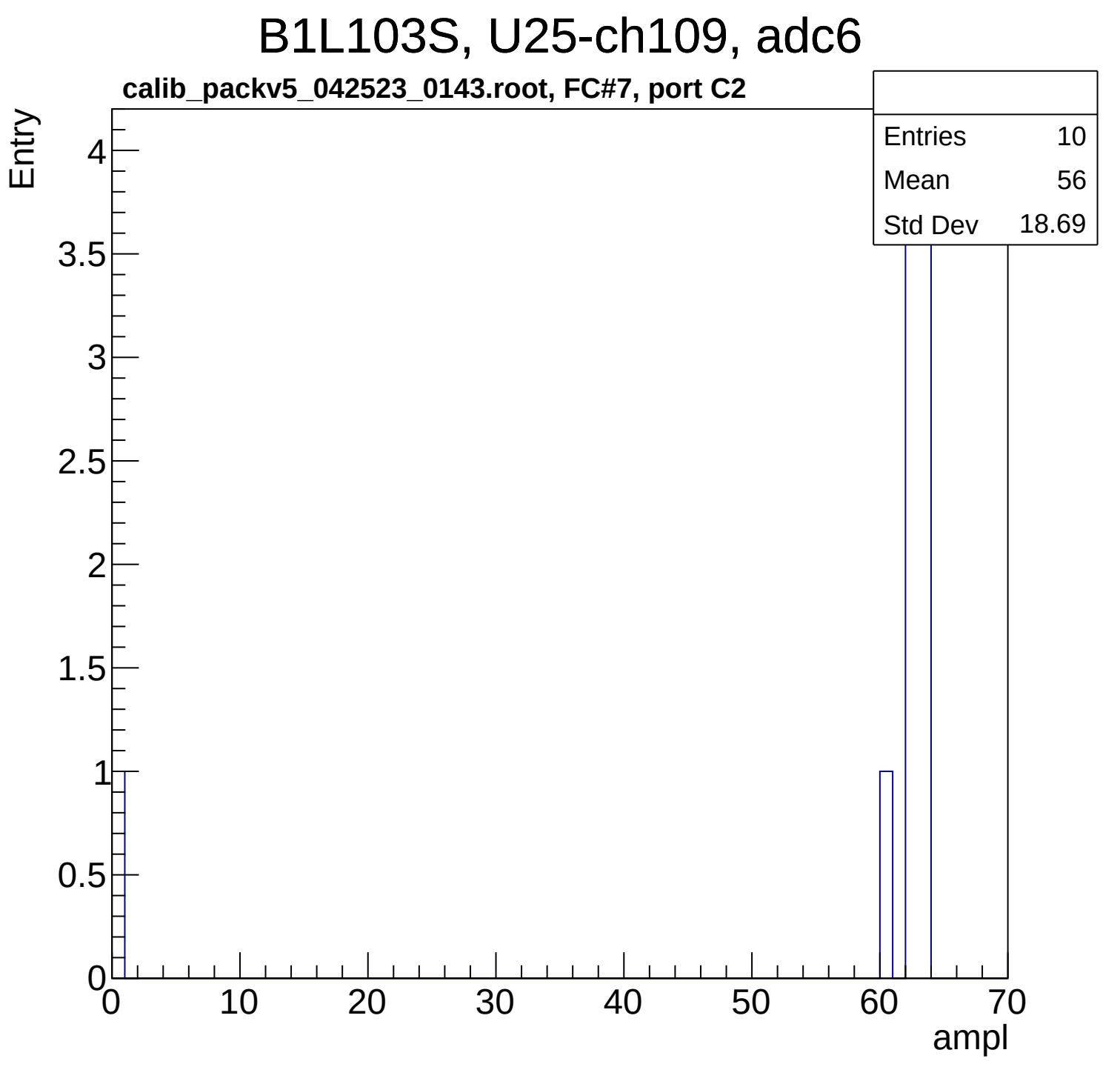
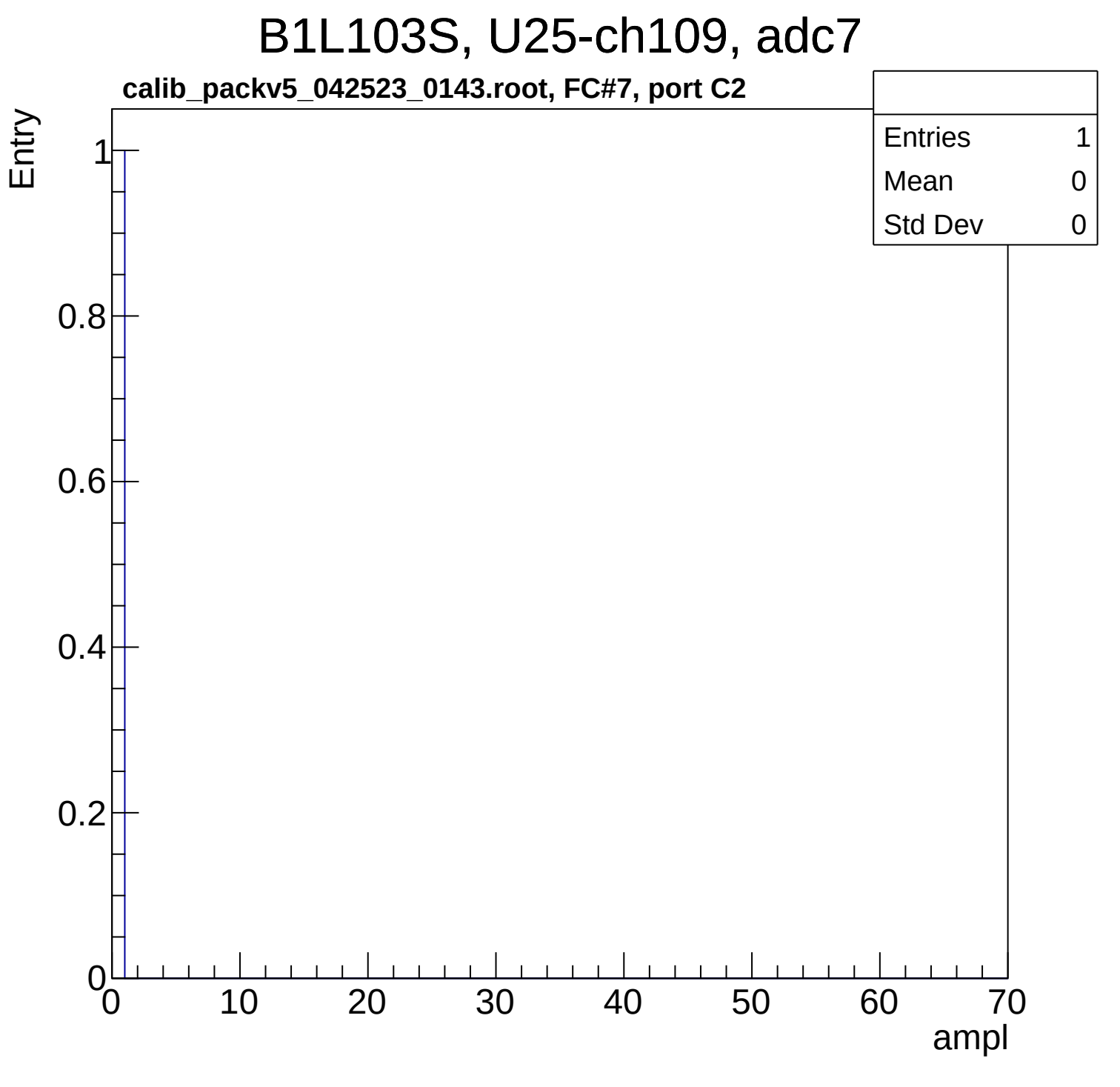
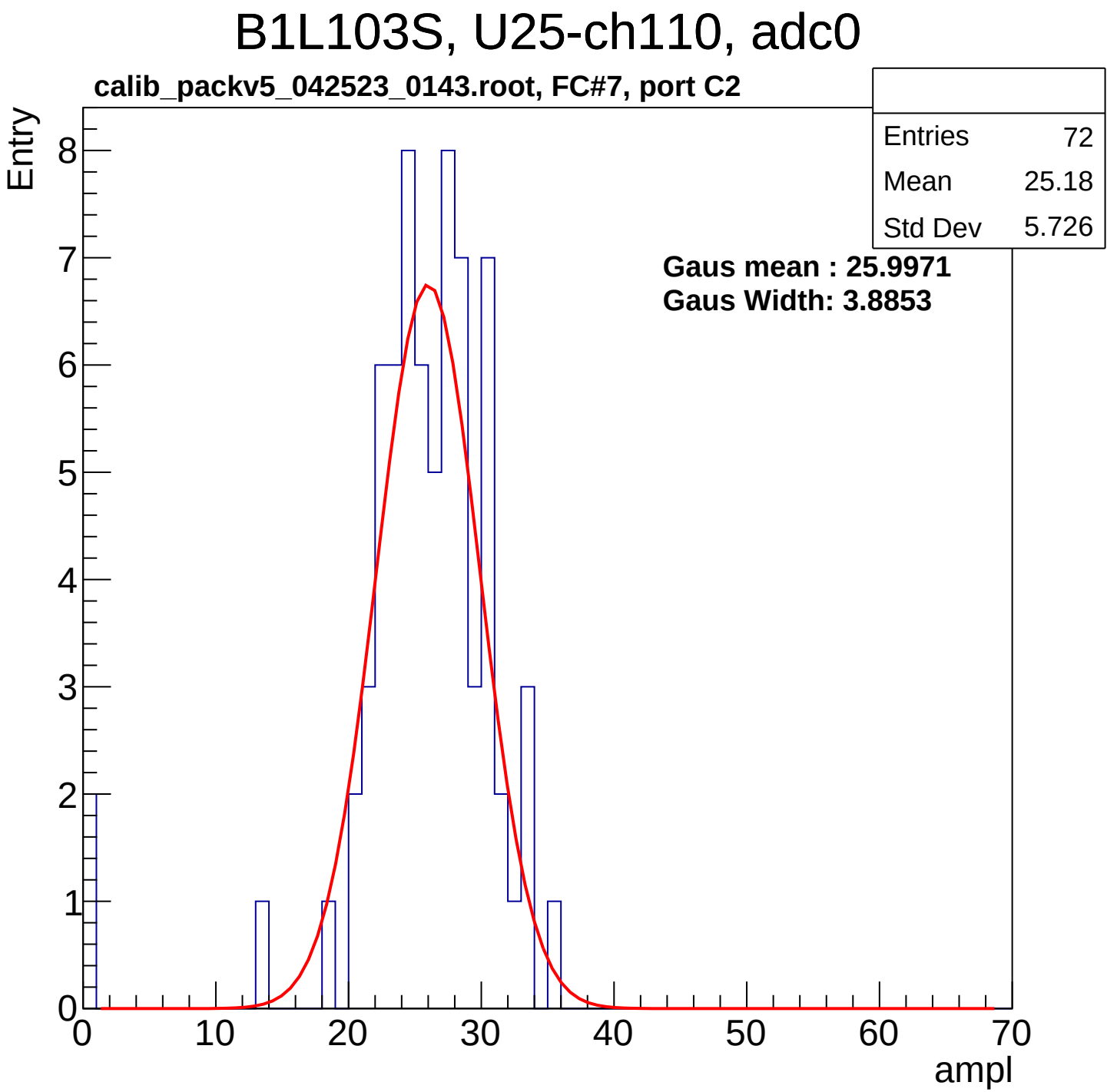
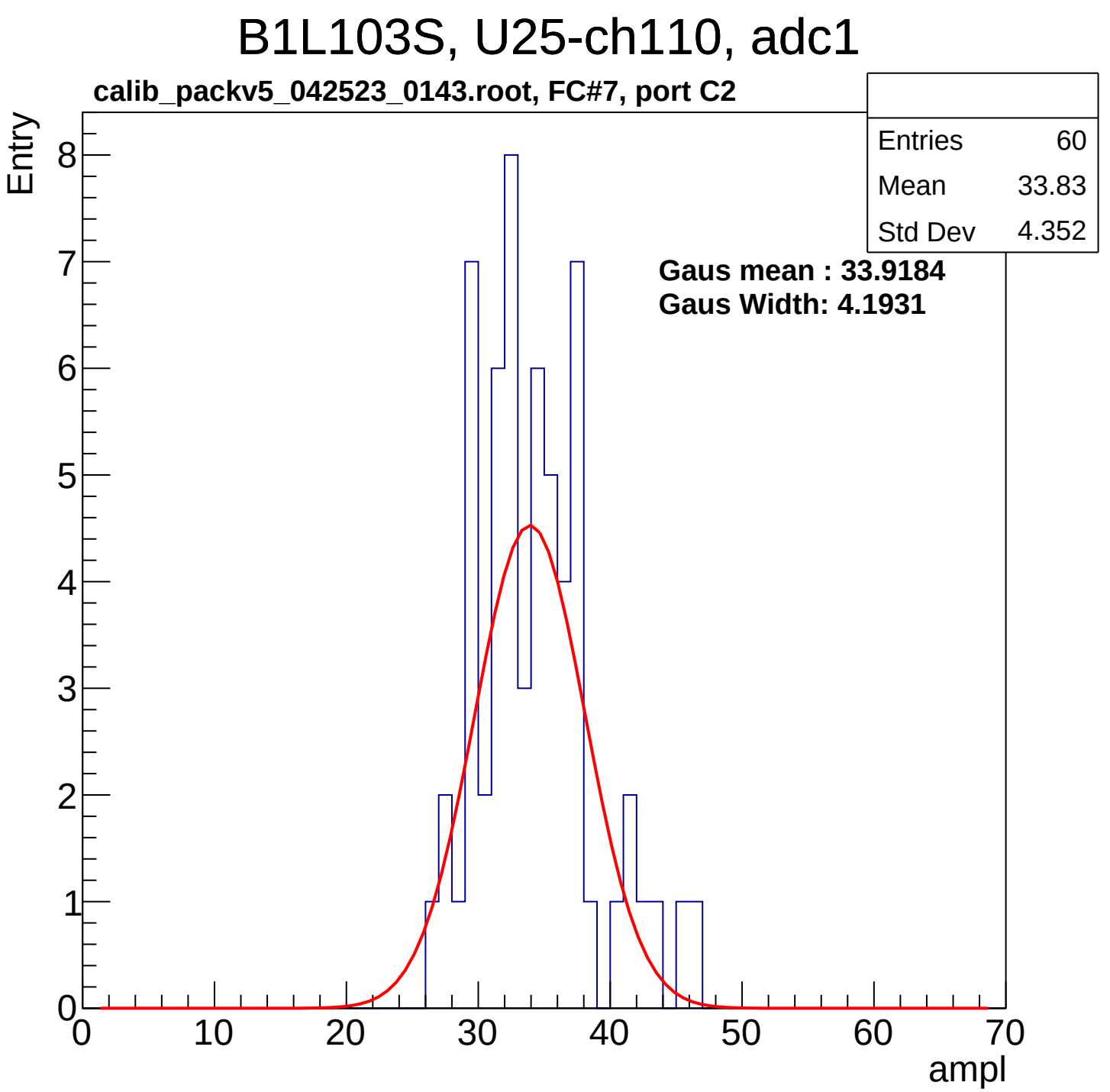
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch109, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 43
Mean 59.37
Std Dev 2.754
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch109, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 56
Std Dev 18.69
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L103S, U25-ch109, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch110, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 25.18
Std Dev 5.726
Gaus mean : 25.9971
Gaus Width: 3.8853
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch110, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 33.83
Std Dev 4.352
Gaus mean : 33.9184
Gaus Width: 4.1931
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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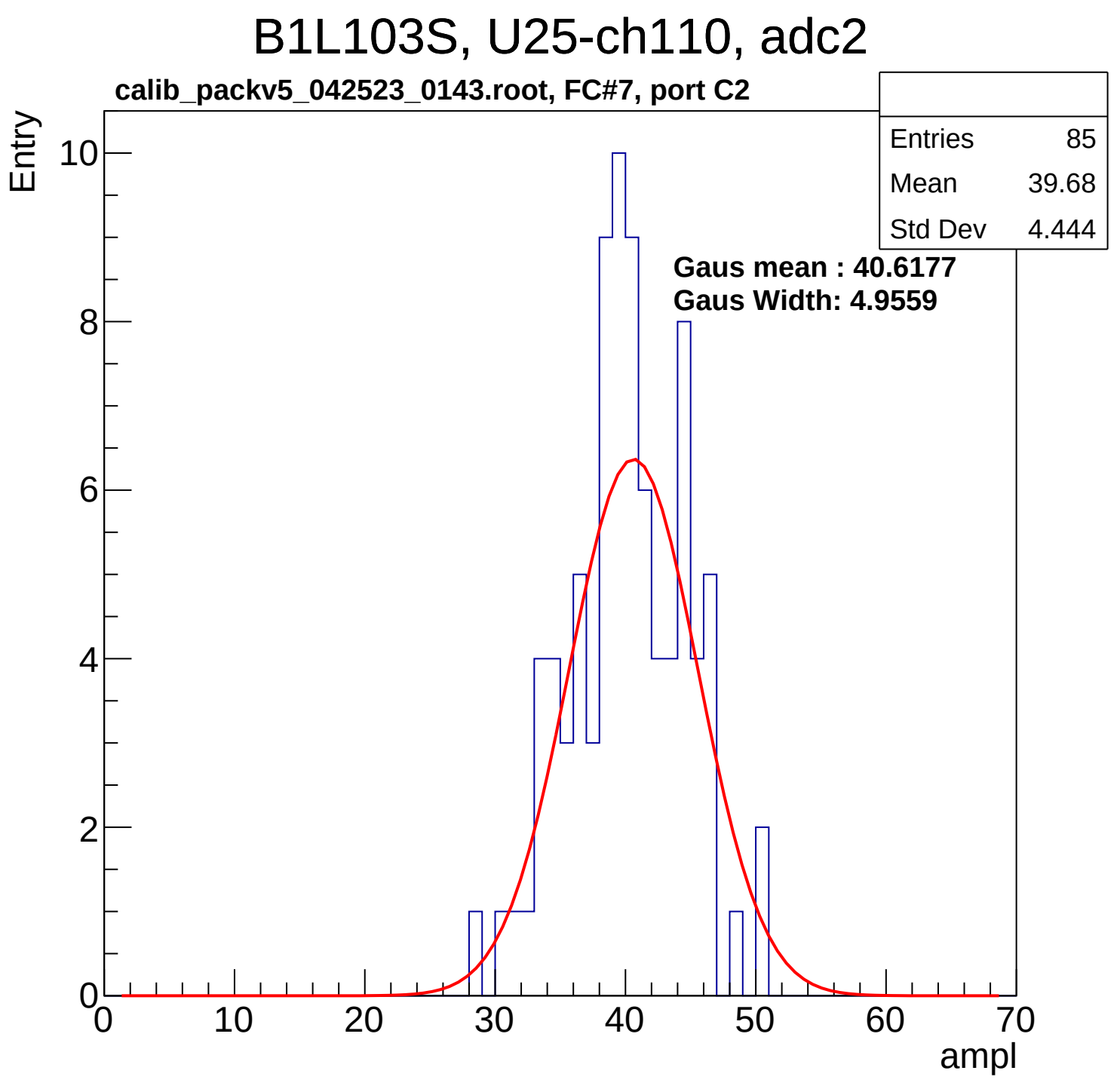
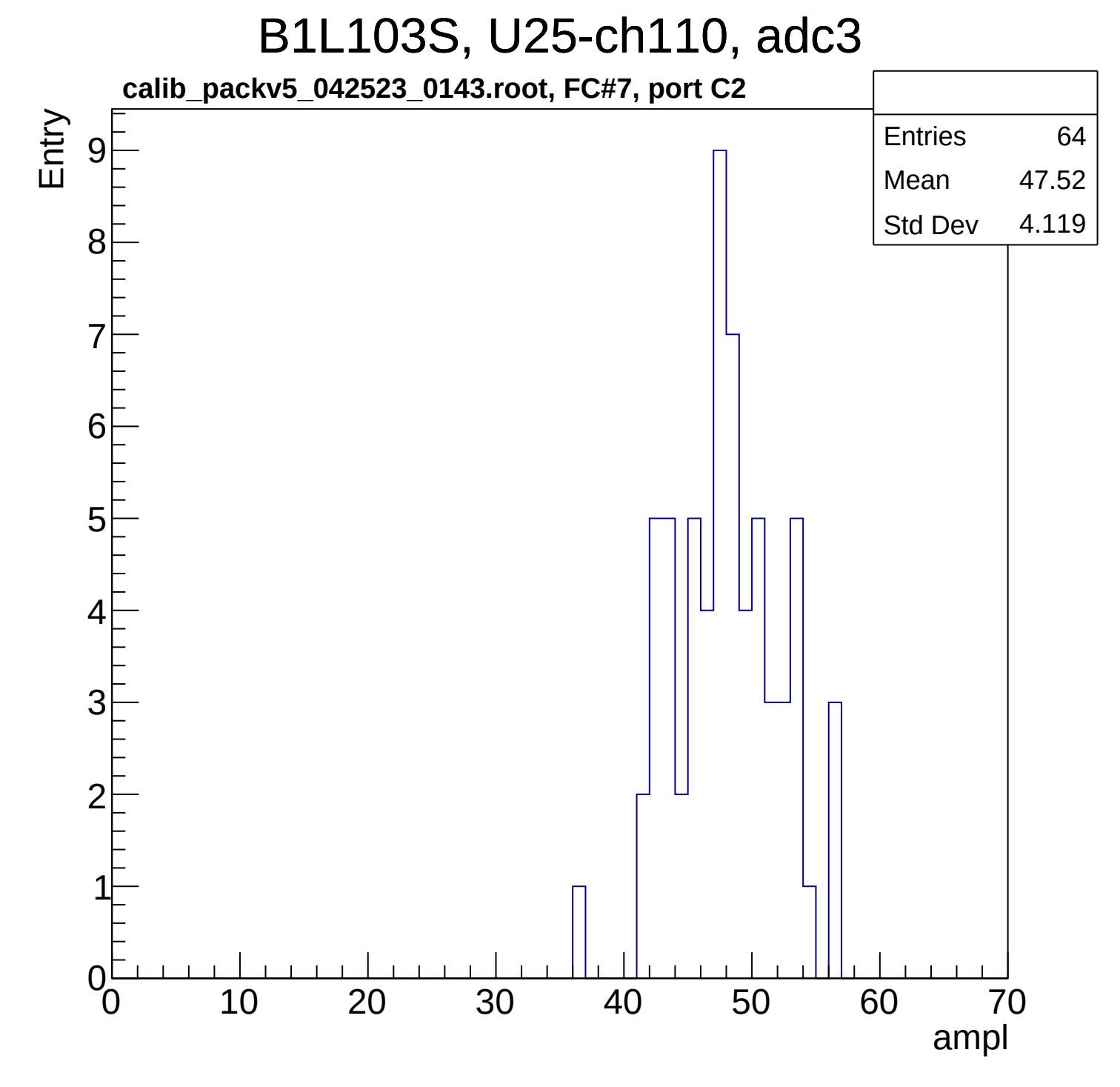

B1L103S, U25-ch110, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 39.68
Std Dev 4.444
Gaus mean : 40.6177
Gaus Width: 4.9559
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch110, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.52
Std Dev 4.119
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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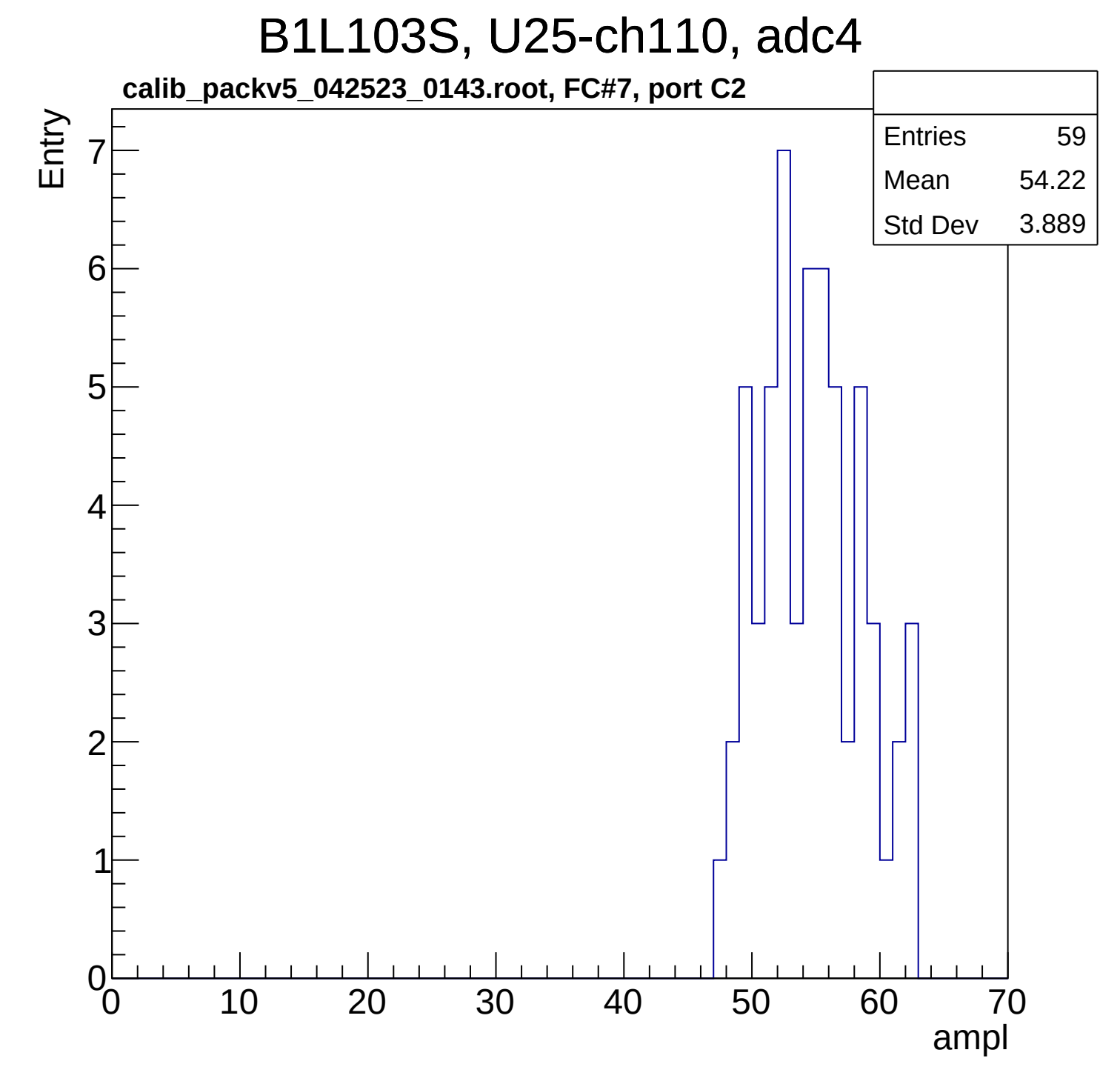
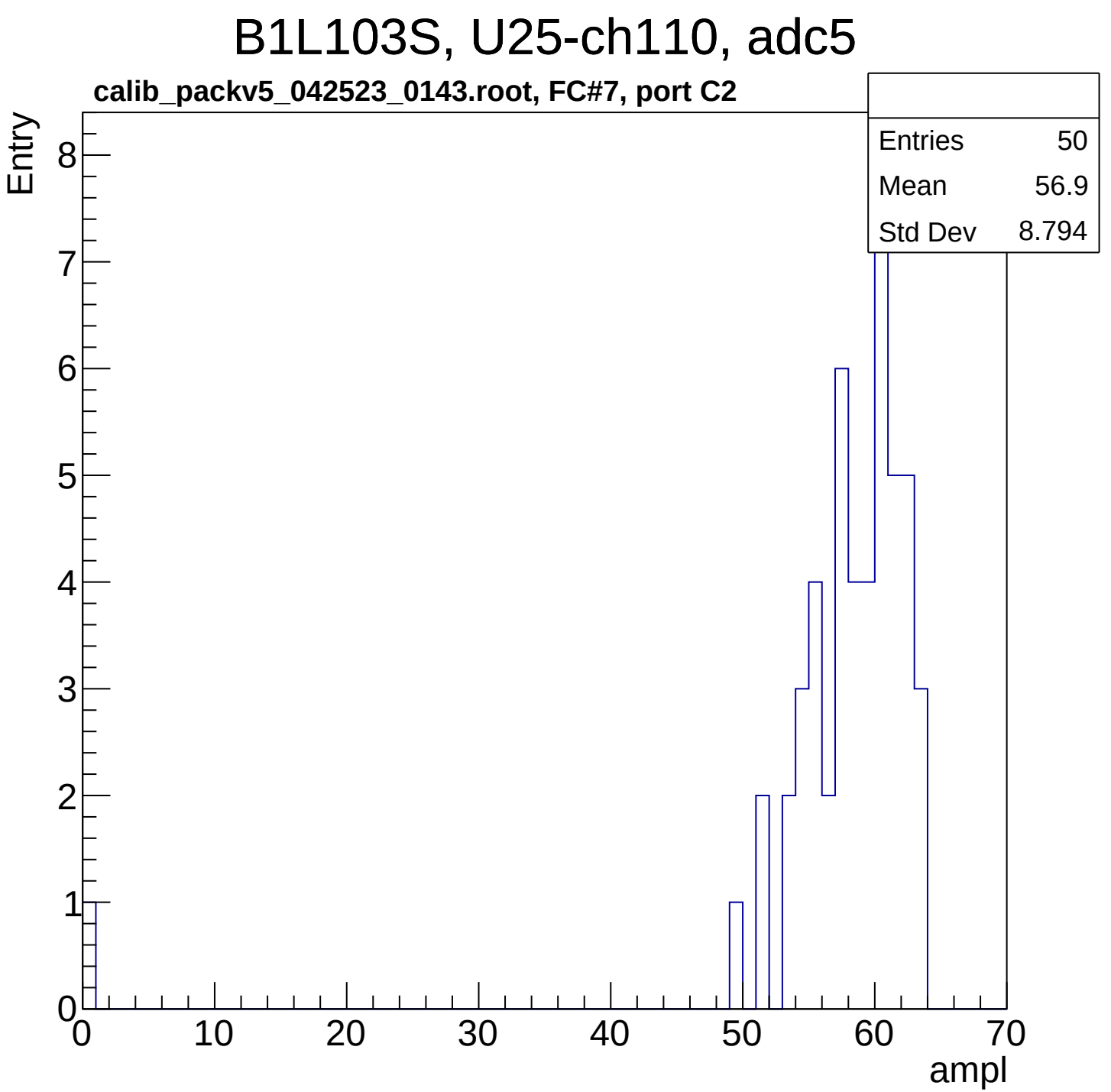

B1L103S, U25-ch110, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 54.22
Std Dev 3.889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch110, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 50
Mean 56.9
Std Dev 8.794
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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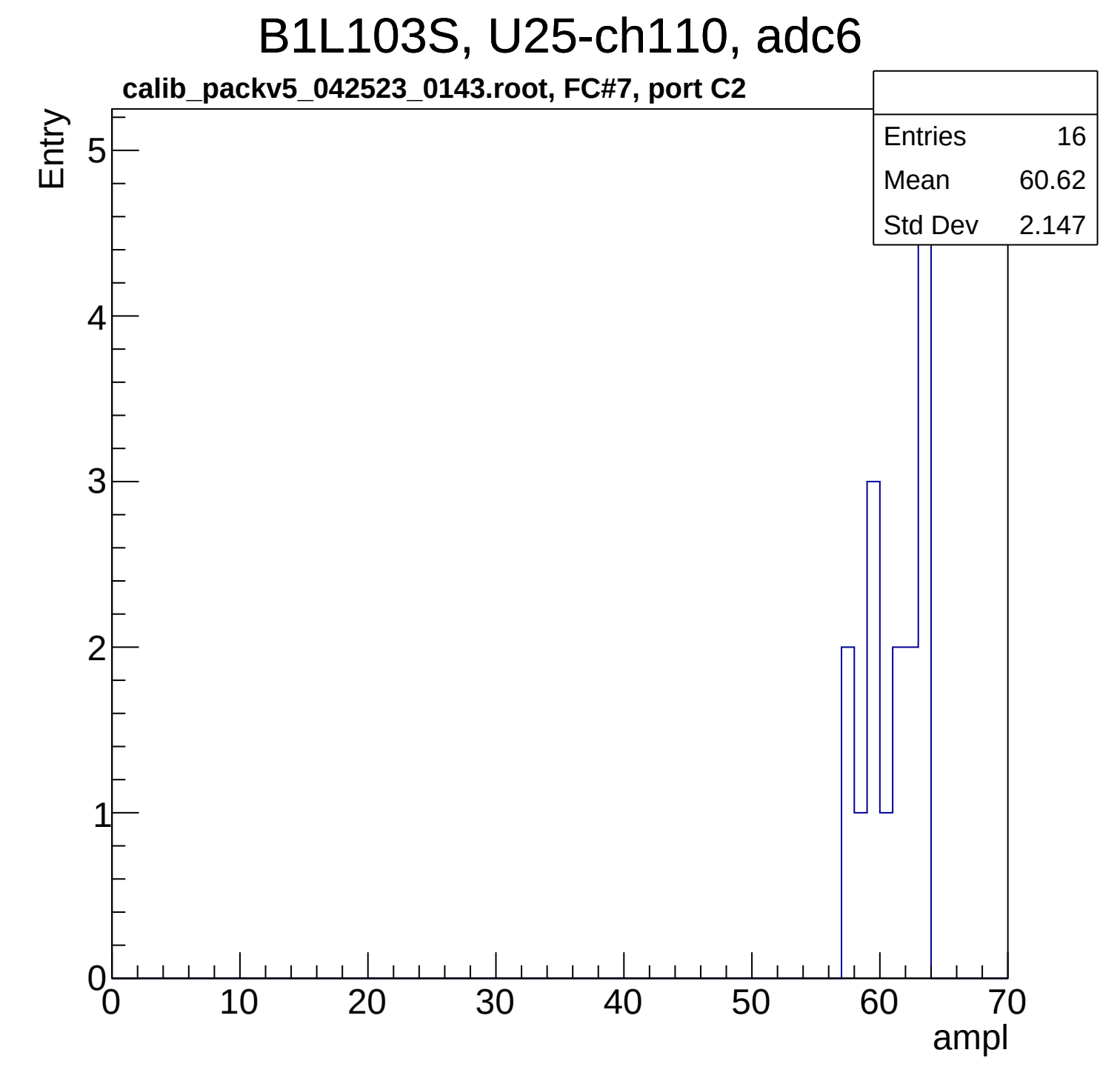
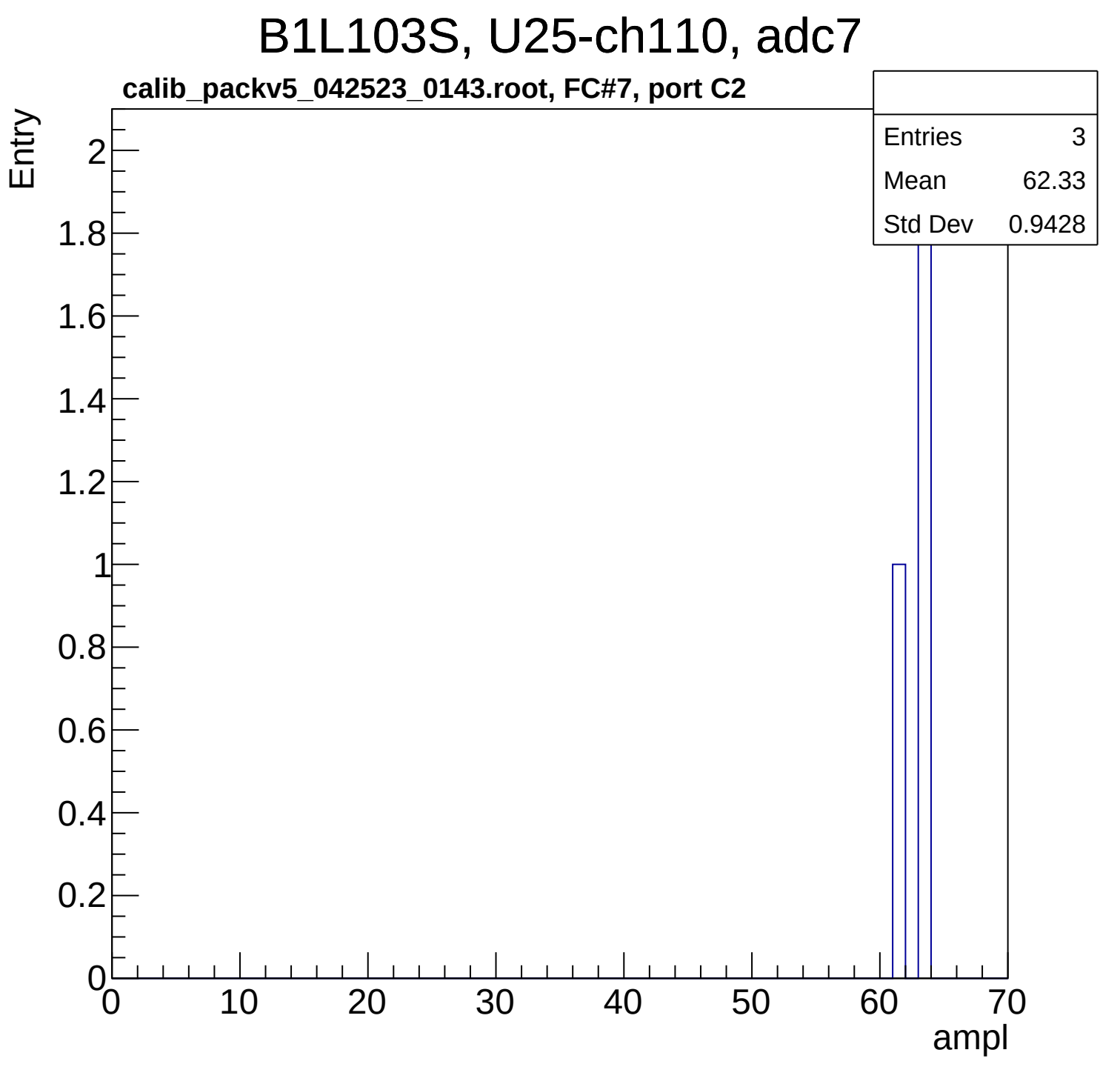
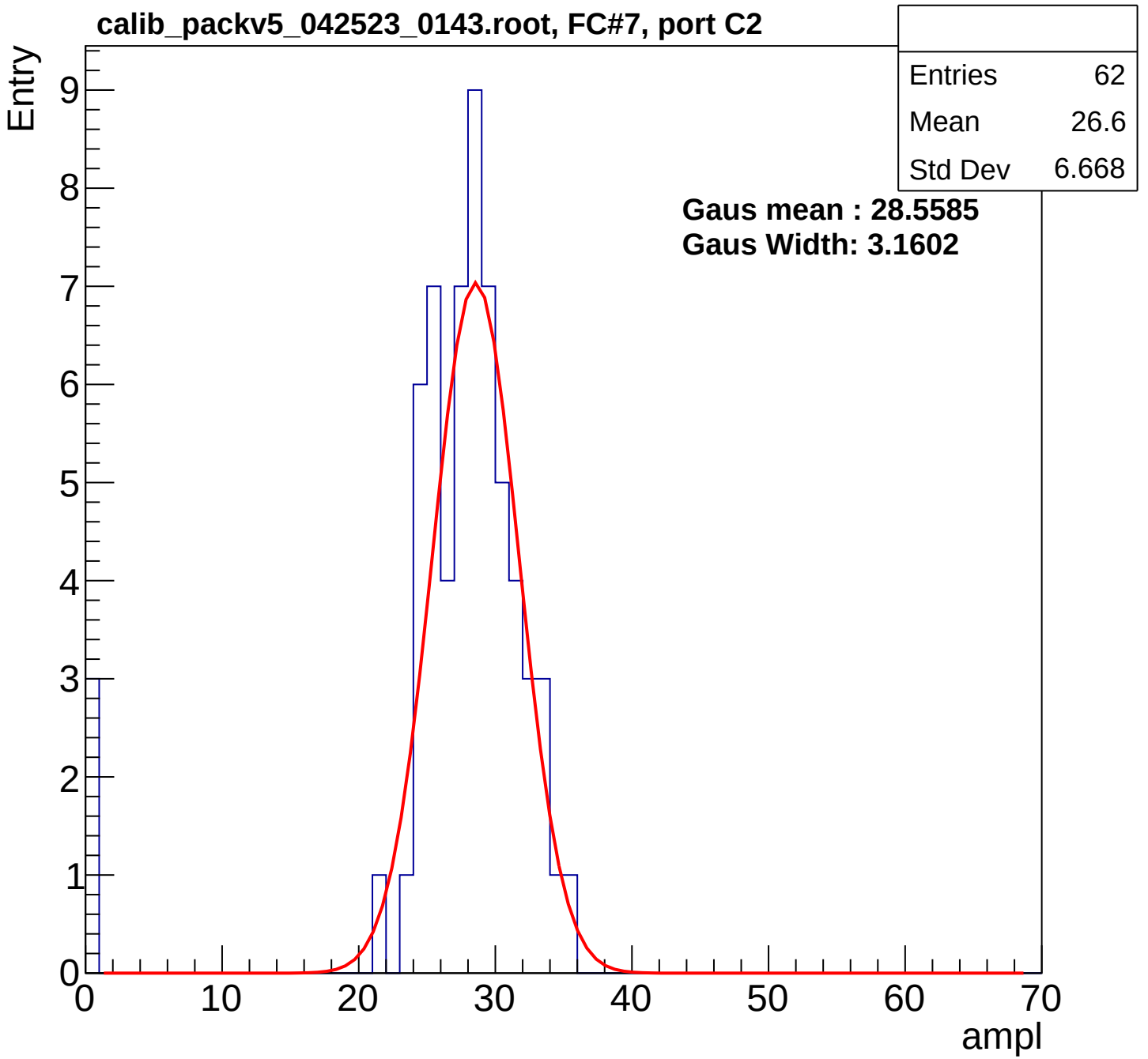

B1L103S, U25-ch110, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 16
Mean 60.62
Std Dev 2.147
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch110, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch111, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 26.6
Std Dev 6.668
Gaus mean : 28.5585
Gaus Width: 3.1602
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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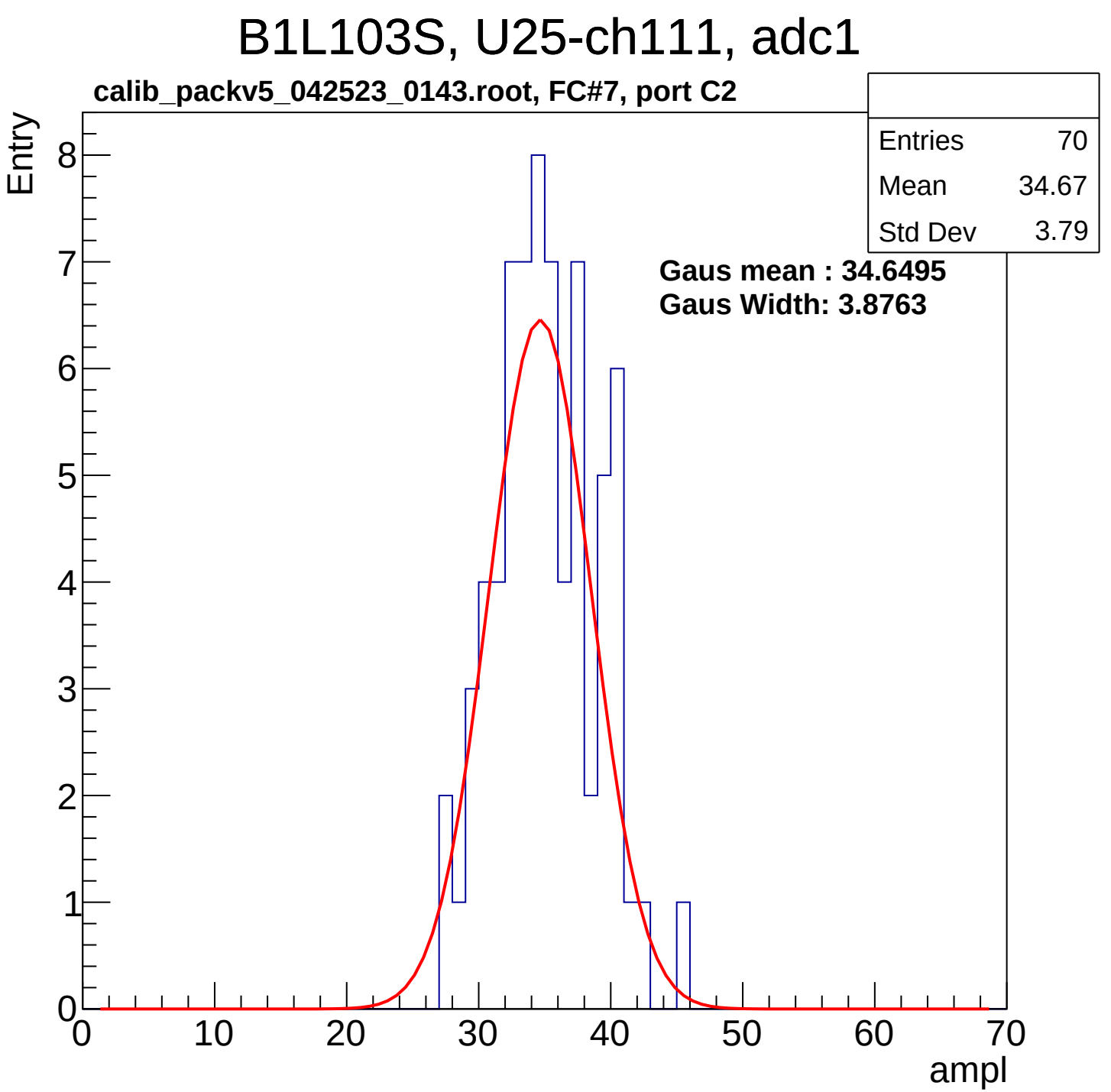
60
70


B1L103S, U25-ch111, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 34.67
Std Dev 3.79
Gaus mean : 34.6495
Gaus Width: 3.8763
Entry
ampl
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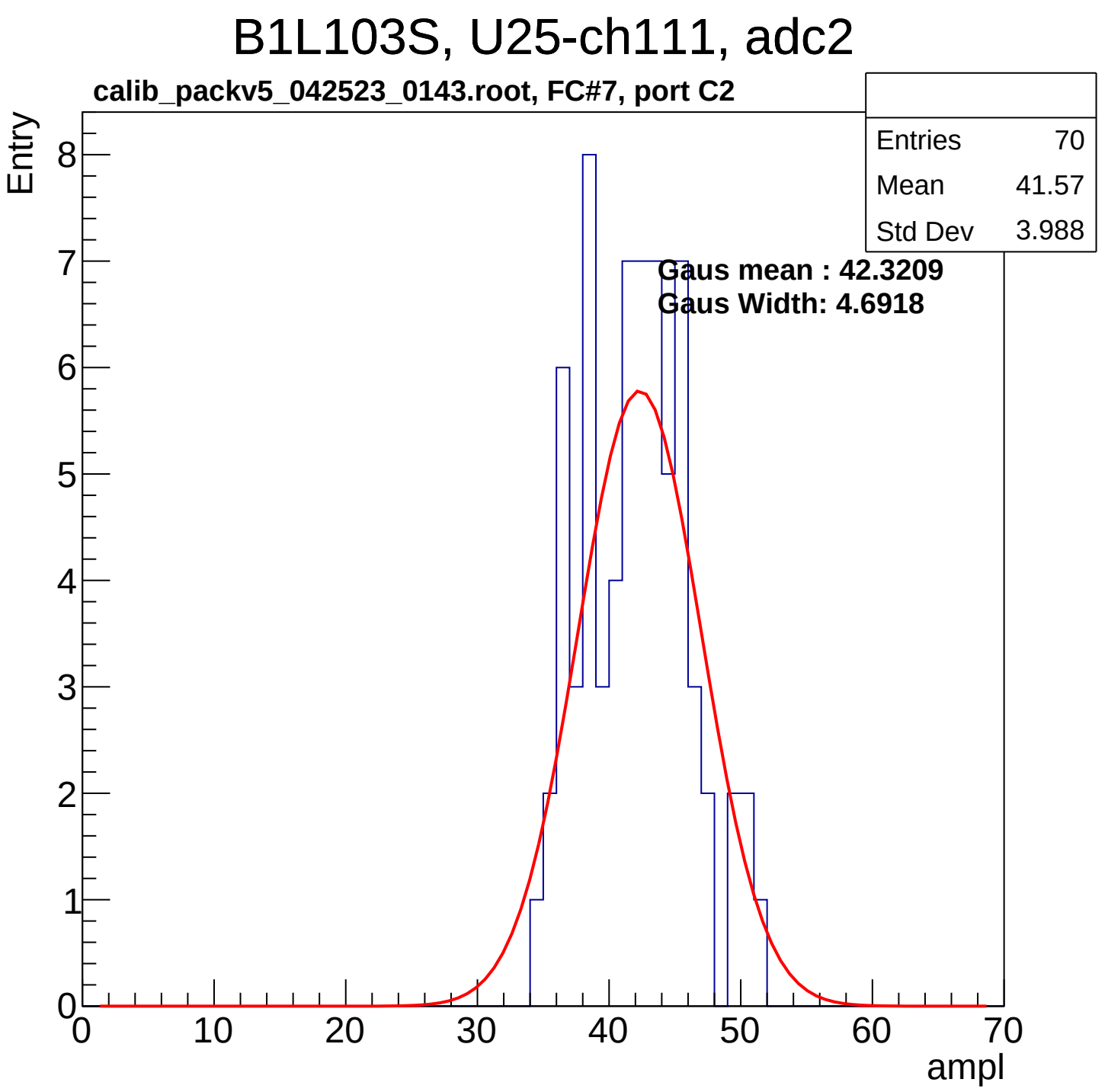
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch111, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 41.57
Std Dev 3.988
Gaus mean : 42.3209
Gaus Width: 4.6918
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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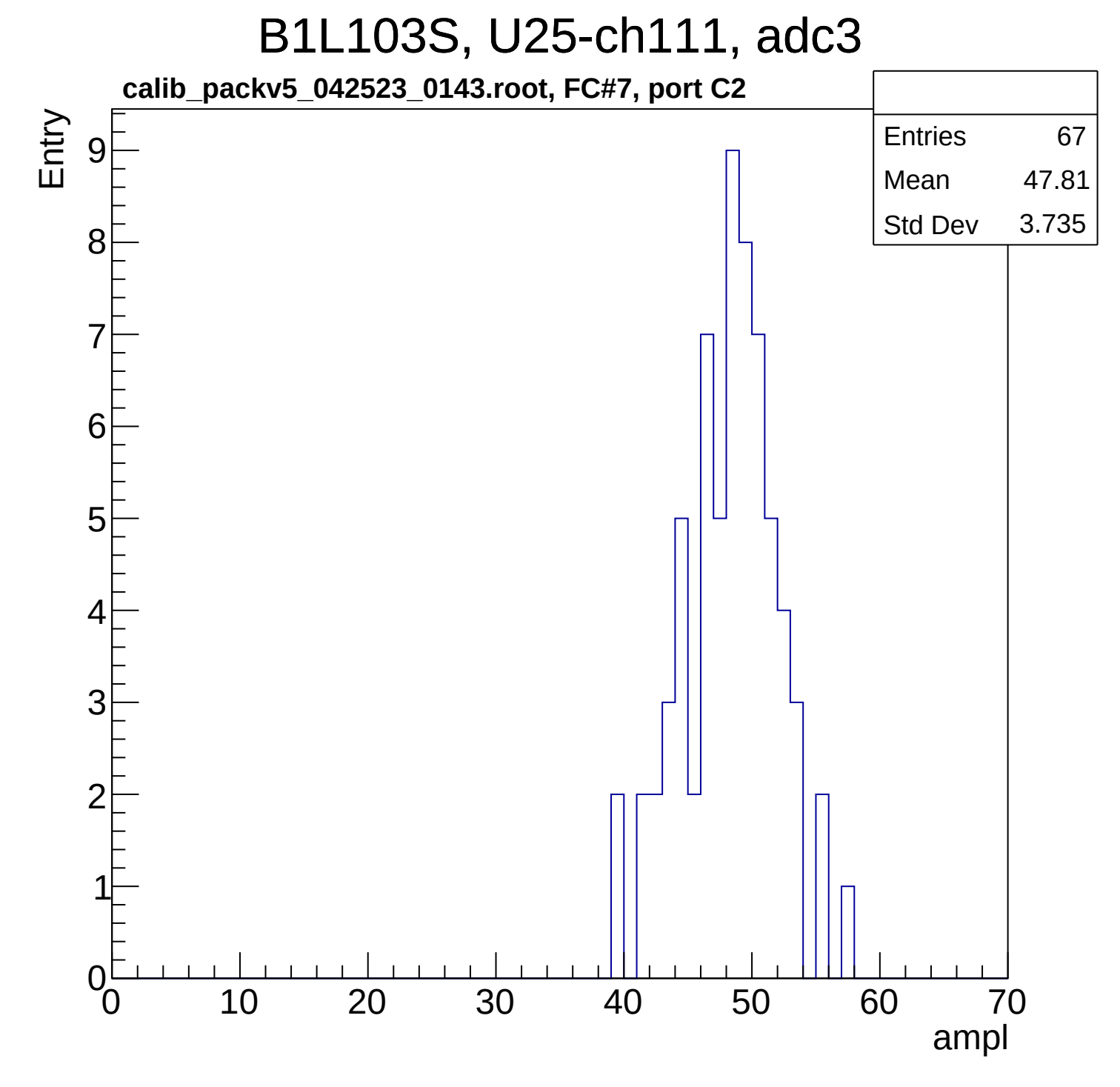
B1L103S, U25-ch111, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 47.81
Std Dev 3.735
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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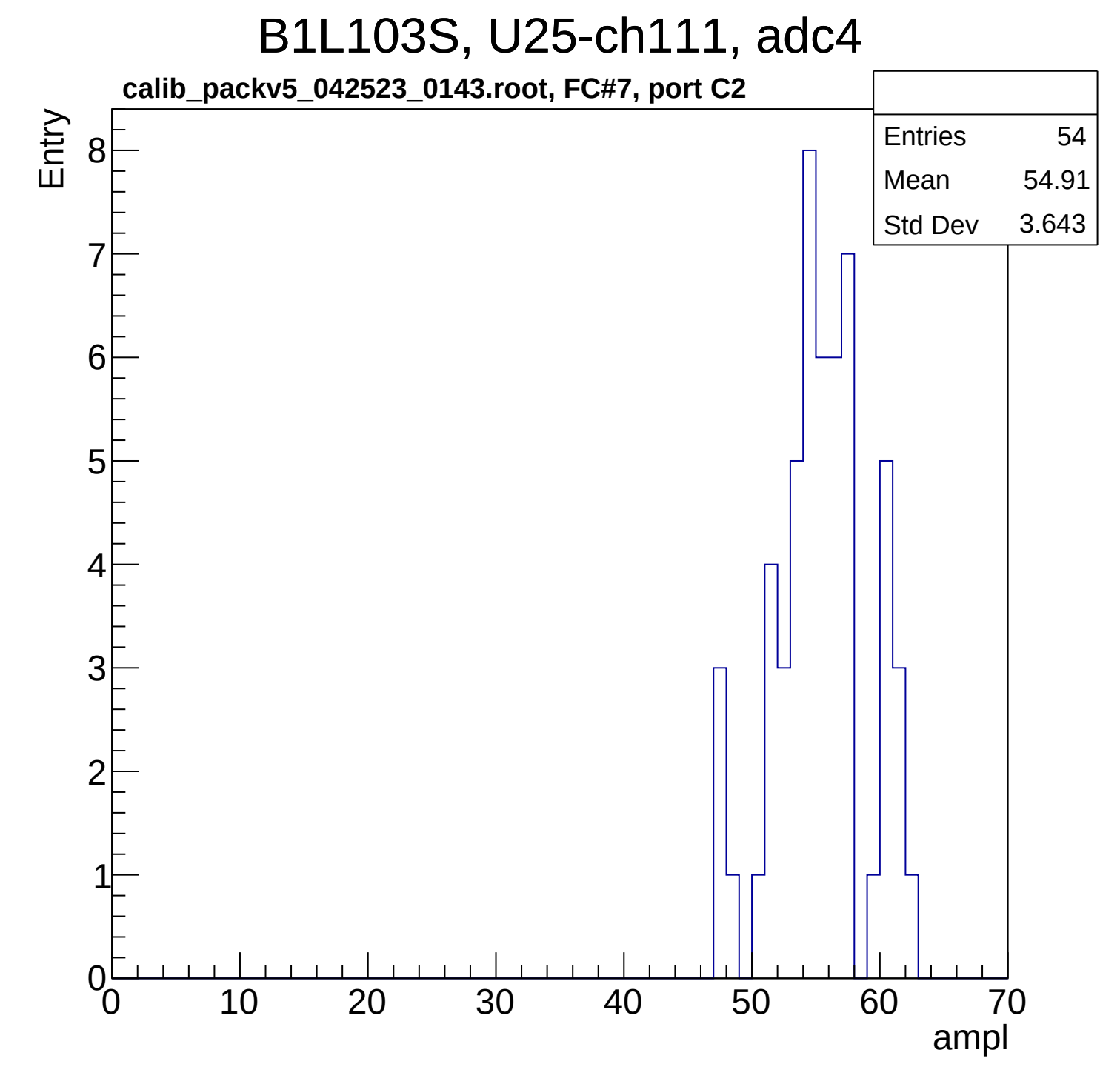
60
70


B1L103S, U25-ch111, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 54.91
Std Dev 3.643
Entry
ampl
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1
2
3
4
5
6
7
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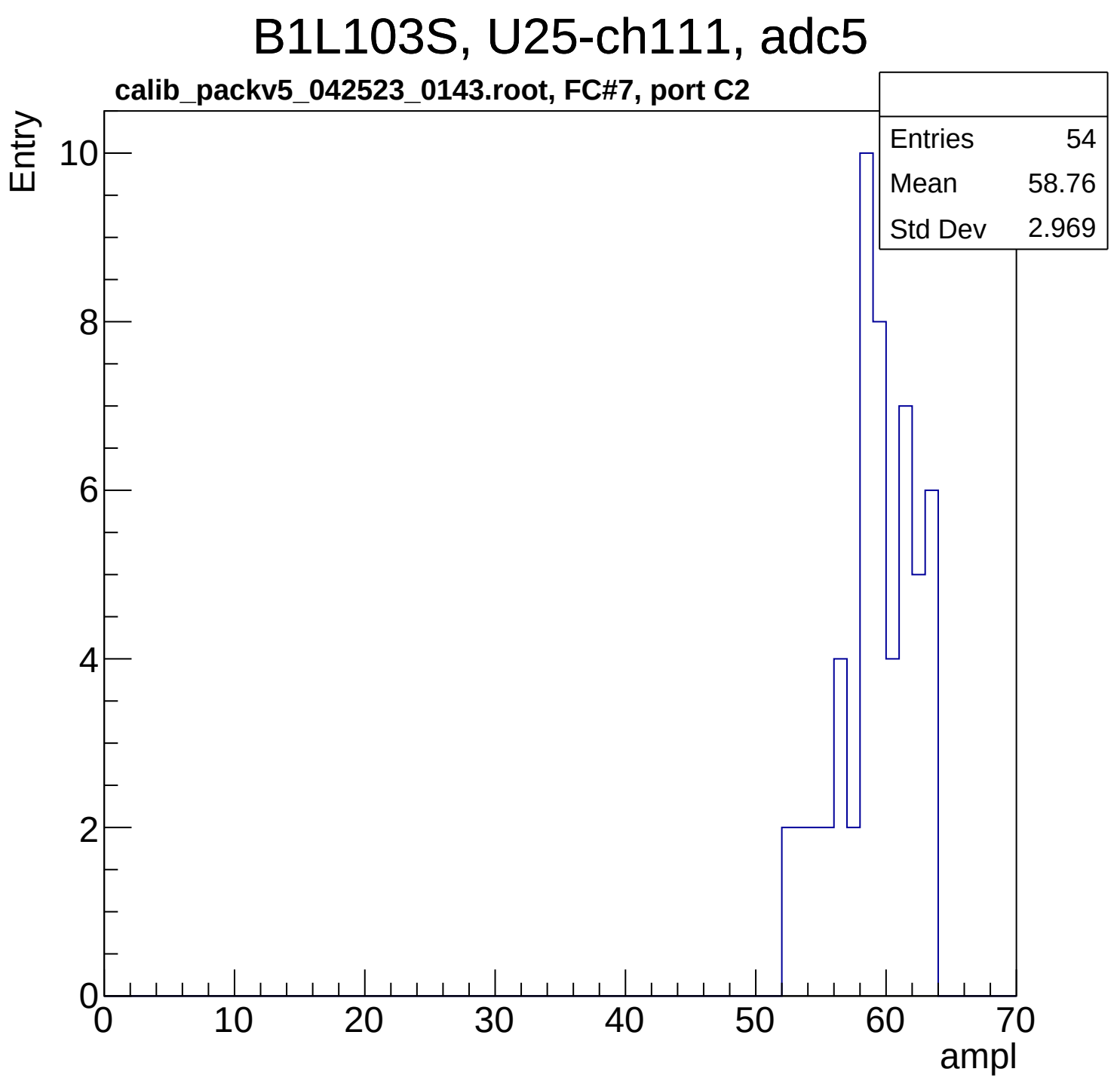
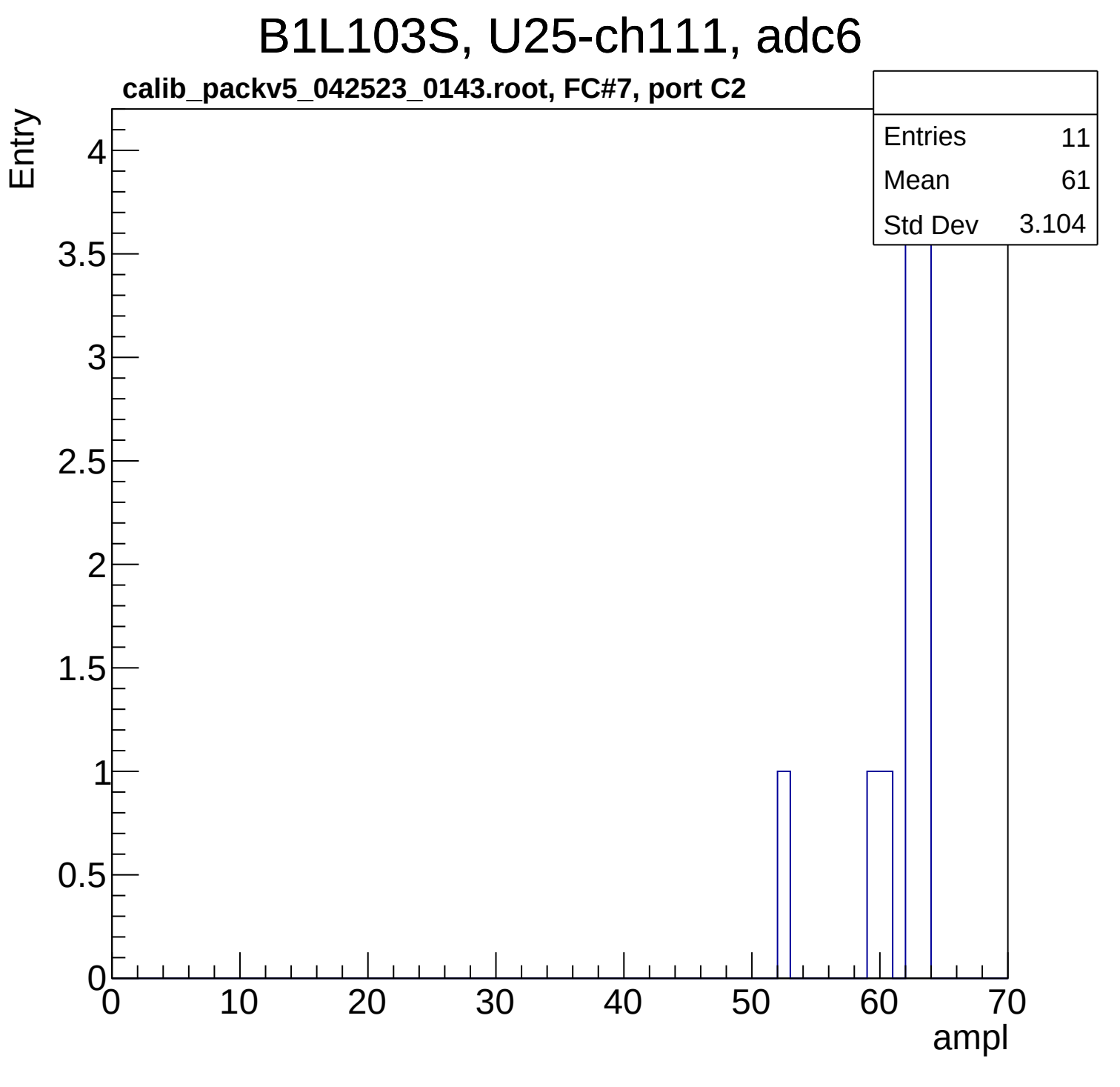
0
10
20
30
40
50
60
70


B1L103S, U25-ch111, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.76
Std Dev 2.969
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch111, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 11
Mean 61
Std Dev 3.104
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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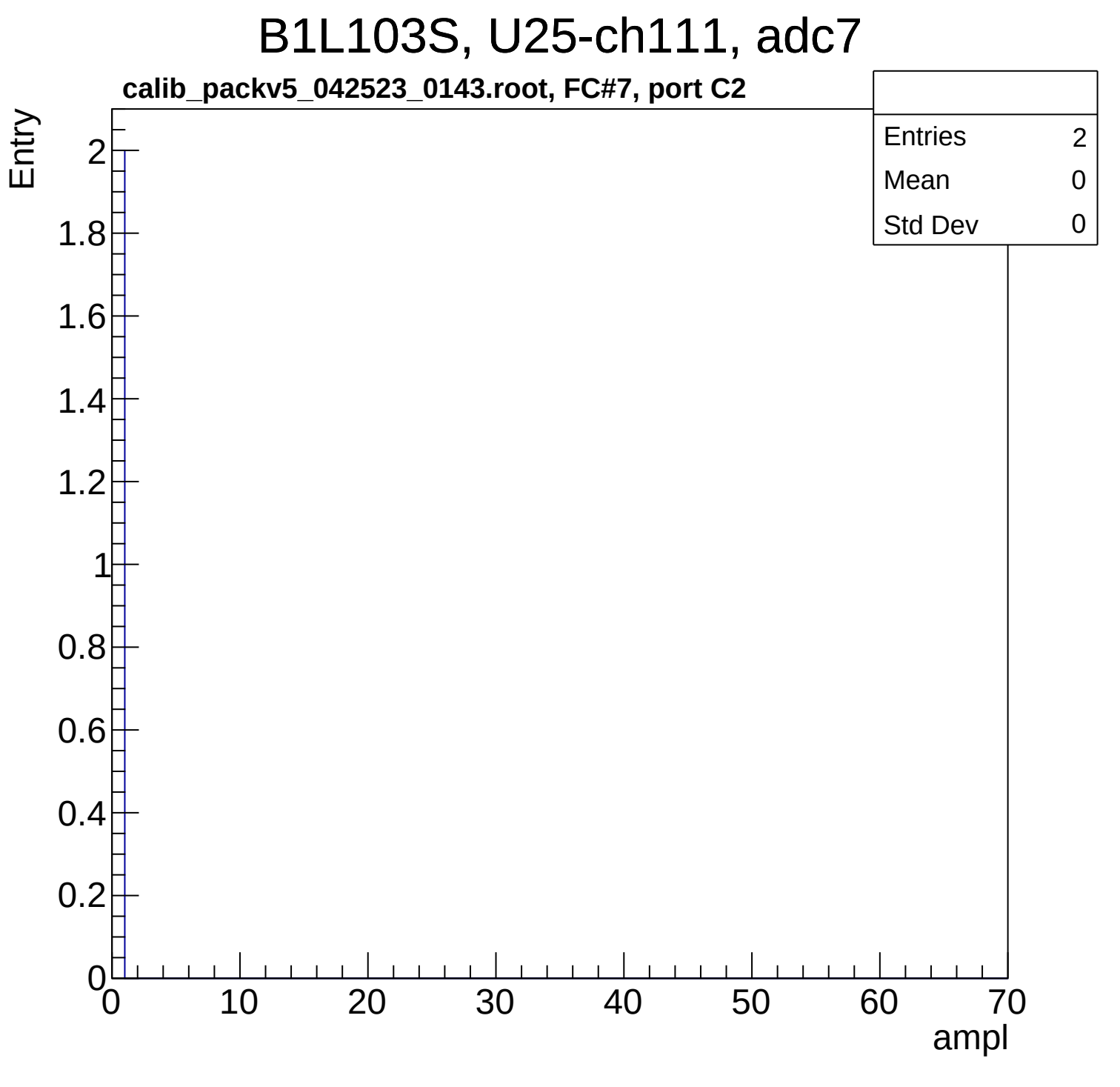
40
50
60
70


B1L103S, U25-ch111, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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0
10
20
30
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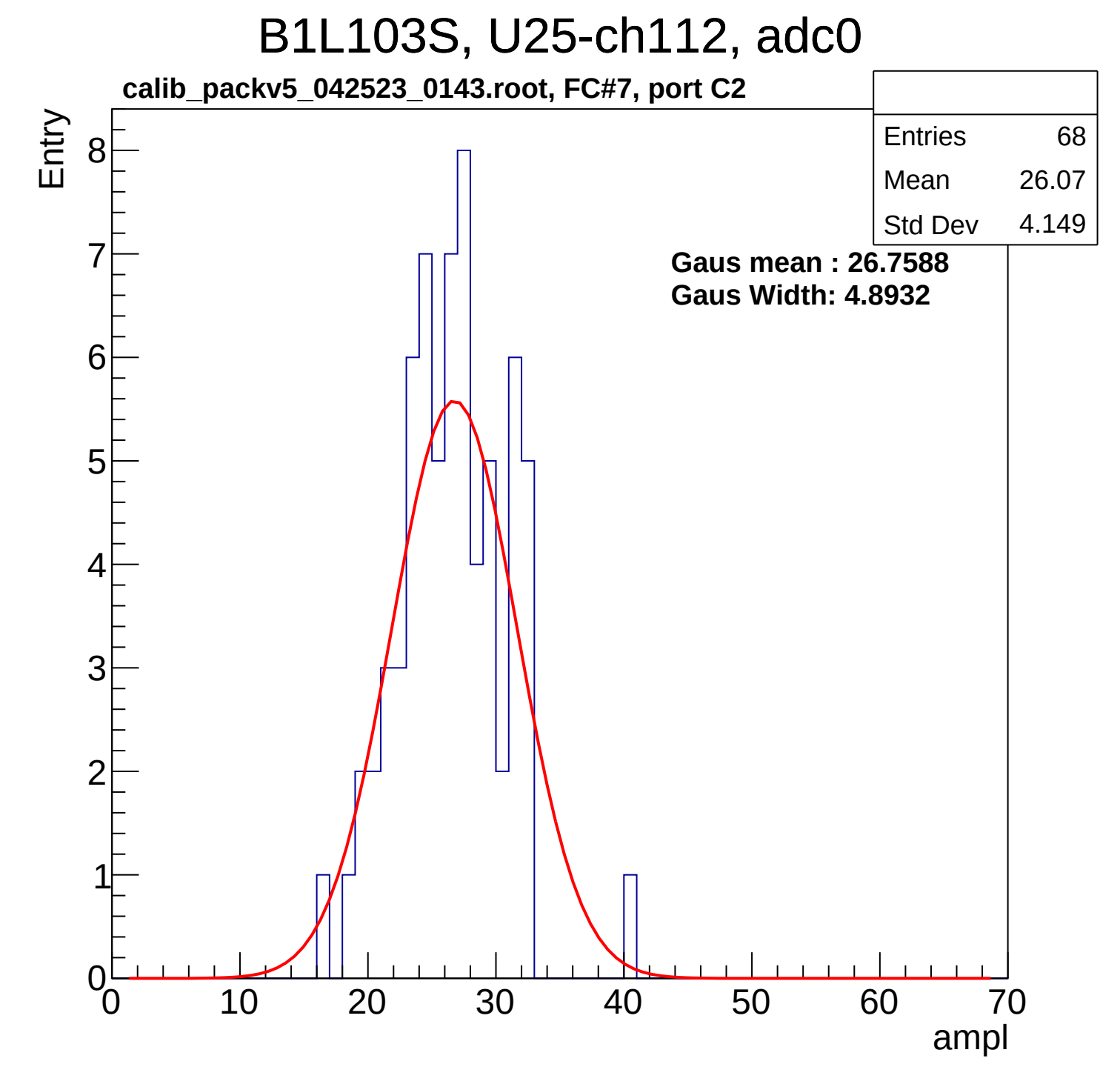
50
60
70
ampl


B1L103S, U25-ch112, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 26.07
Std Dev 4.149
Gaus mean : 26.7588
Gaus Width: 4.8932
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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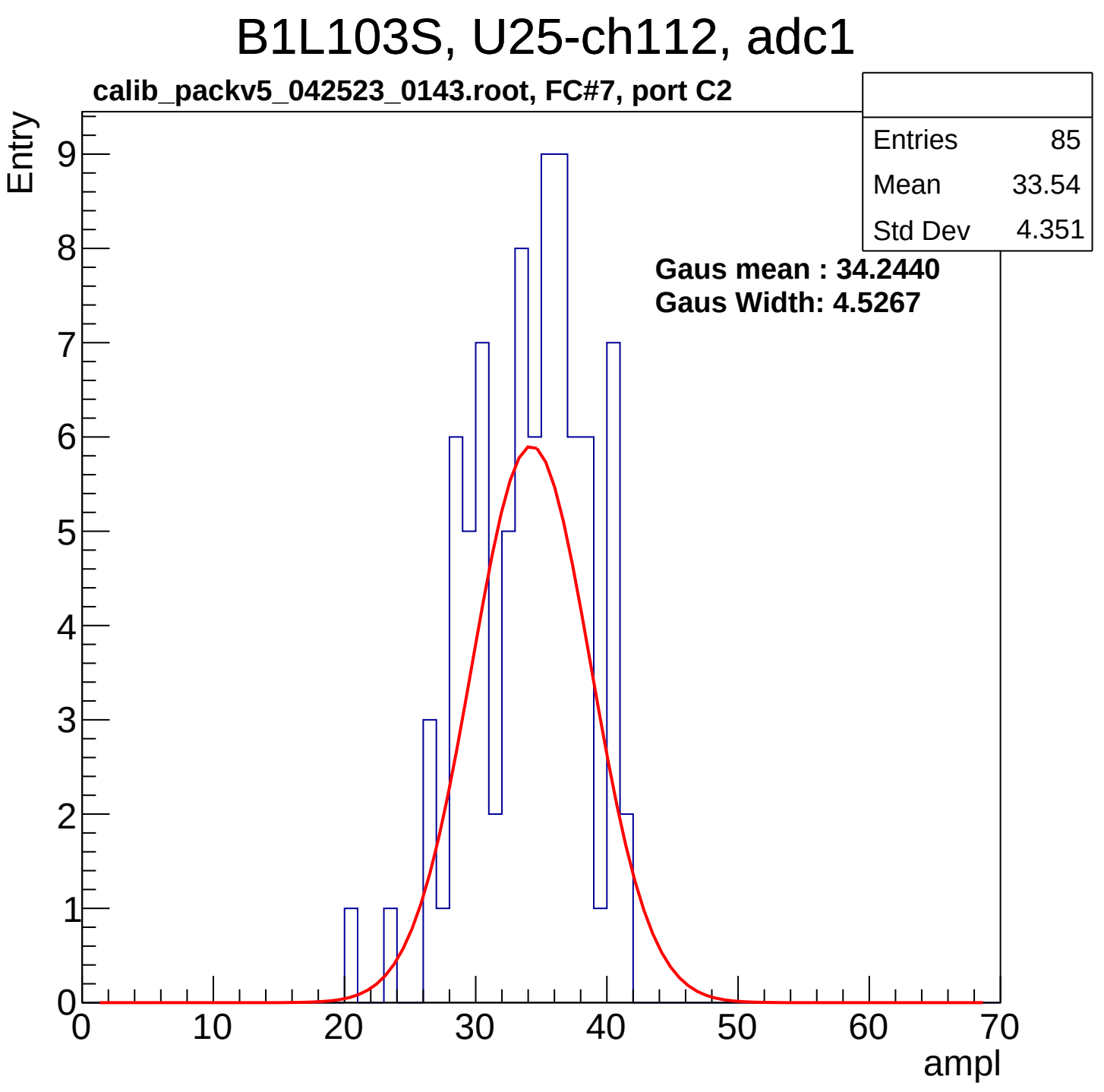
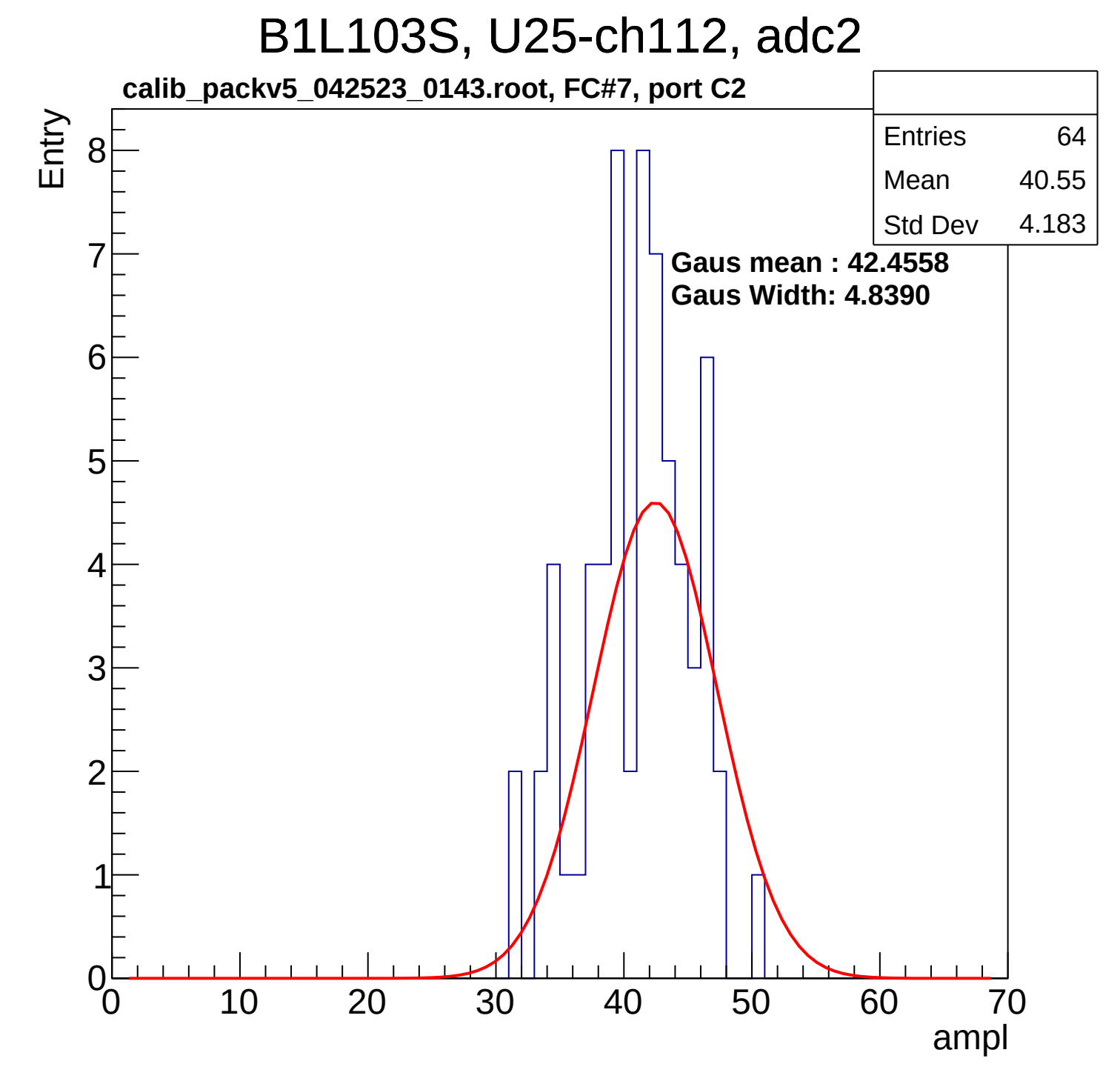

B1L103S, U25-ch112, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 85
Mean 33.54
Std Dev 4.351
Gaus mean : 34.2440
Gaus Width: 4.5267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch112, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.55
Std Dev 4.183
Gaus mean : 42.4558
Gaus Width: 4.8390
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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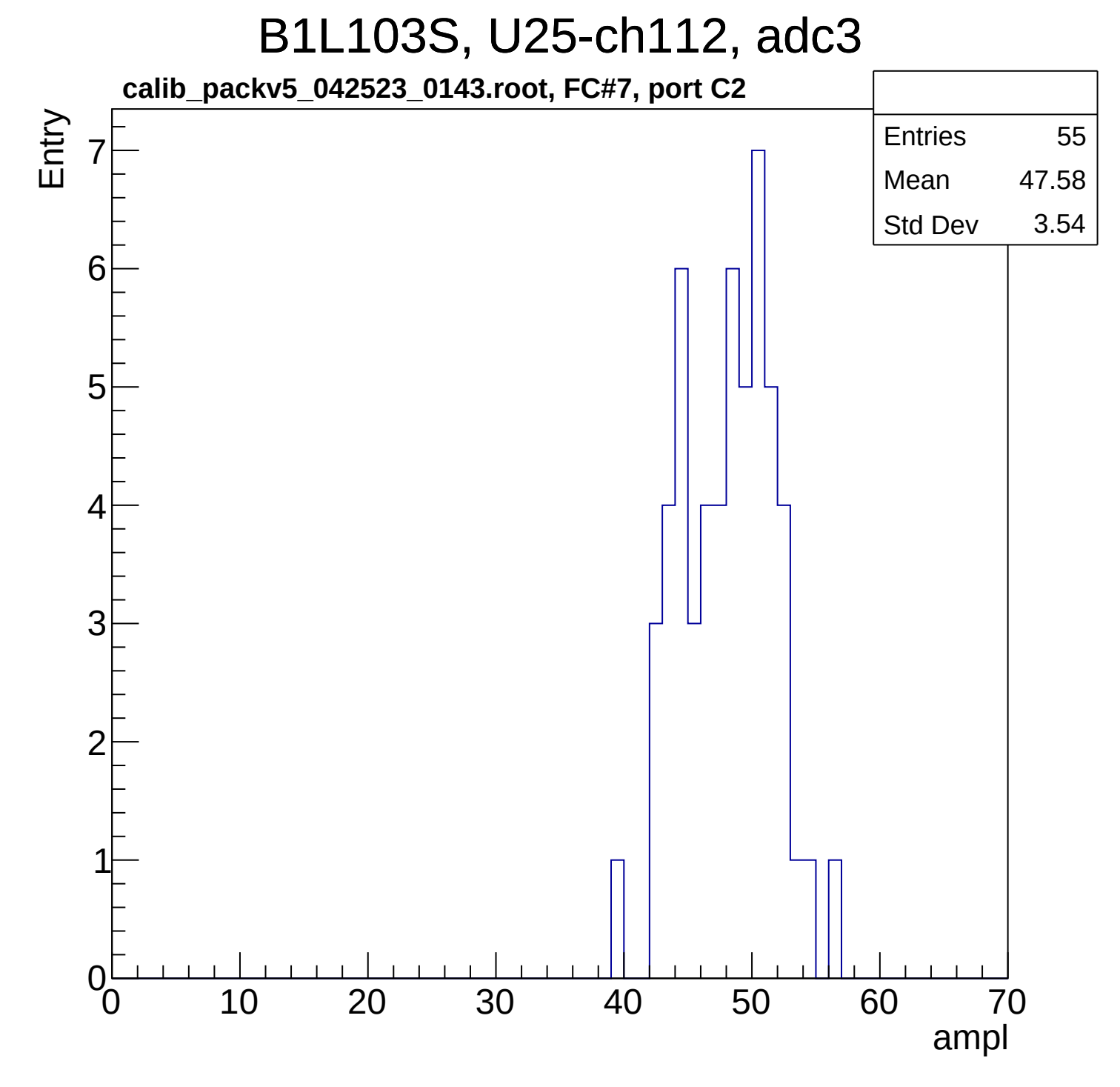
B1L103S, U25-ch112, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 47.58
Std Dev 3.54
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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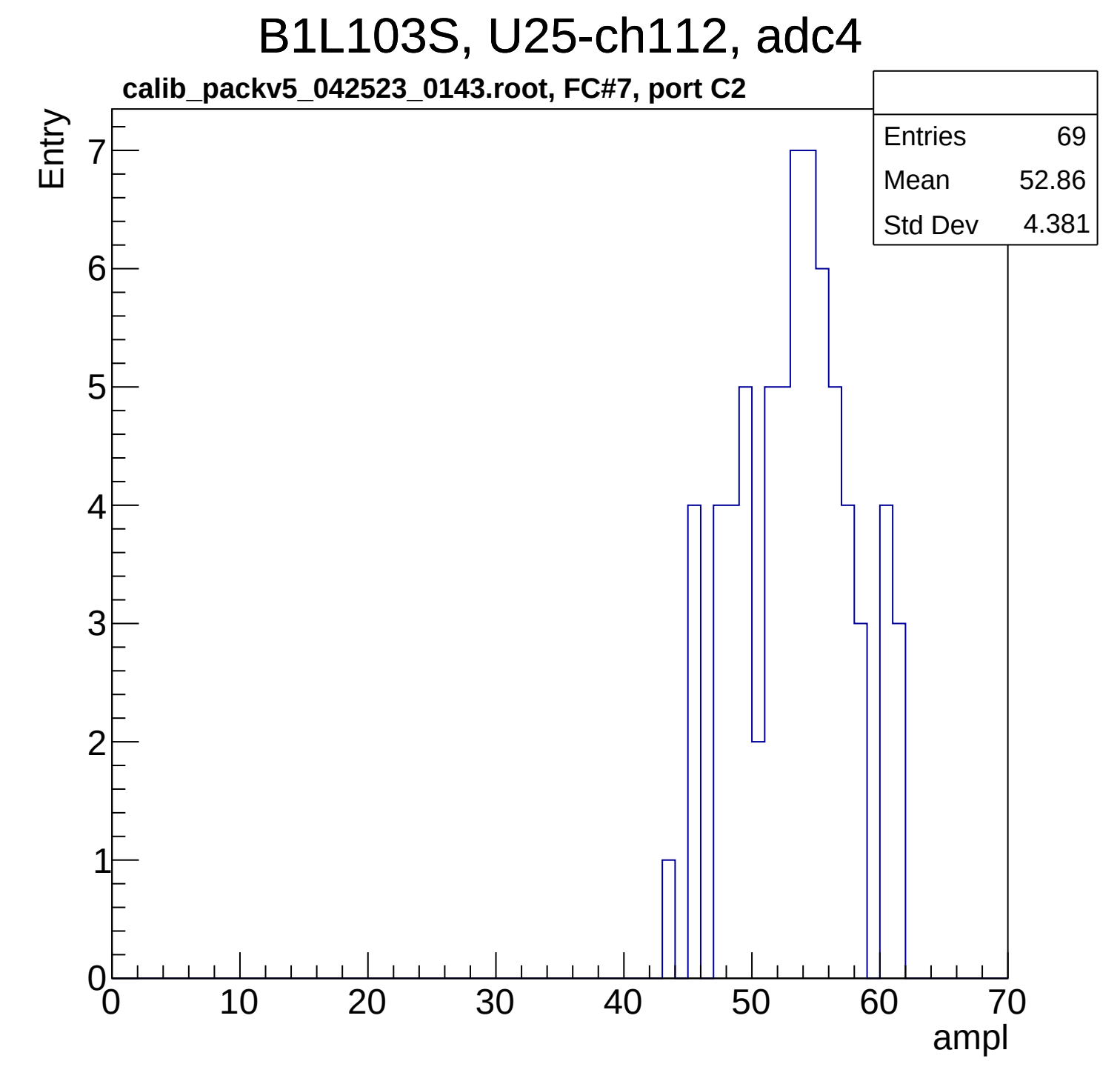
B1L103S, U25-ch112, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 52.86
Std Dev 4.381
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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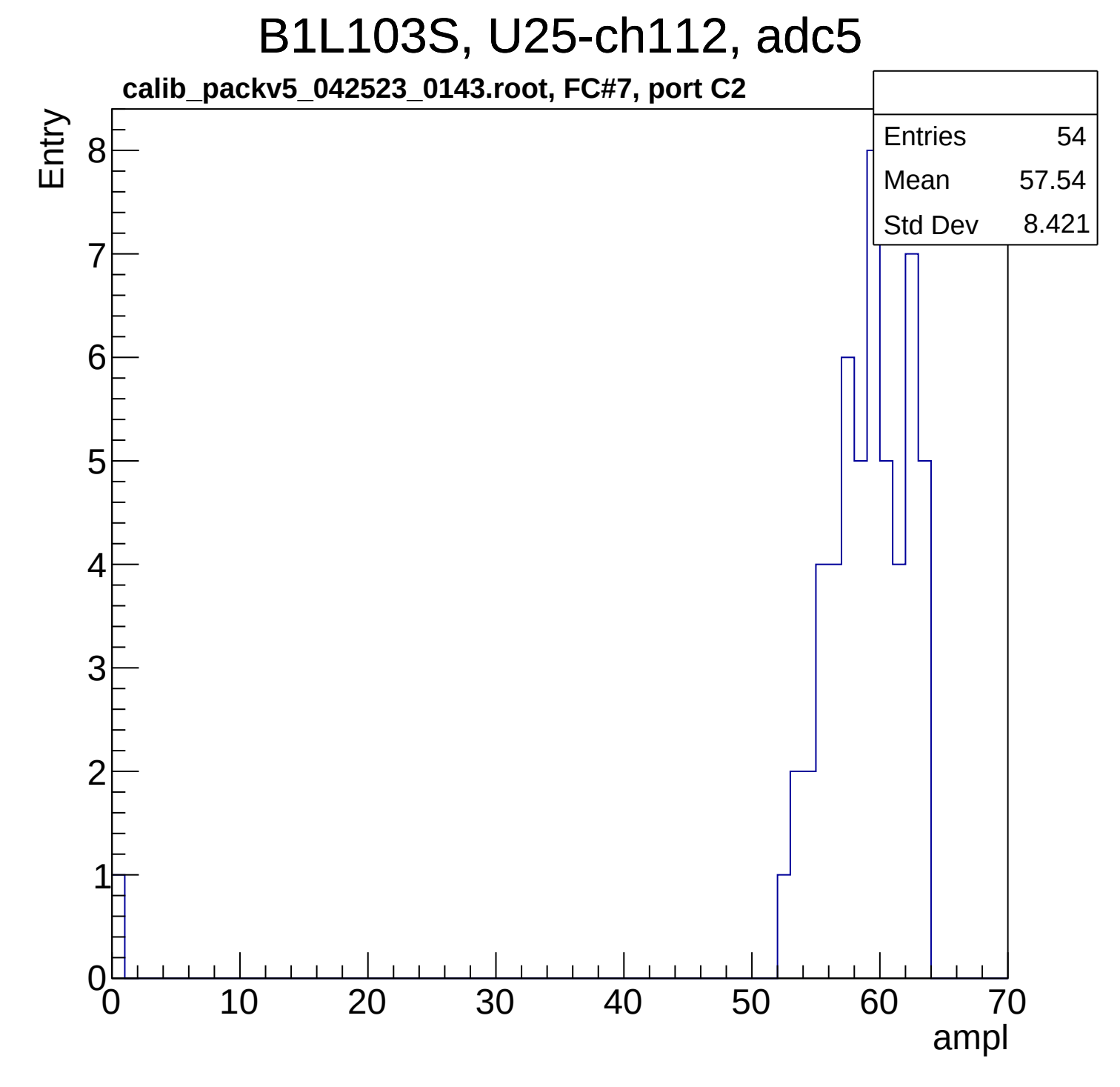
B1L103S, U25-ch112, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 57.54
Std Dev 8.421
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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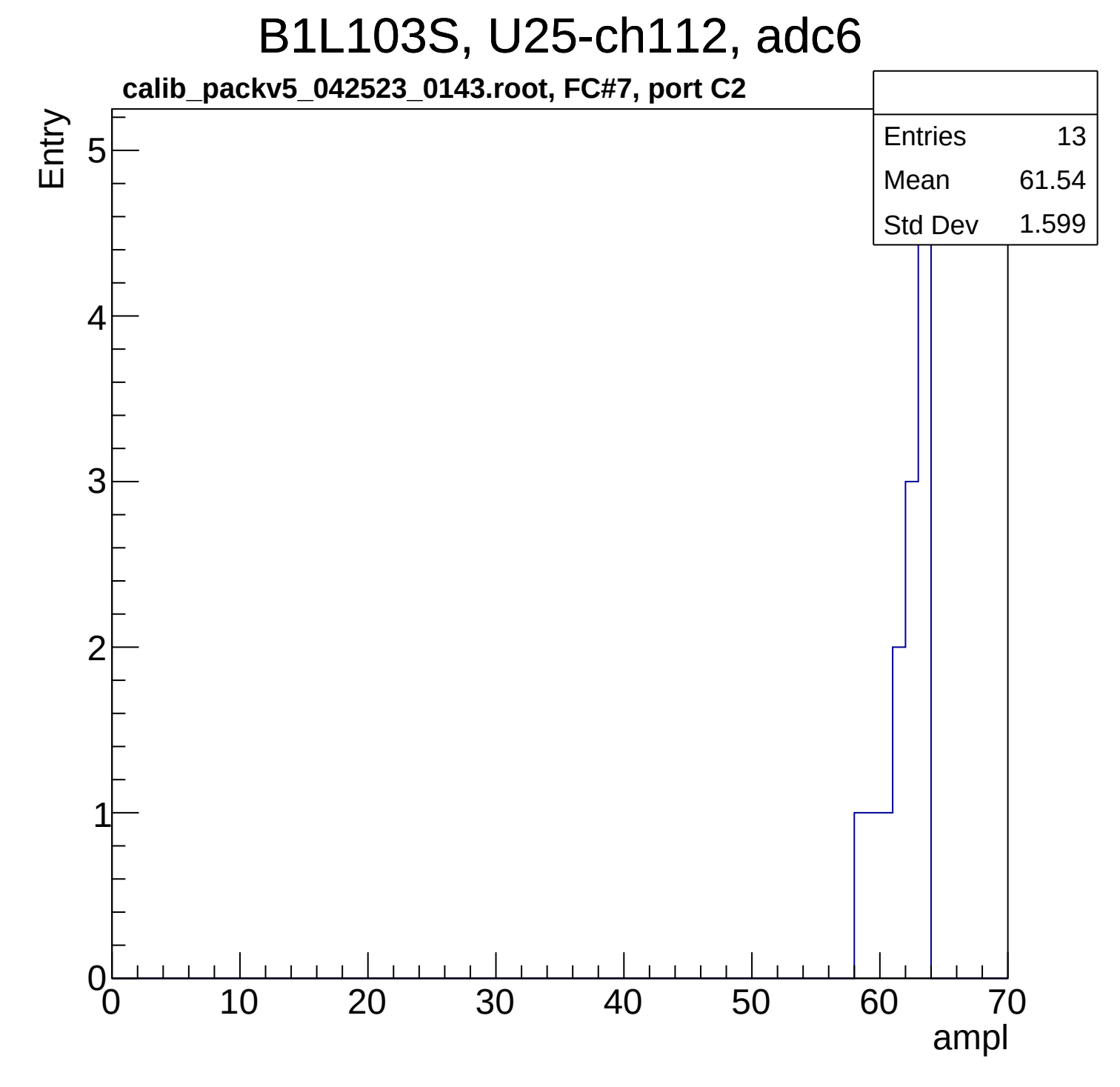
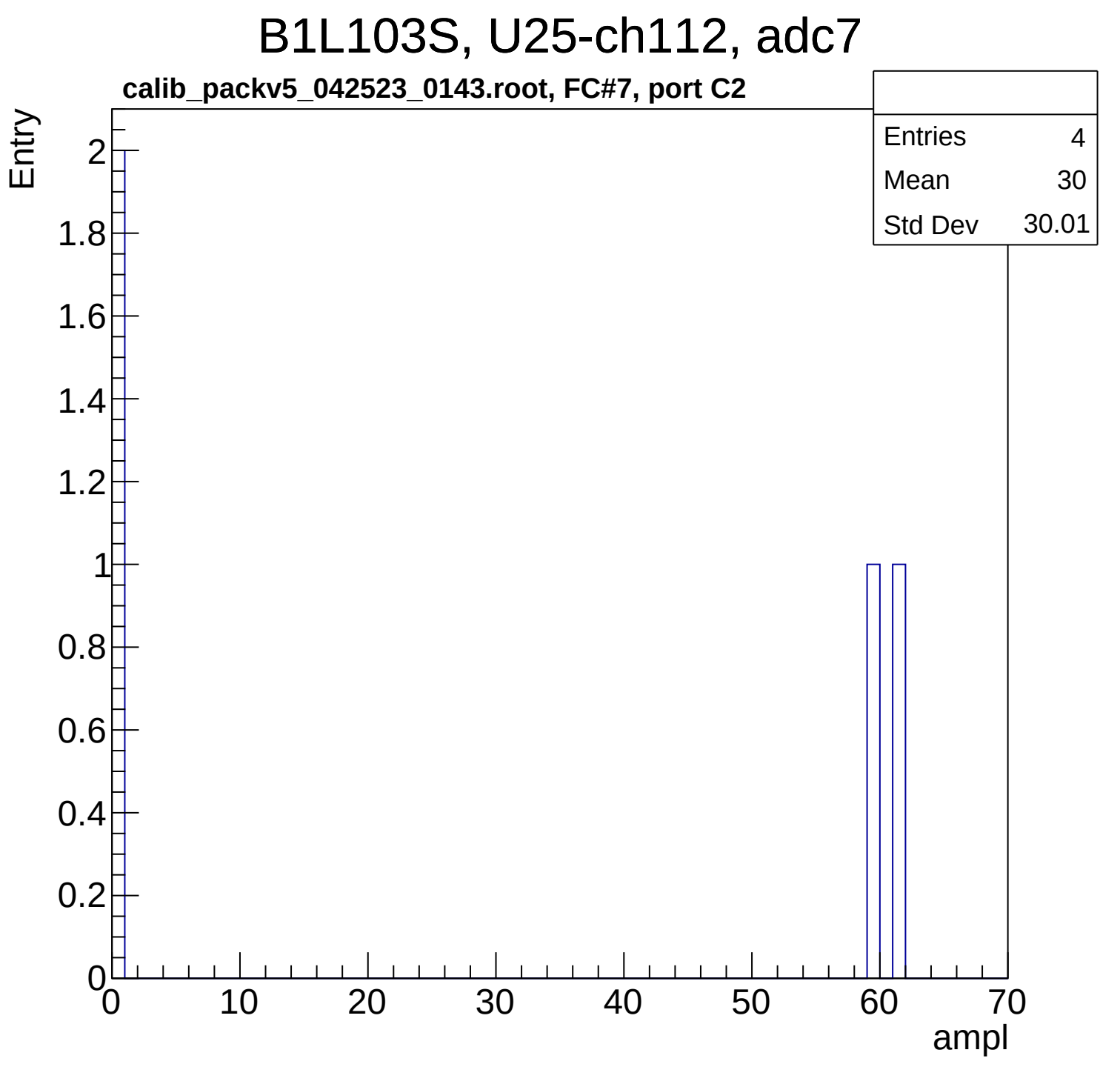

B1L103S, U25-ch112, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.54
Std Dev 1.599
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch112, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 30
Std Dev 30.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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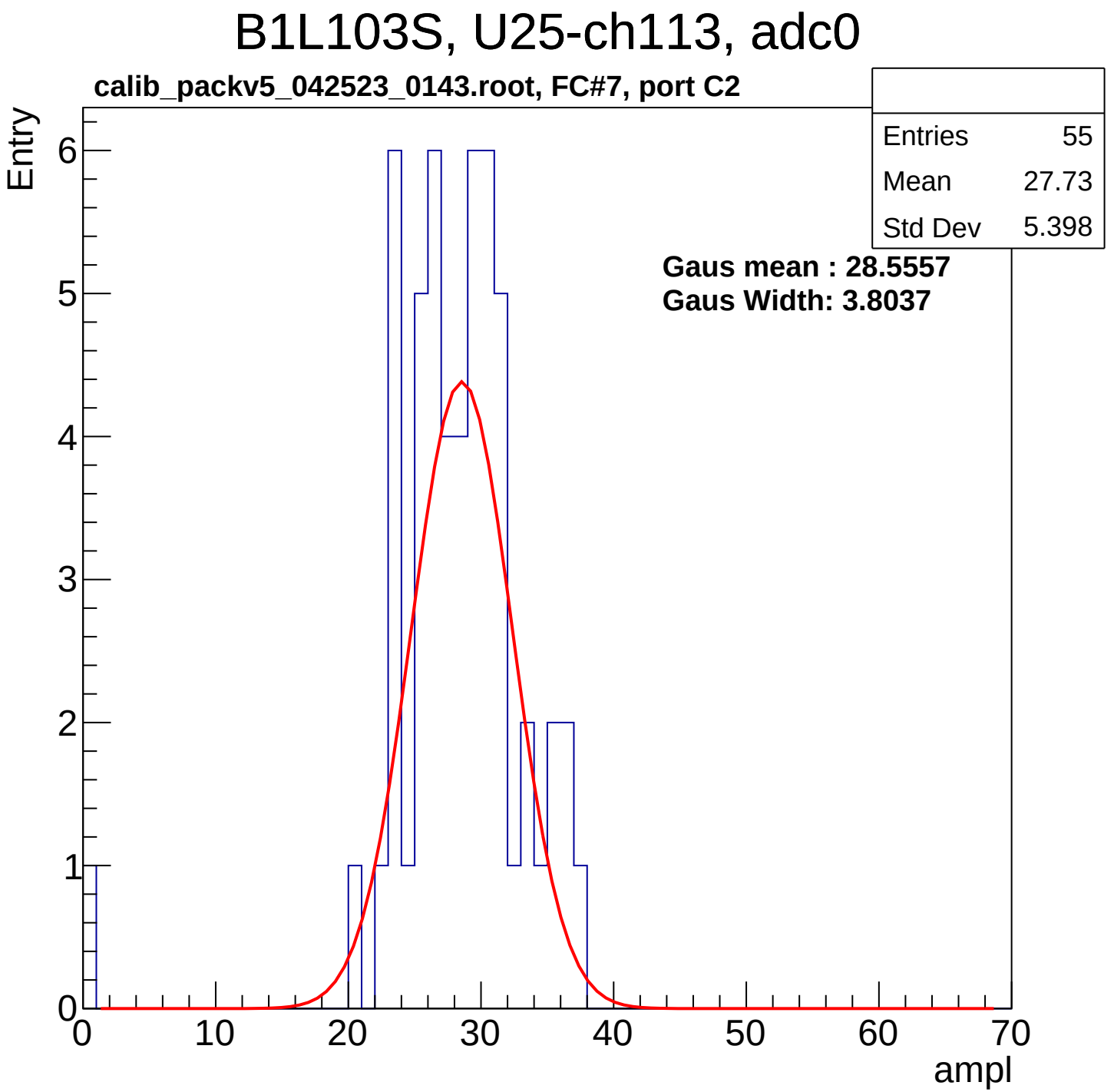
B1L103S, U25-ch113, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 27.73
Std Dev 5.398
Gaus mean : 28.5557
Gaus Width: 3.8037
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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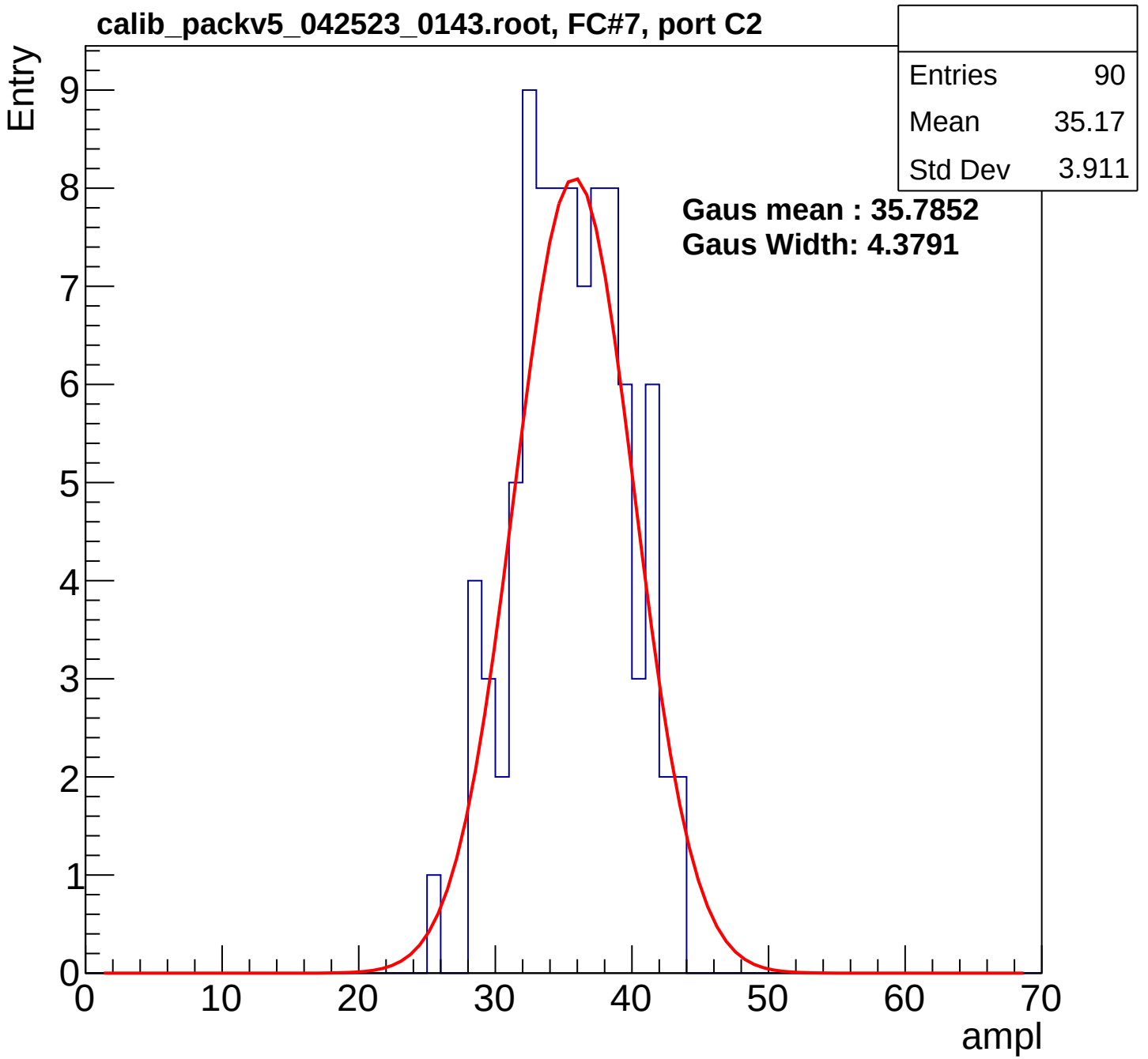
B1L103S, U25-ch113, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 90
Mean 35.17
Std Dev 3.911
Gaus mean : 35.7852
Gaus Width: 4.3791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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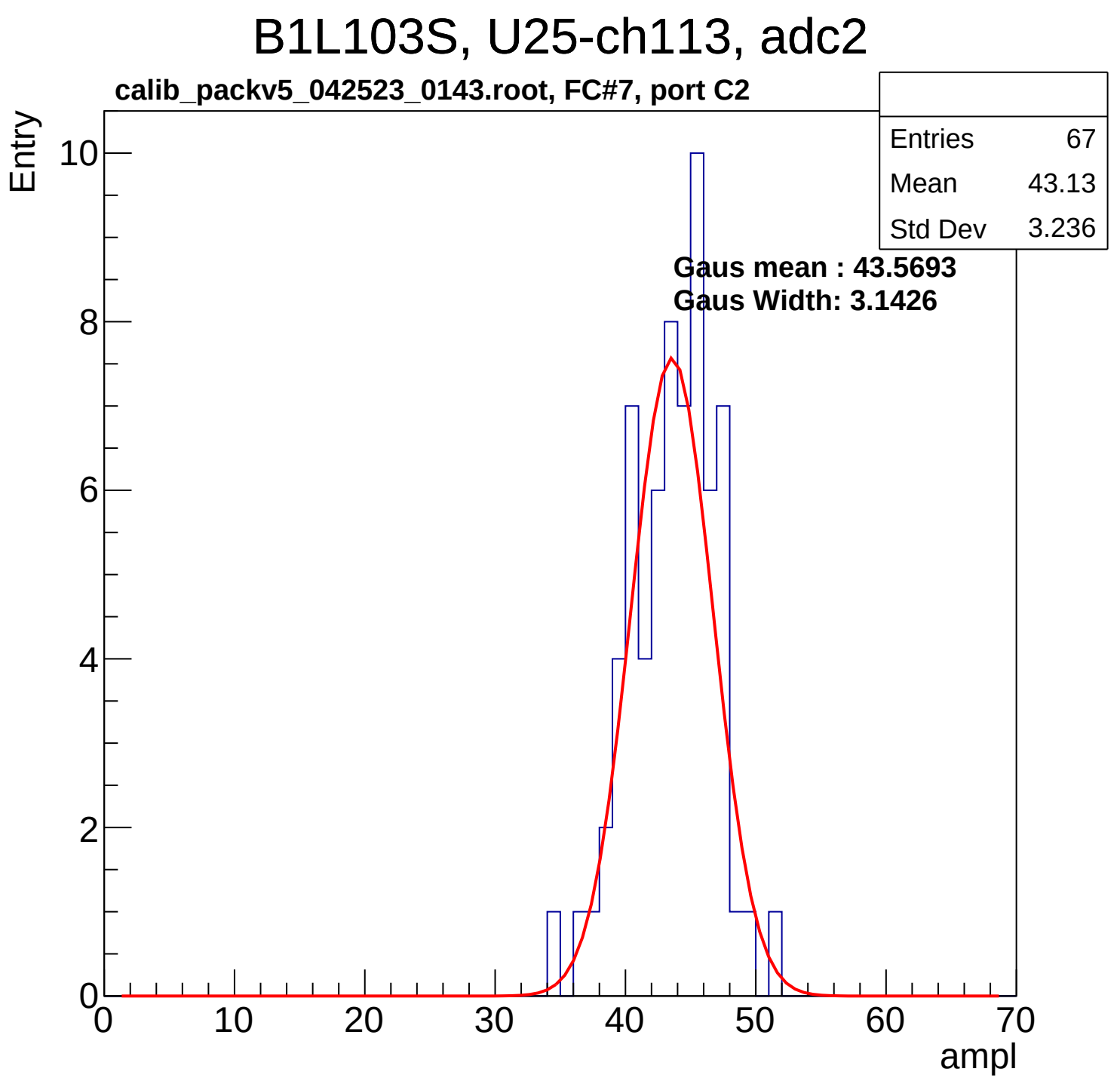
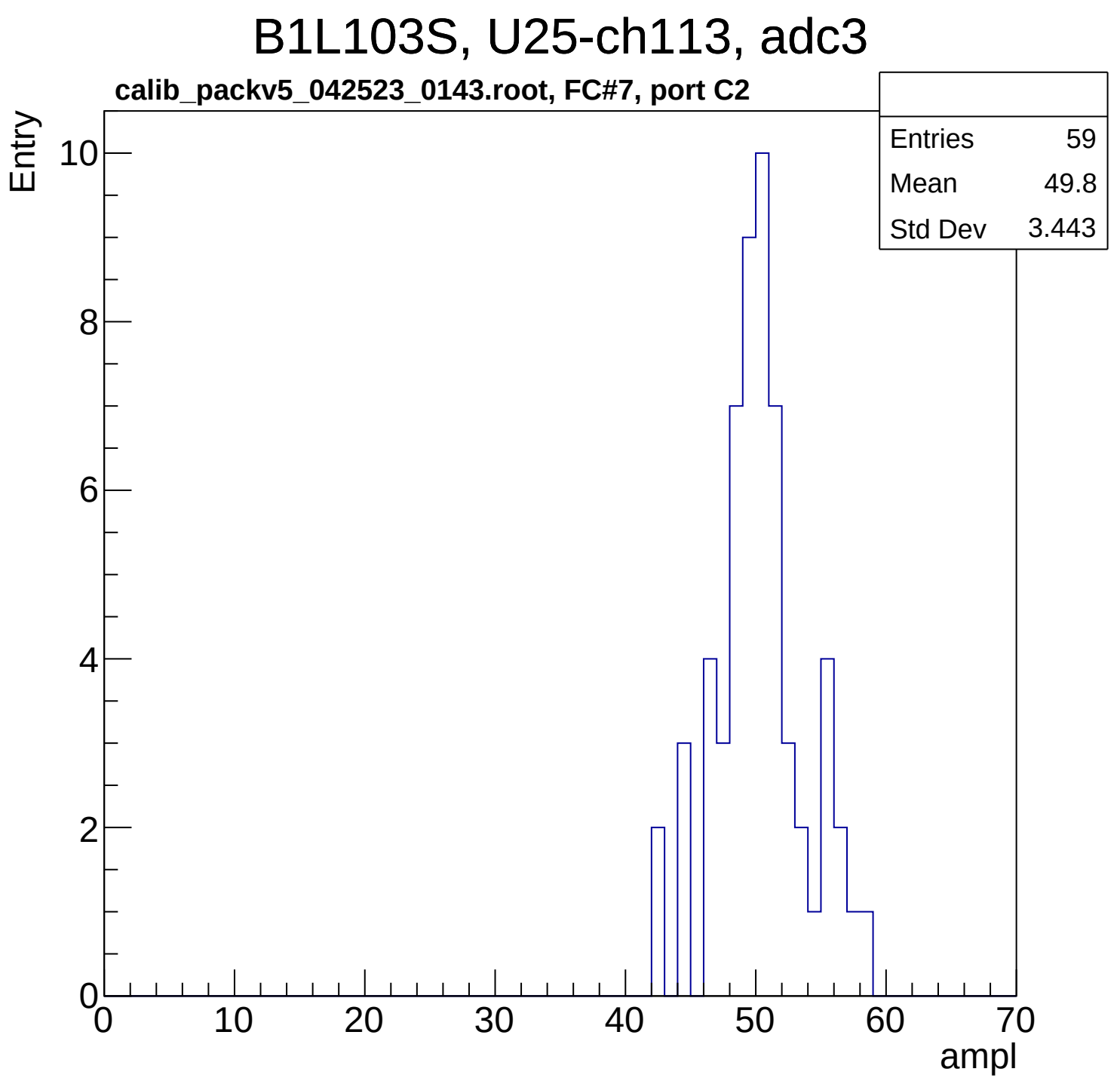
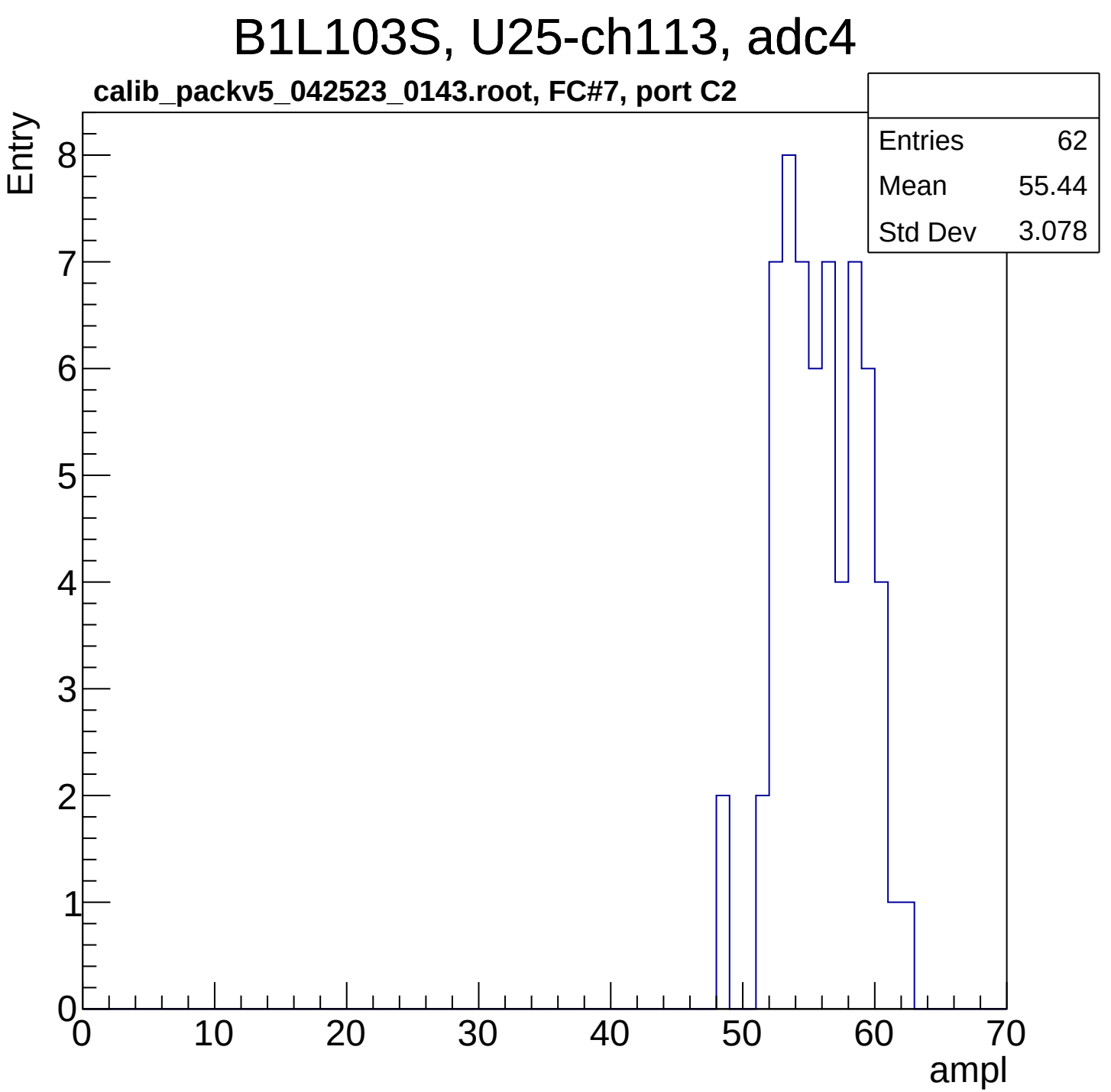

B1L103S, U25-ch113, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 43.13
Std Dev 3.236
Gaus mean : 43.5693
Gaus Width: 3.1426
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch113, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 49.8
Std Dev 3.443
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch113, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 55.44
Std Dev 3.078
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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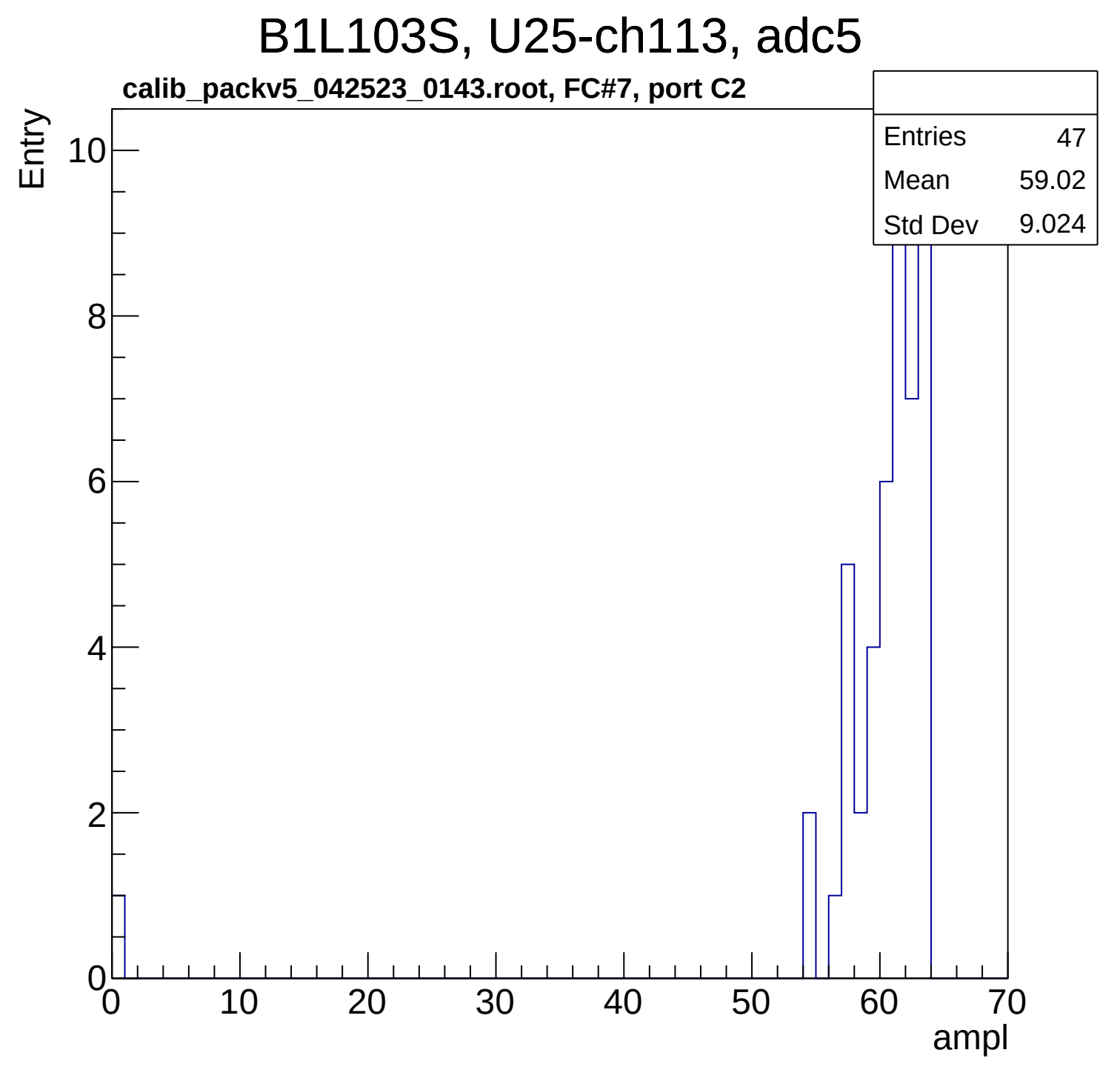
B1L103S, U25-ch113, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 59.02
Std Dev 9.024
Entry
ampl
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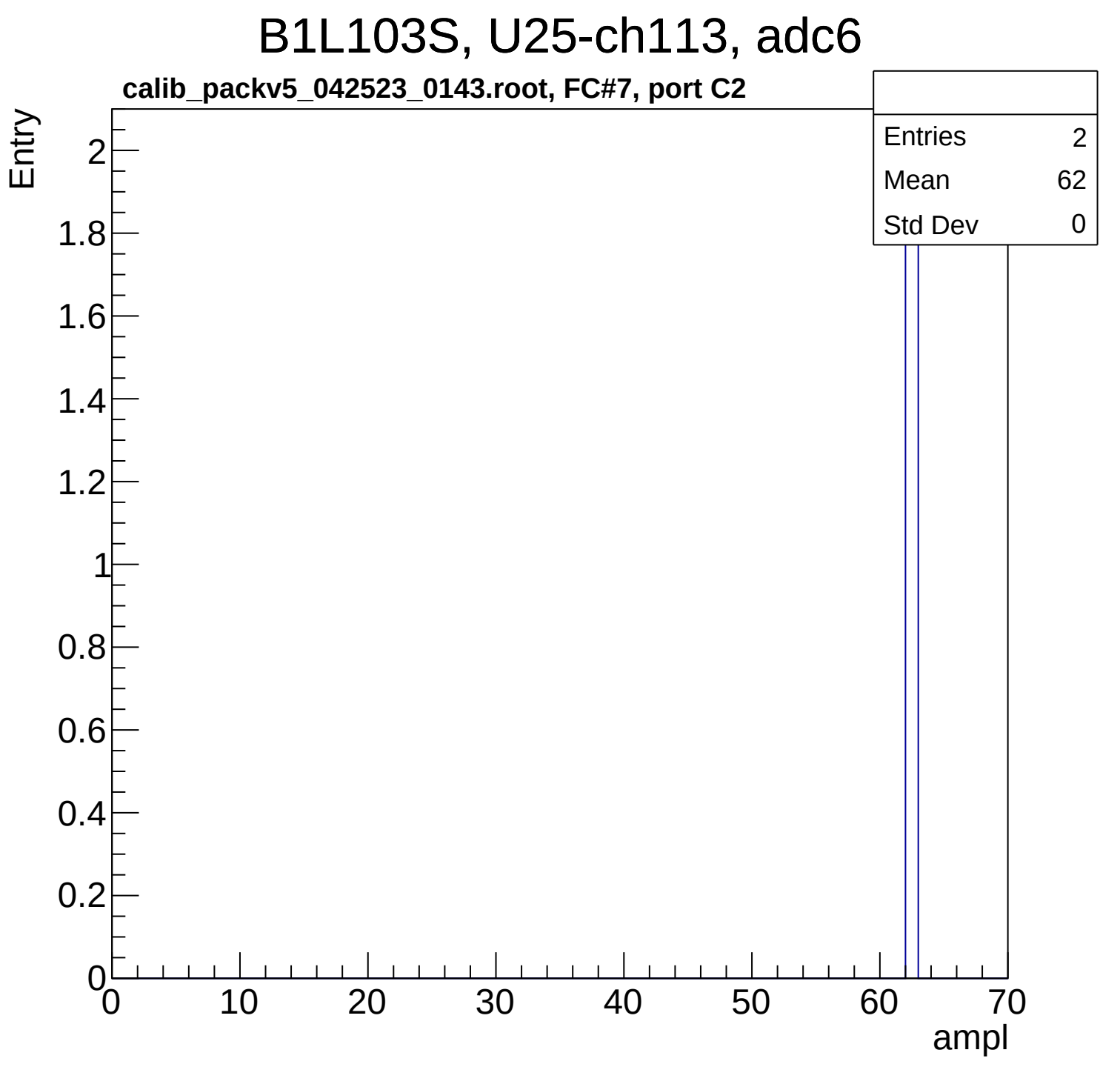
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch113, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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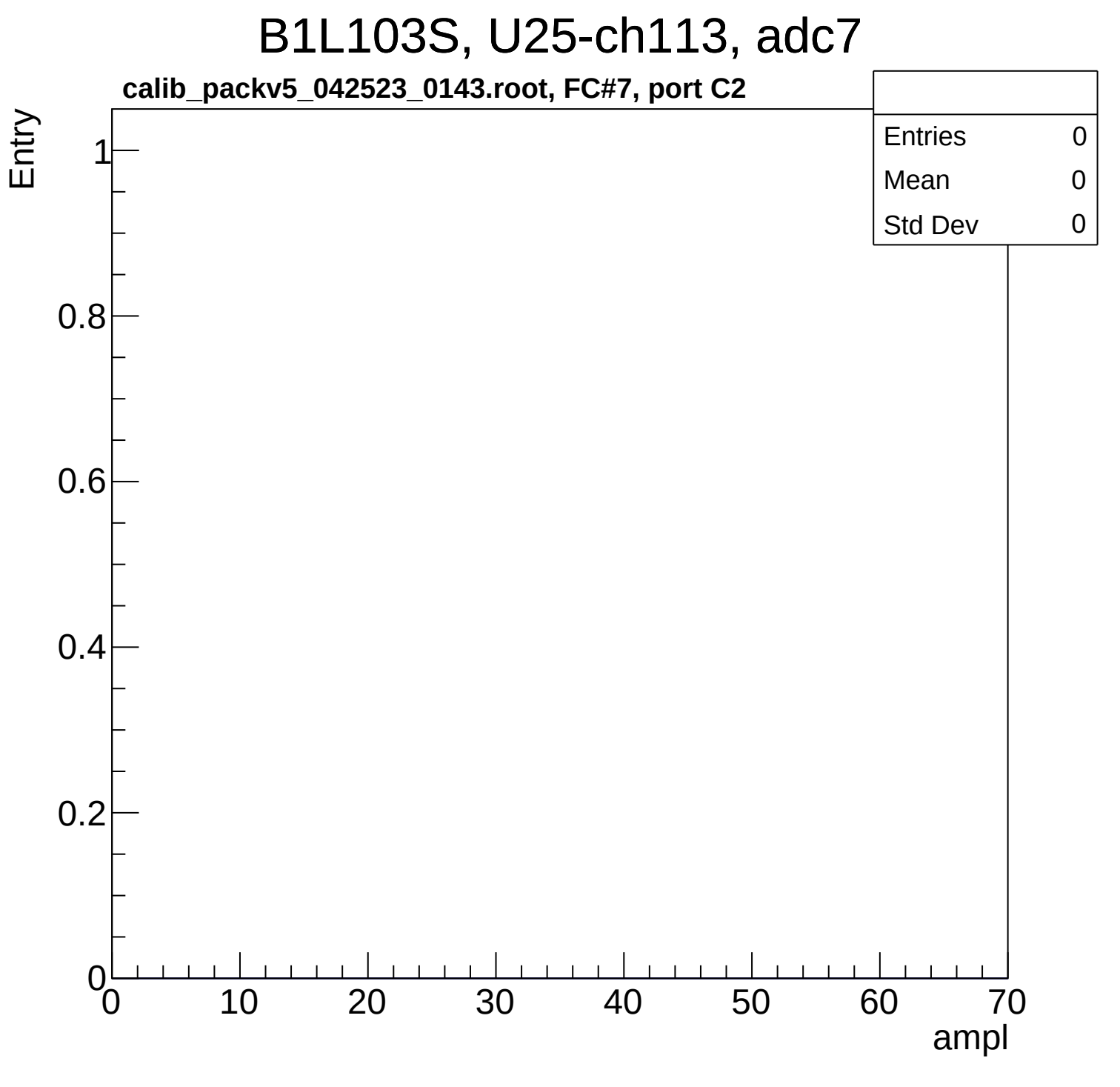
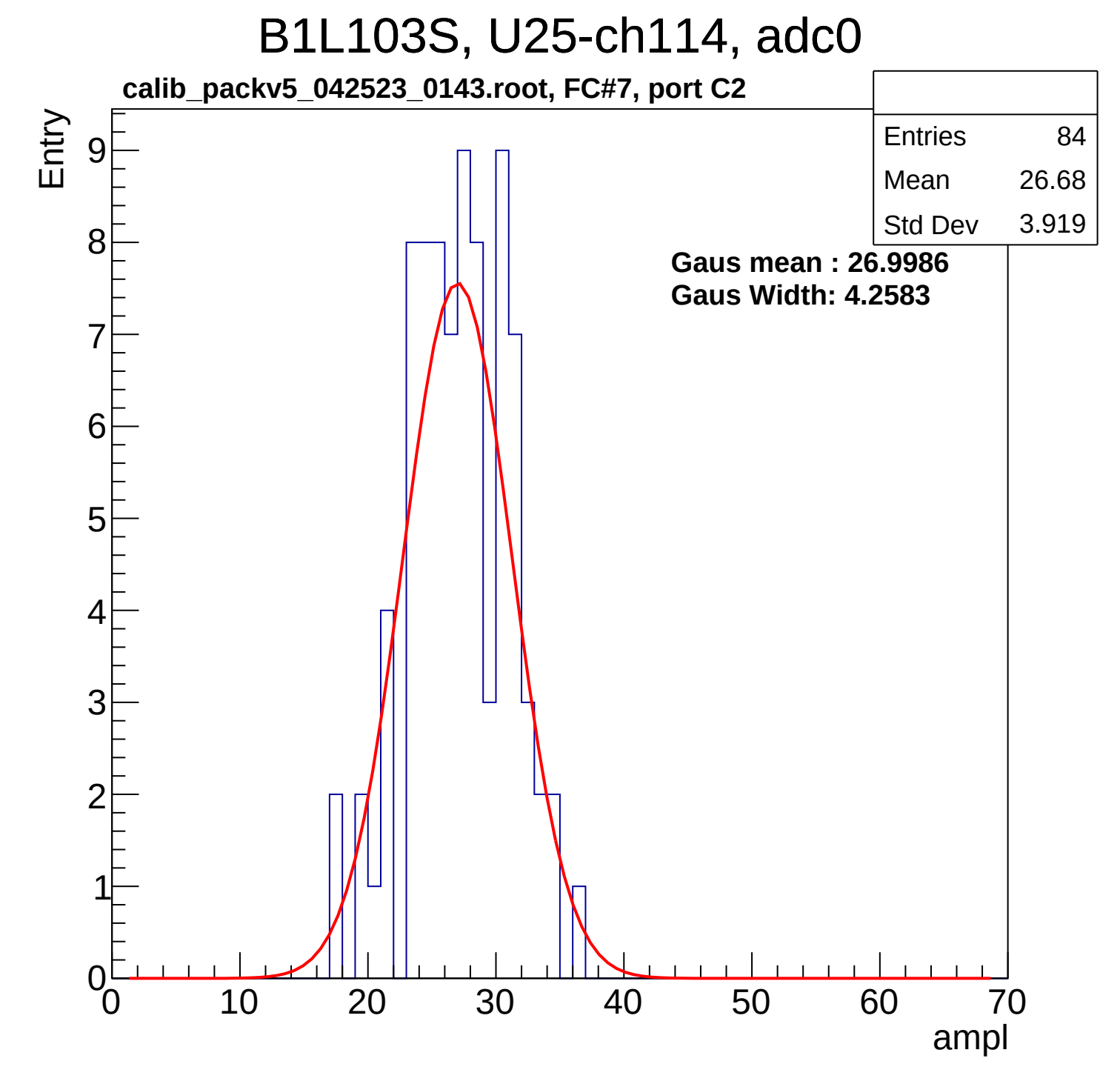

B1L103S, U25-ch113, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch114, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 26.68
Std Dev 3.919
Gaus mean : 26.9986
Gaus Width: 4.2583
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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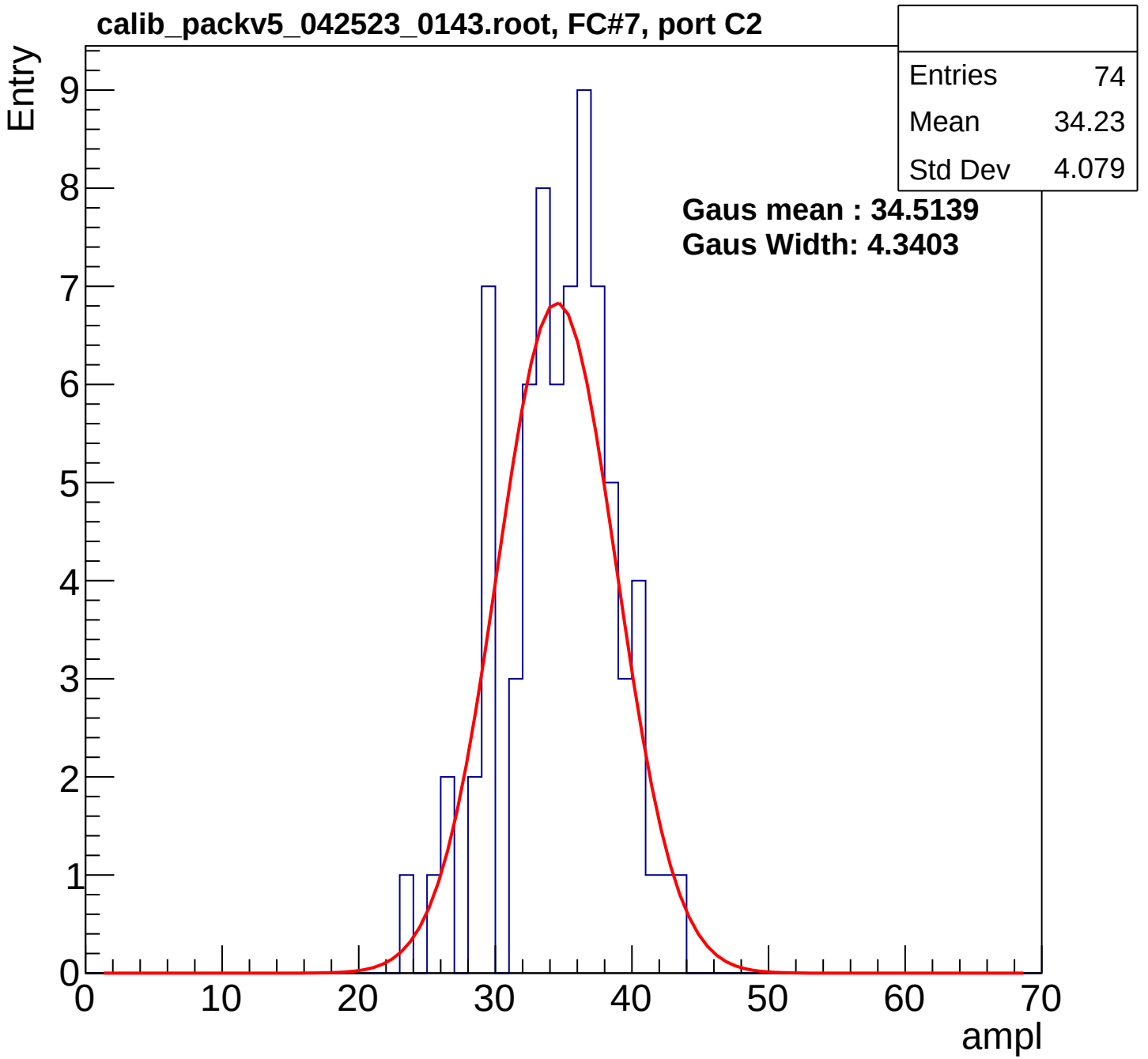
B1L103S, U25-ch114, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 34.23
Std Dev 4.079
Gaus mean : 34.5139
Gaus Width: 4.3403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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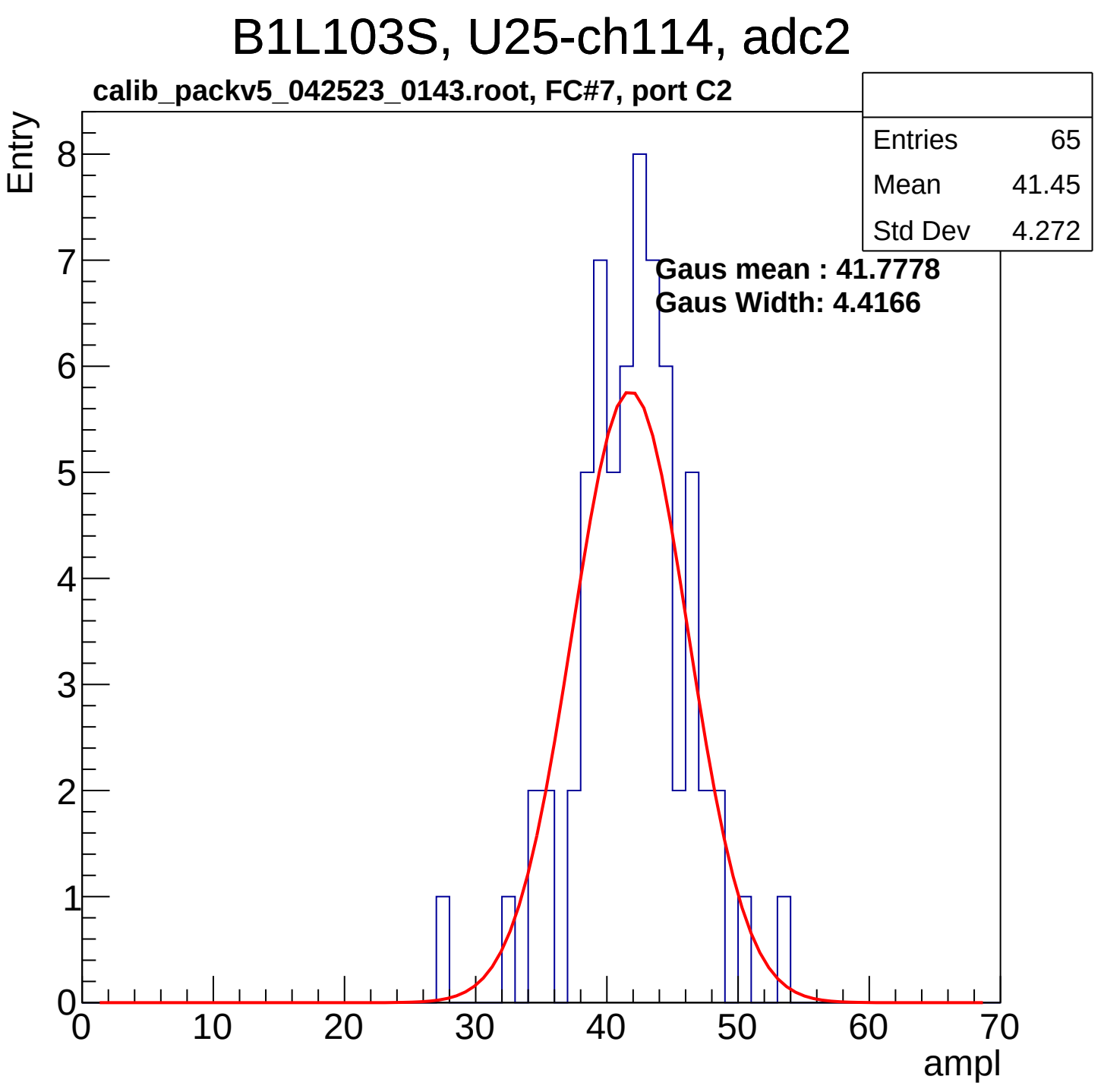
B1L103S, U25-ch114, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 41.45
Std Dev 4.272
Gaus mean : 41.7778
Gaus Width: 4.4166
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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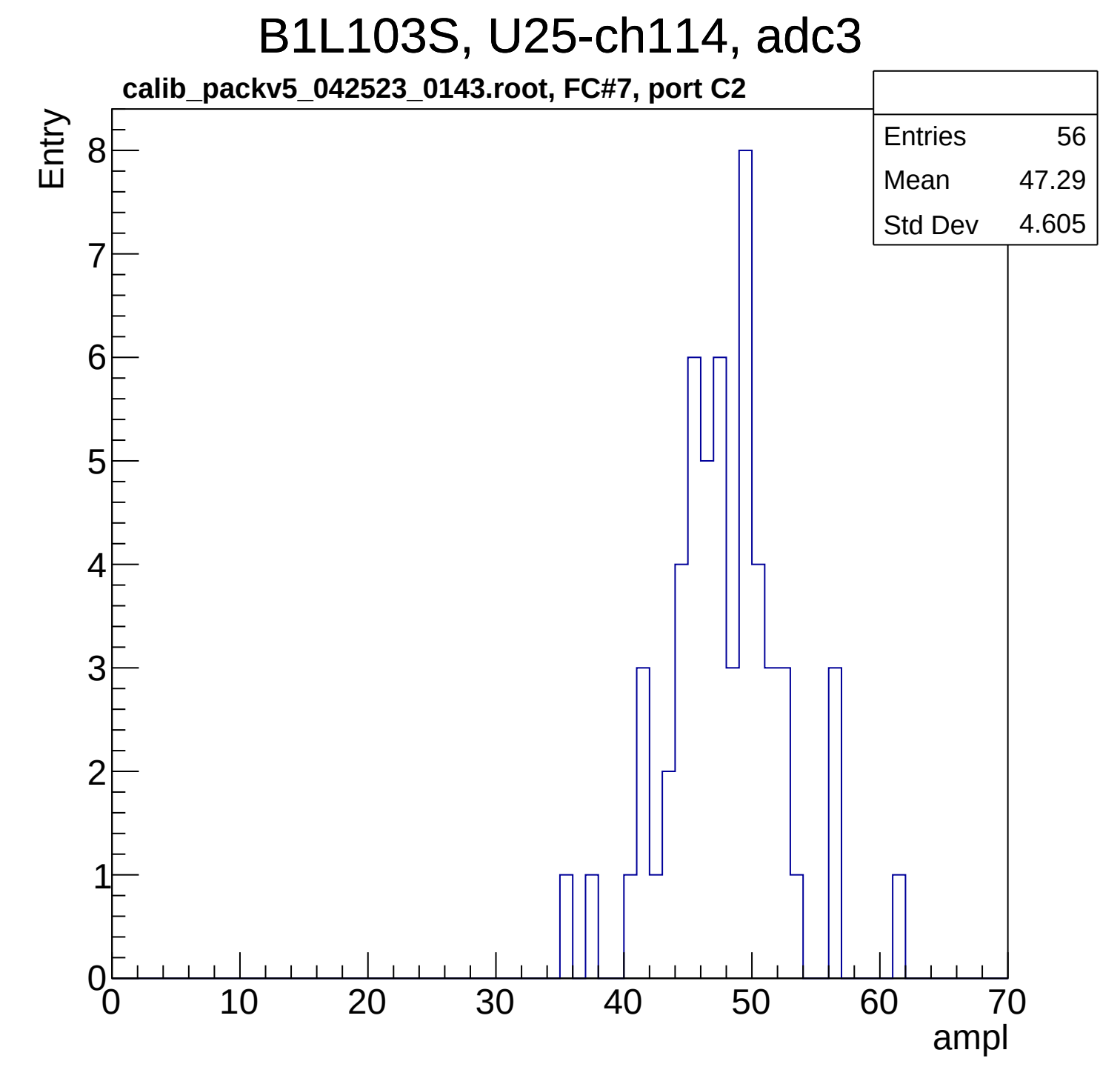
B1L103S, U25-ch114, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 47.29
Std Dev 4.605
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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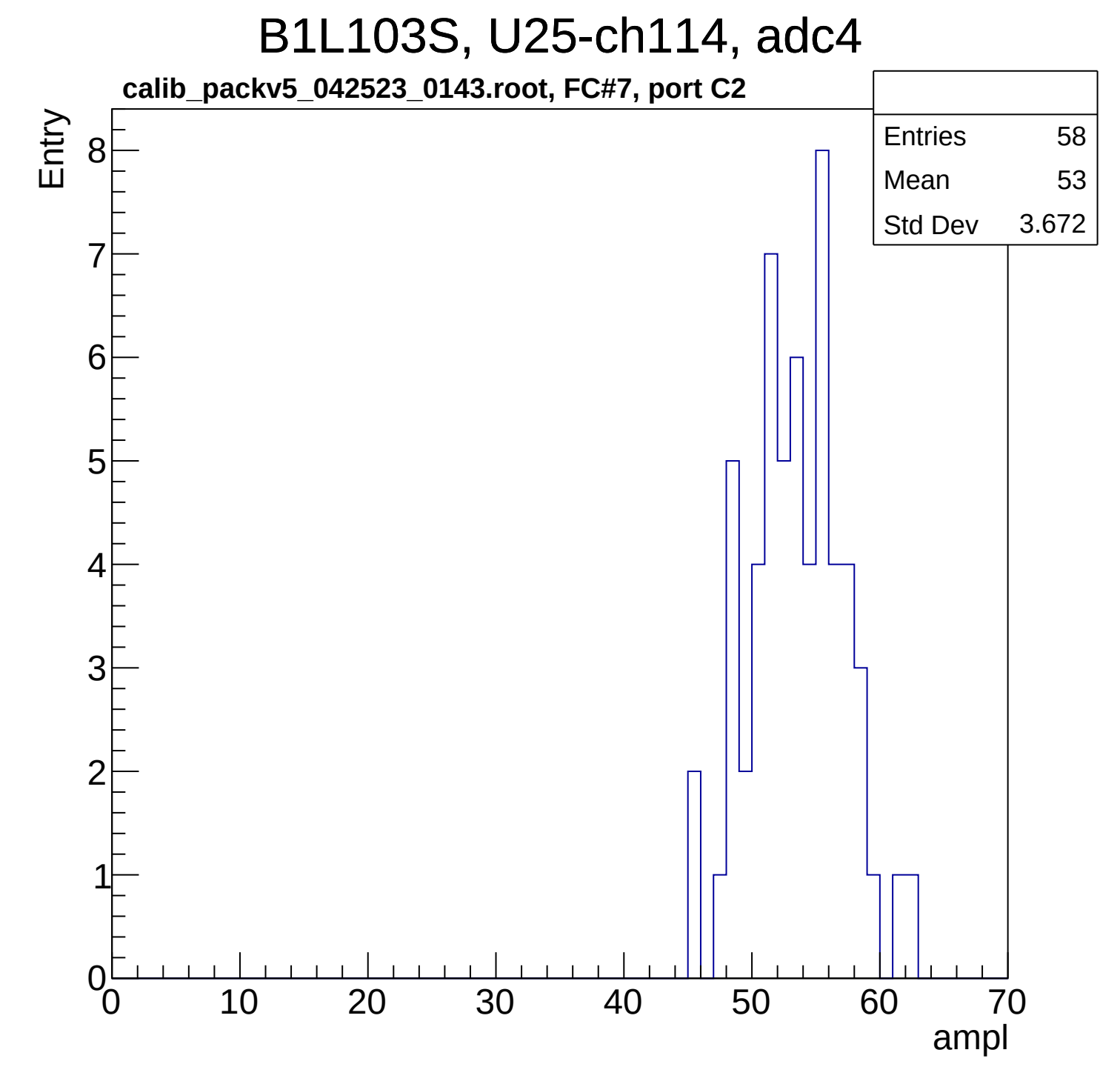
B1L103S, U25-ch114, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 53
Std Dev 3.672
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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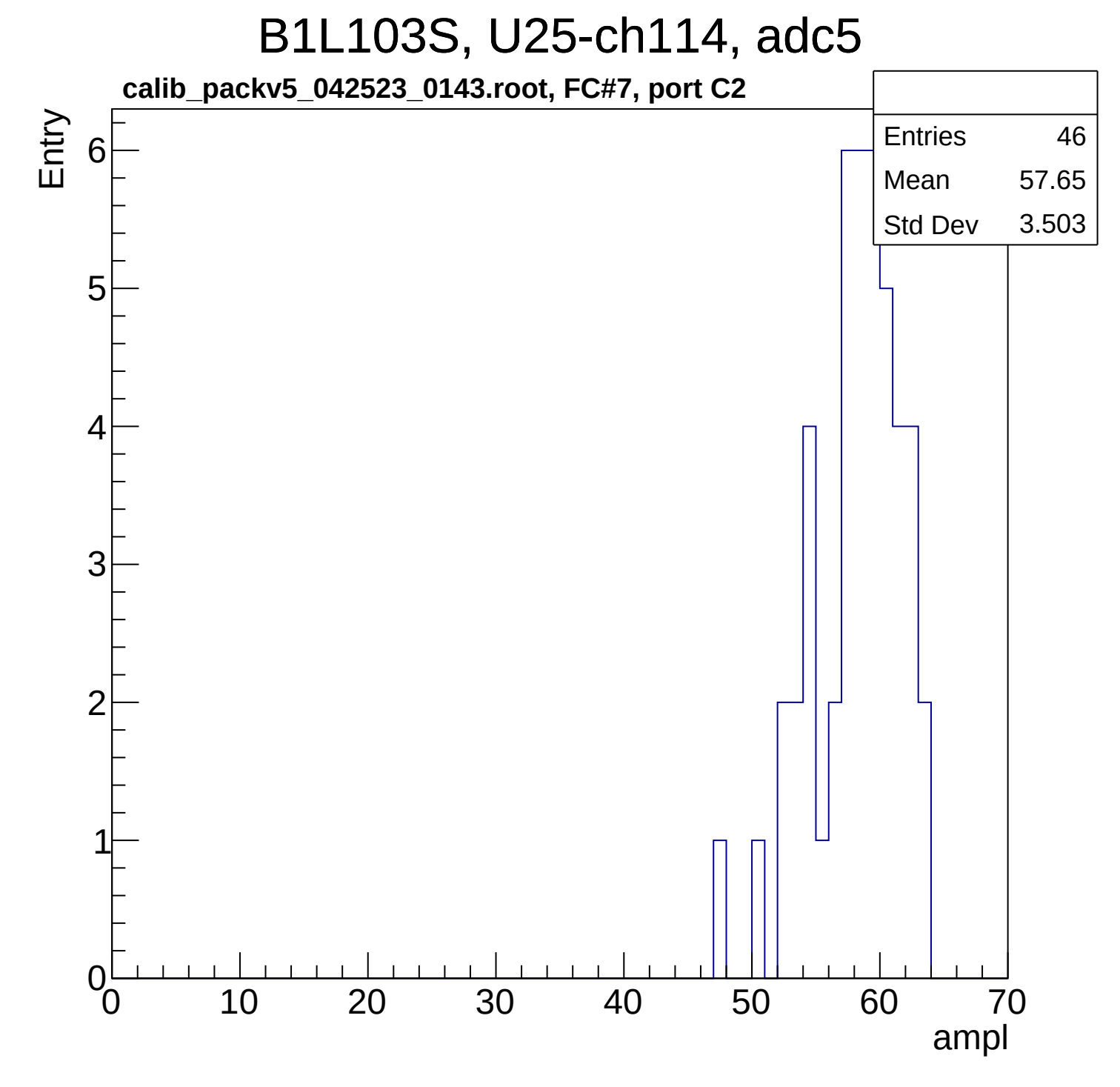
B1L103S, U25-ch114, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 57.65
Std Dev 3.503
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
50
60
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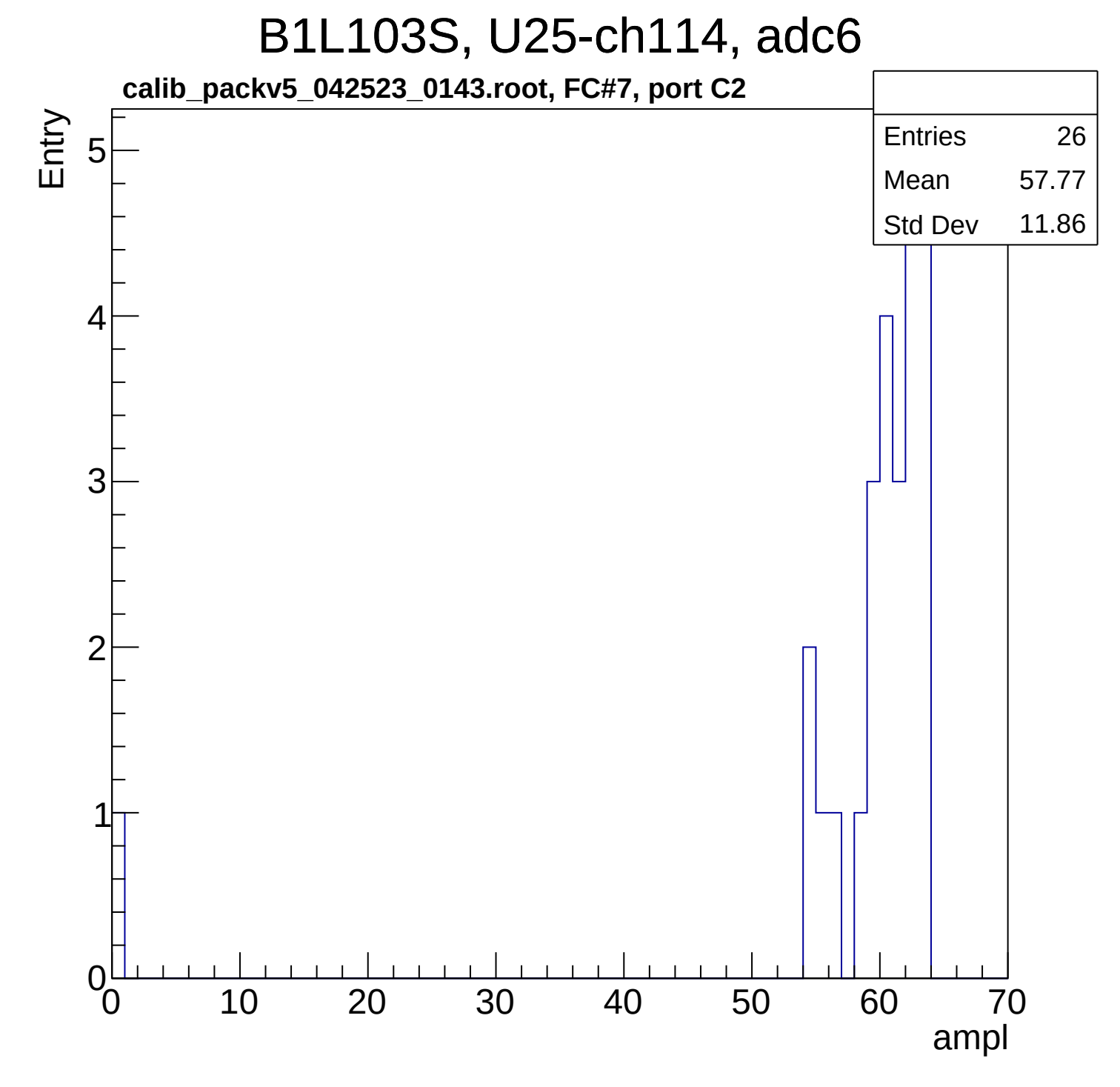
B1L103S, U25-ch114, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 26
Mean 57.77
Std Dev 11.86
Entry
ampl
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2
3
4
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0
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20
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40
50
60
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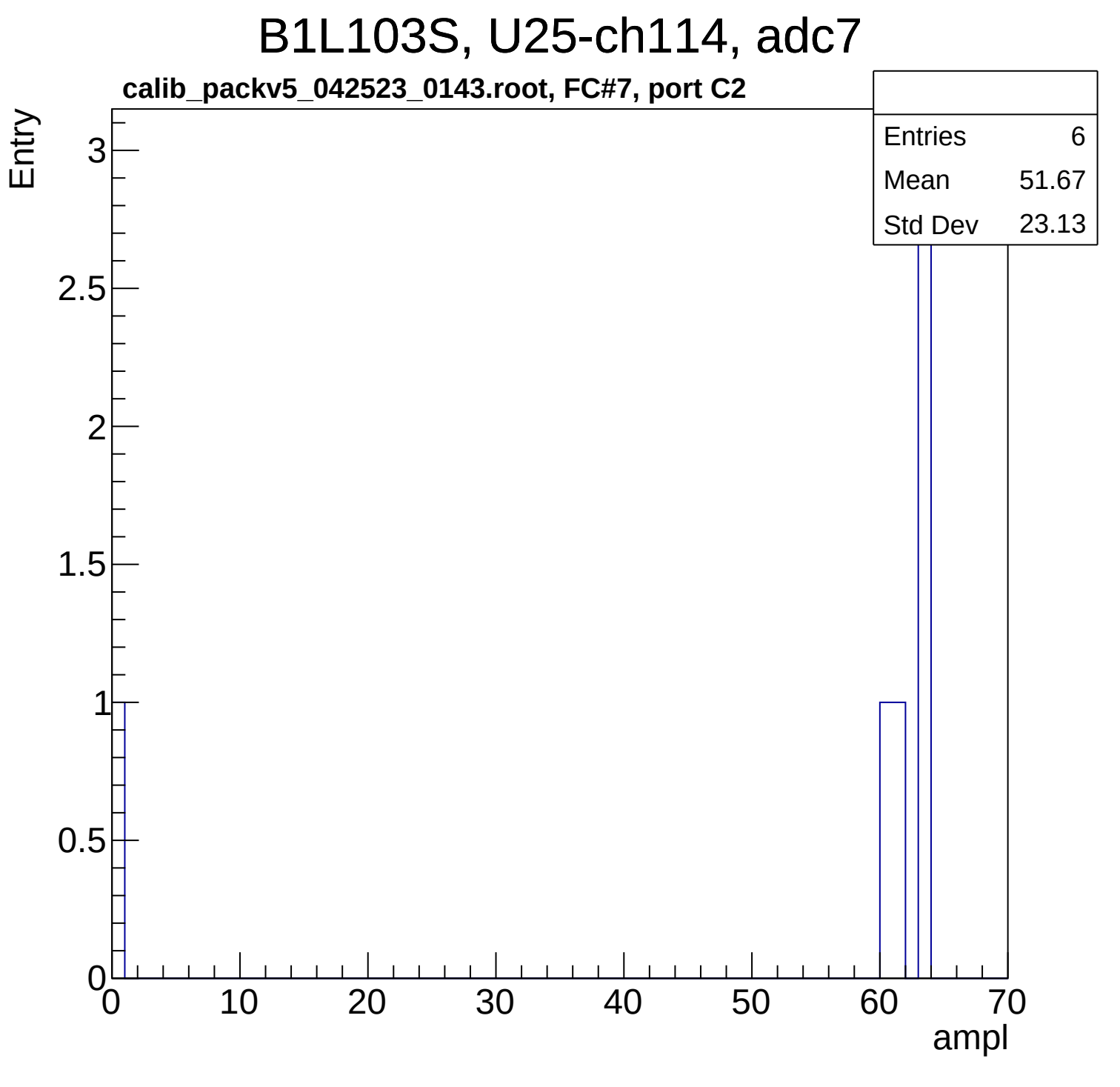
B1L103S, U25-ch114, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
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60
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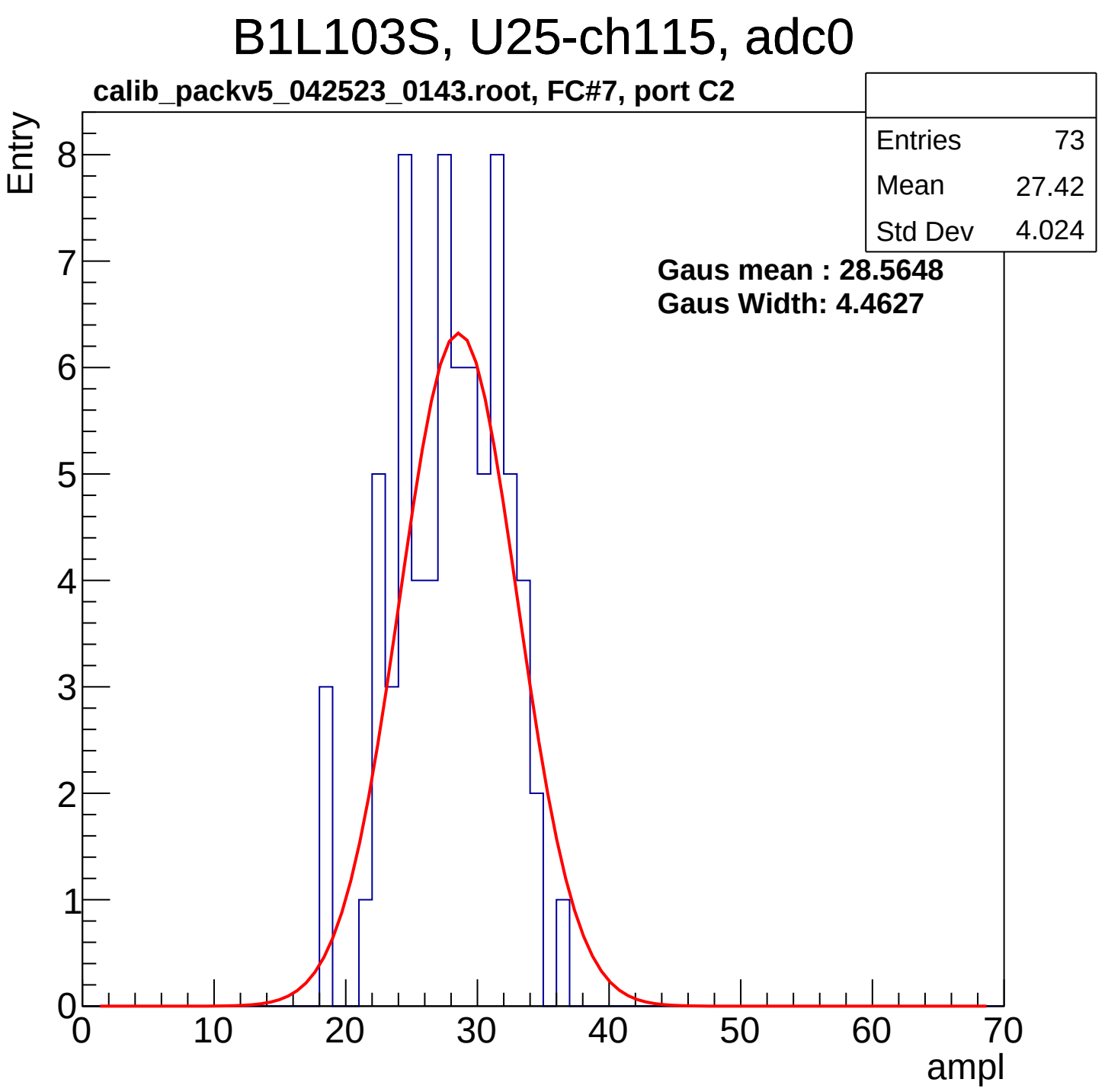
B1L103S, U25-ch115, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 27.42
Std Dev 4.024
Gaus mean : 28.5648
Gaus Width: 4.4627
Entry
ampl
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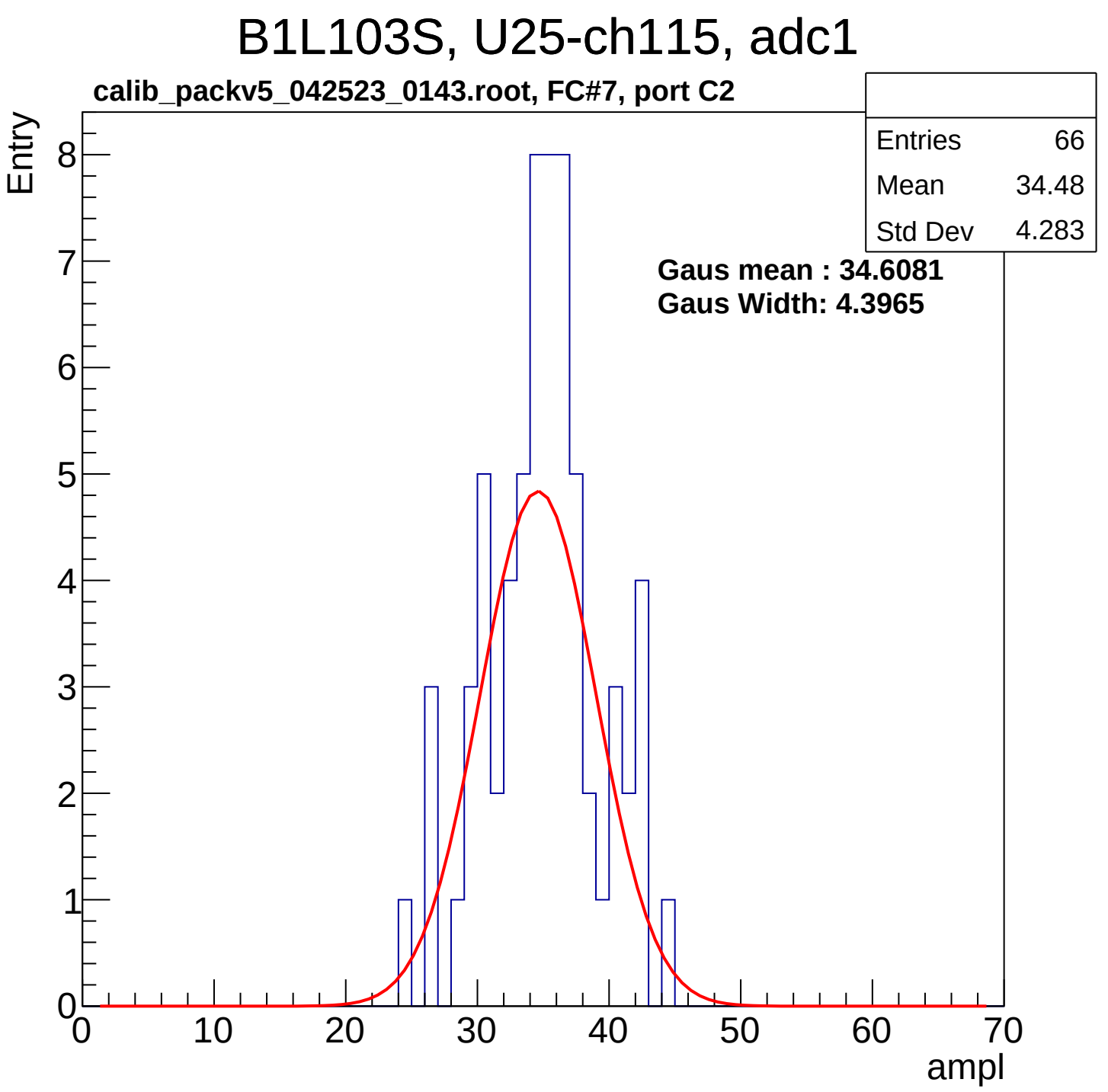
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch115, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 34.48
Std Dev 4.283
Gaus mean : 34.6081
Gaus Width: 4.3965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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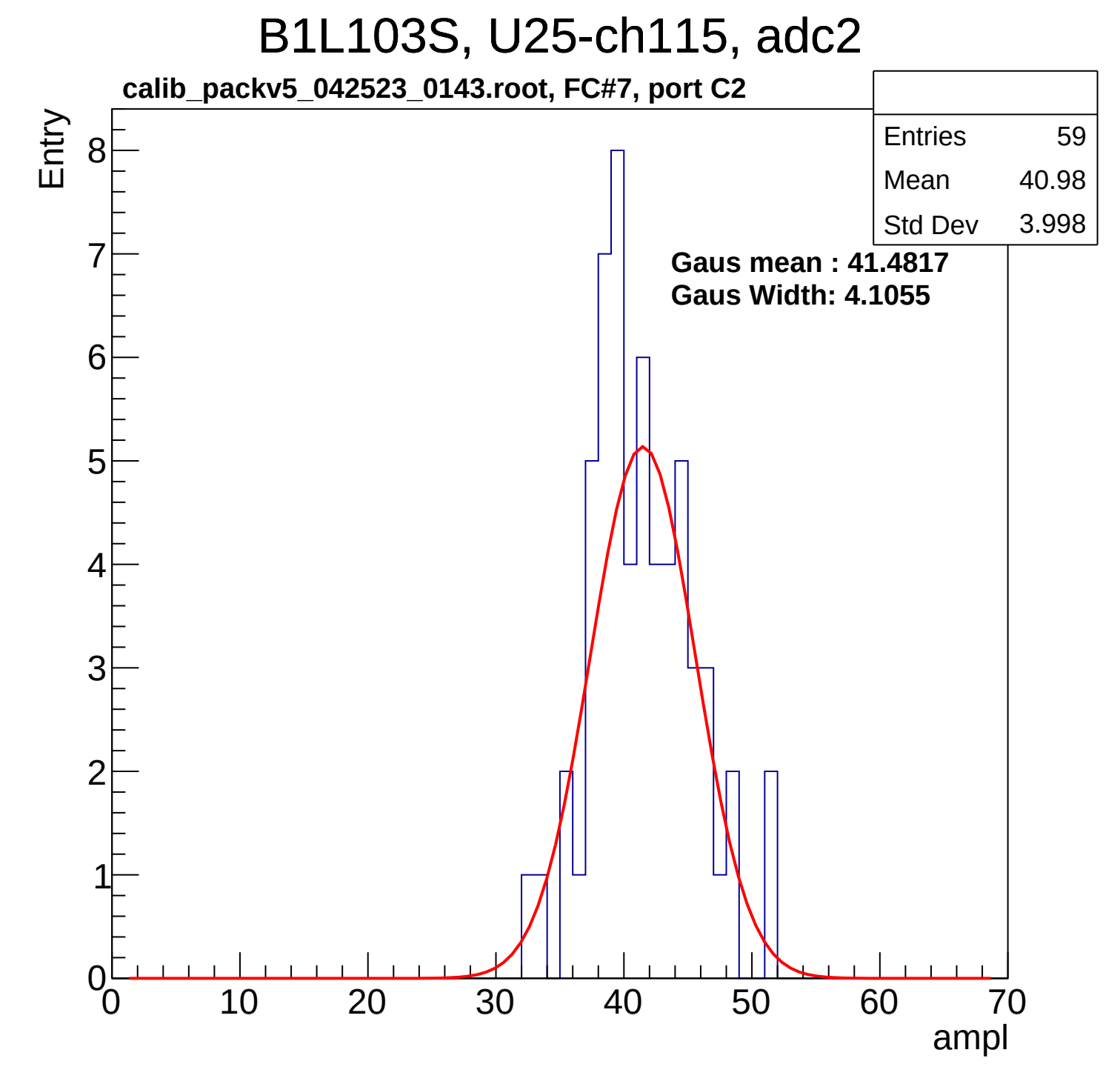
B1L103S, U25-ch115, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 40.98
Std Dev 3.998
Gaus mean : 41.4817
Gaus Width: 4.1055
Entry
ampl
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1
2
3
4
5
6
7
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0
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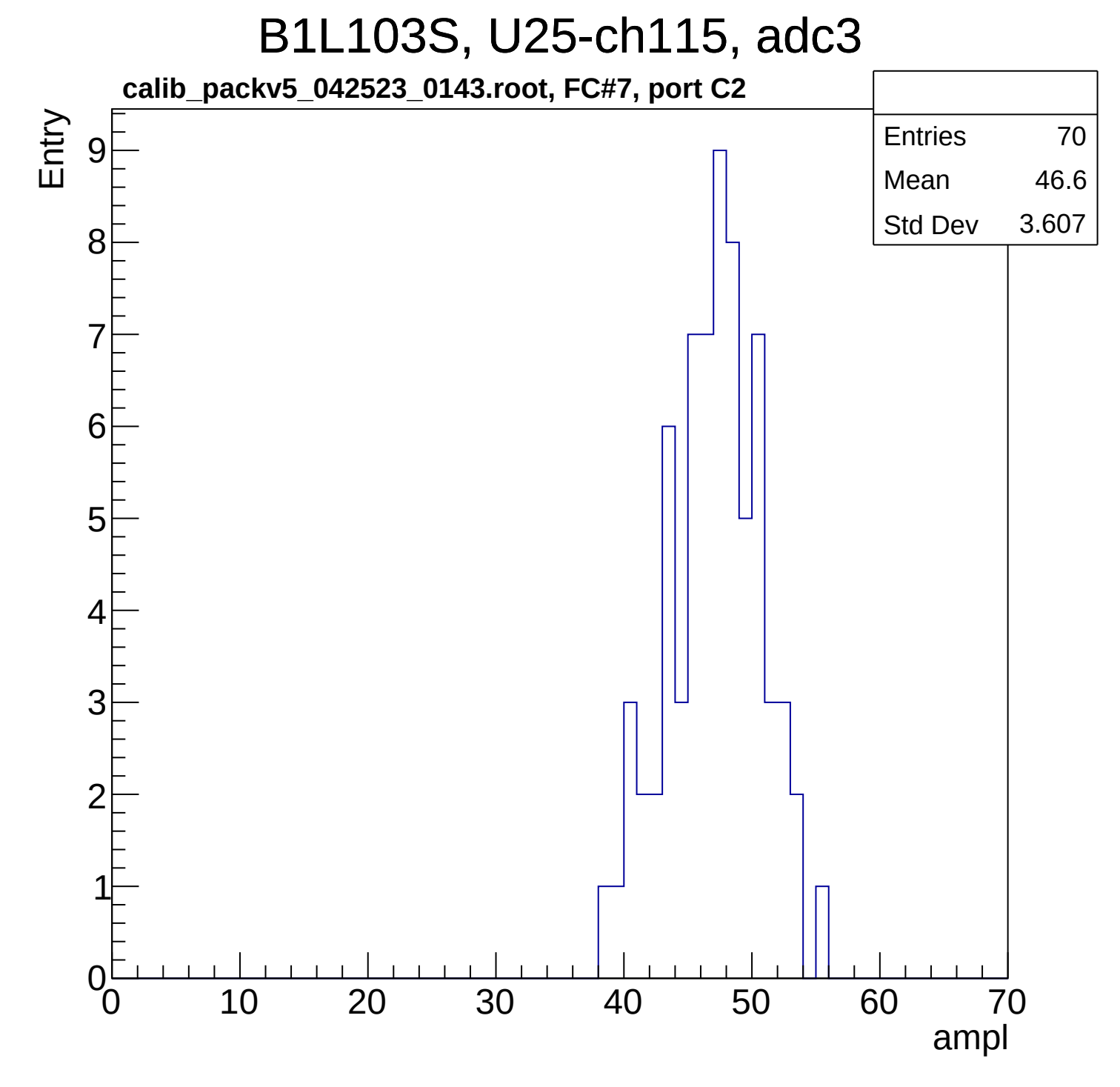
B1L103S, U25-ch115, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 70
Mean 46.6
Std Dev 3.607
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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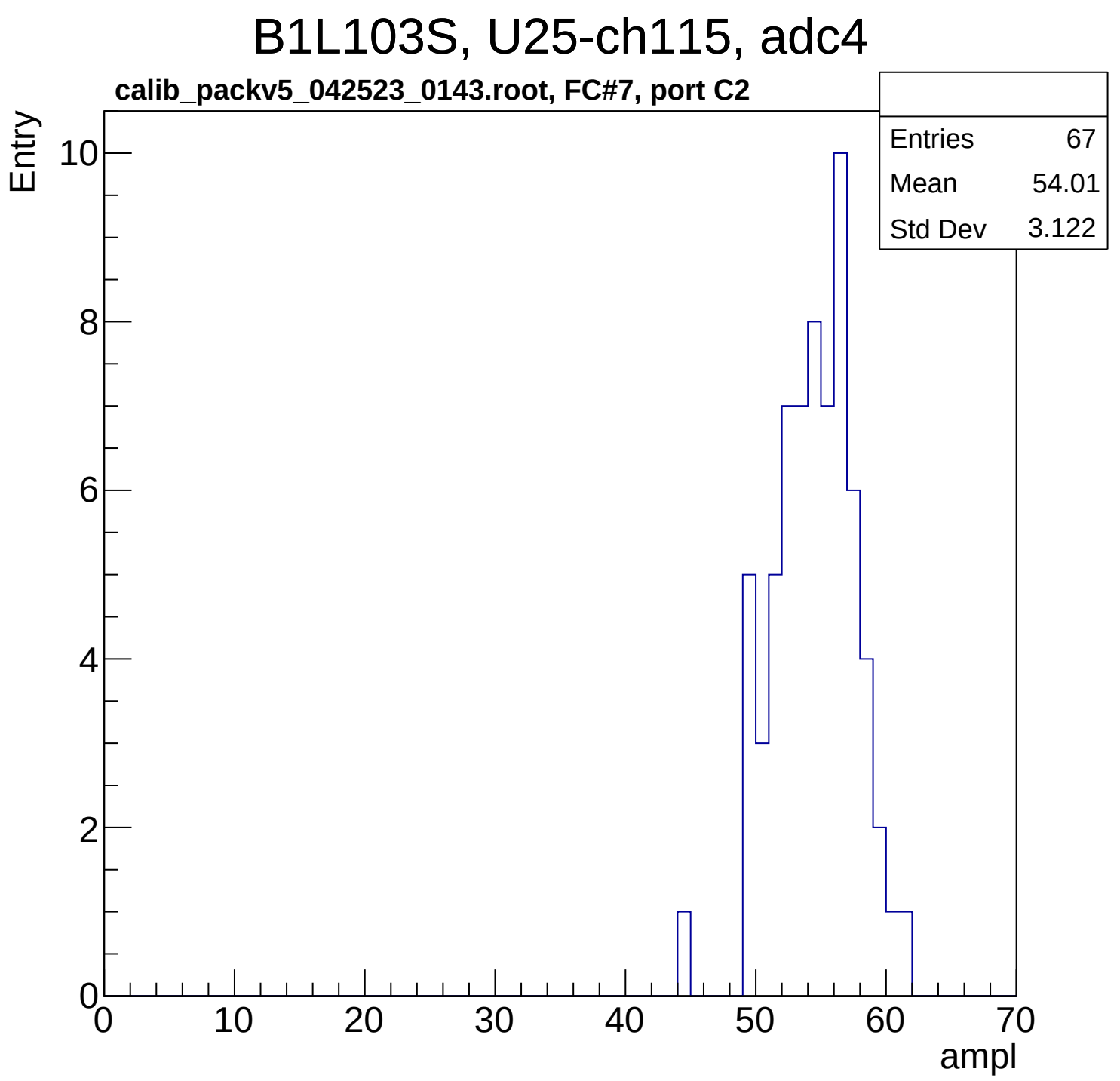
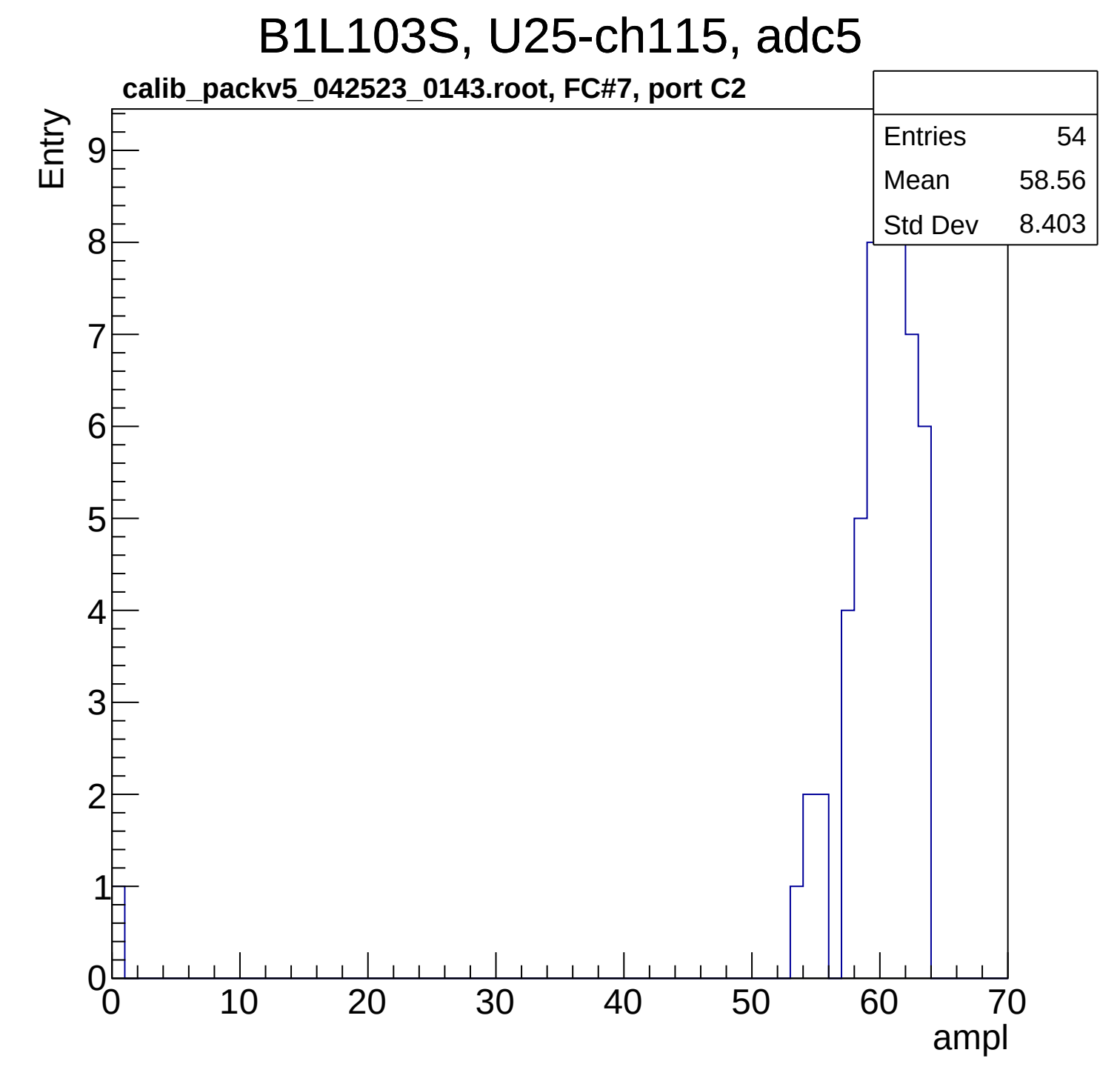

B1L103S, U25-ch115, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 54.01
Std Dev 3.122
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch115, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 58.56
Std Dev 8.403
Entry
ampl
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2
3
4
5
6
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0
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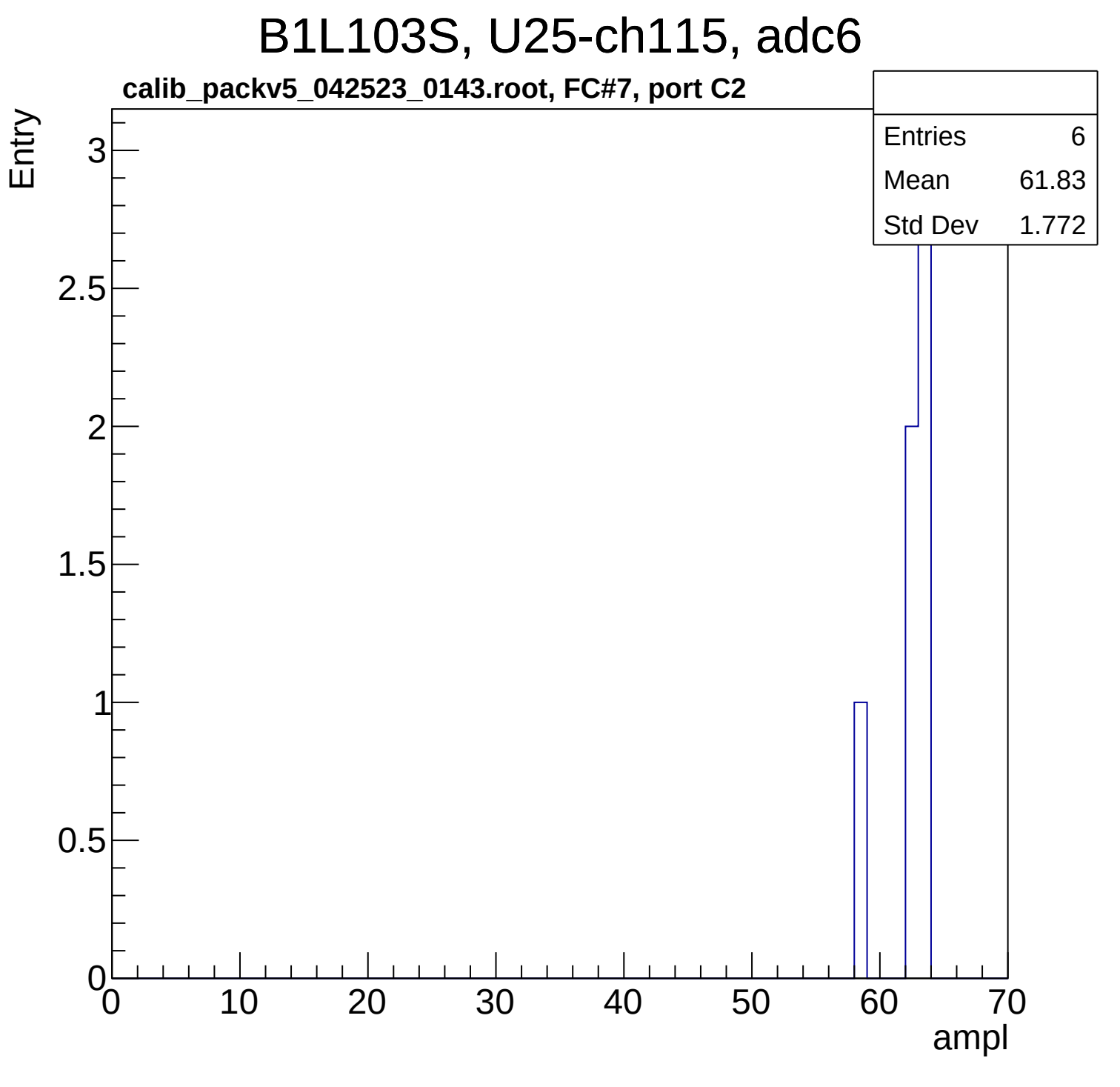
B1L103S, U25-ch115, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.83
Std Dev 1.772
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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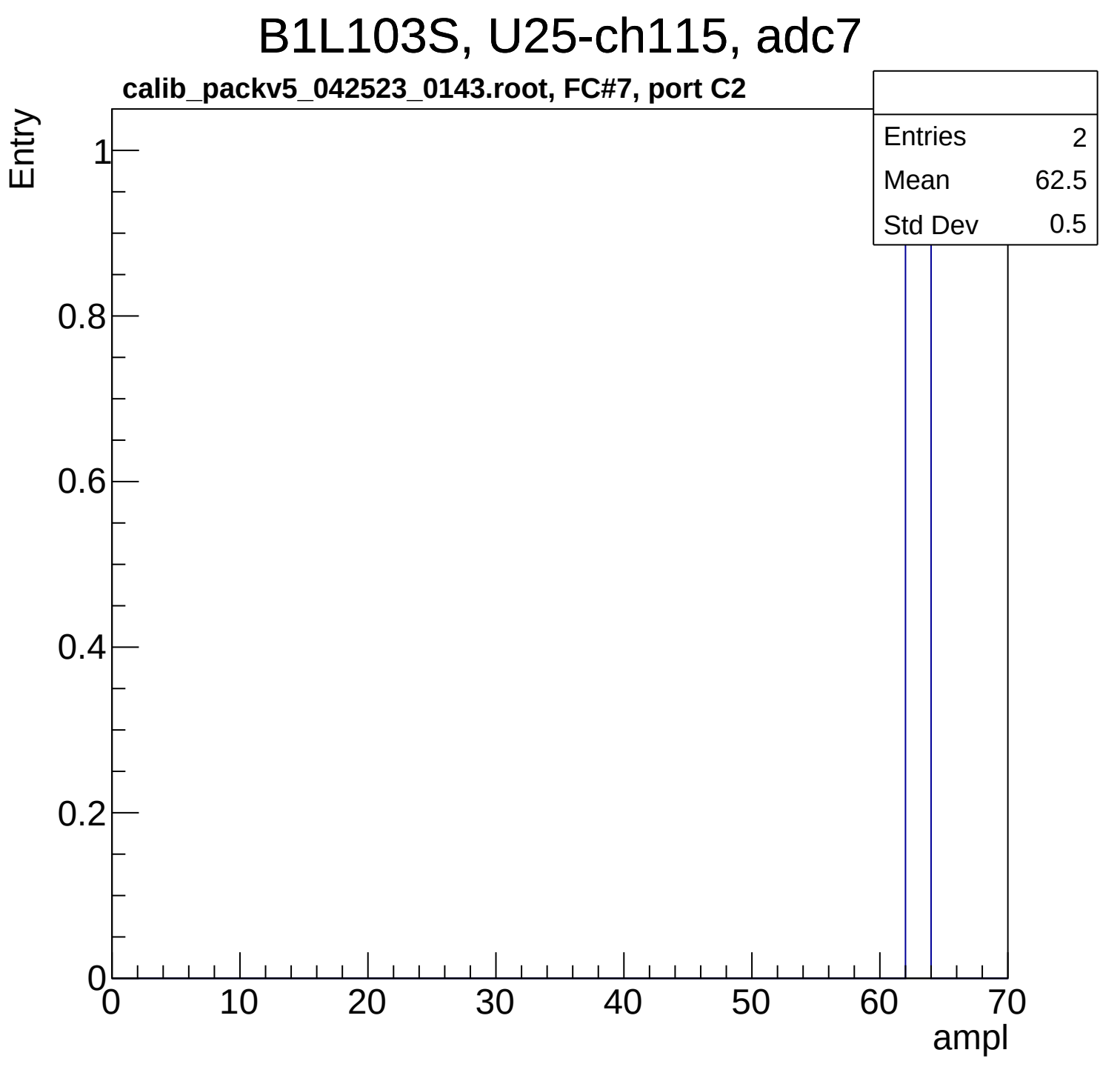
B1L103S, U25-ch115, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.8
0.6
0.4
0.2
0
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10
20
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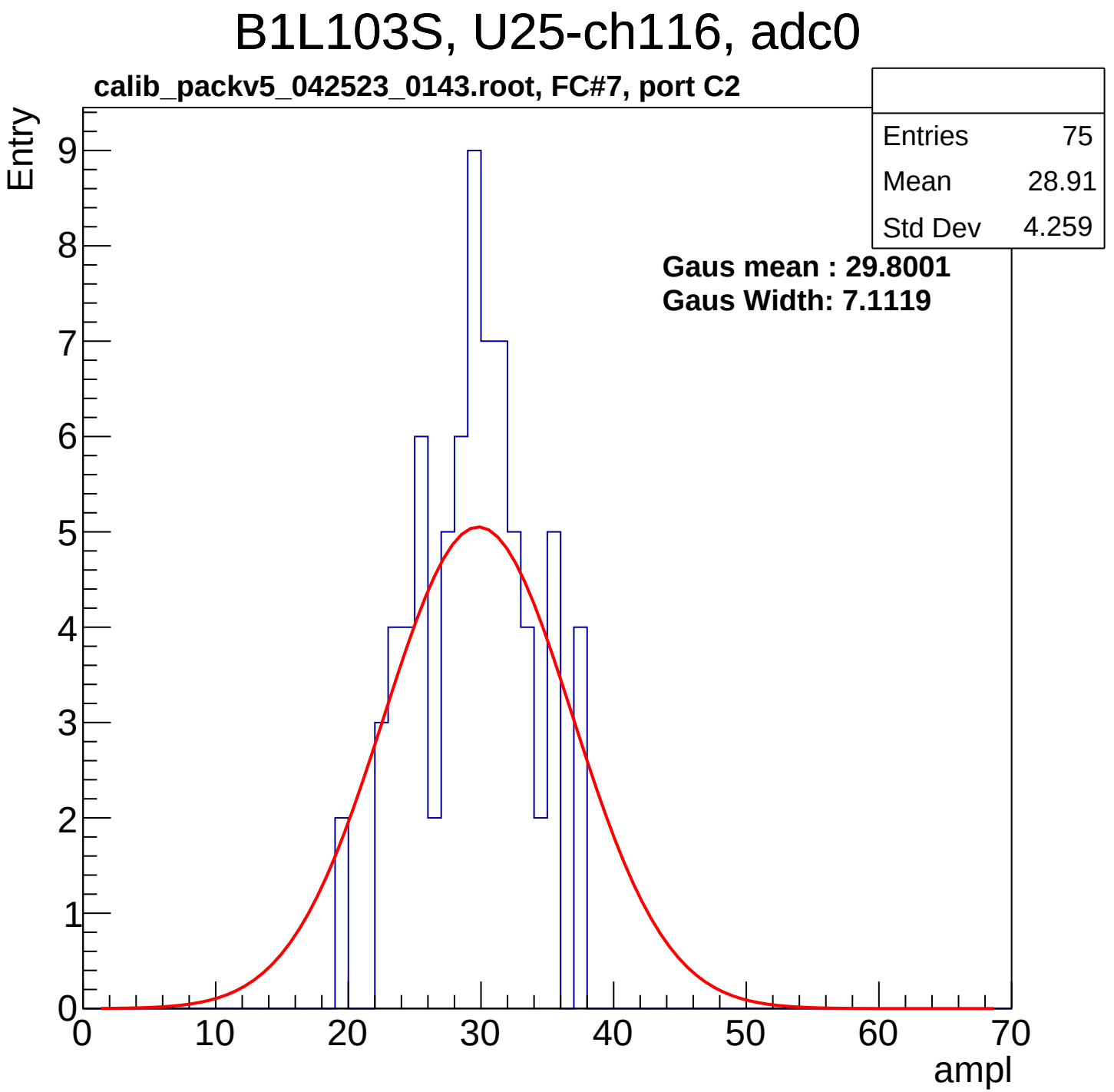
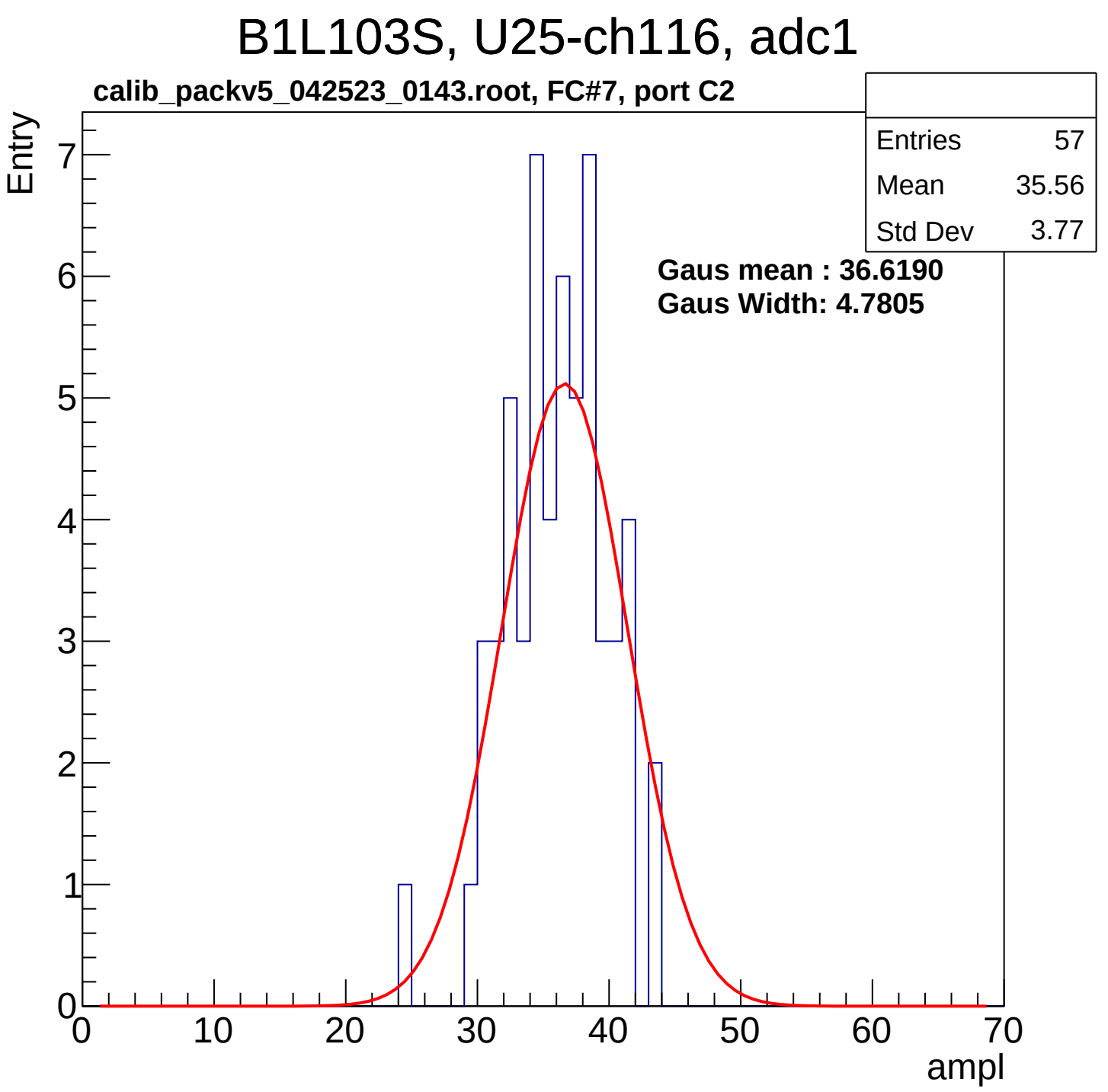
40
50
60
70
ampl


B1L103S, U25-ch116, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 75
Mean 28.91
Std Dev 4.259
Gaus mean : 29.8001
Gaus Width: 7.1119
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch116, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 35.56
Std Dev 3.77
Gaus mean : 36.6190
Gaus Width: 4.7805
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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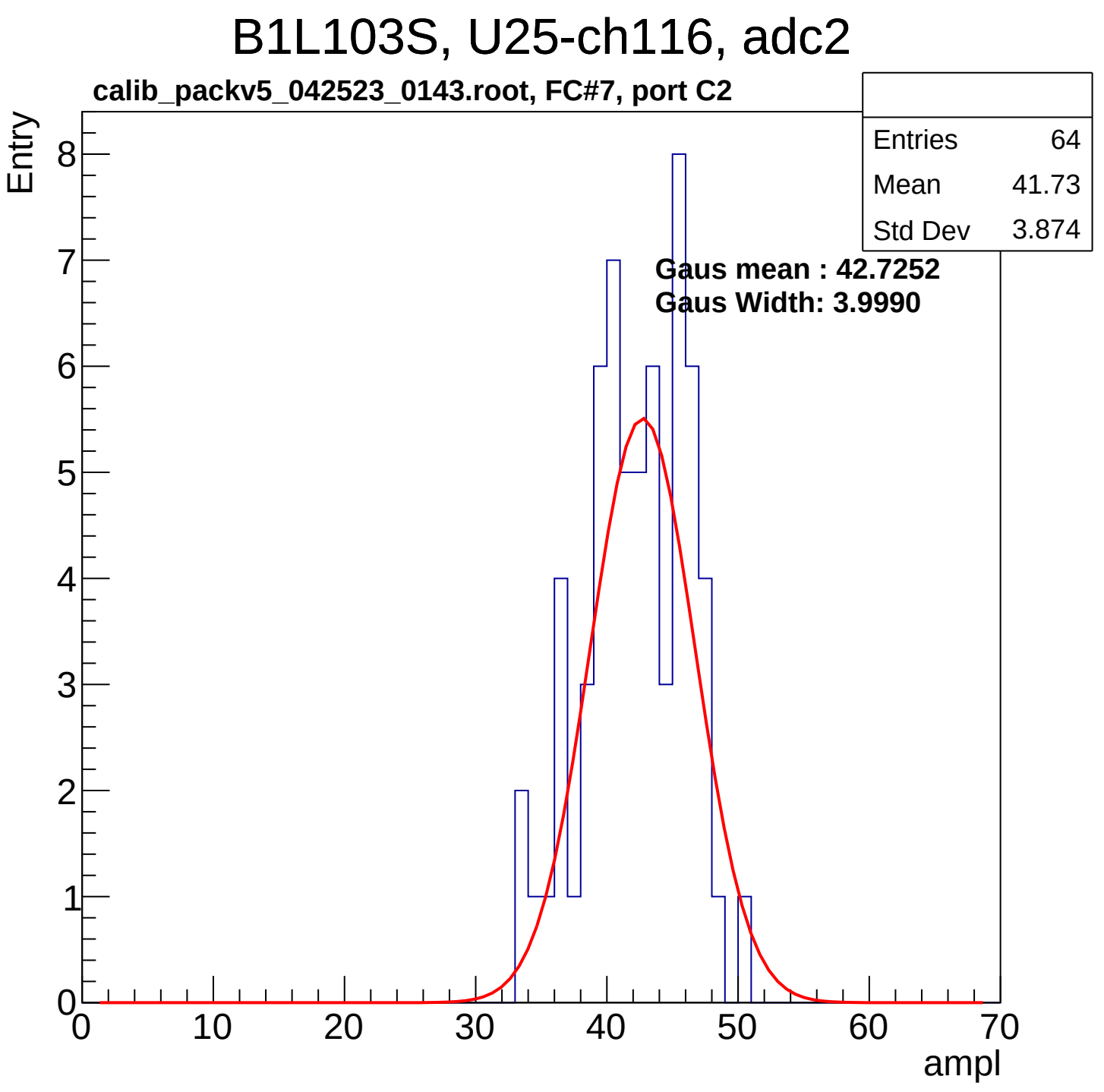
B1L103S, U25-ch116, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 41.73
Std Dev 3.874
Gaus mean : 42.7252
Gaus Width: 3.9990
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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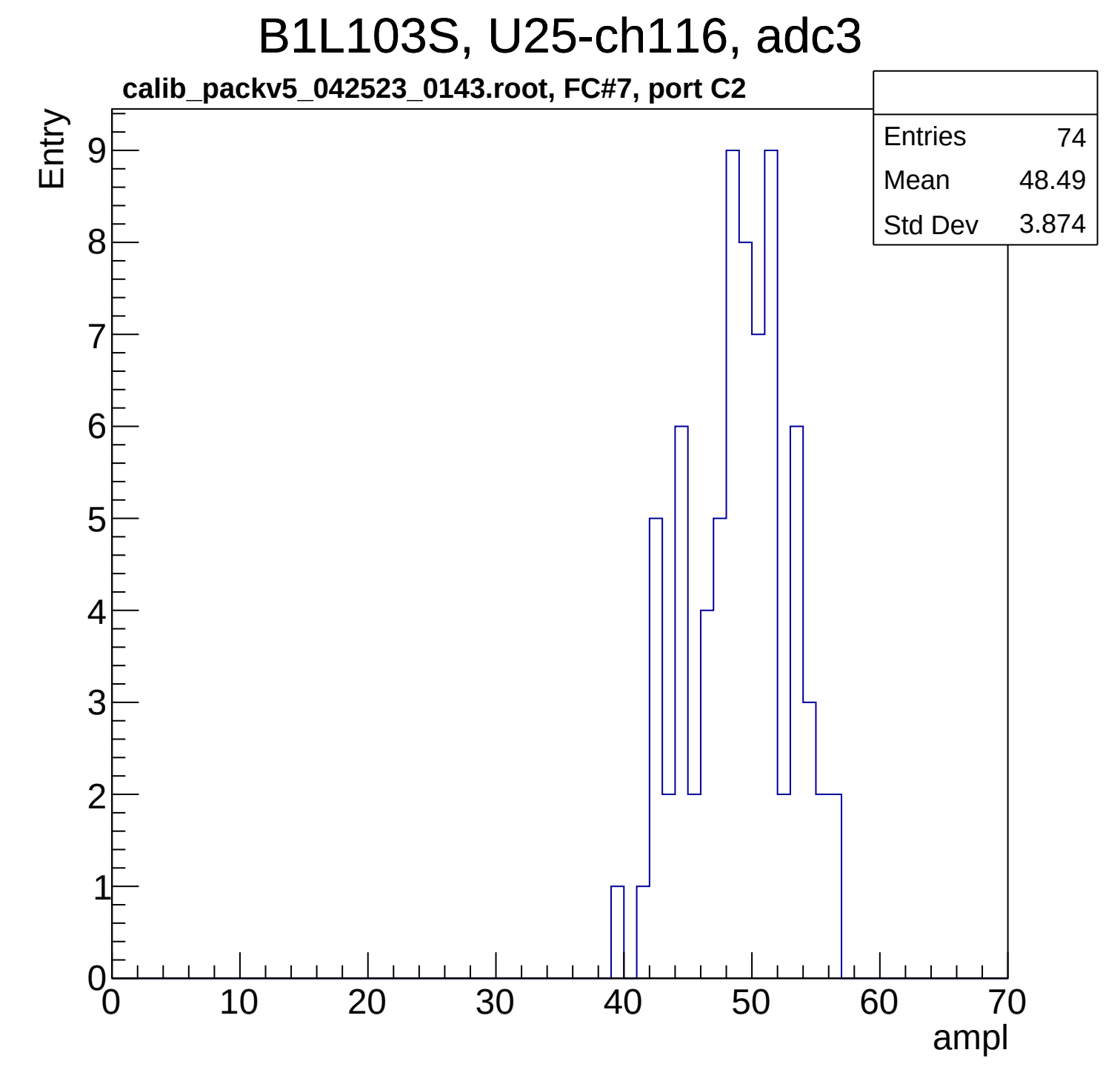
B1L103S, U25-ch116, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 74
Mean 48.49
Std Dev 3.874
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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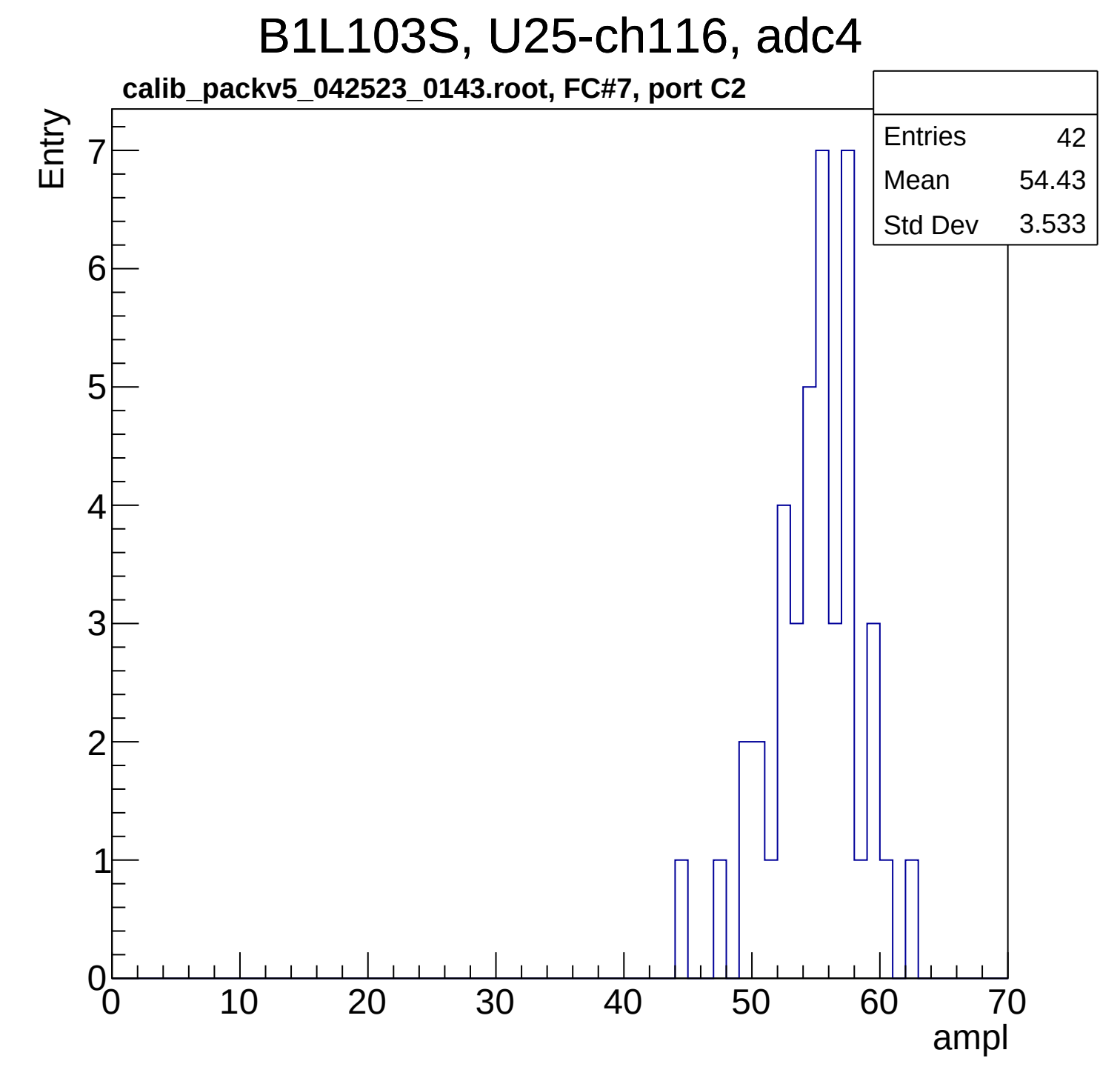
B1L103S, U25-ch116, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 54.43
Std Dev 3.533
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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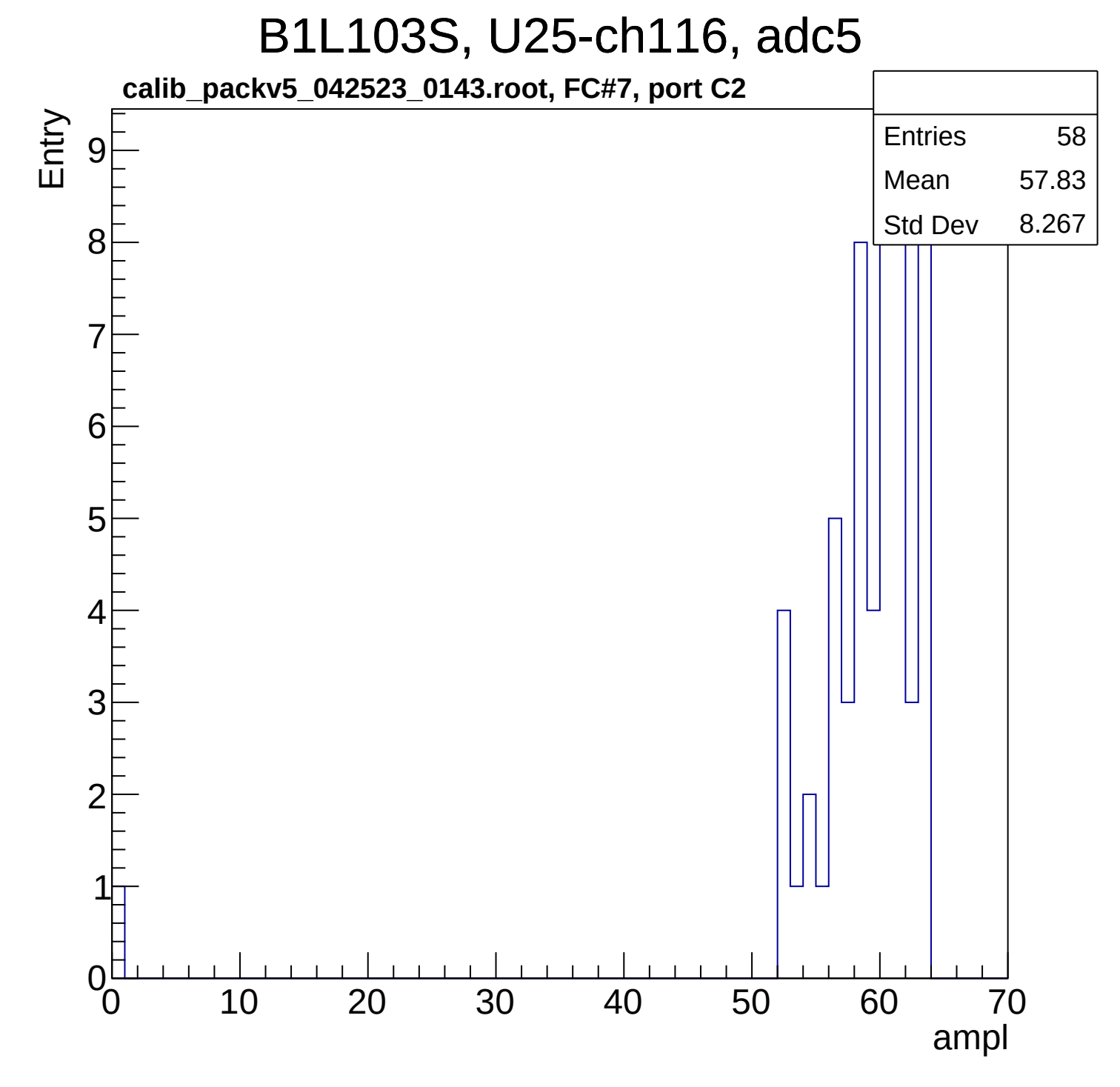
B1L103S, U25-ch116, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 58
Mean 57.83
Std Dev 8.267
Entry
ampl
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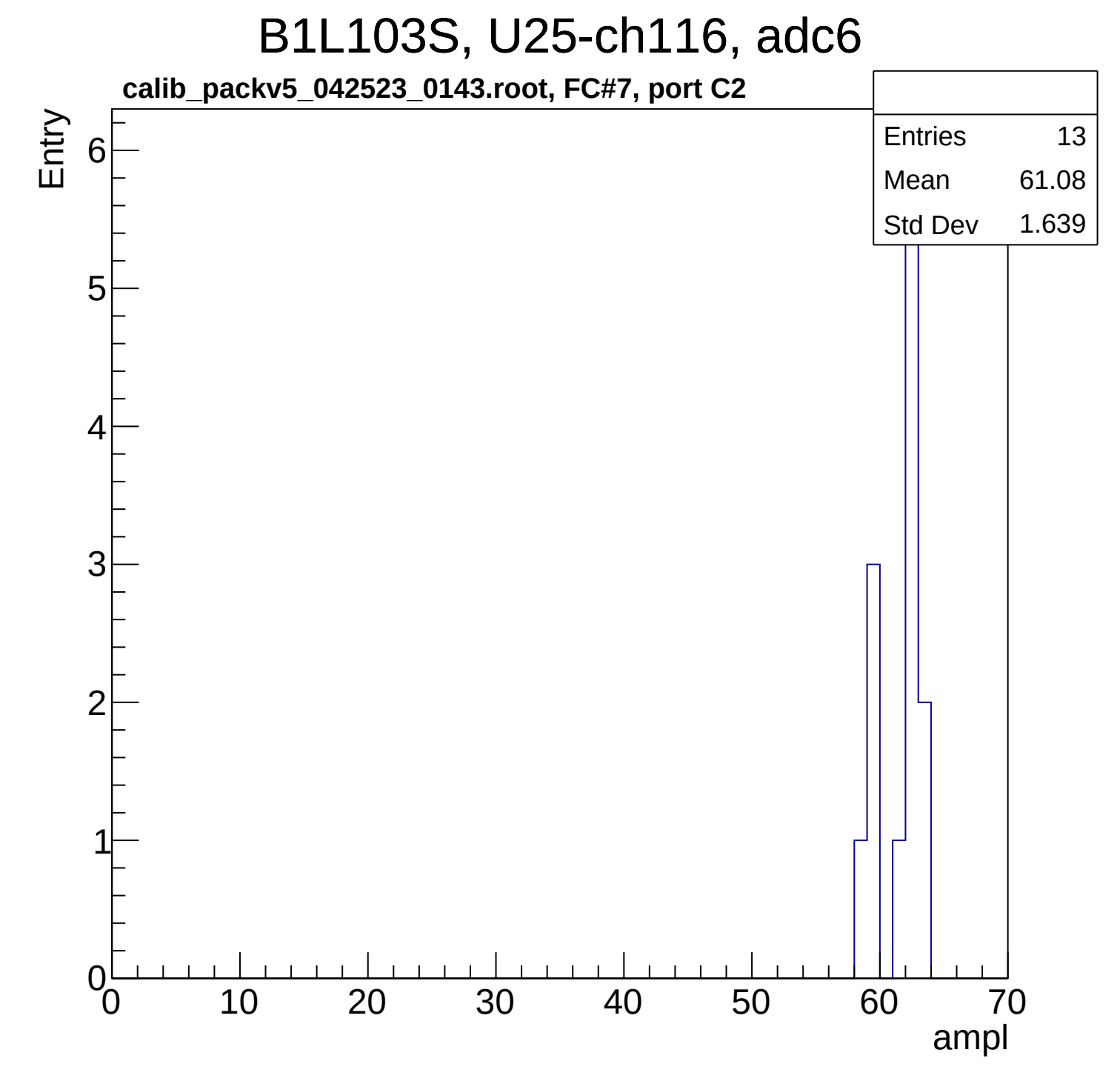
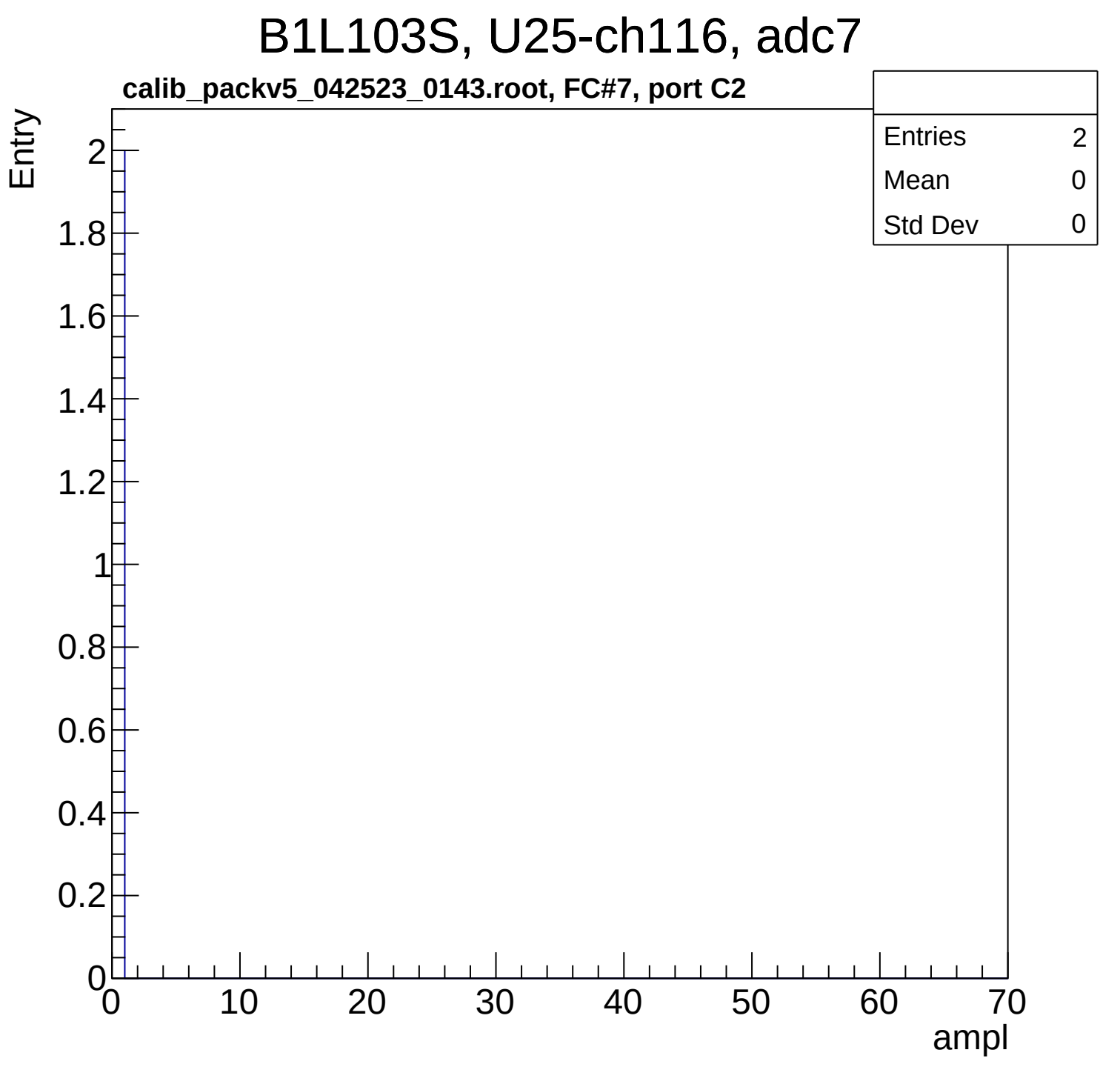
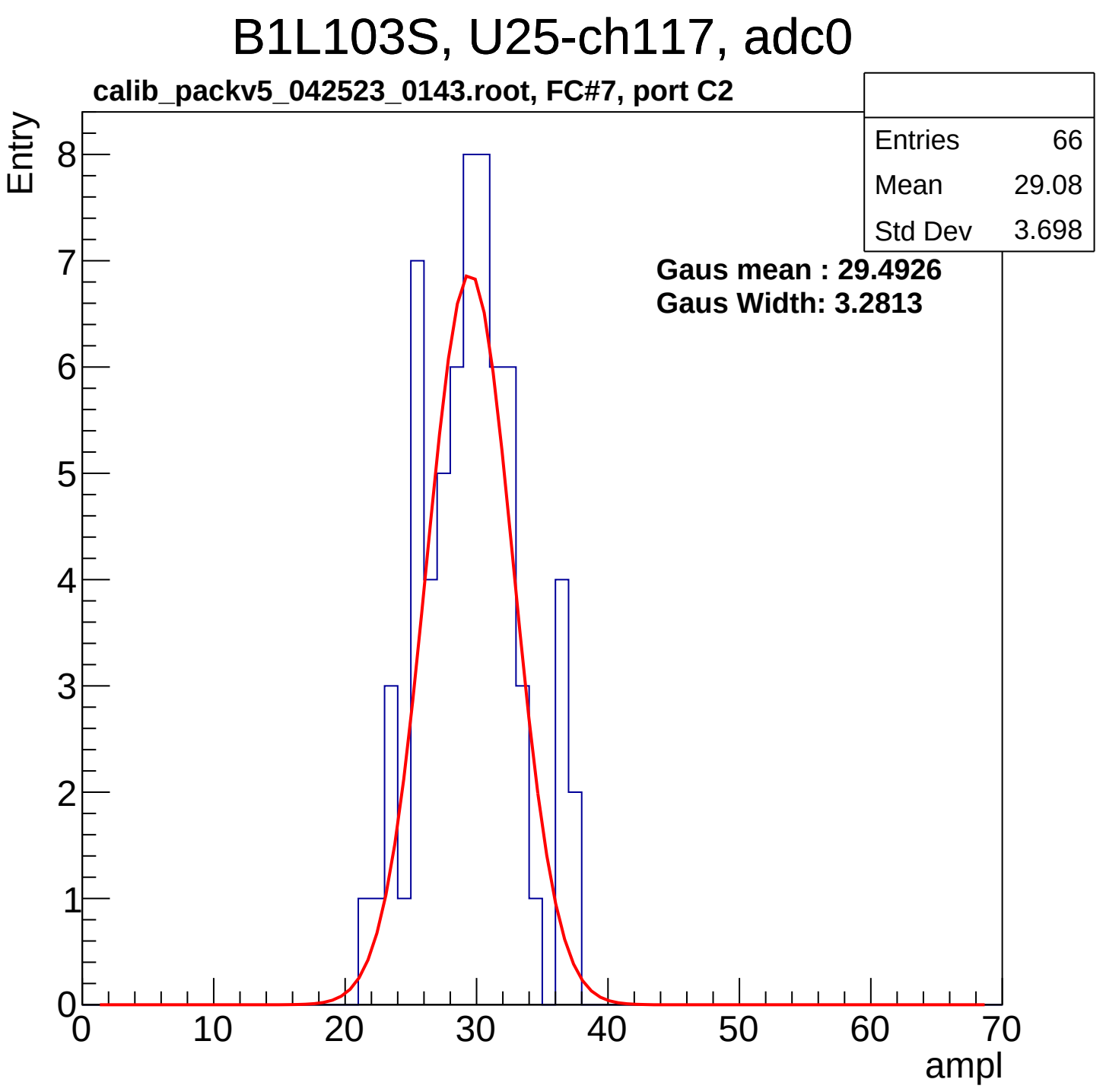
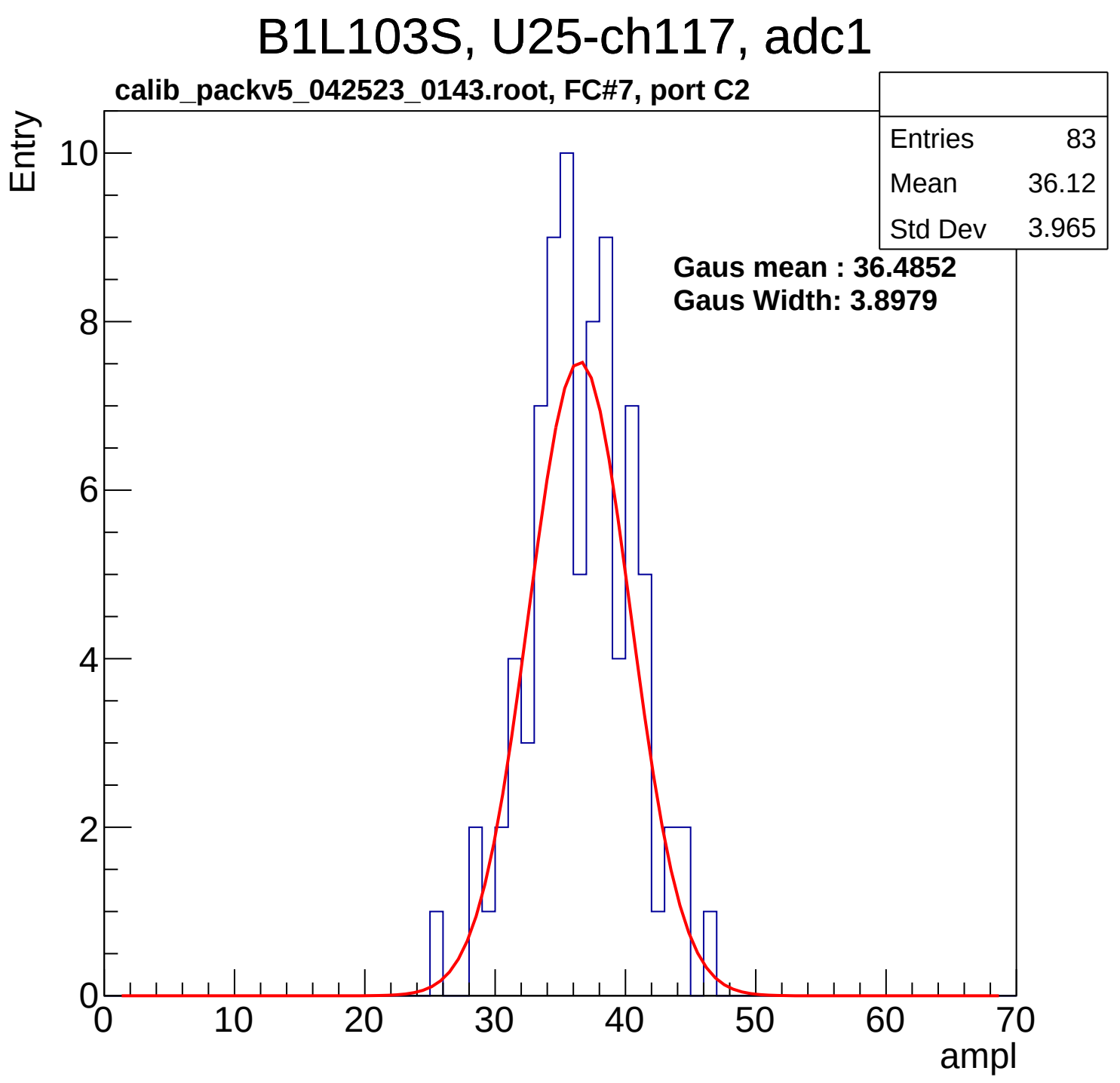
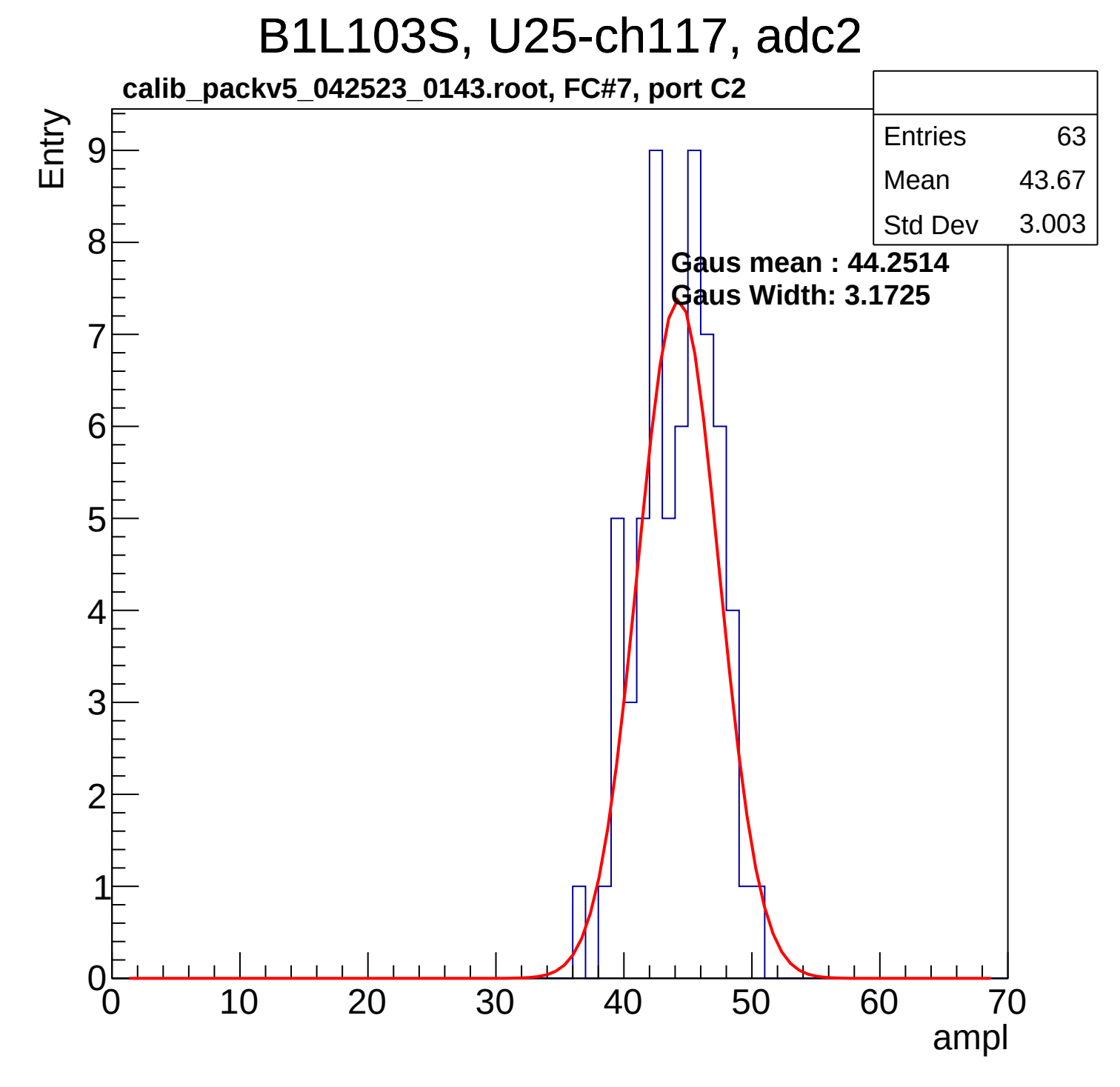
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch116, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 61.08
Std Dev 1.639
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch116, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch117, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 29.08
Std Dev 3.698
Gaus mean : 29.4926
Gaus Width: 3.2813
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch117, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 83
Mean 36.12
Std Dev 3.965
Gaus mean : 36.4852
Gaus Width: 3.8979
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch117, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.67
Std Dev 3.003
Gaus mean : 44.2514
Gaus Width: 3.1725
Entry
ampl
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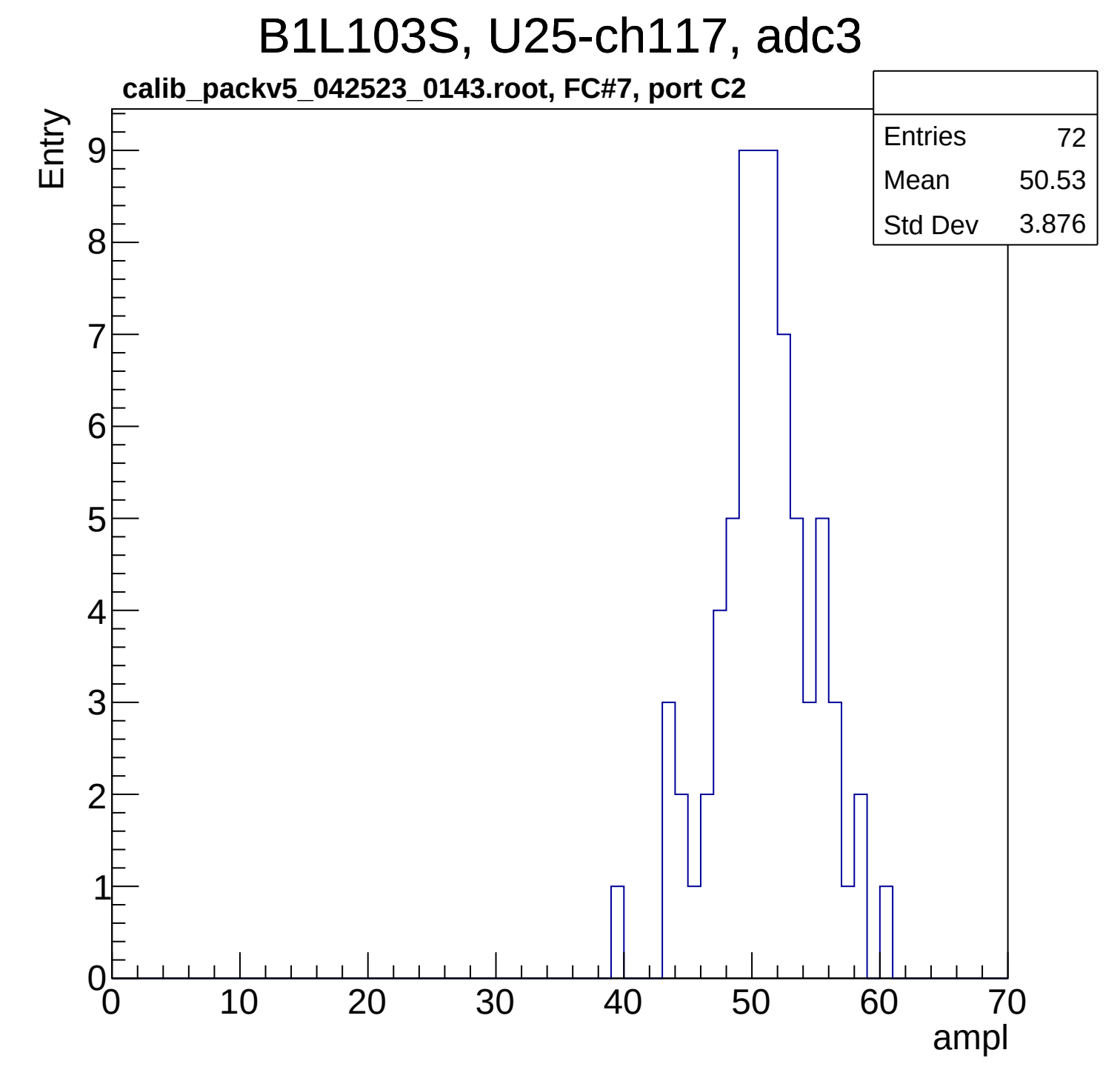
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch117, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 50.53
Std Dev 3.876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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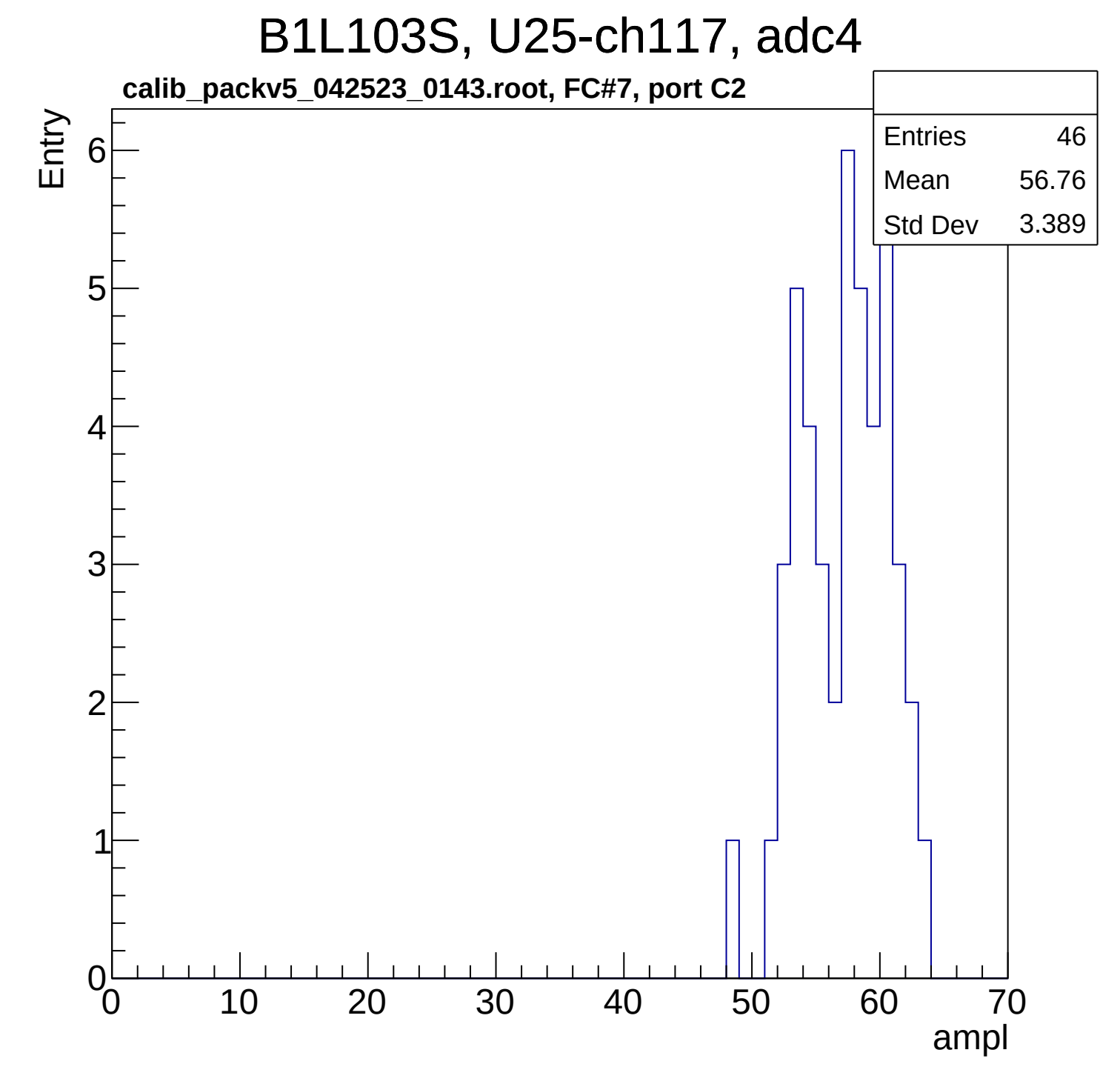
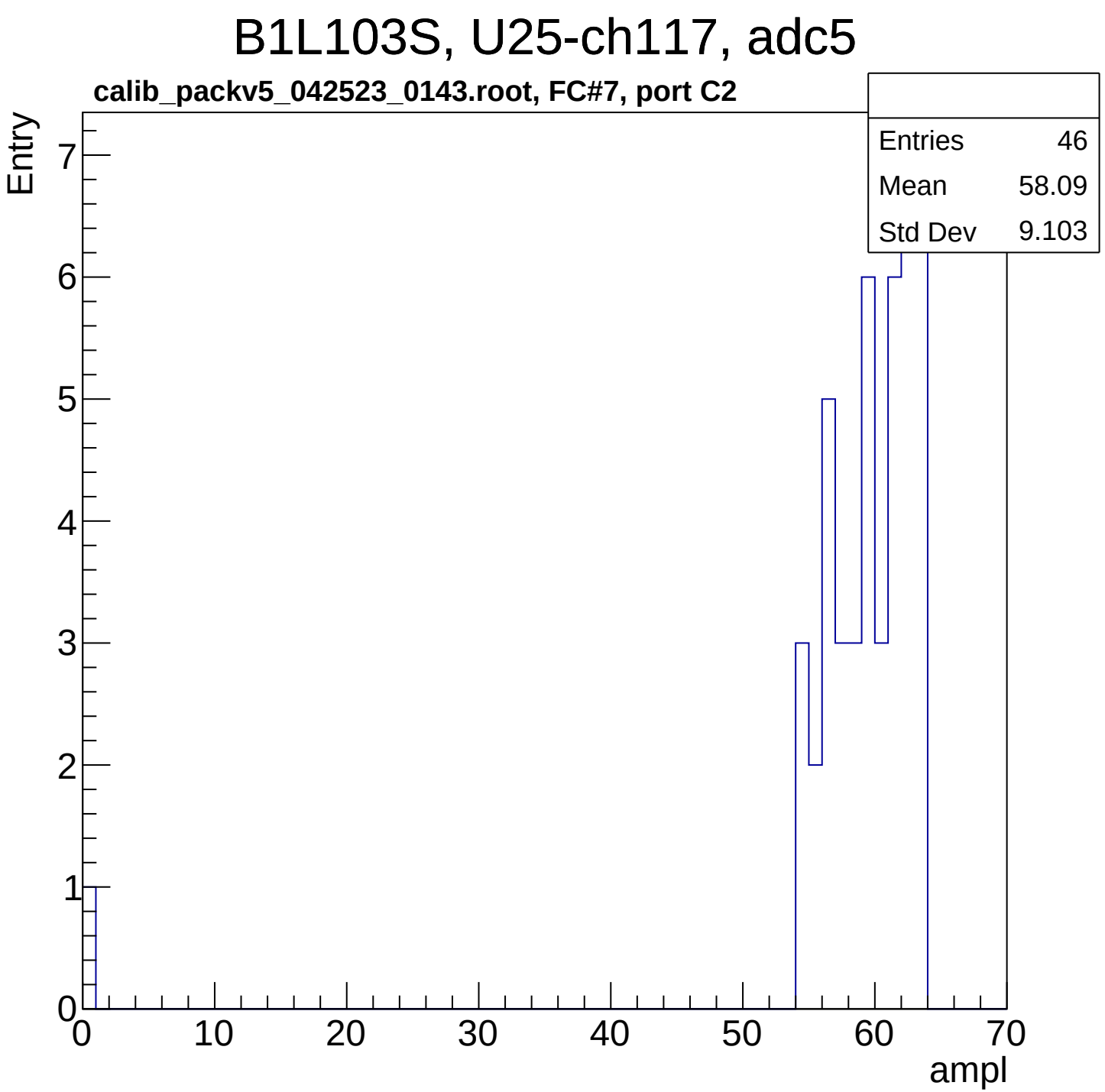

B1L103S, U25-ch117, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 56.76
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L103S, U25-ch117, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 46
Mean 58.09
Std Dev 9.103
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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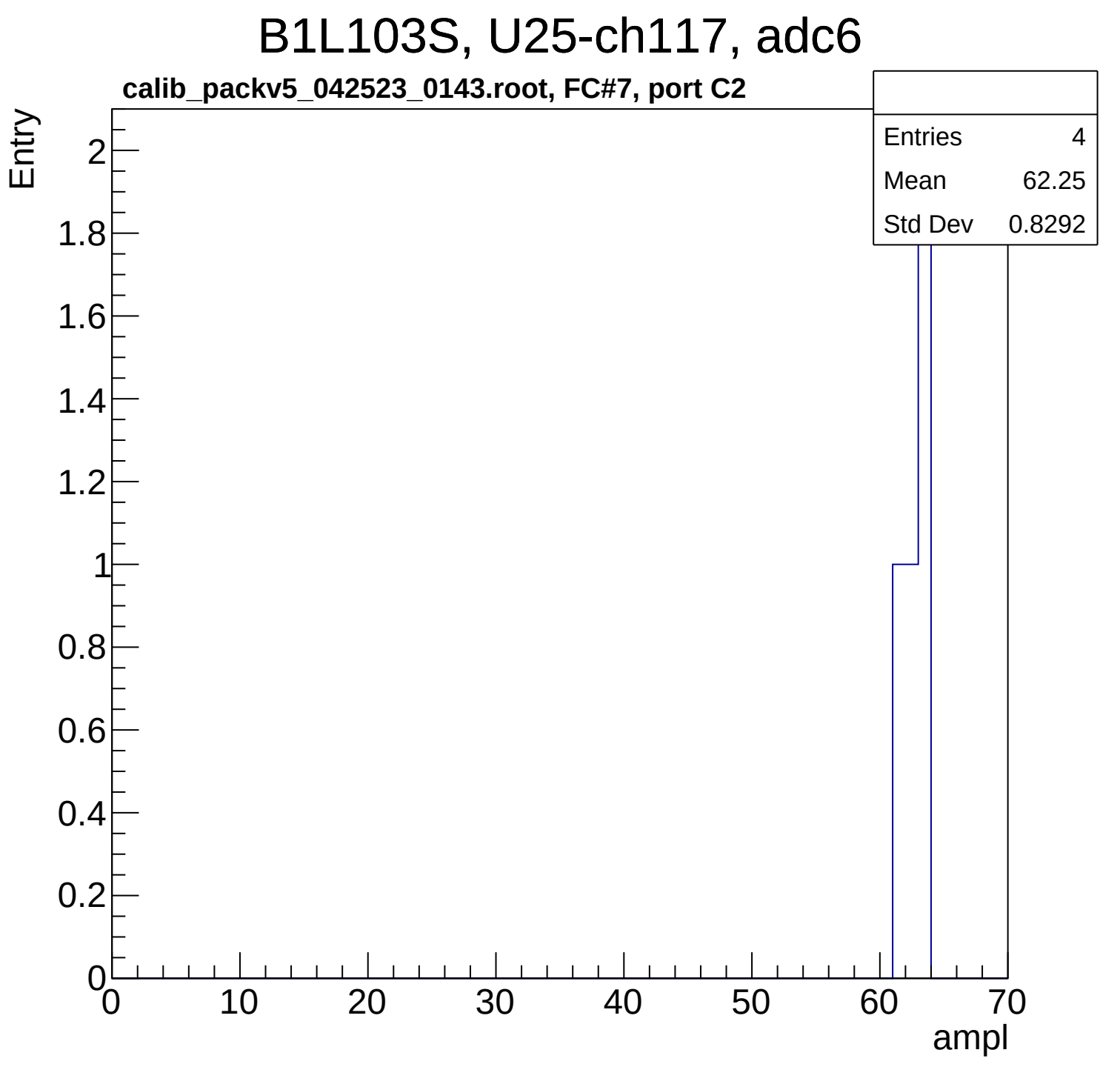
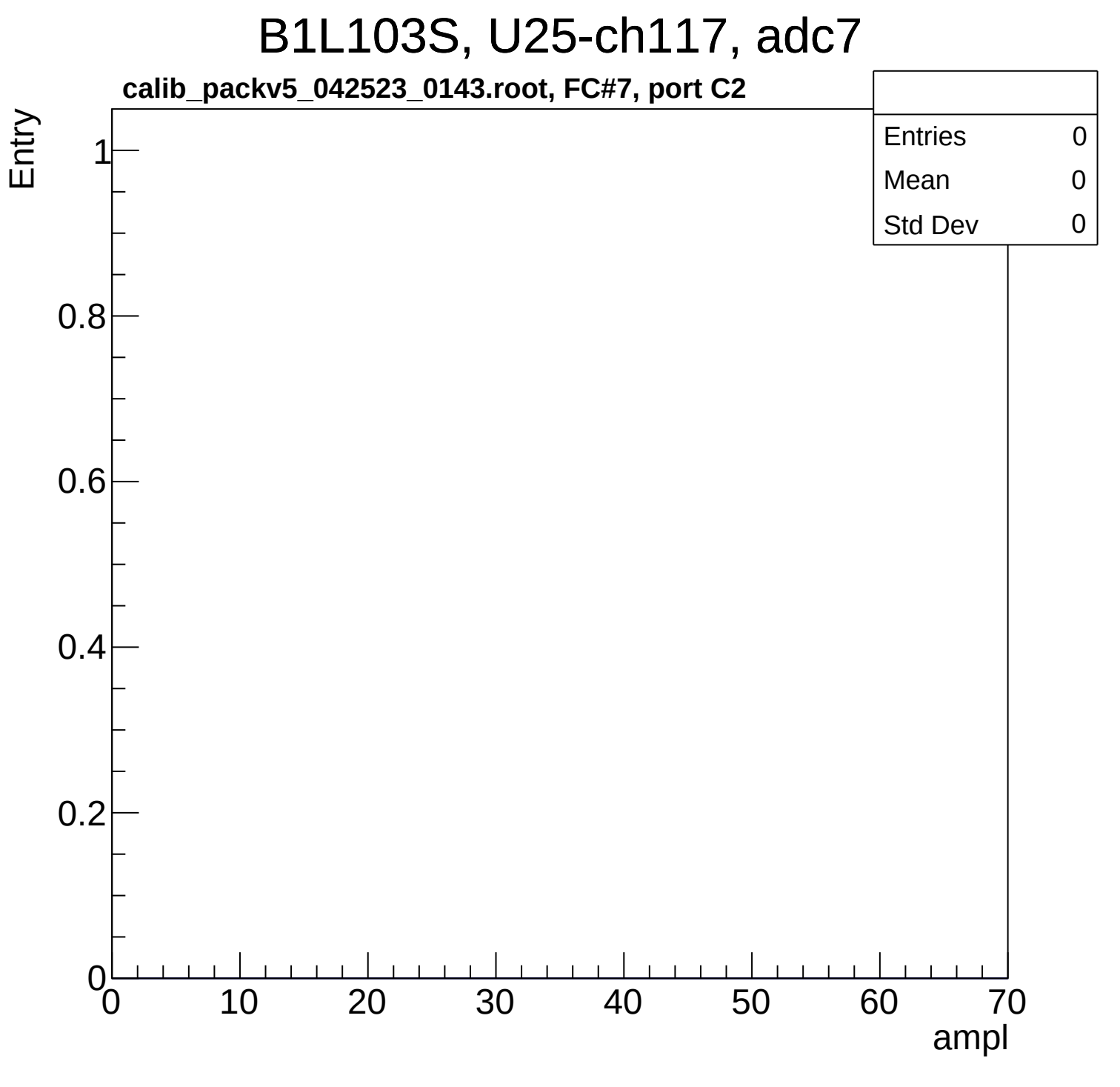
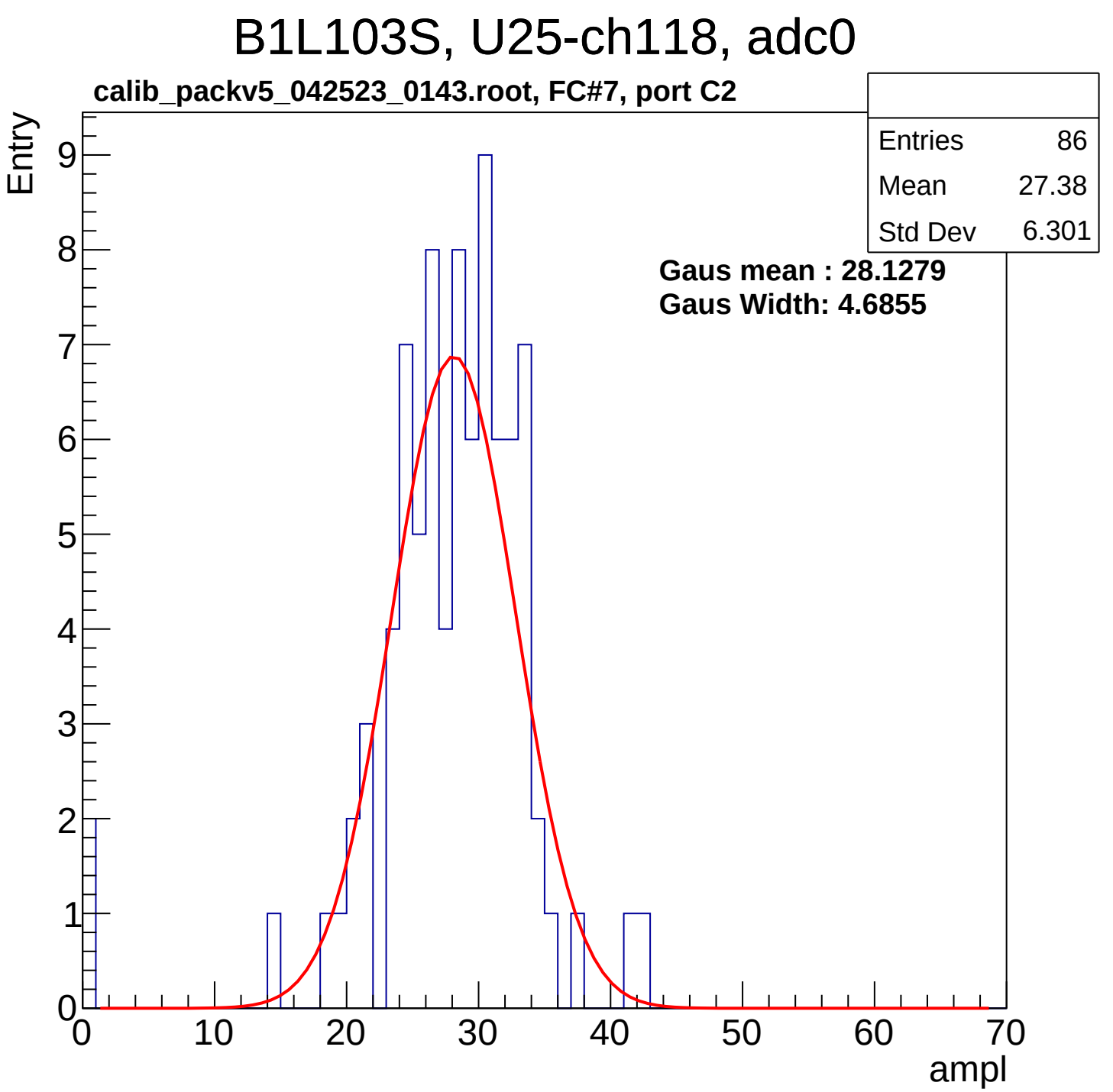
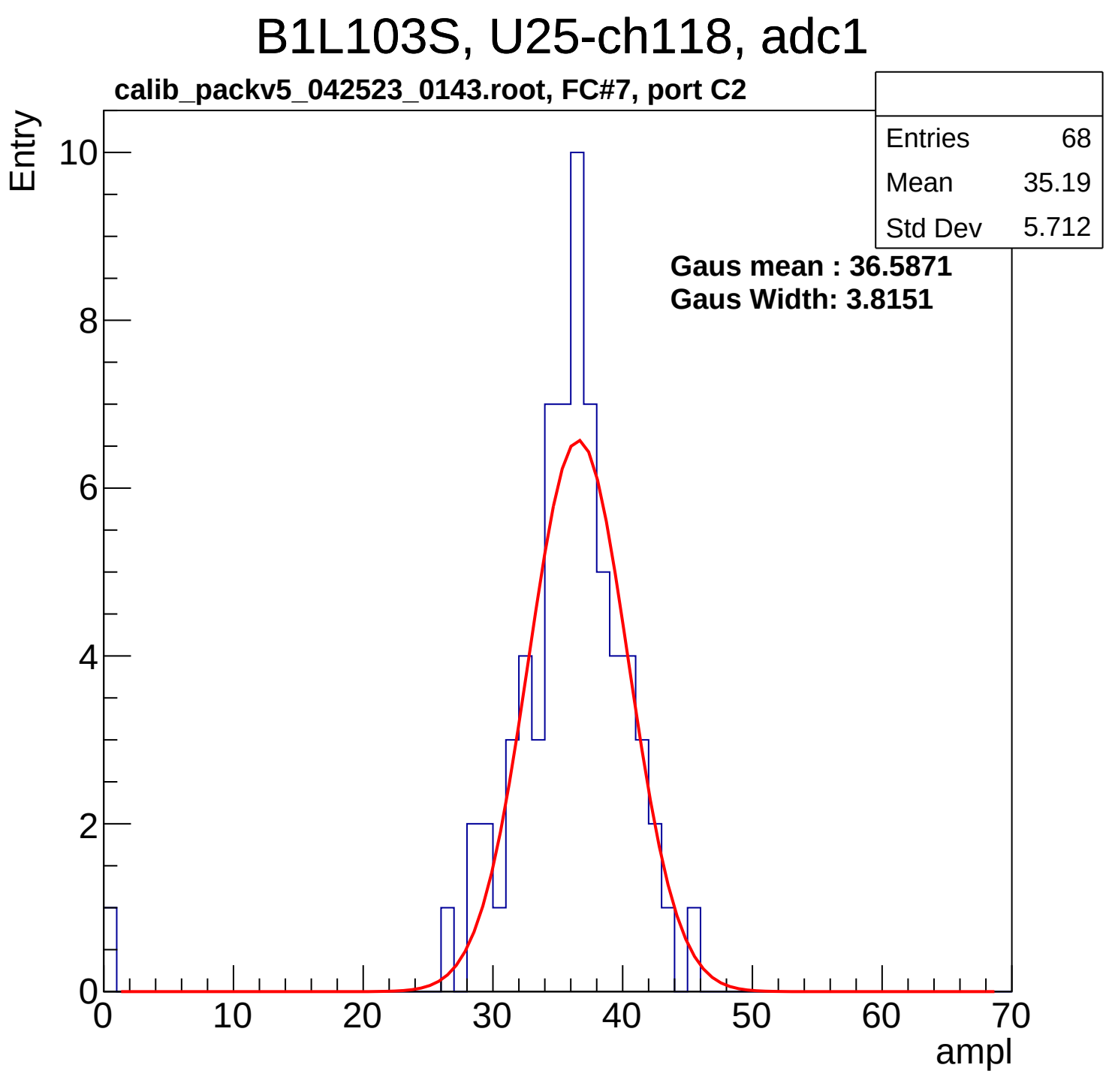
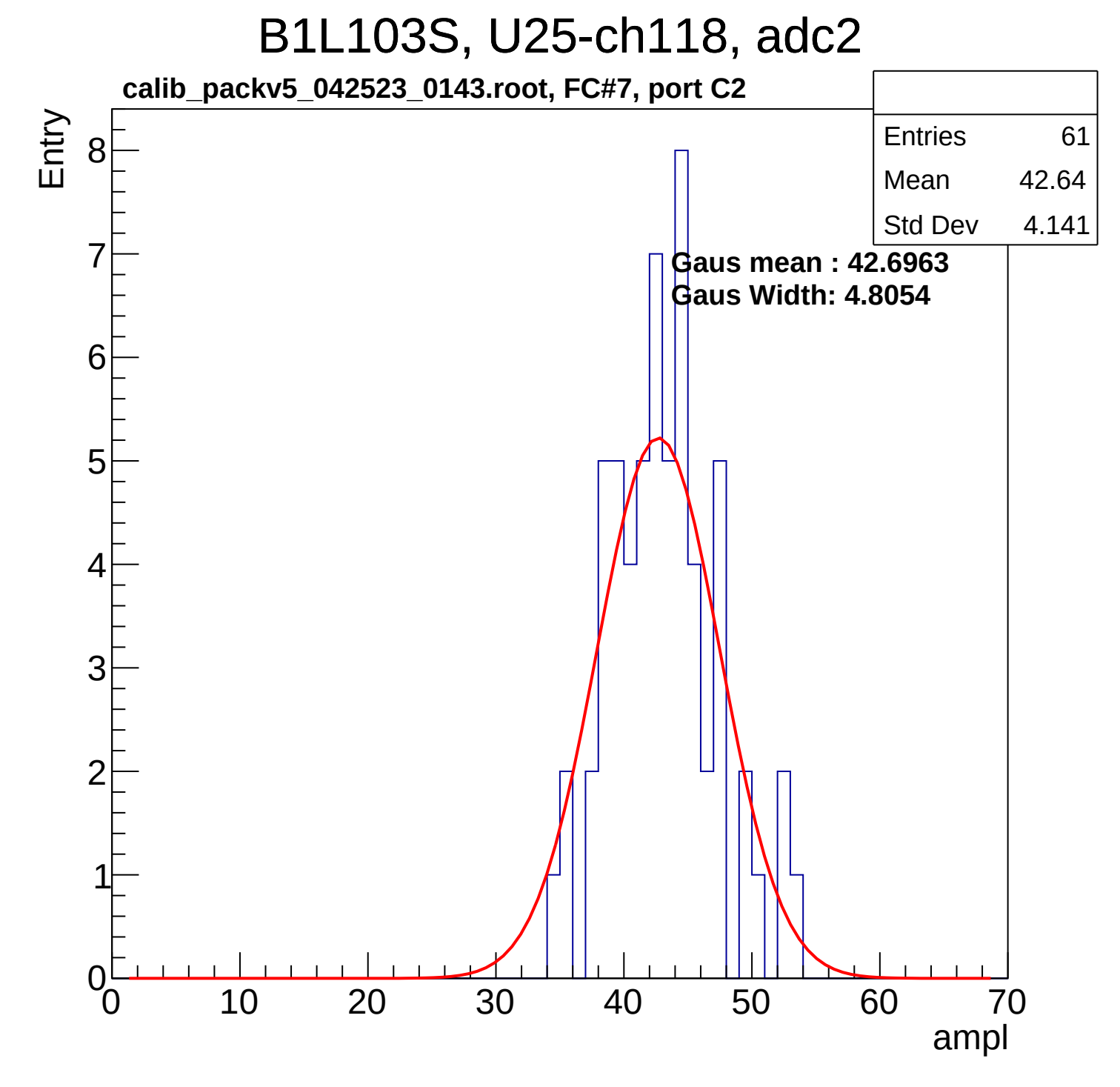

B1L103S, U25-ch117, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch117, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch118, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 86
Mean 27.38
Std Dev 6.301
Gaus mean : 28.1279
Gaus Width: 4.6855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 68
Mean 35.19
Std Dev 5.712
Gaus mean : 36.5871
Gaus Width: 3.8151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 61
Mean 42.64
Std Dev 4.141
Gaus mean : 42.6963
Gaus Width: 4.8054
Entry
ampl
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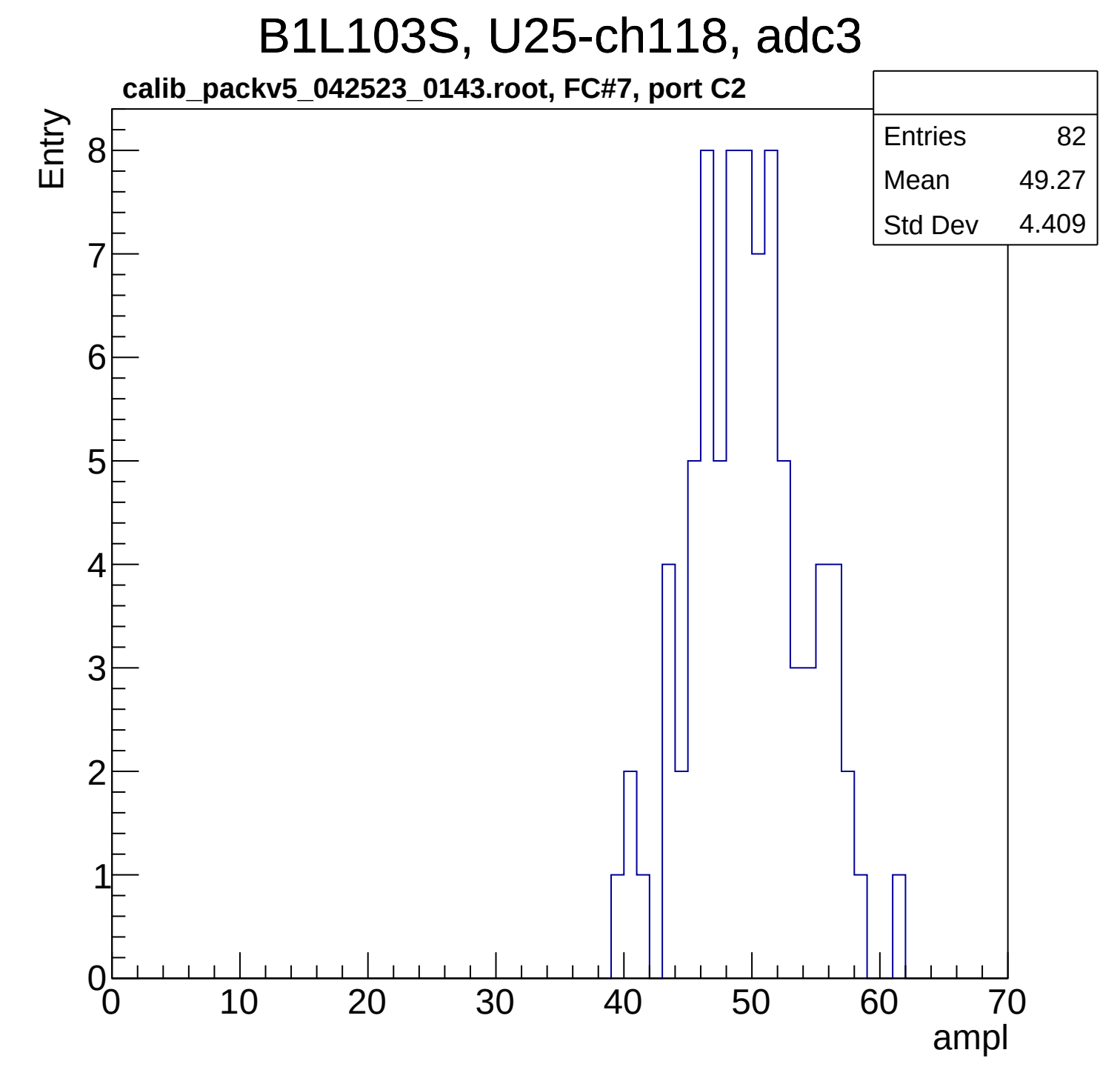
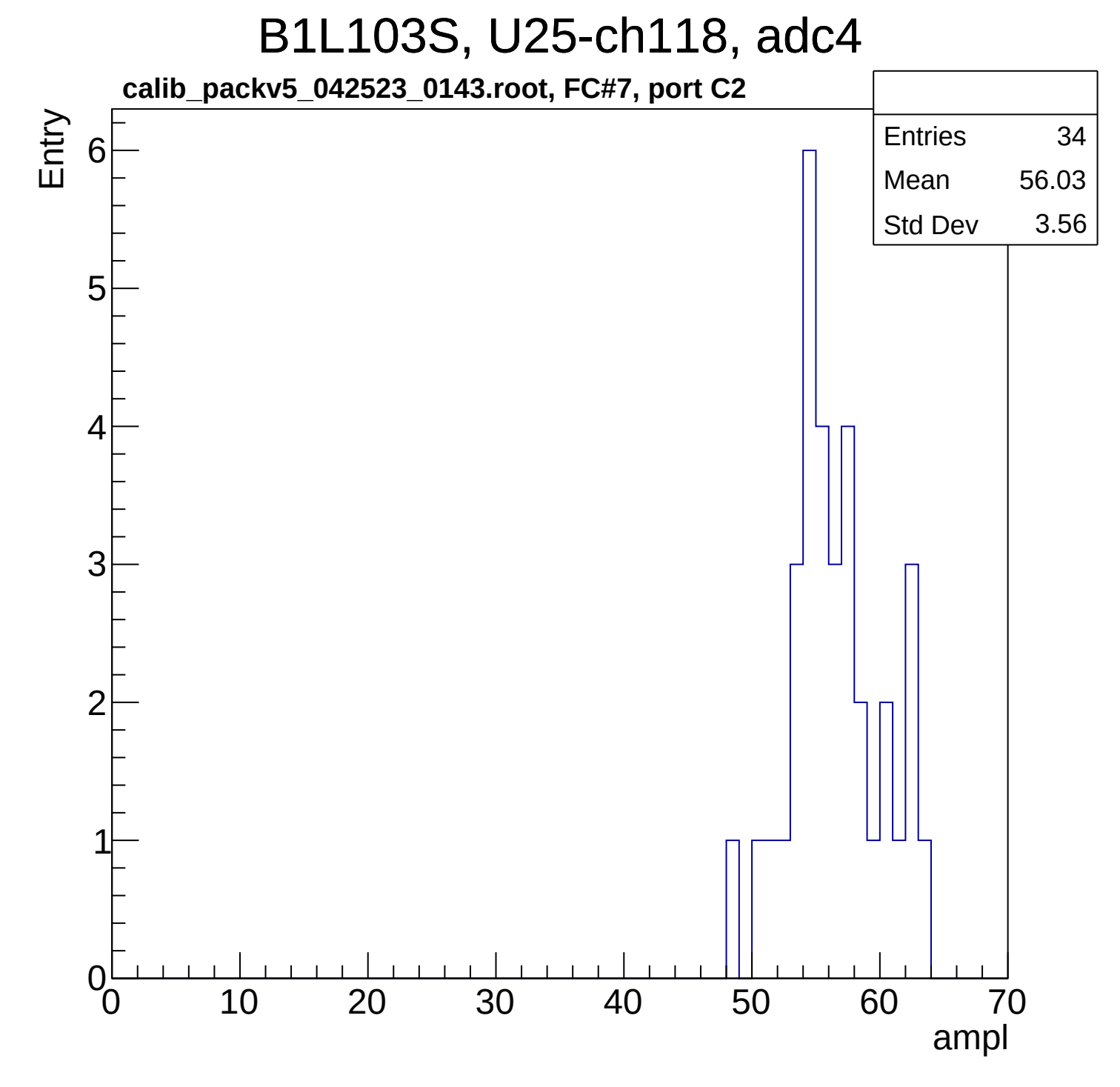
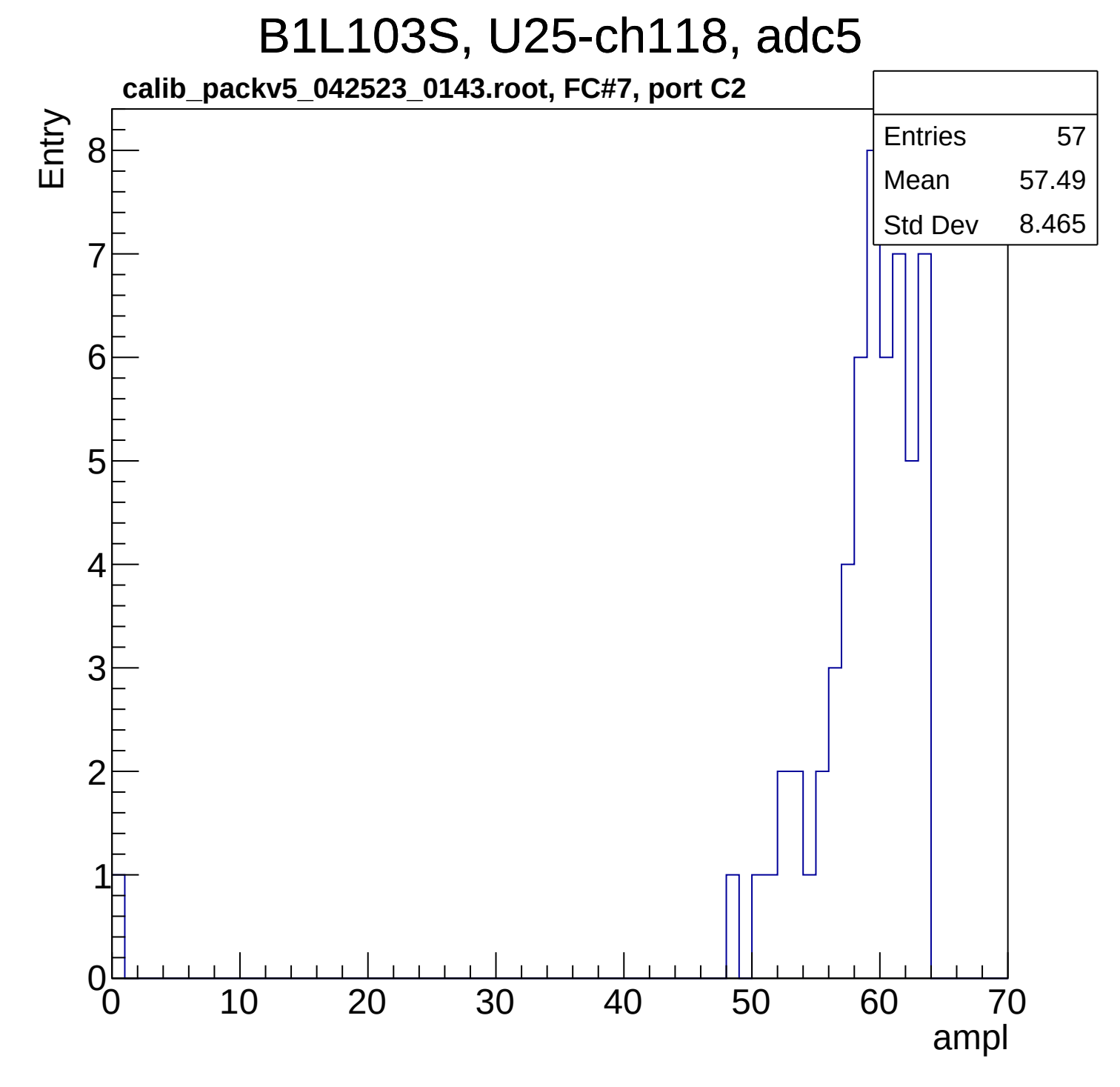
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 49.27
Std Dev 4.409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 34
Mean 56.03
Std Dev 3.56
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch118, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 57
Mean 57.49
Std Dev 8.465
Entry
ampl
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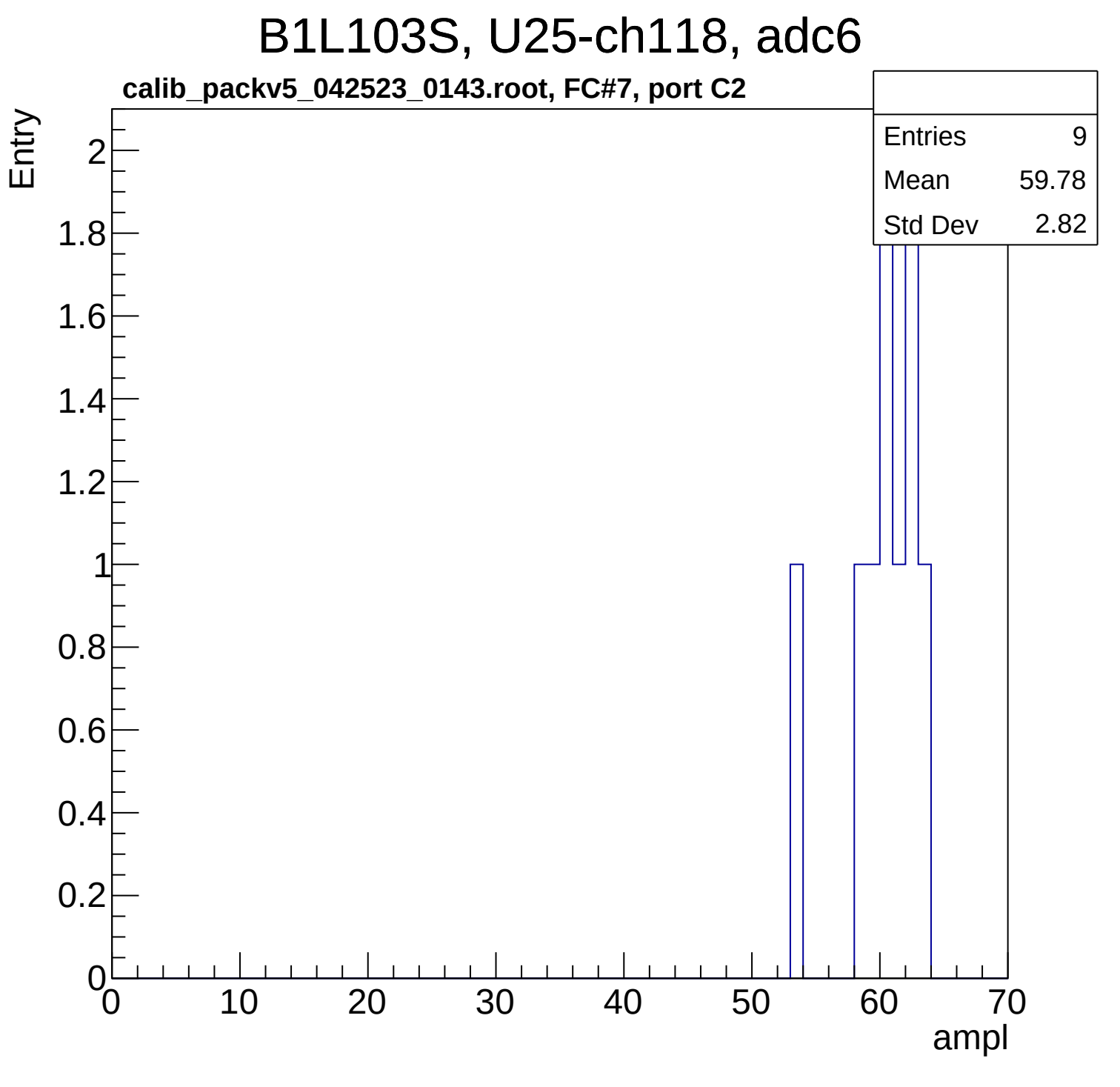
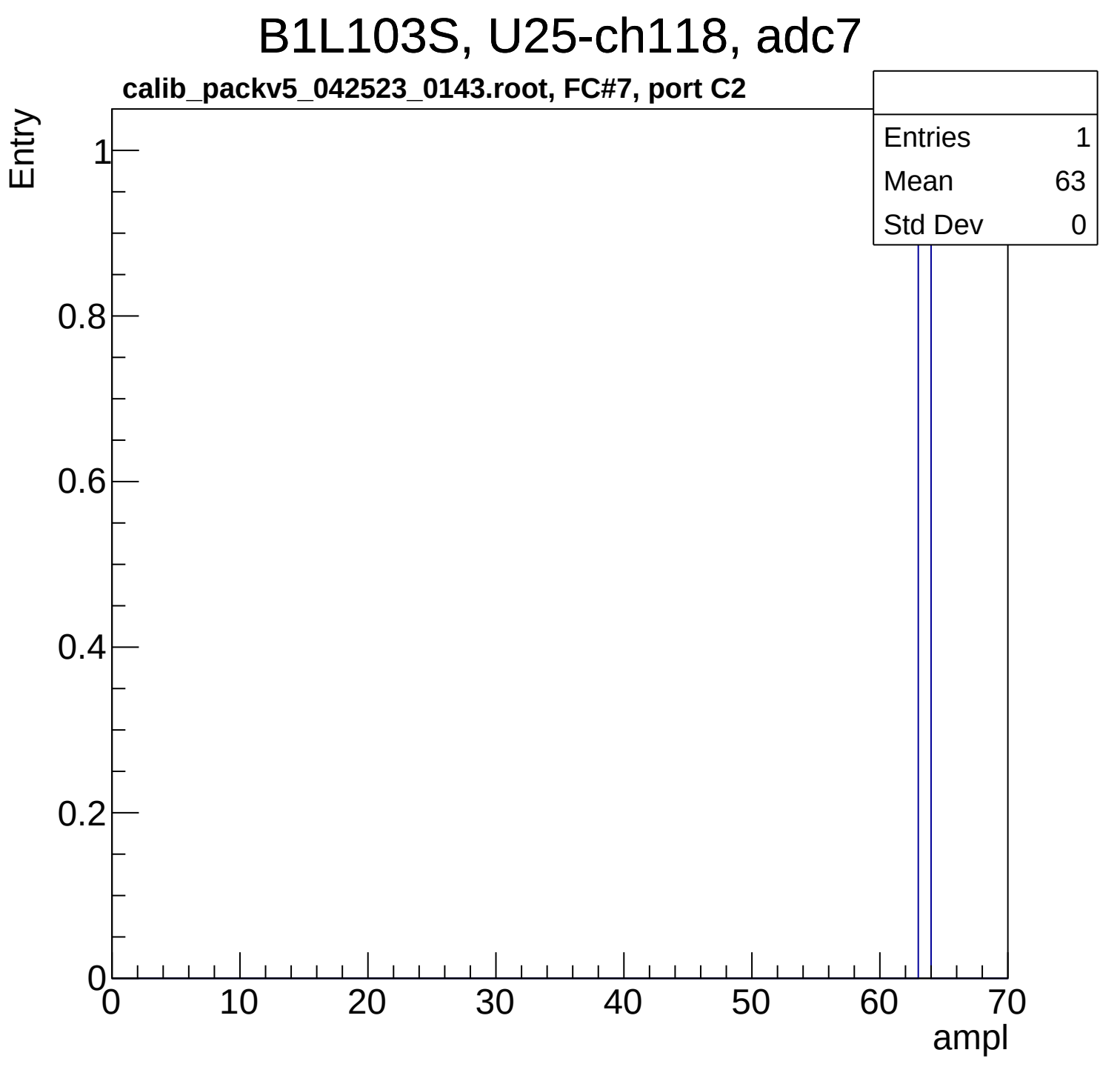
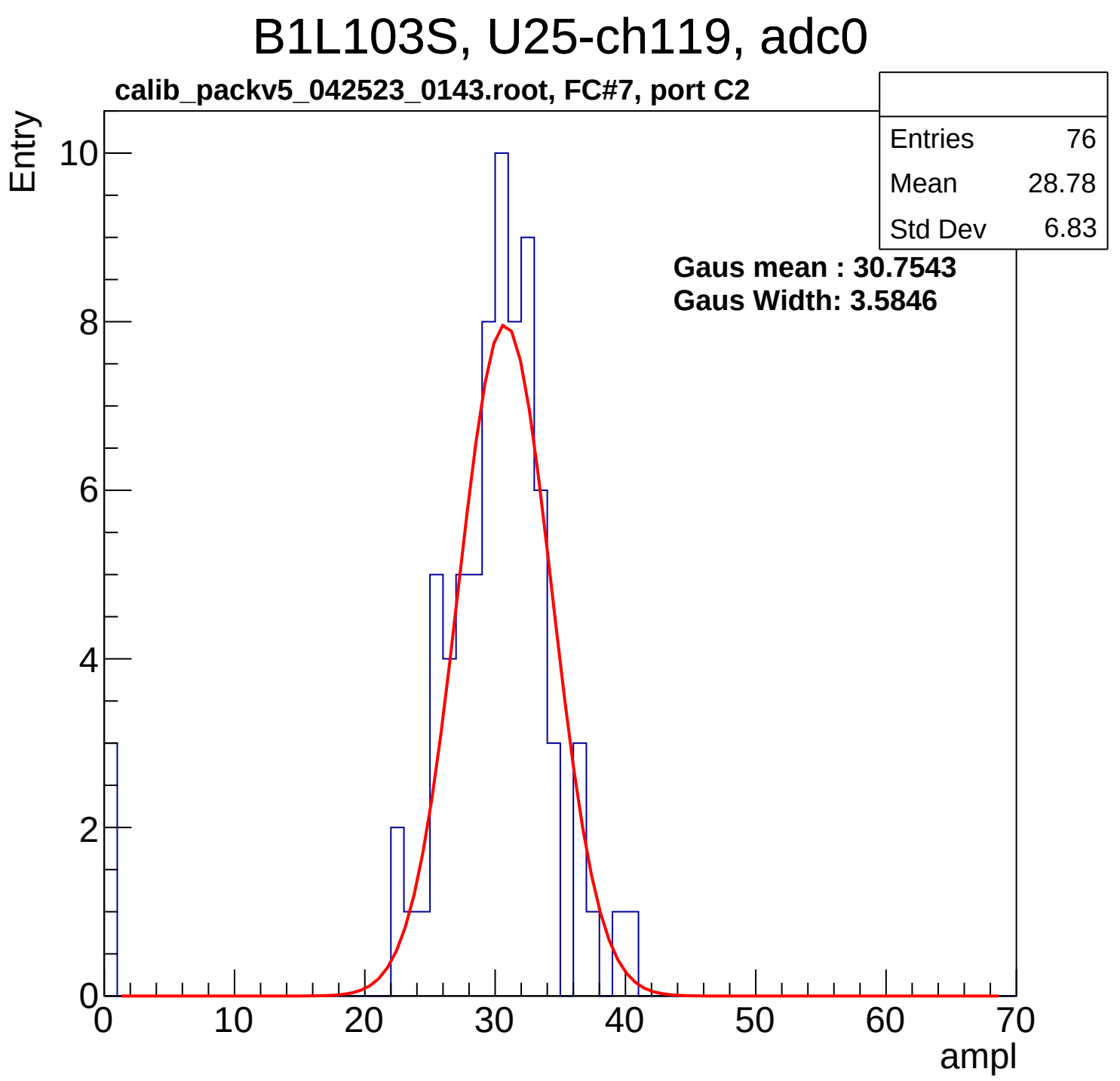
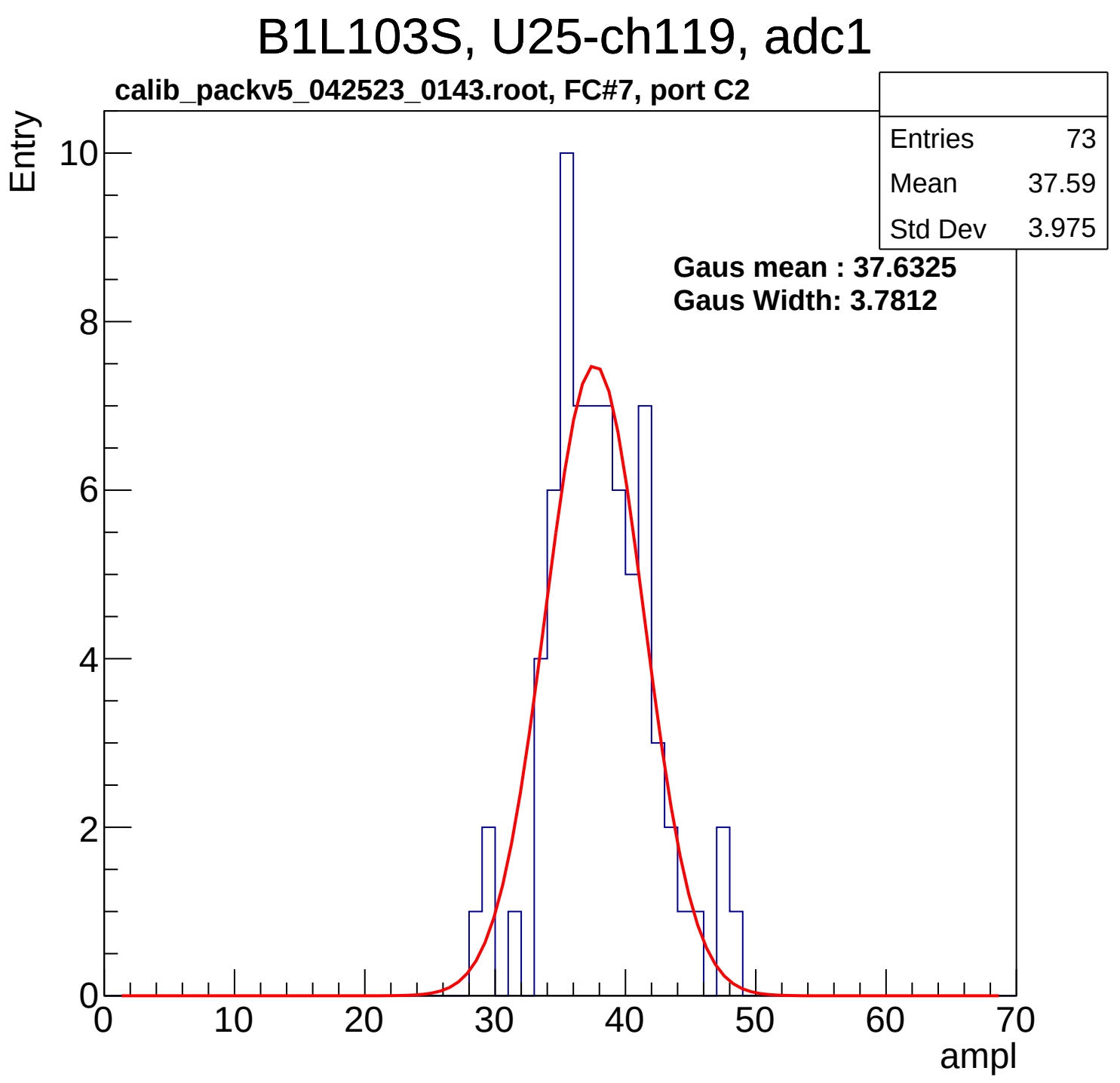
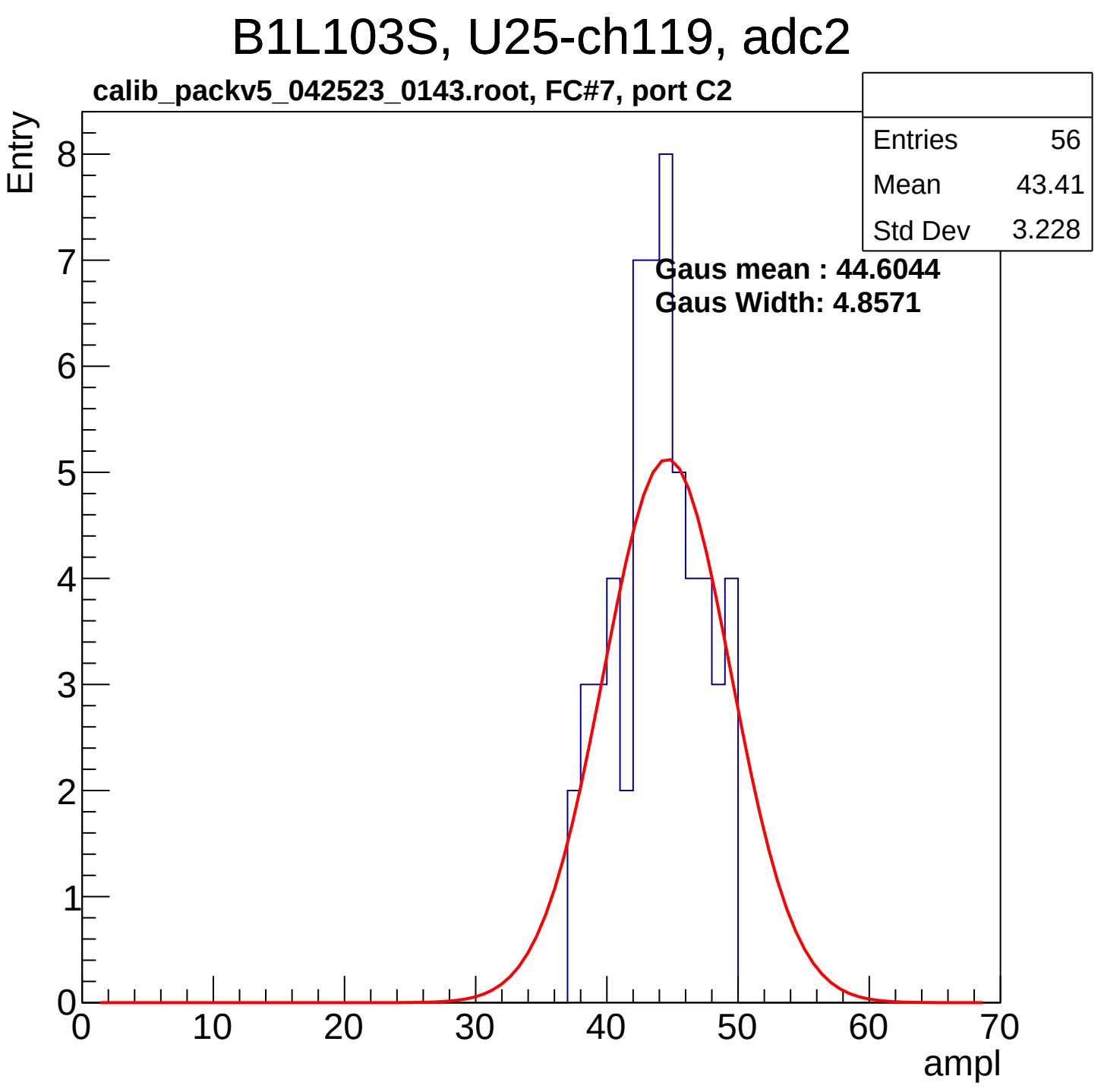
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 9
Mean 59.78
Std Dev 2.82
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch118, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch119, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 28.78
Std Dev 6.83
Gaus mean : 30.7543
Gaus Width: 3.5846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch119, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 37.59
Std Dev 3.975
Gaus mean : 37.6325
Gaus Width: 3.7812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch119, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 43.41
Std Dev 3.228
Gaus mean : 44.6044
Gaus Width: 4.8571
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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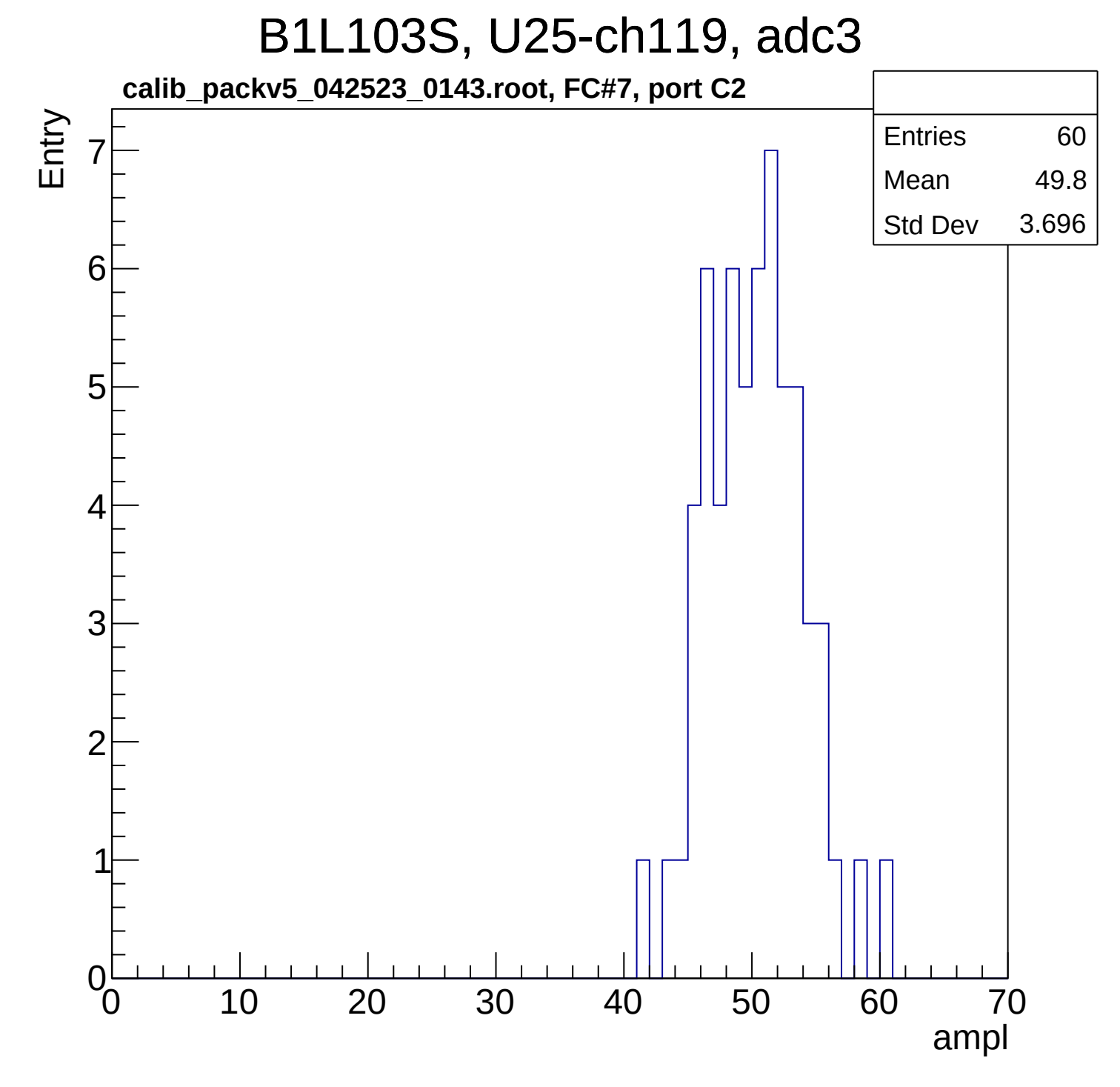
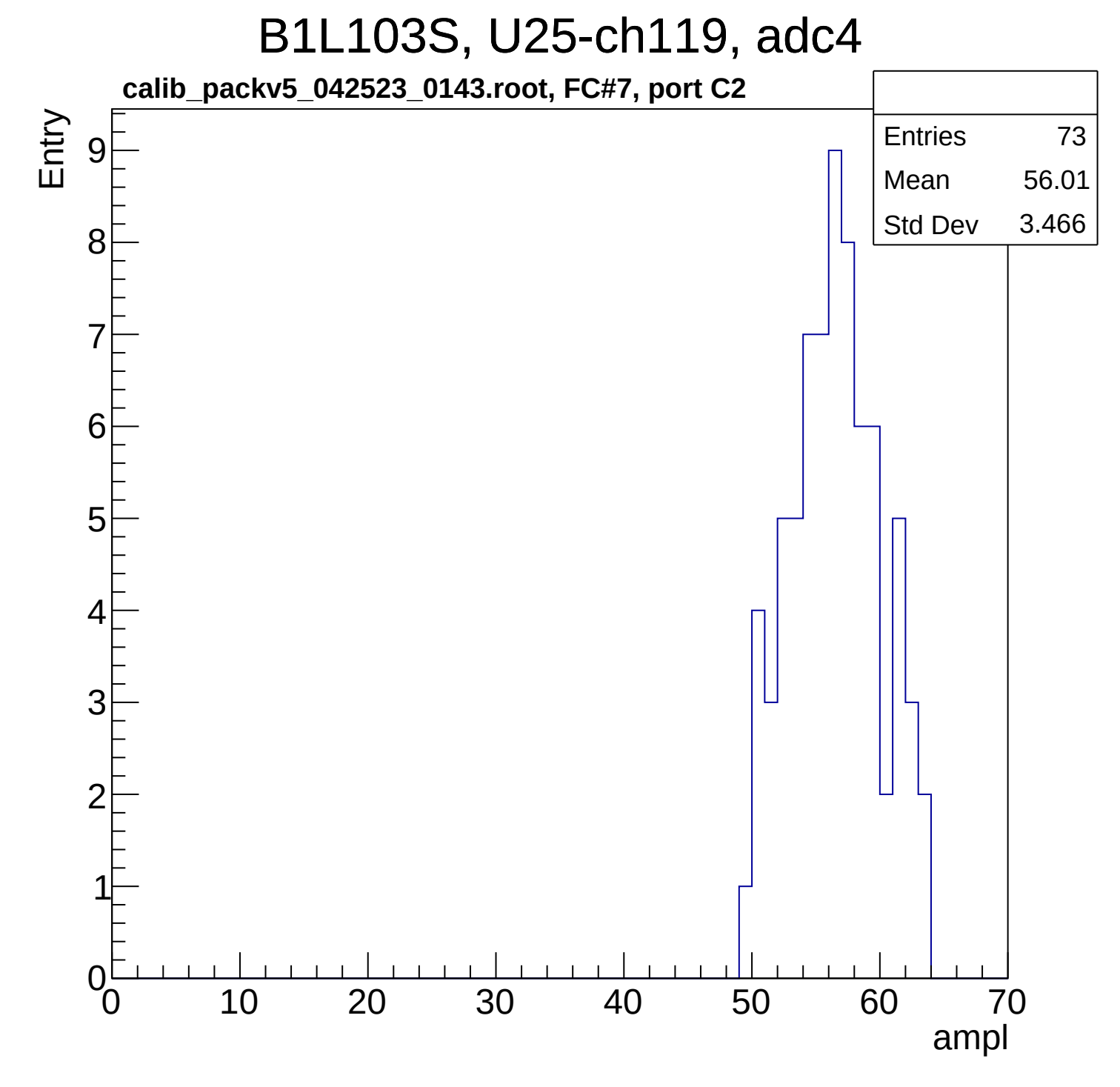

B1L103S, U25-ch119, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 49.8
Std Dev 3.696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch119, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 56.01
Std Dev 3.466
Entry
ampl
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2
3
4
5
6
7
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60
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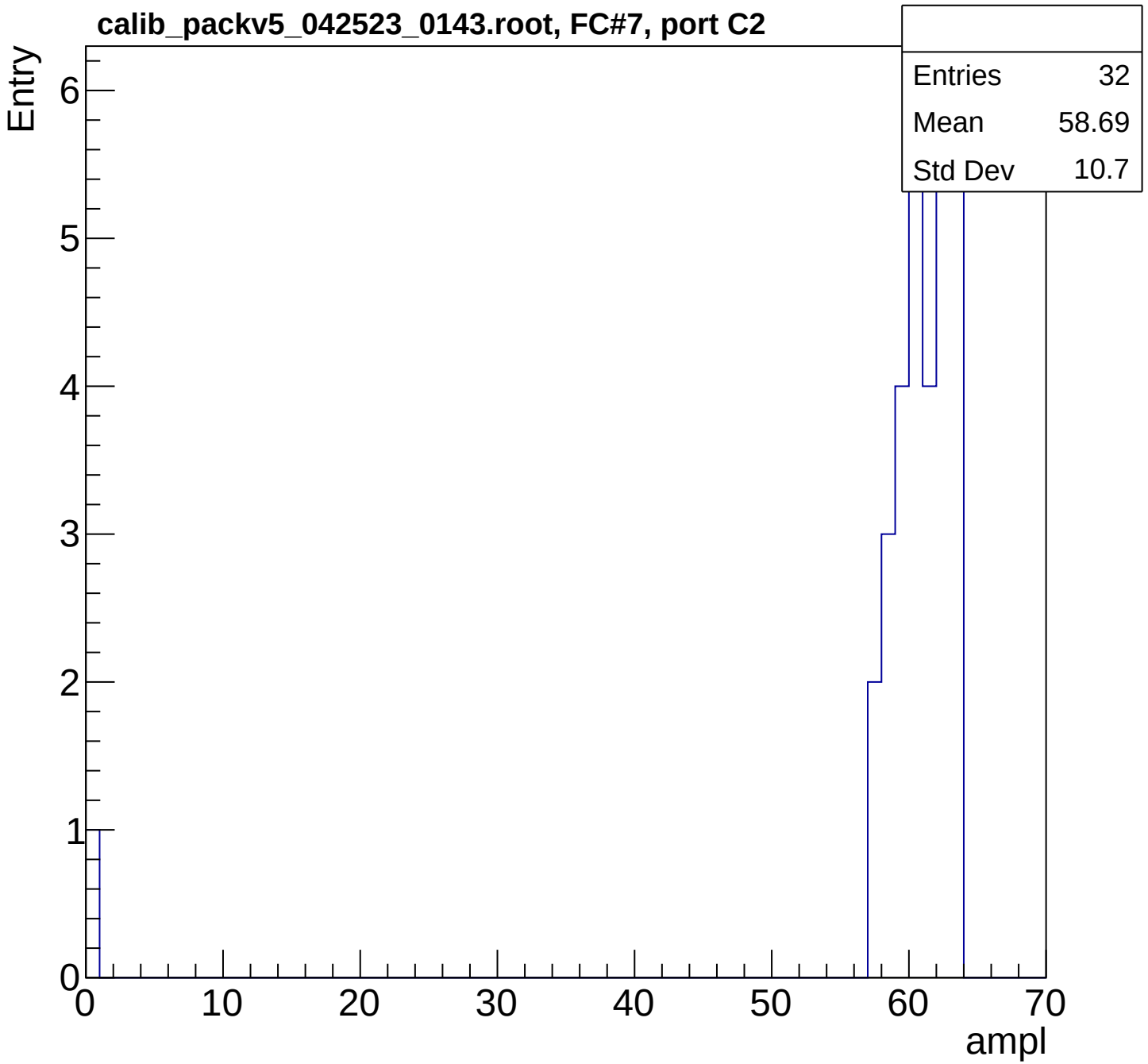
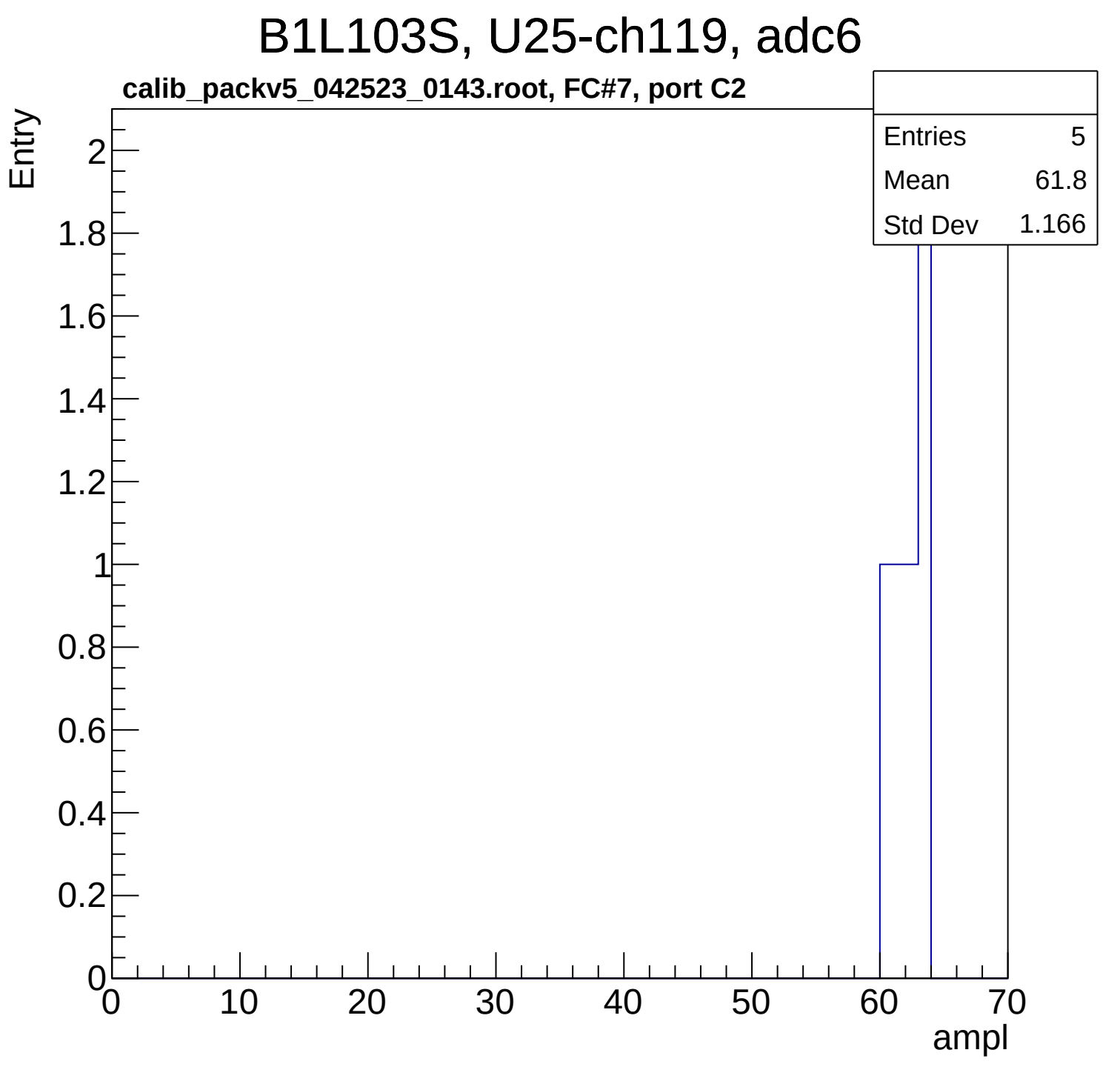
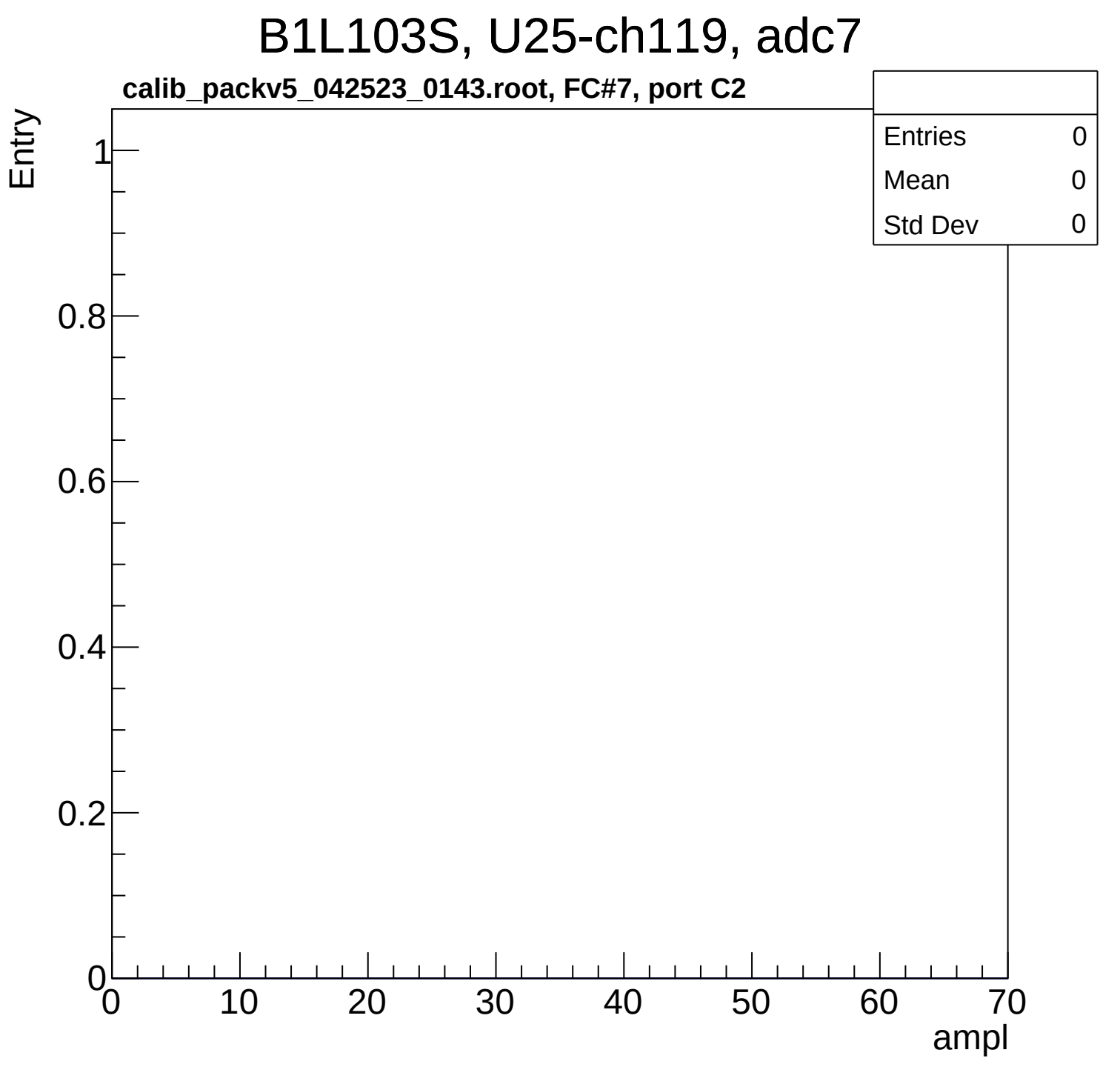

B1L103S, U25-ch119, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 32
Mean 58.69
Std Dev 10.7
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch119, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L103S, U25-ch119, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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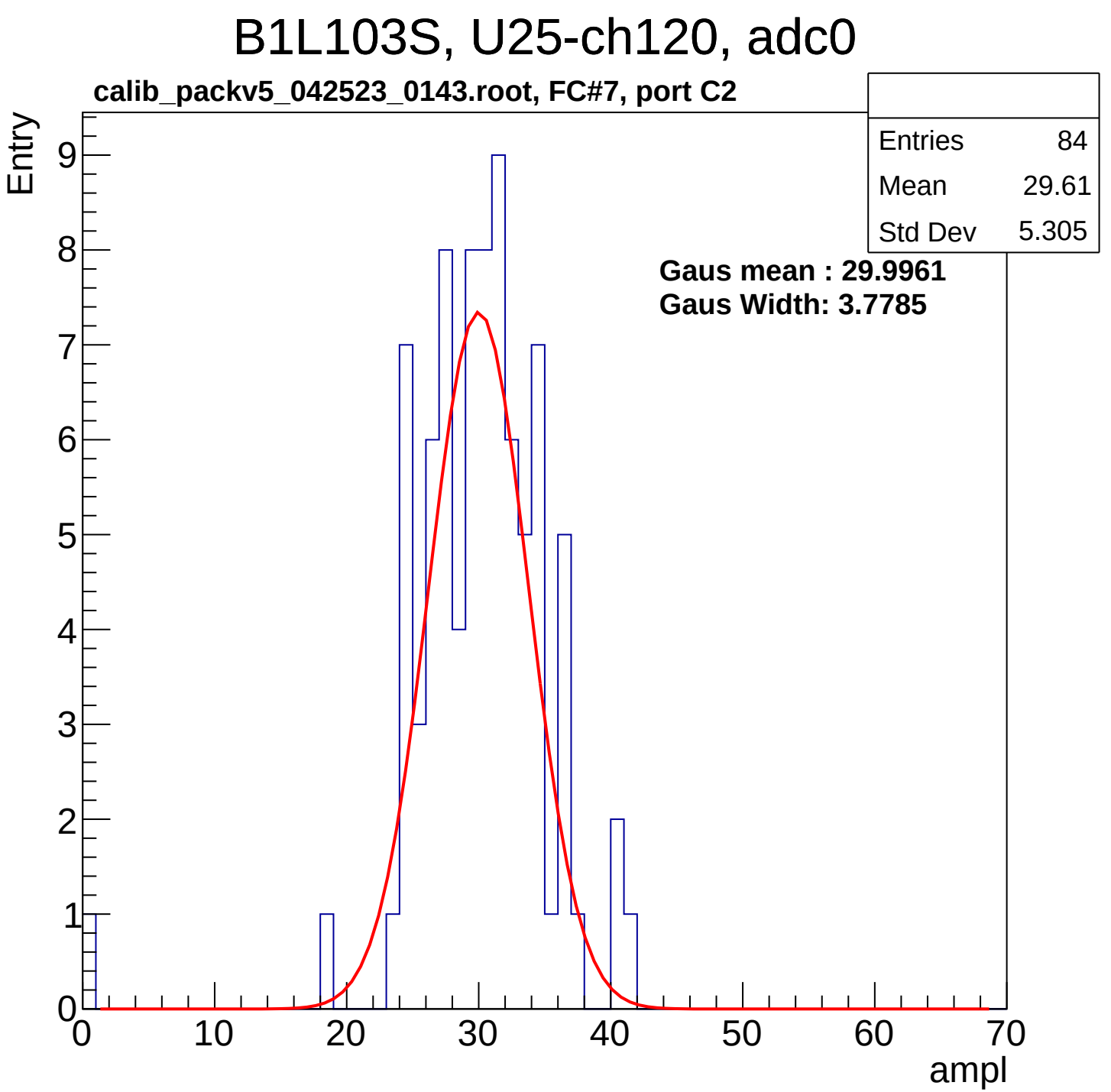
ampl


B1L103S, U25-ch120, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 84
Mean 29.61
Std Dev 5.305
Gaus mean : 29.9961
Gaus Width: 3.7785
Entry
ampl
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2
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4
5
6
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8
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0
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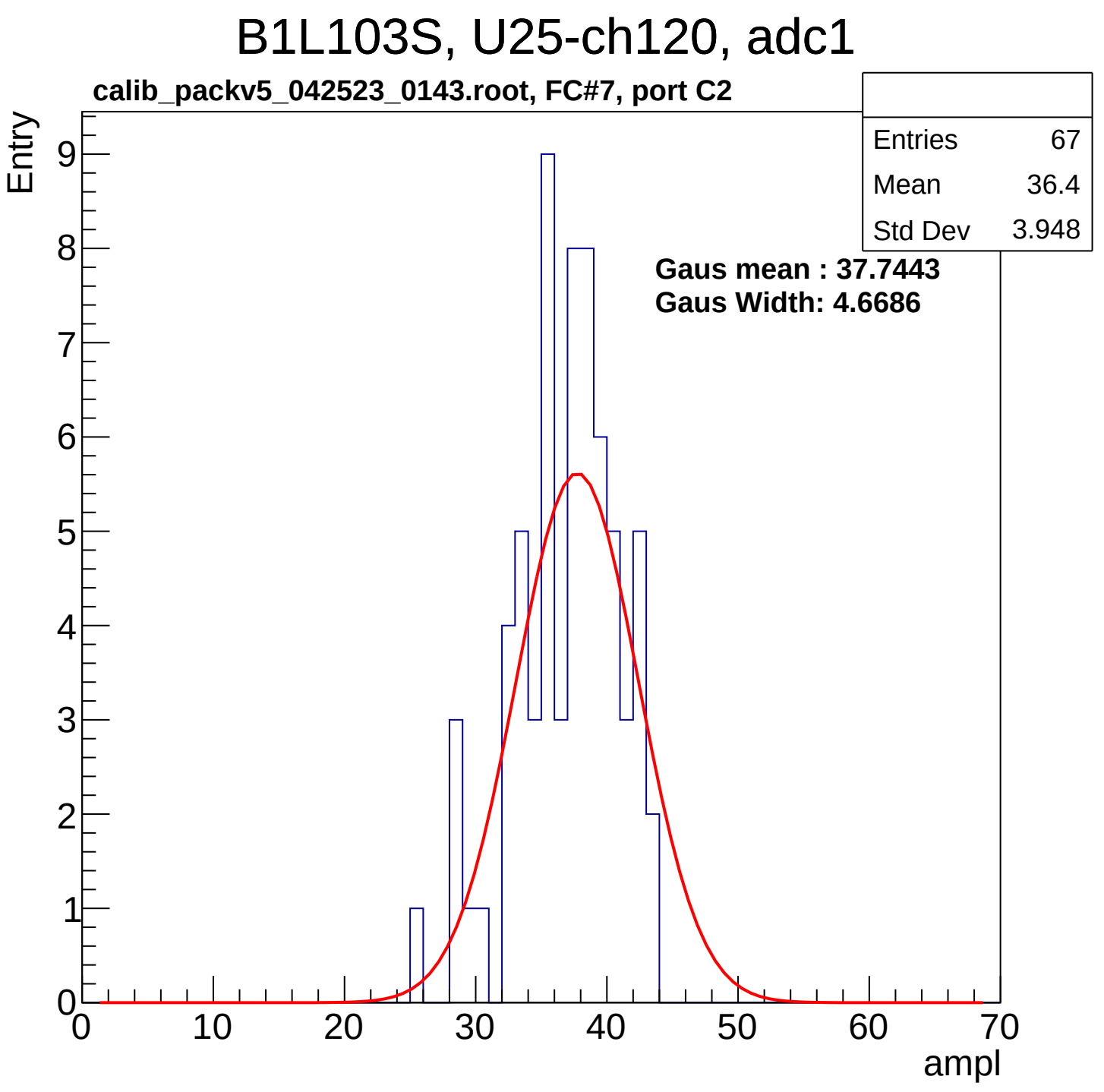
B1L103S, U25-ch120, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 36.4
Std Dev 3.948
Gaus mean : 37.7443
Gaus Width: 4.6686
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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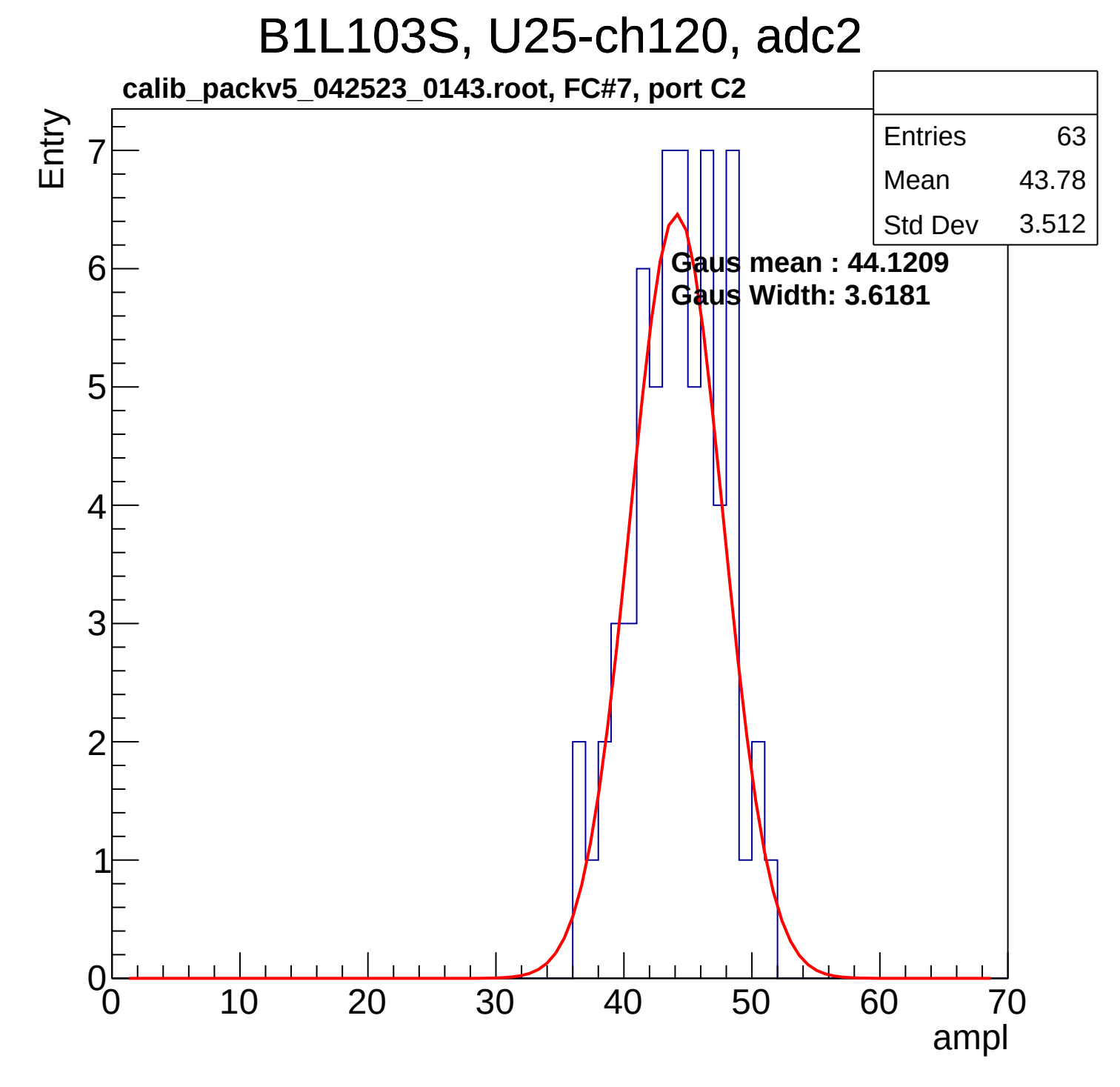
B1L103S, U25-ch120, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 43.78
Std Dev 3.512
Gaus mean : 44.1209
Gaus Width: 3.6181
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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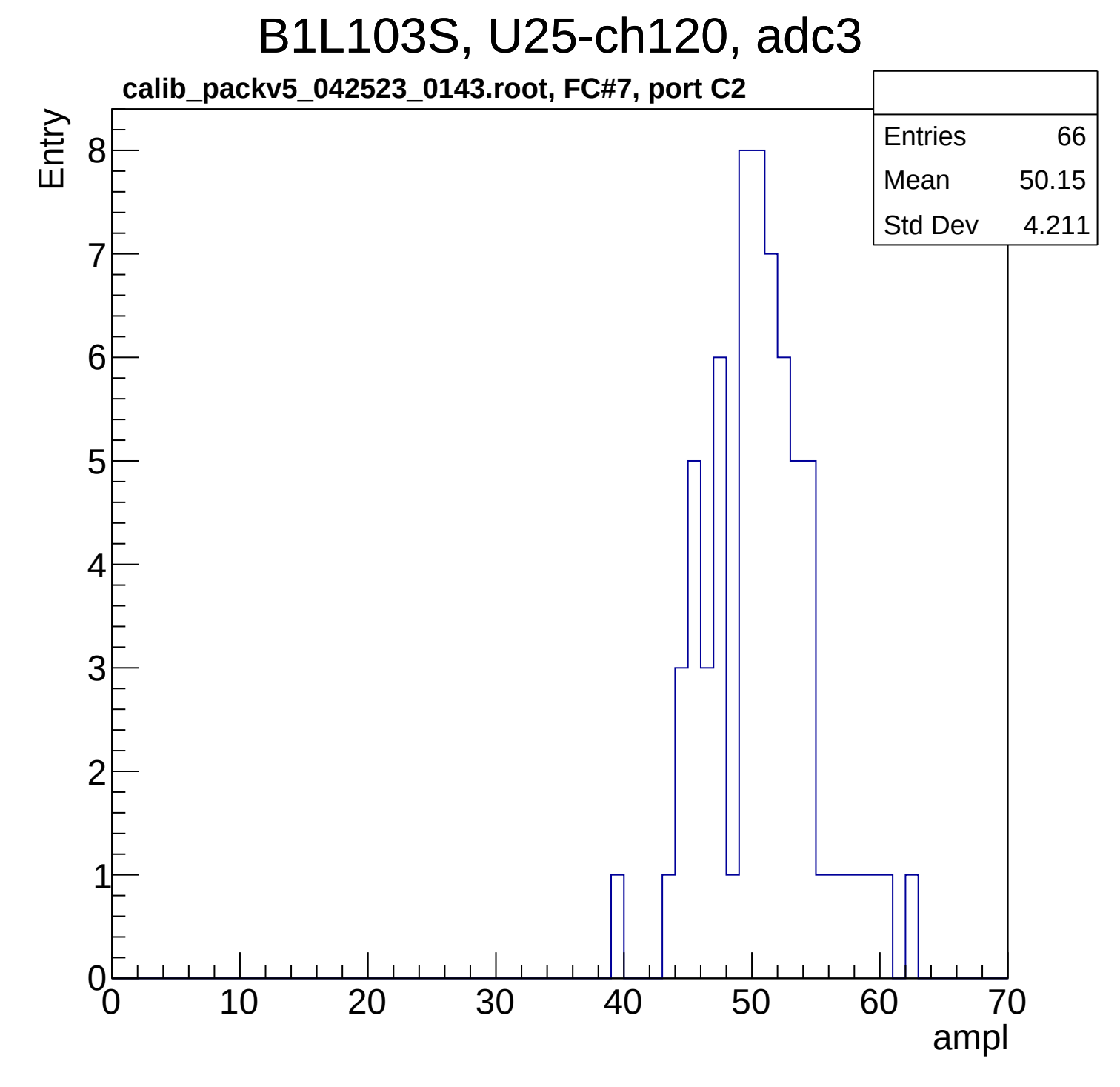
B1L103S, U25-ch120, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 66
Mean 50.15
Std Dev 4.211
Entry
ampl
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2
3
4
5
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7
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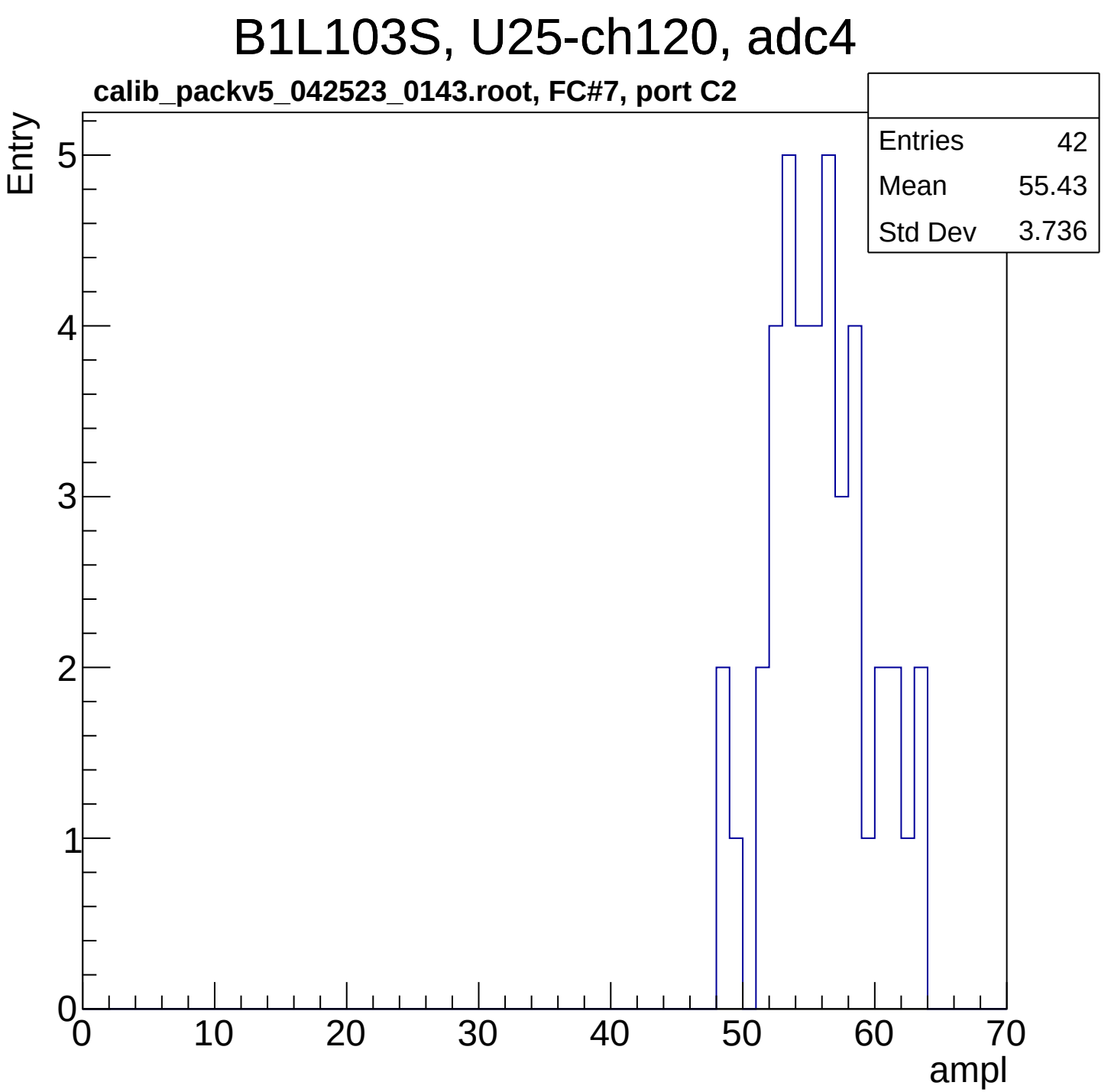
B1L103S, U25-ch120, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 42
Mean 55.43
Std Dev 3.736
Entry
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1
2
3
4
5
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10
20
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40
50
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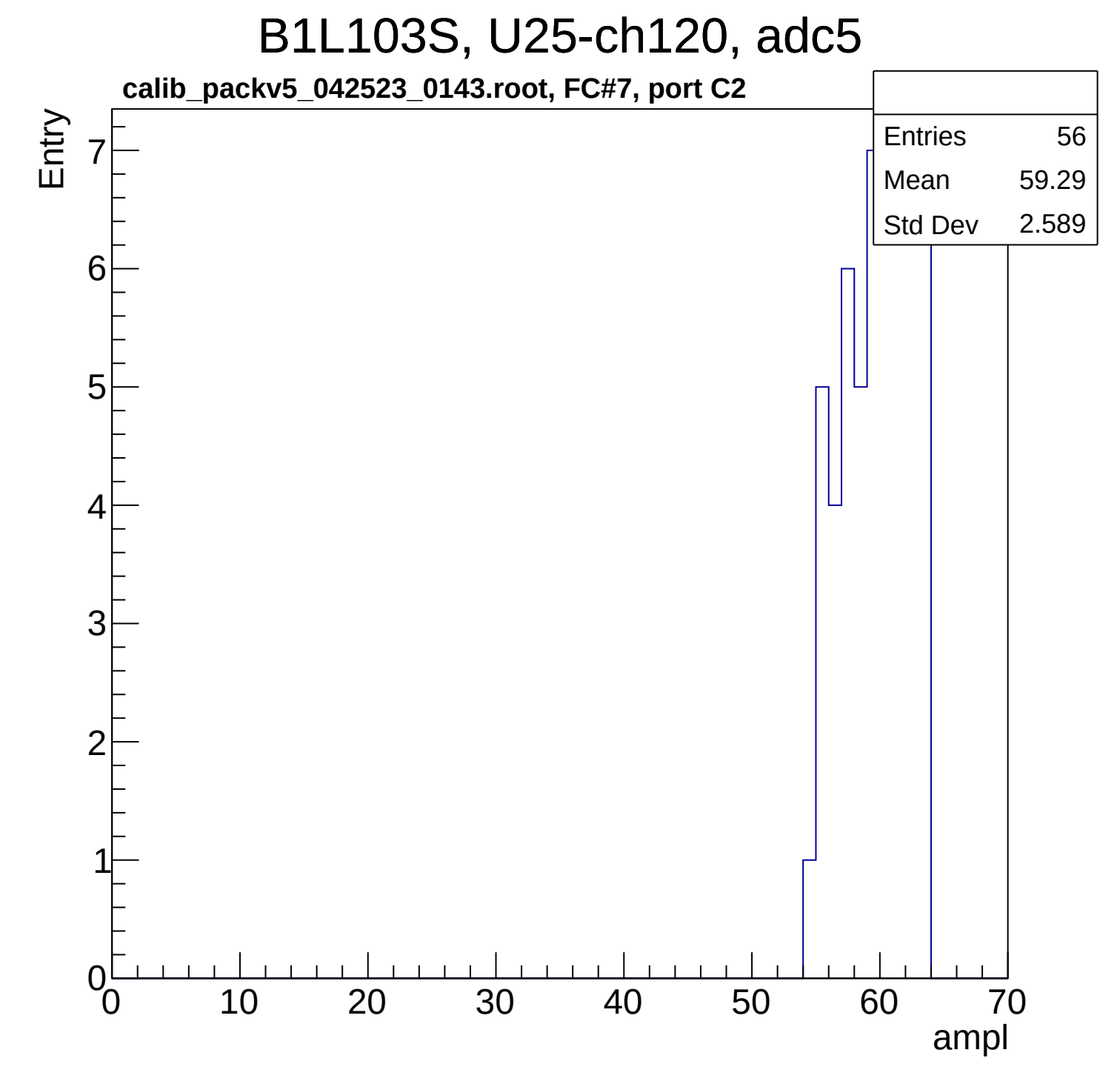
70
ampl


B1L103S, U25-ch120, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 56
Mean 59.29
Std Dev 2.589
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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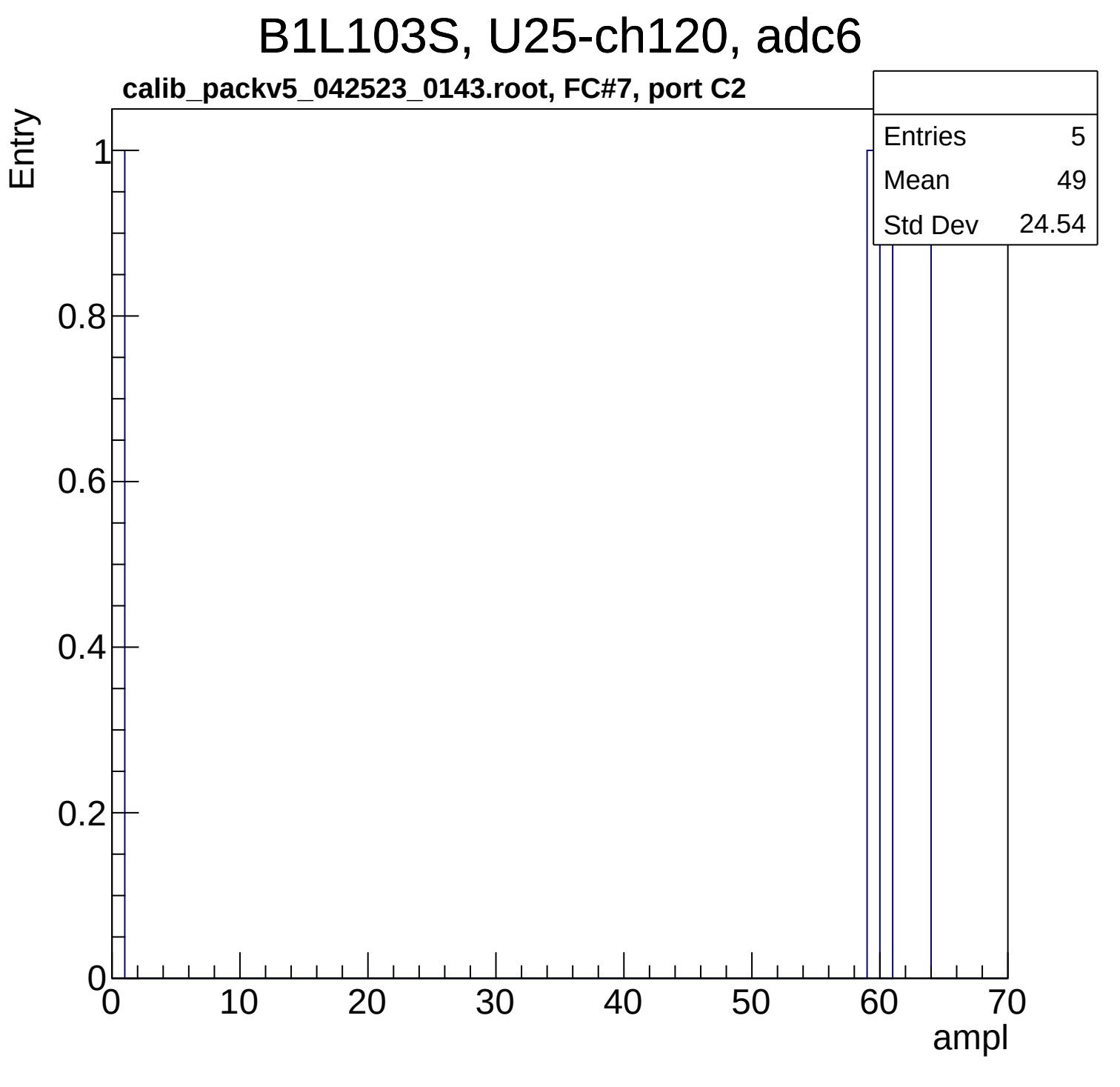
B1L103S, U25-ch120, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 5
Mean 49
Std Dev 24.54
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
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50
60
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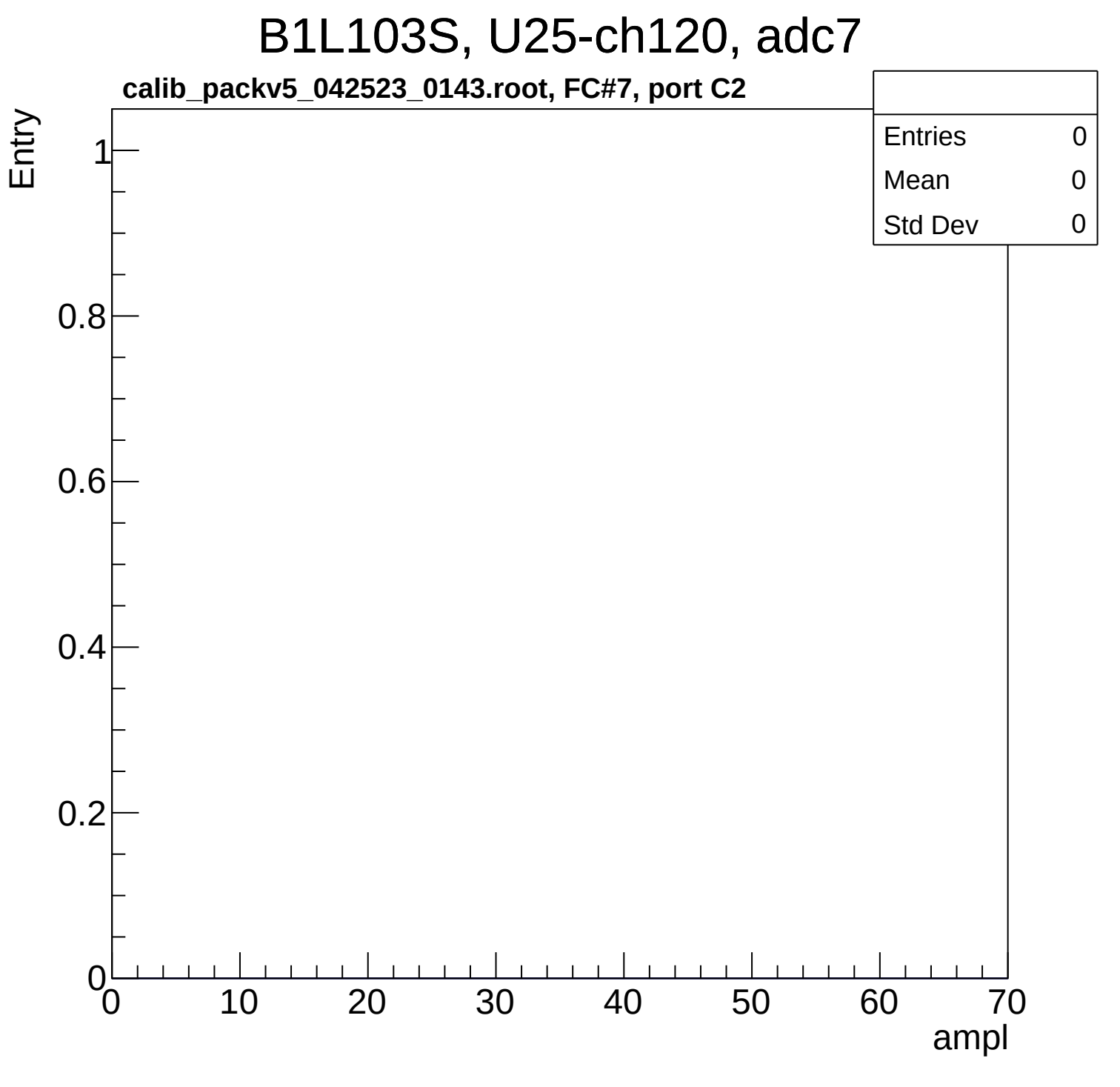
B1L103S, U25-ch120, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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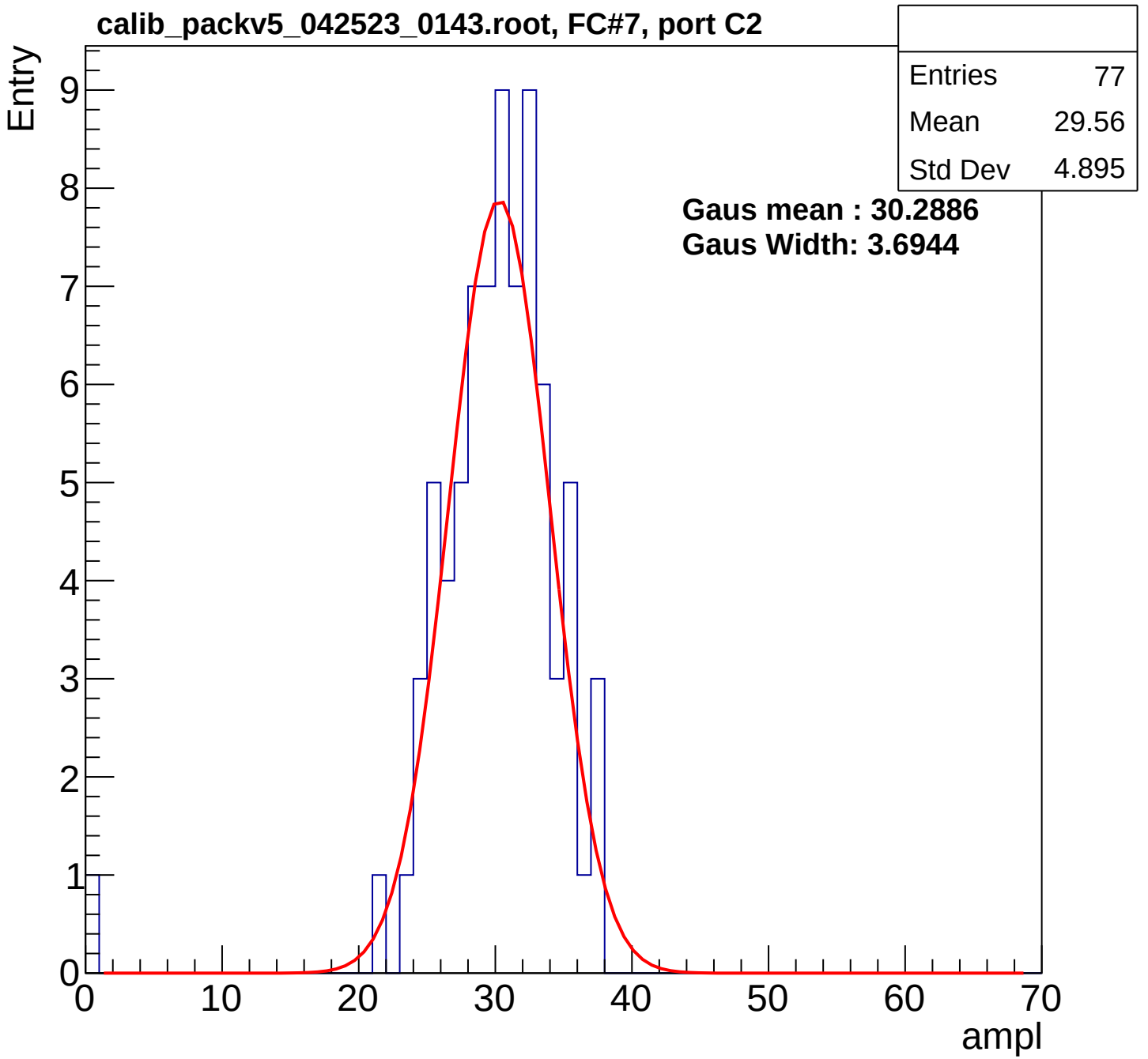
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch121, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 29.56
Std Dev 4.895
Gaus mean : 30.2886
Gaus Width: 3.6944
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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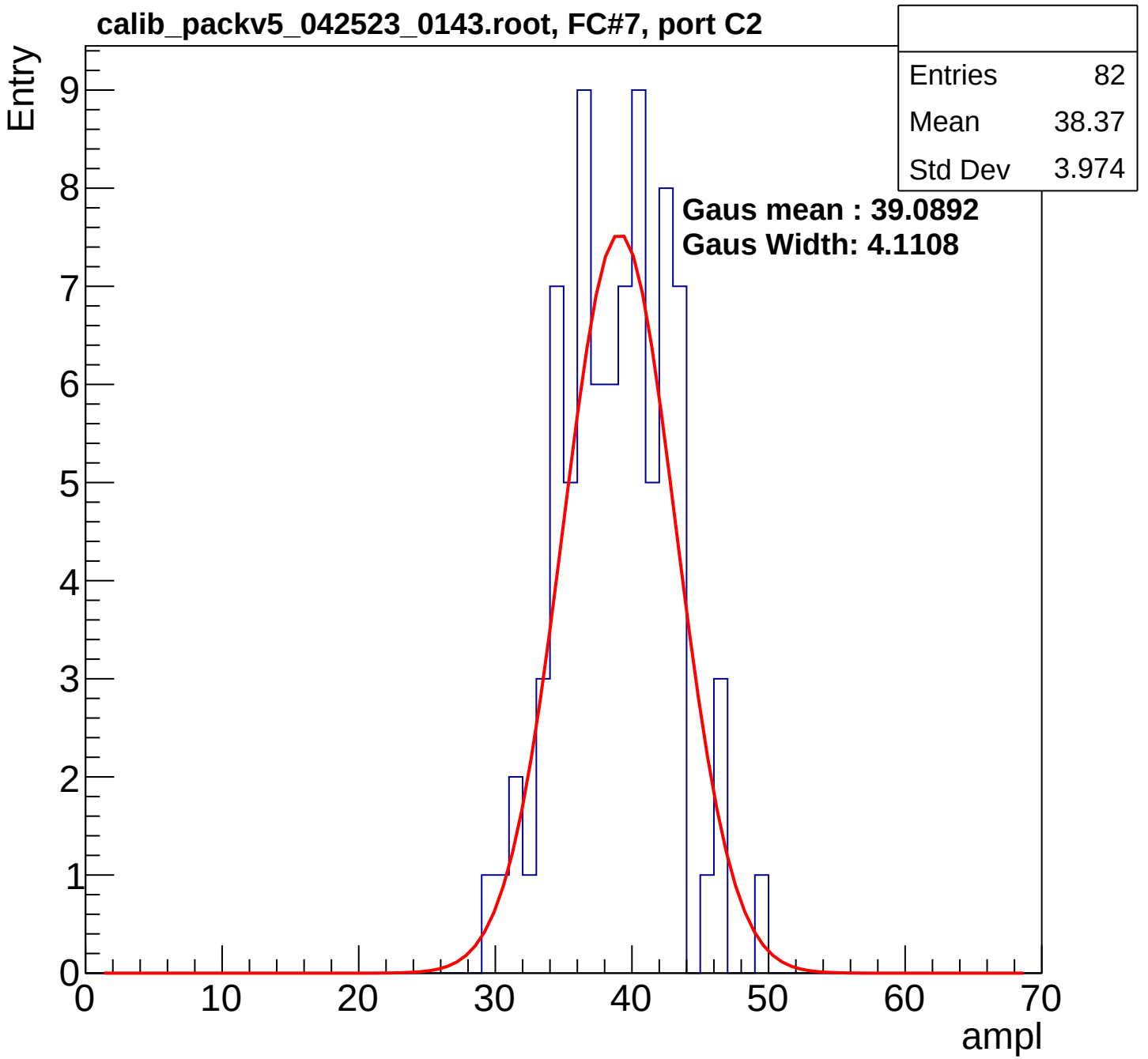
B1L103S, U25-ch121, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 82
Mean 38.37
Std Dev 3.974
Gaus mean : 39.0892
Gaus Width: 4.1108
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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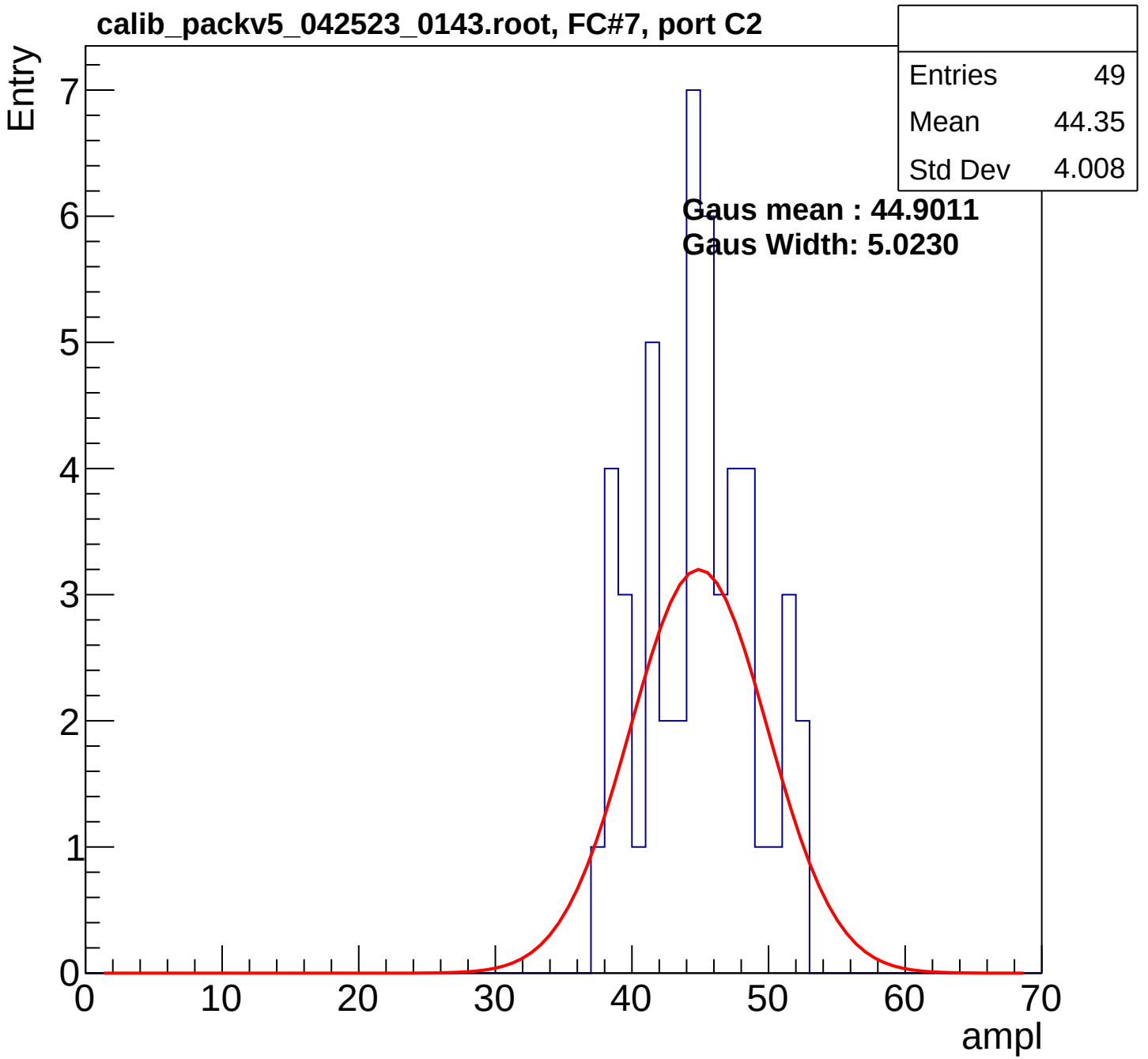
B1L103S, U25-ch121, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 44.35
Std Dev 4.008
Gaus mean : 44.9011
Gaus Width: 5.0230
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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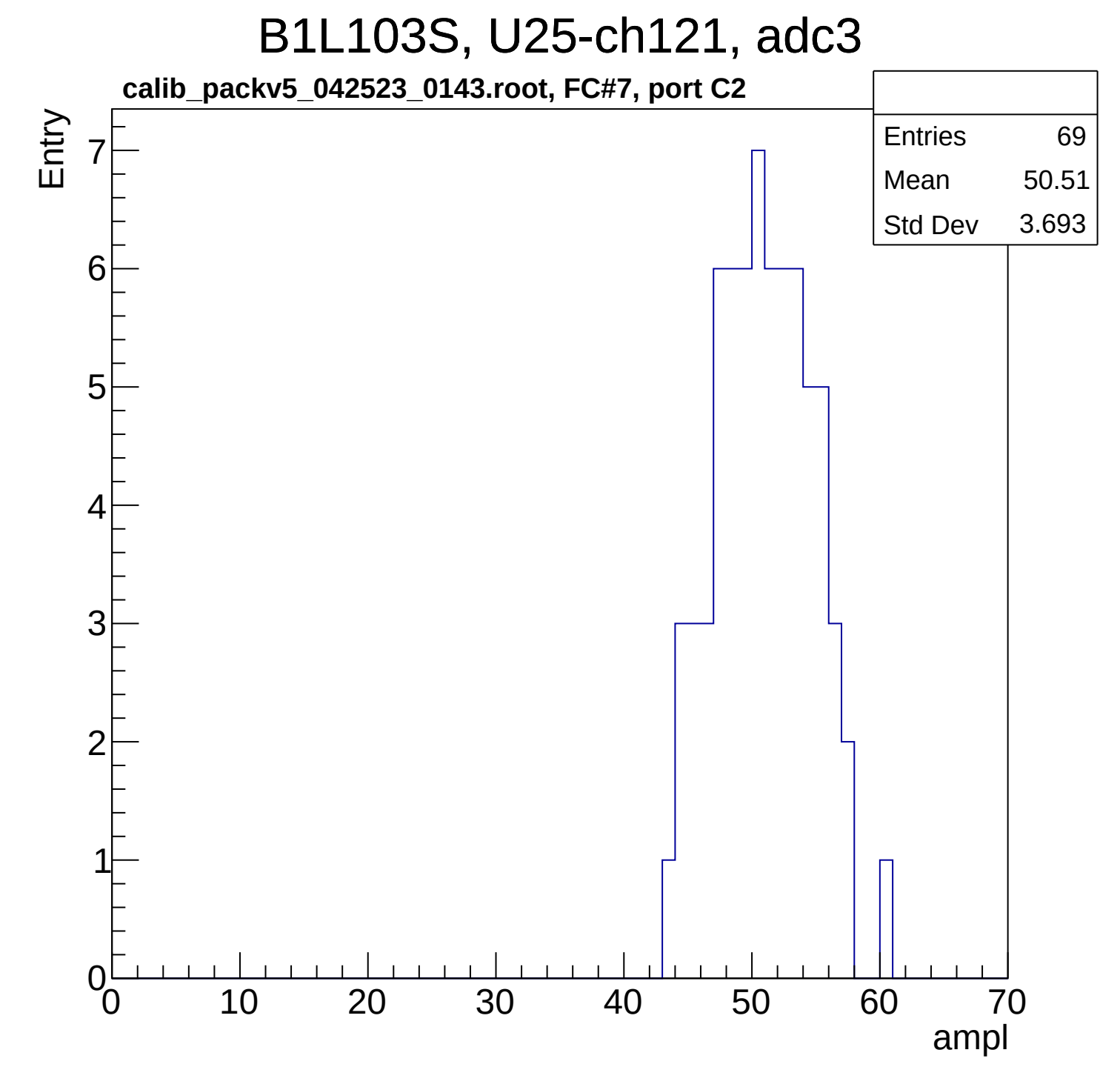
B1L103S, U25-ch121, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 50.51
Std Dev 3.693
Entry
ampl
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2
3
4
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6
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0
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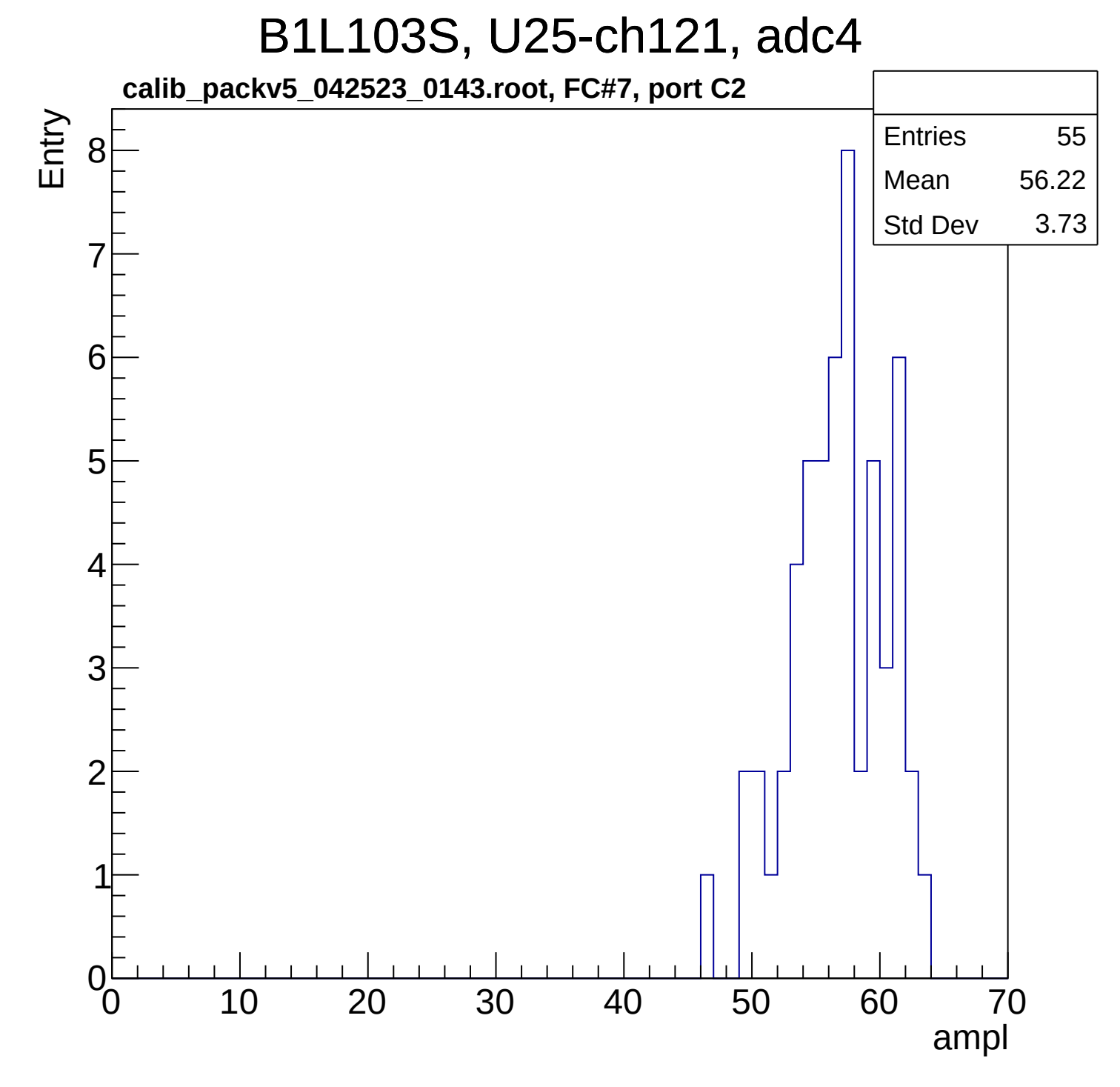
B1L103S, U25-ch121, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 56.22
Std Dev 3.73
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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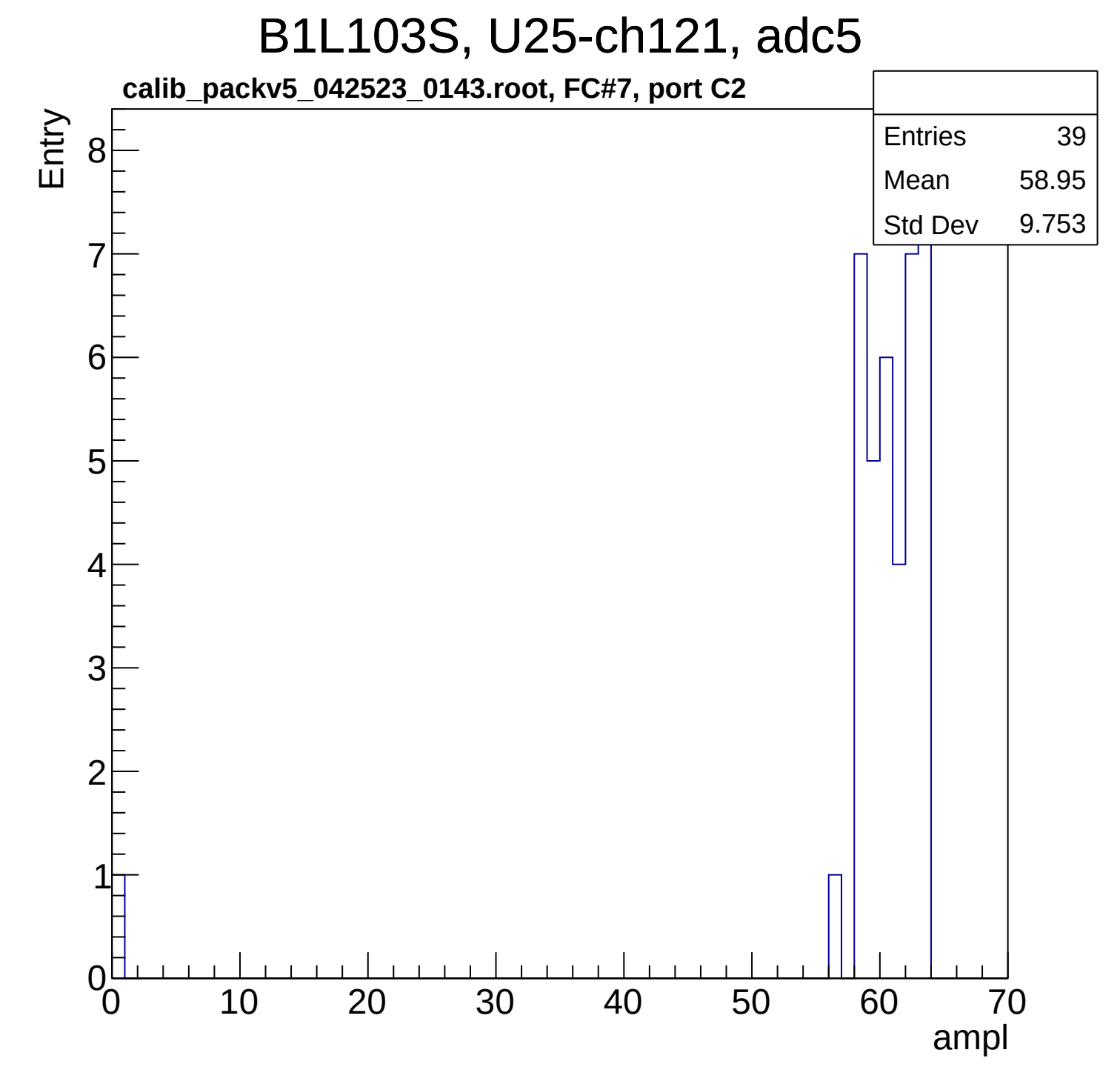
B1L103S, U25-ch121, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 58.95
Std Dev 9.753
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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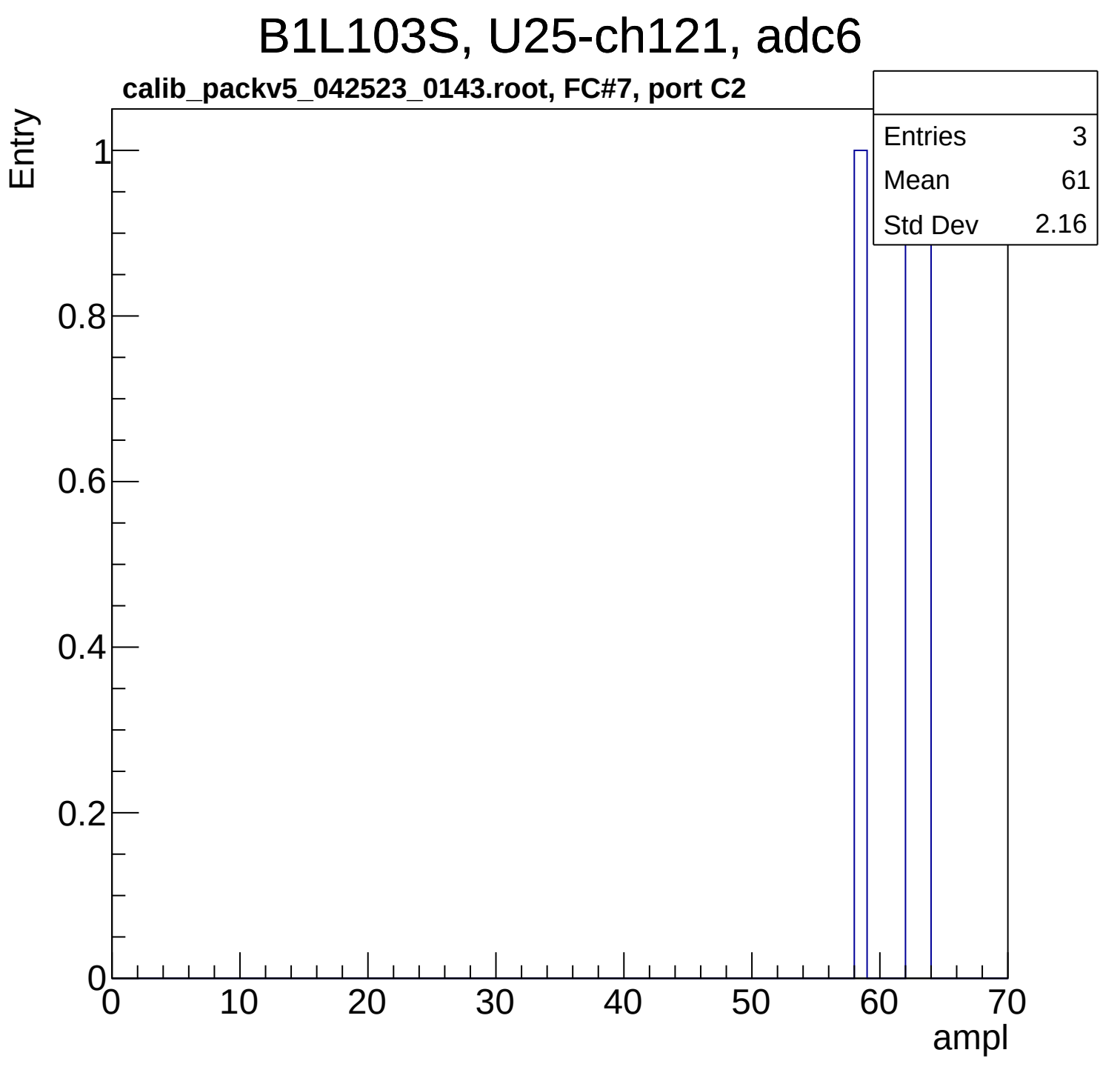
B1L103S, U25-ch121, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 61
Std Dev 2.16
Entry
ampl
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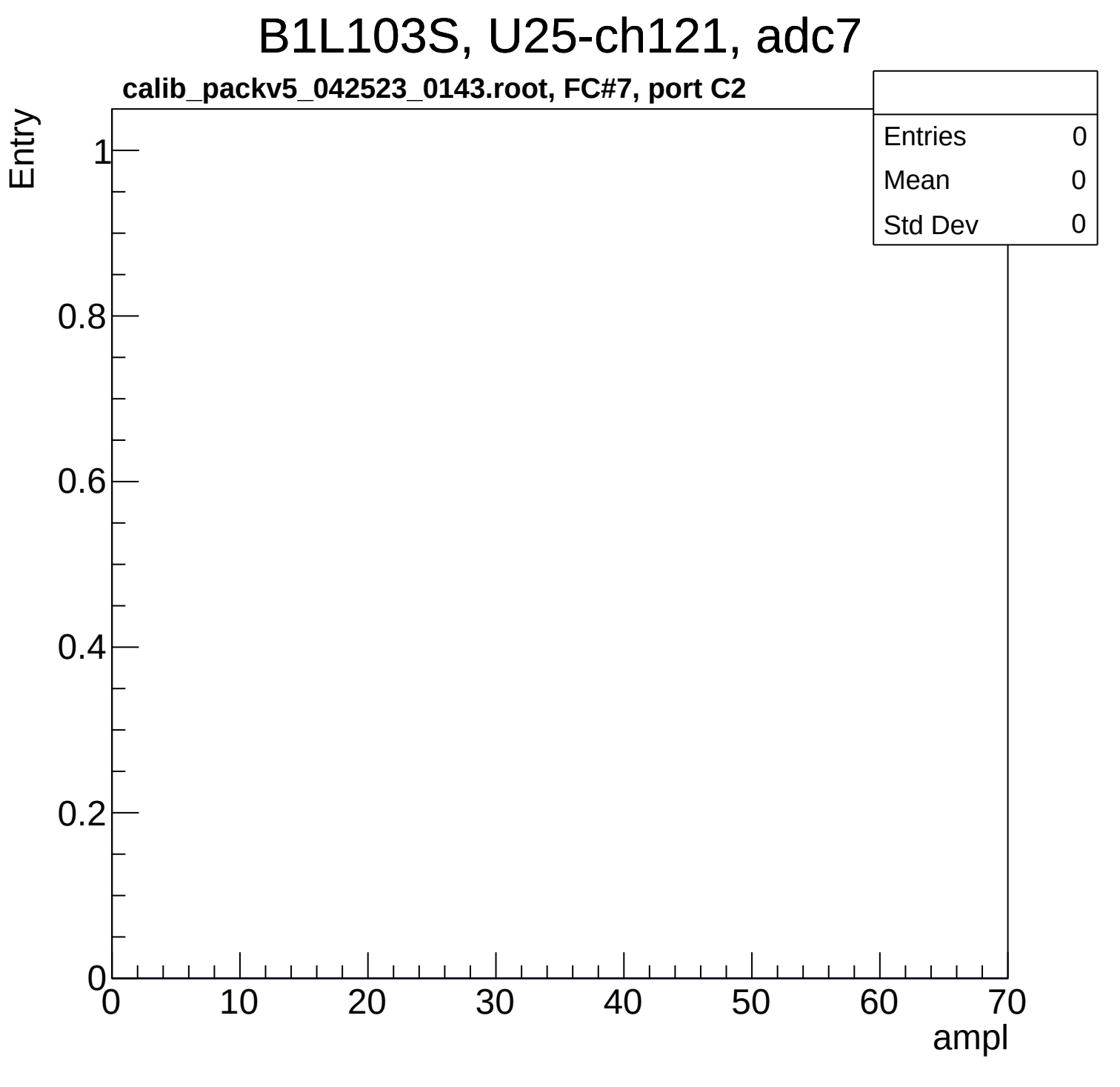
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U25-ch121, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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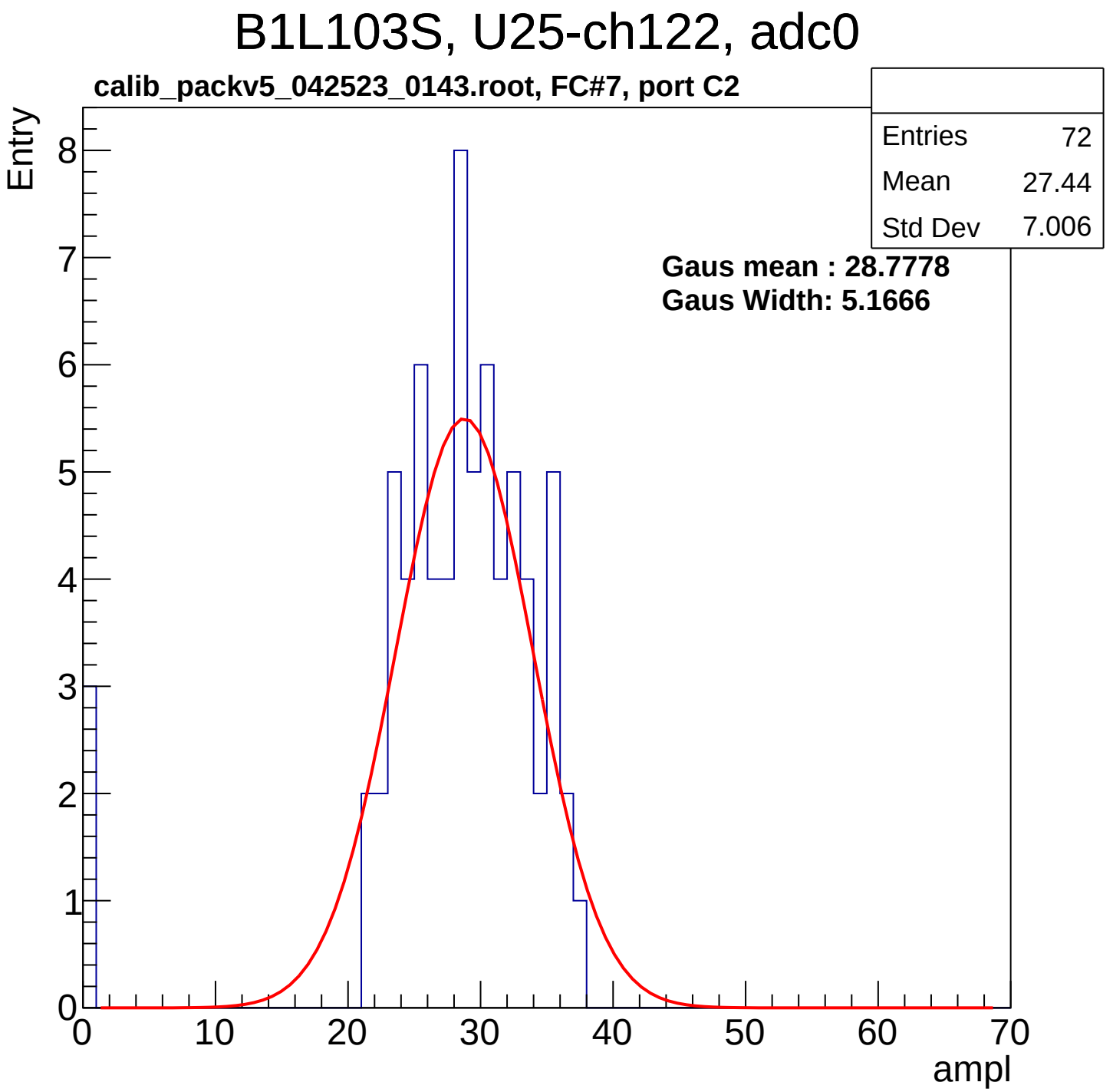
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch122, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 27.44
Std Dev 7.006
Gaus mean : 28.7778
Gaus Width: 5.1666
Entry
ampl
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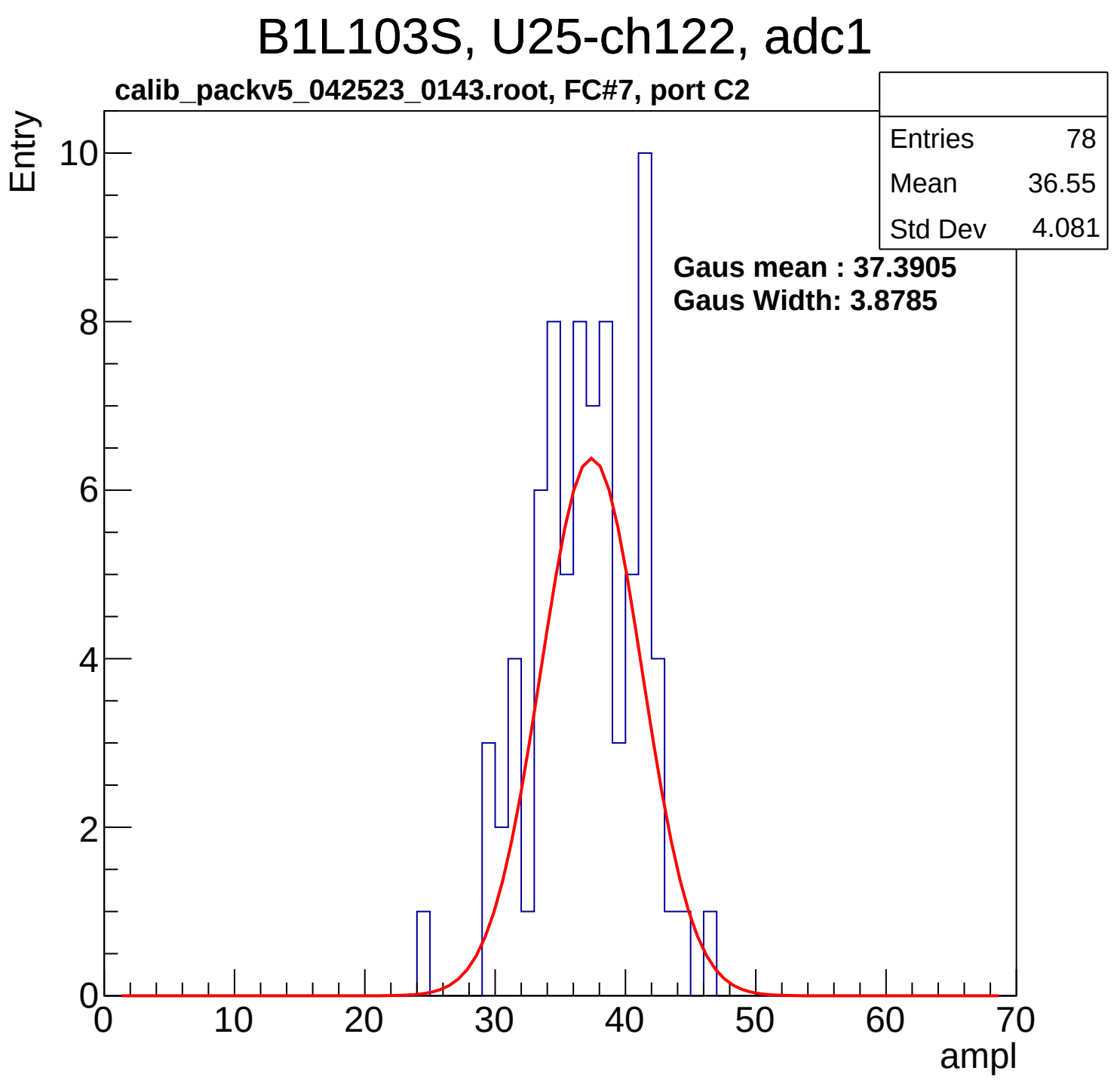
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch122, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 78
Mean 36.55
Std Dev 4.081
Gaus mean : 37.3905
Gaus Width: 3.8785
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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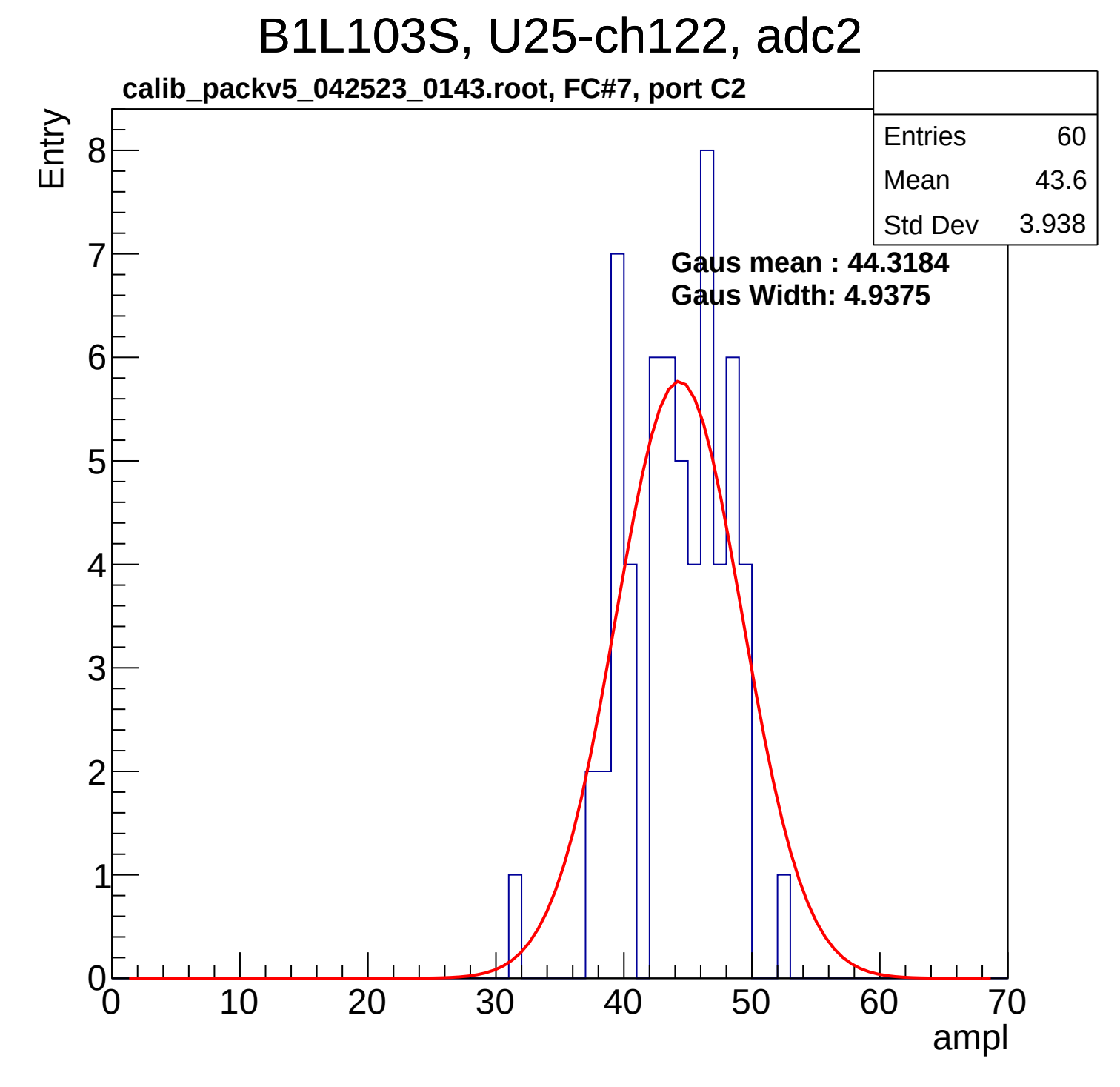
B1L103S, U25-ch122, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 43.6
Std Dev 3.938
Gaus mean : 44.3184
Gaus Width: 4.9375
Entry
ampl
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2
3
4
5
6
7
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0
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60
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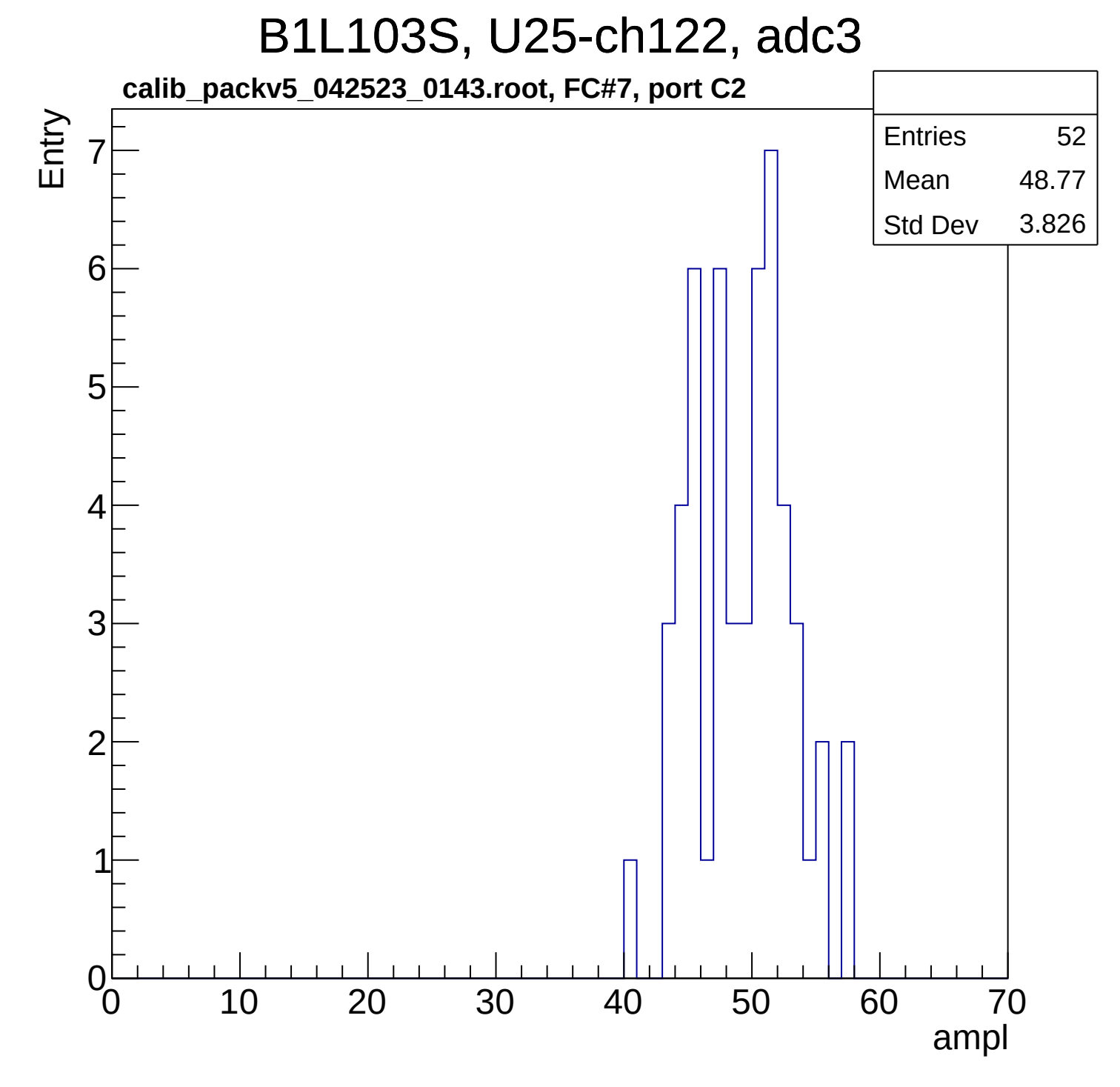
B1L103S, U25-ch122, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 52
Mean 48.77
Std Dev 3.826
Entry
ampl
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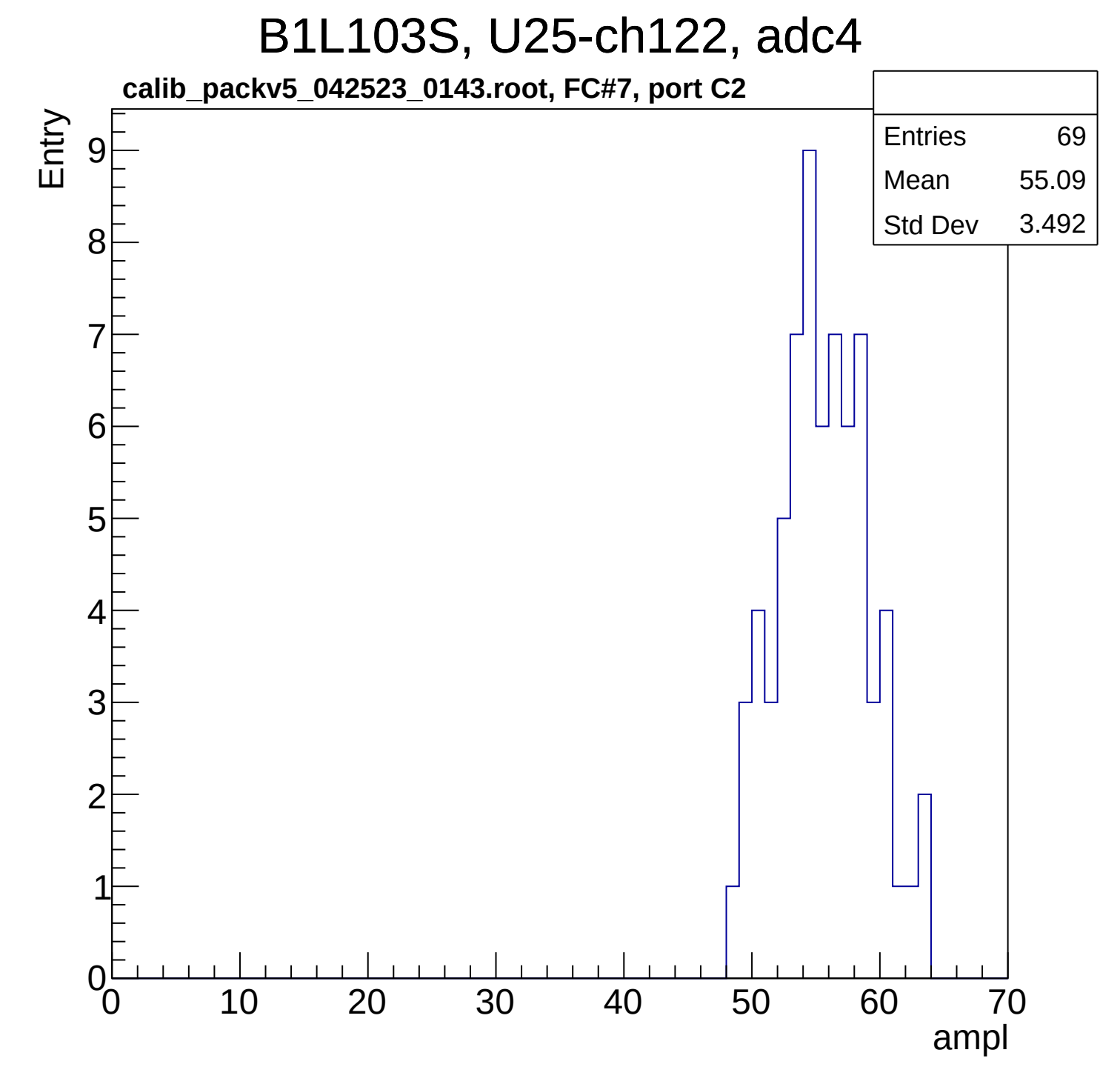
B1L103S, U25-ch122, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 55.09
Std Dev 3.492
Entry
ampl
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7
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0
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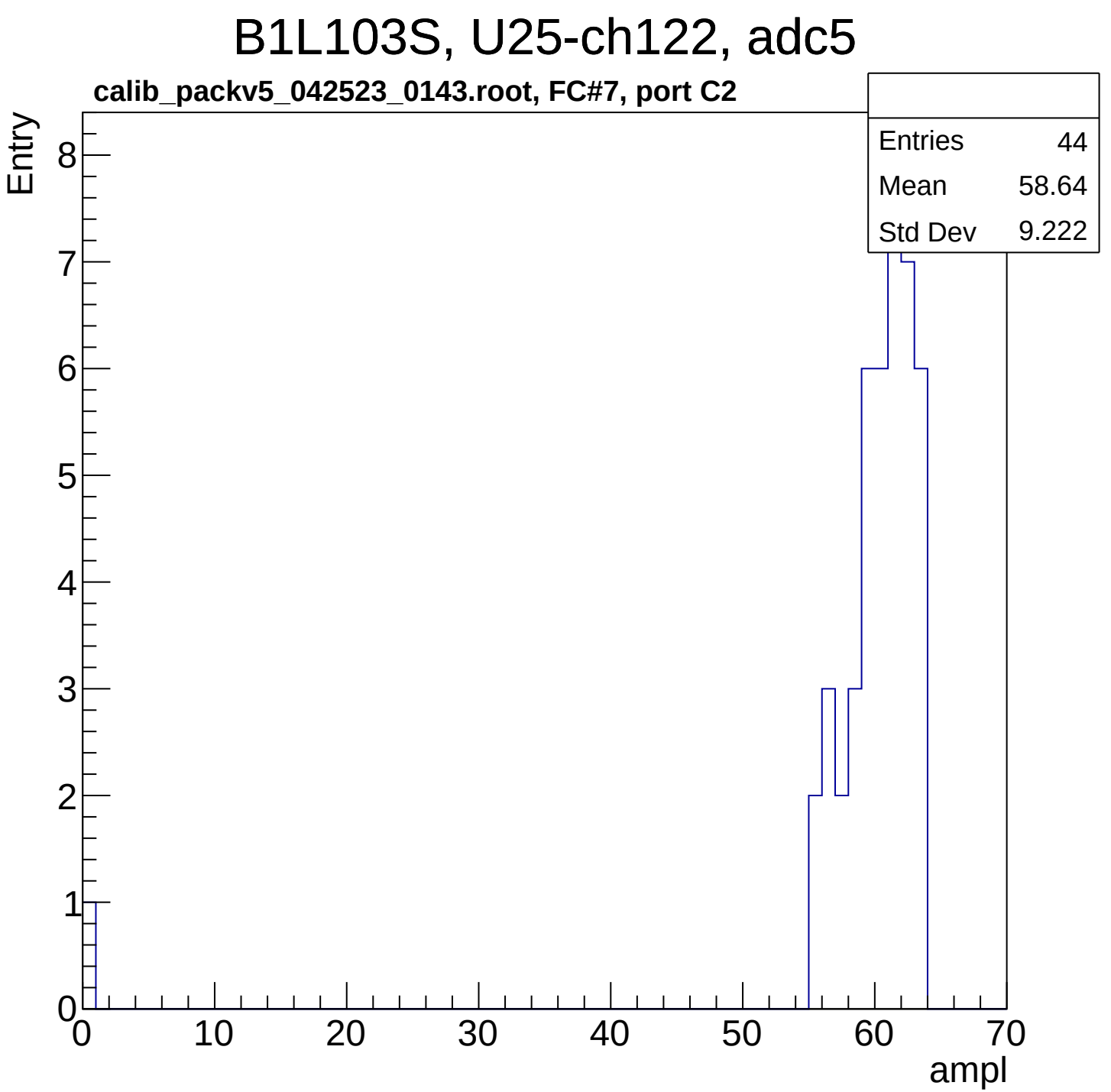
B1L103S, U25-ch122, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 44
Mean 58.64
Std Dev 9.222
Entry
ampl
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4
5
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7
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0
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40
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60
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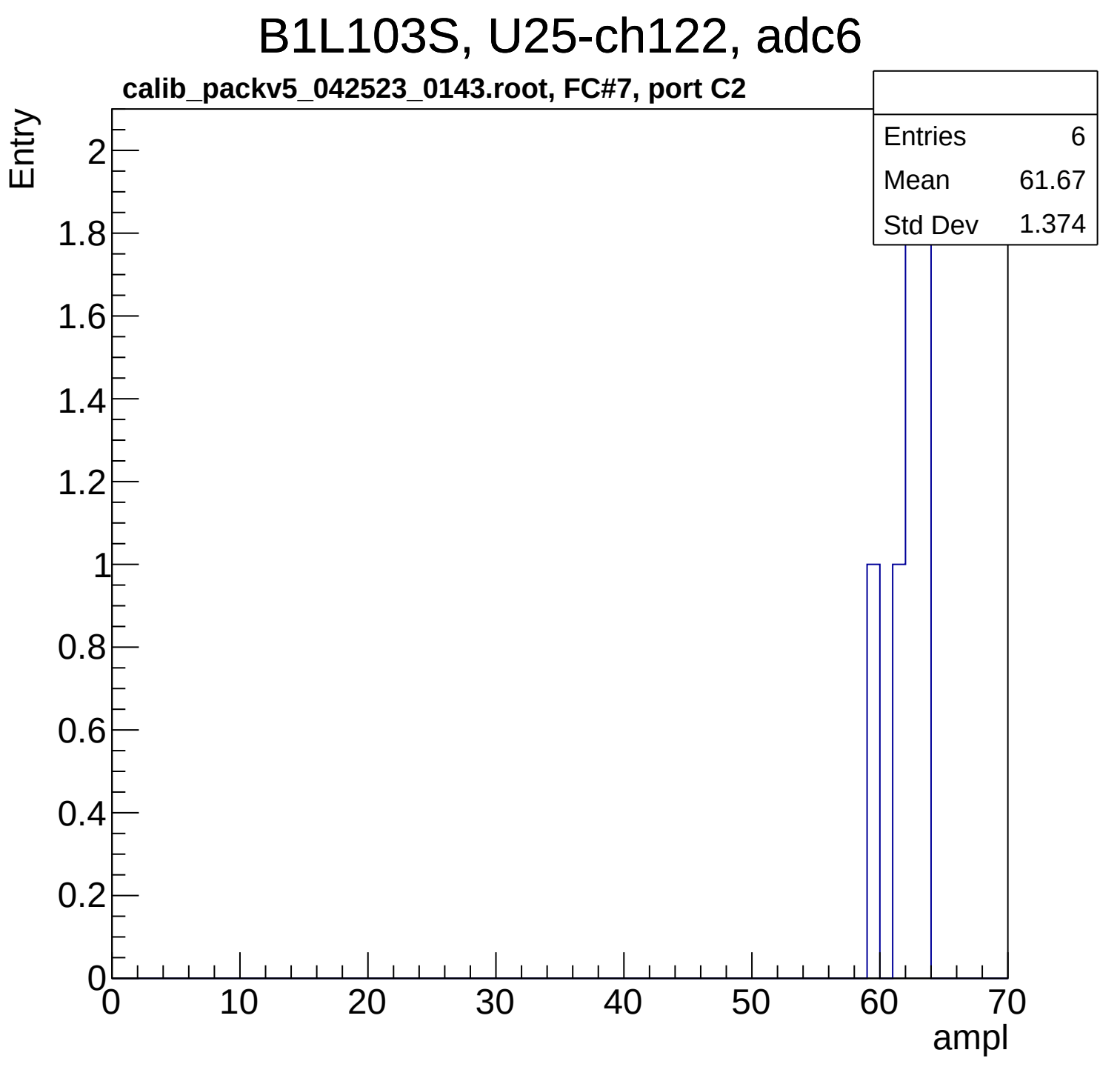
B1L103S, U25-ch122, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
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0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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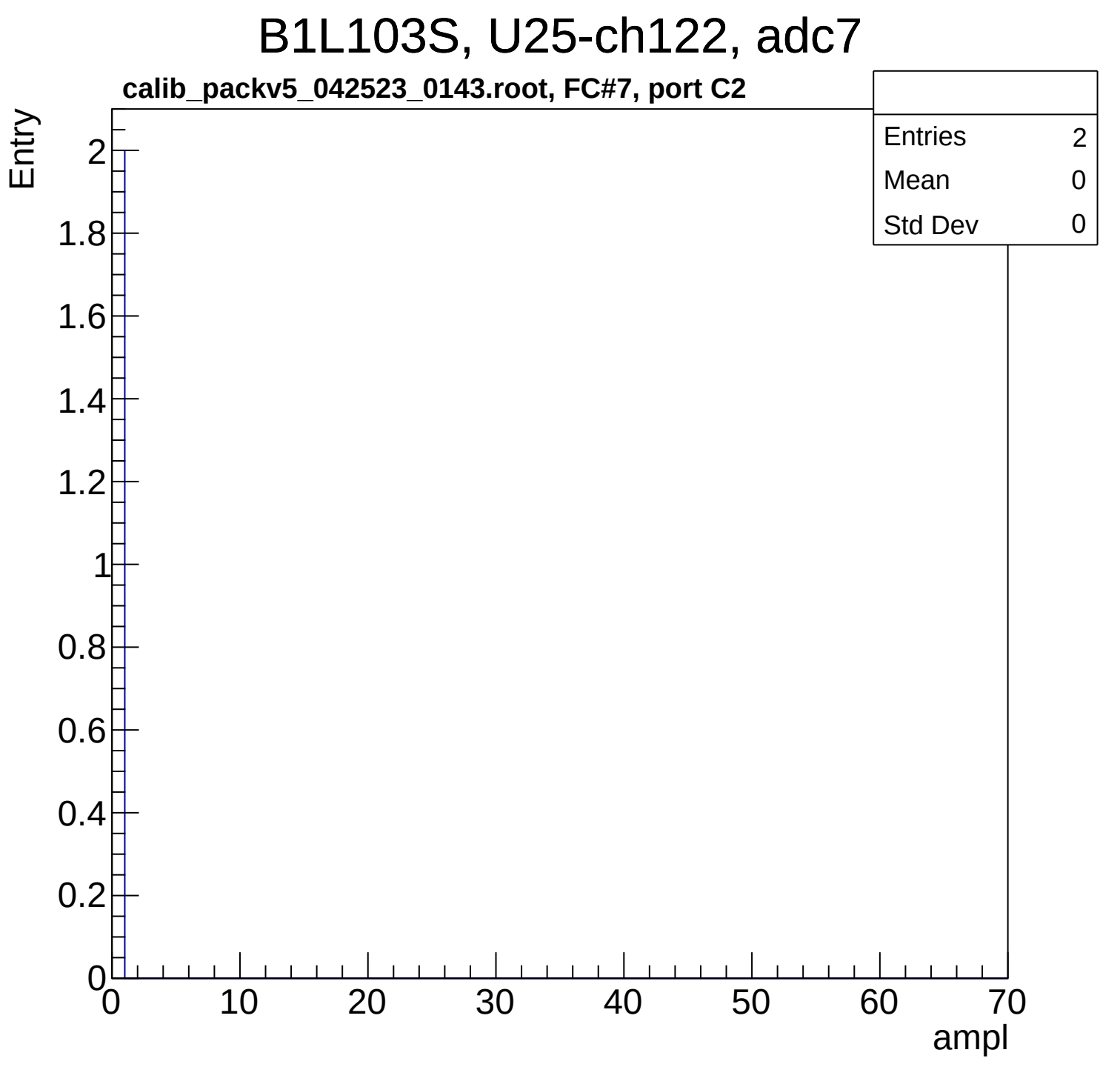
B1L103S, U25-ch122, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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10
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30
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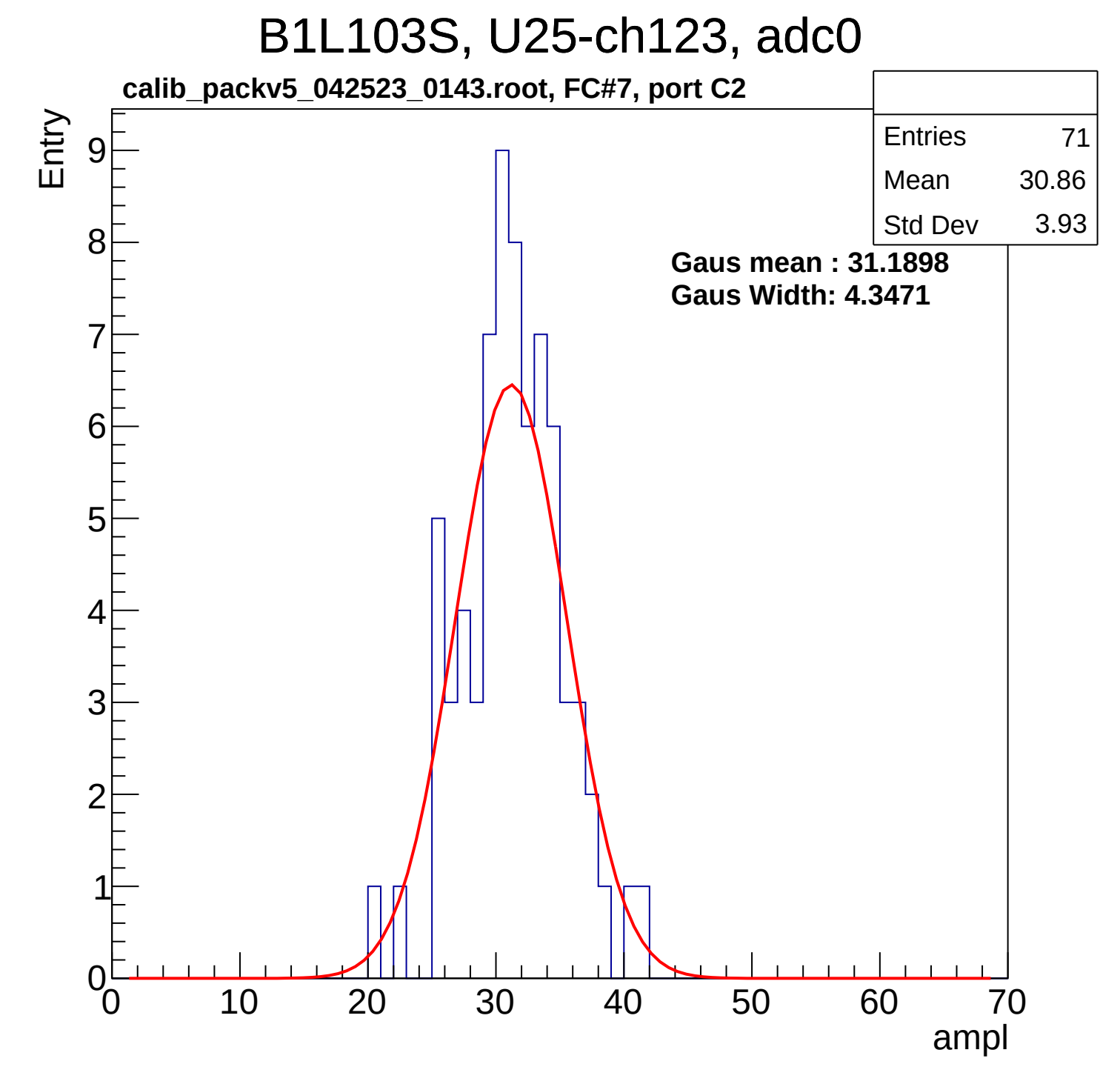
50
60
70
ampl


B1L103S, U25-ch123, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 71
Mean 30.86
Std Dev 3.93
Gaus mean : 31.1898
Gaus Width: 4.3471
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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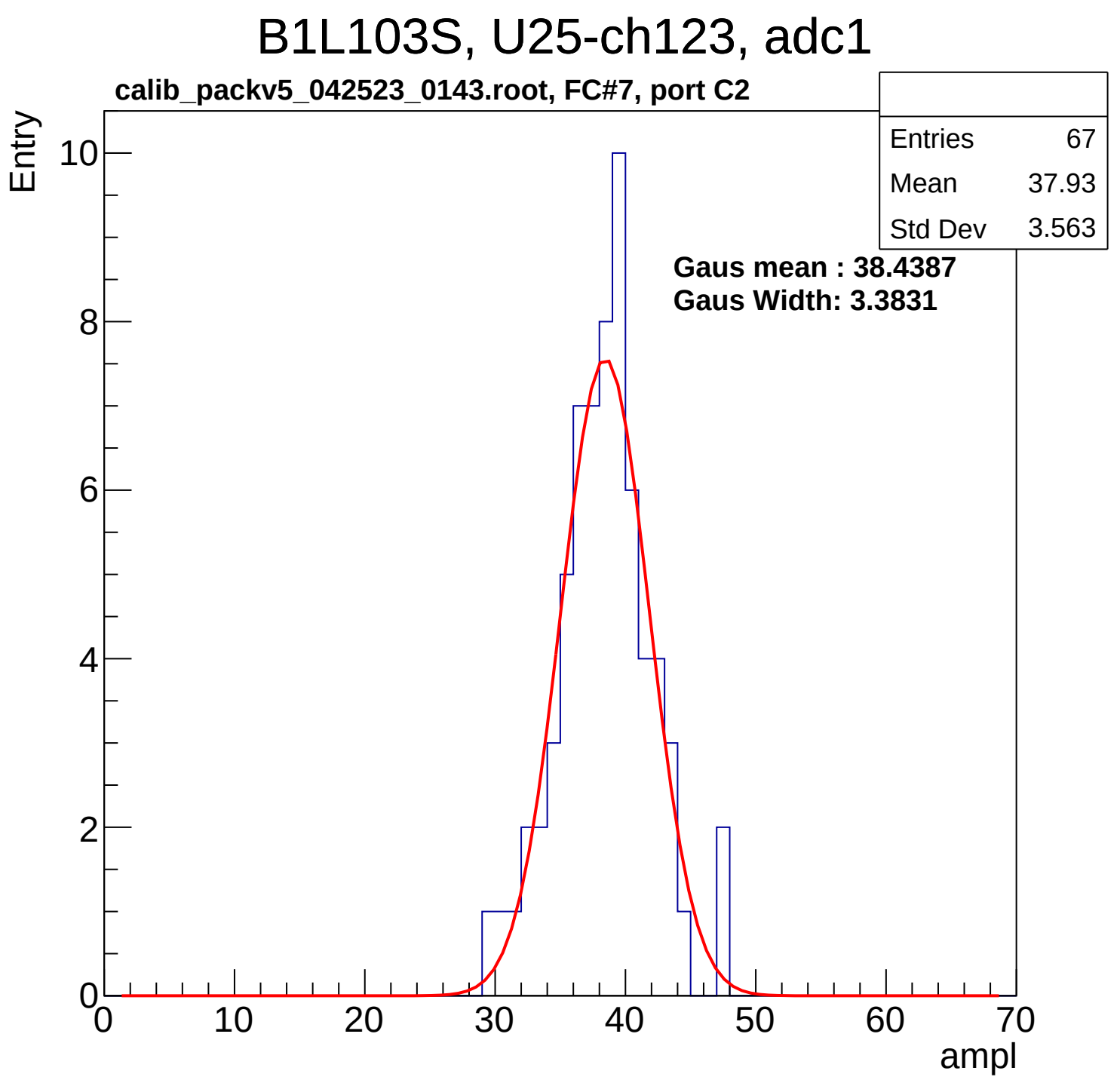
B1L103S, U25-ch123, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 67
Mean 37.93
Std Dev 3.563
Gaus mean : 38.4387
Gaus Width: 3.3831
Entry
ampl
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4
6
8
10
0
10
20
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50
60
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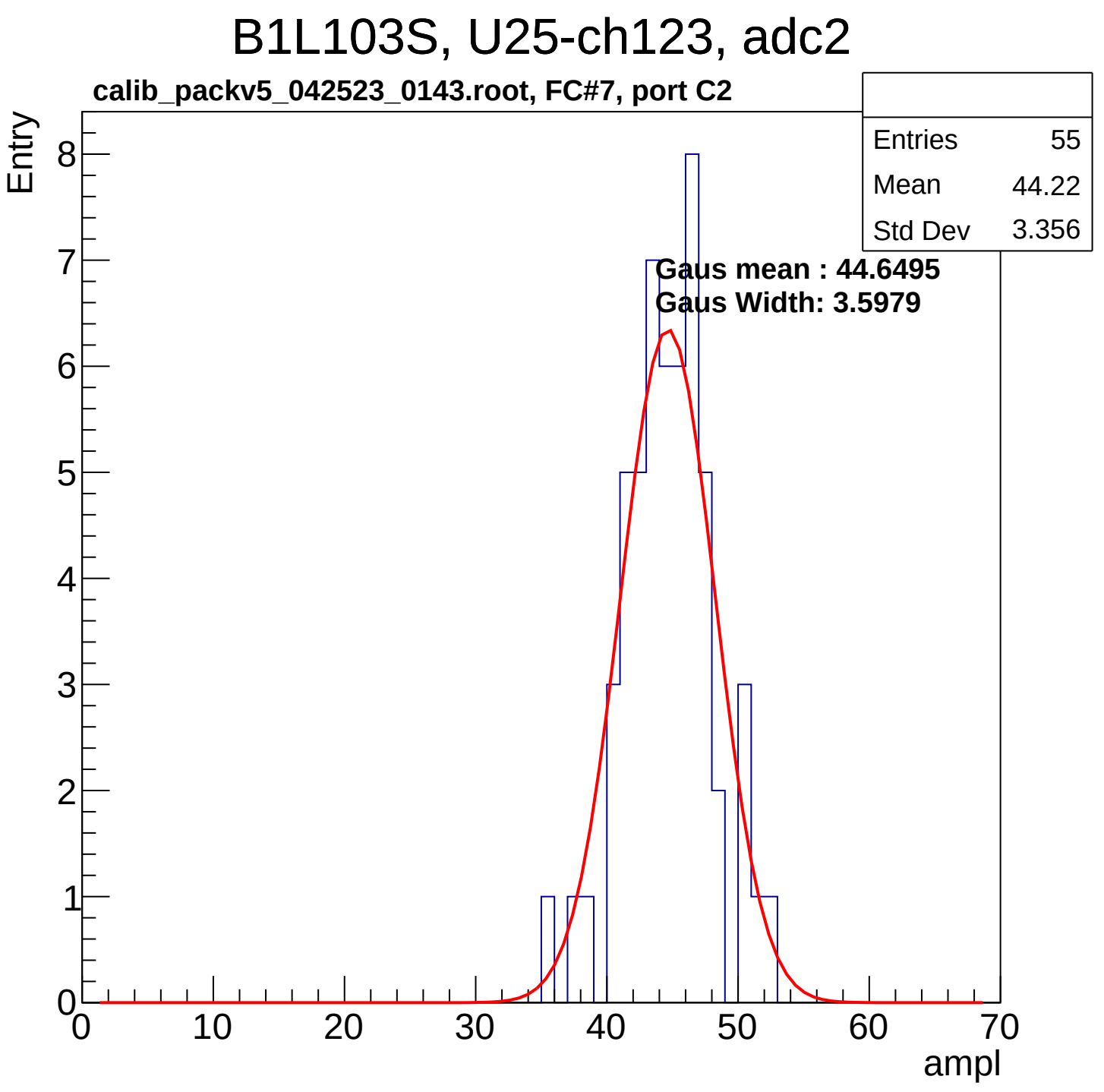
B1L103S, U25-ch123, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 44.22
Std Dev 3.356
Gaus mean : 44.6495
Gaus Width: 3.5979
Entry
ampl
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2
3
4
5
6
7
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0
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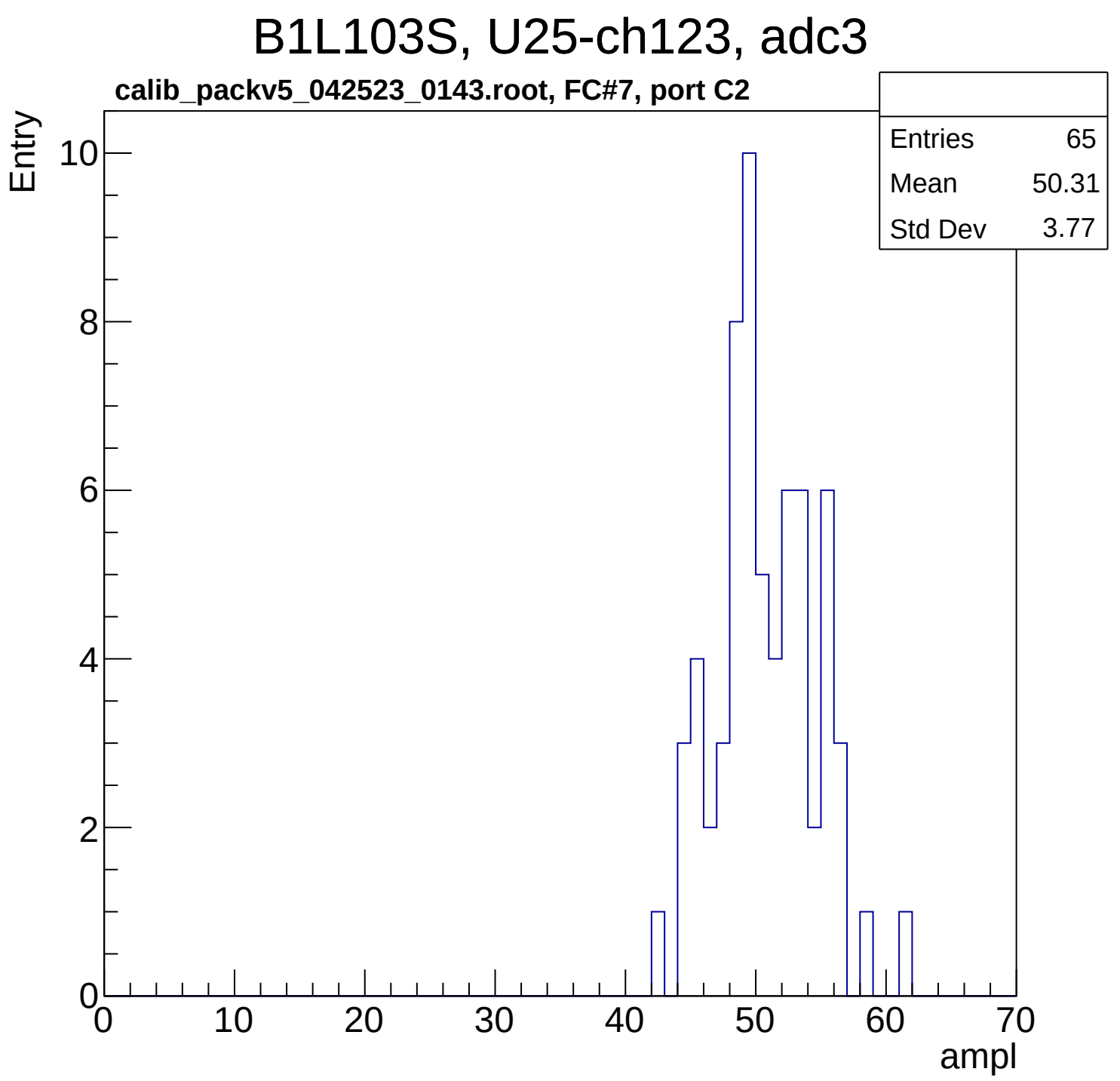
B1L103S, U25-ch123, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 65
Mean 50.31
Std Dev 3.77
Entry
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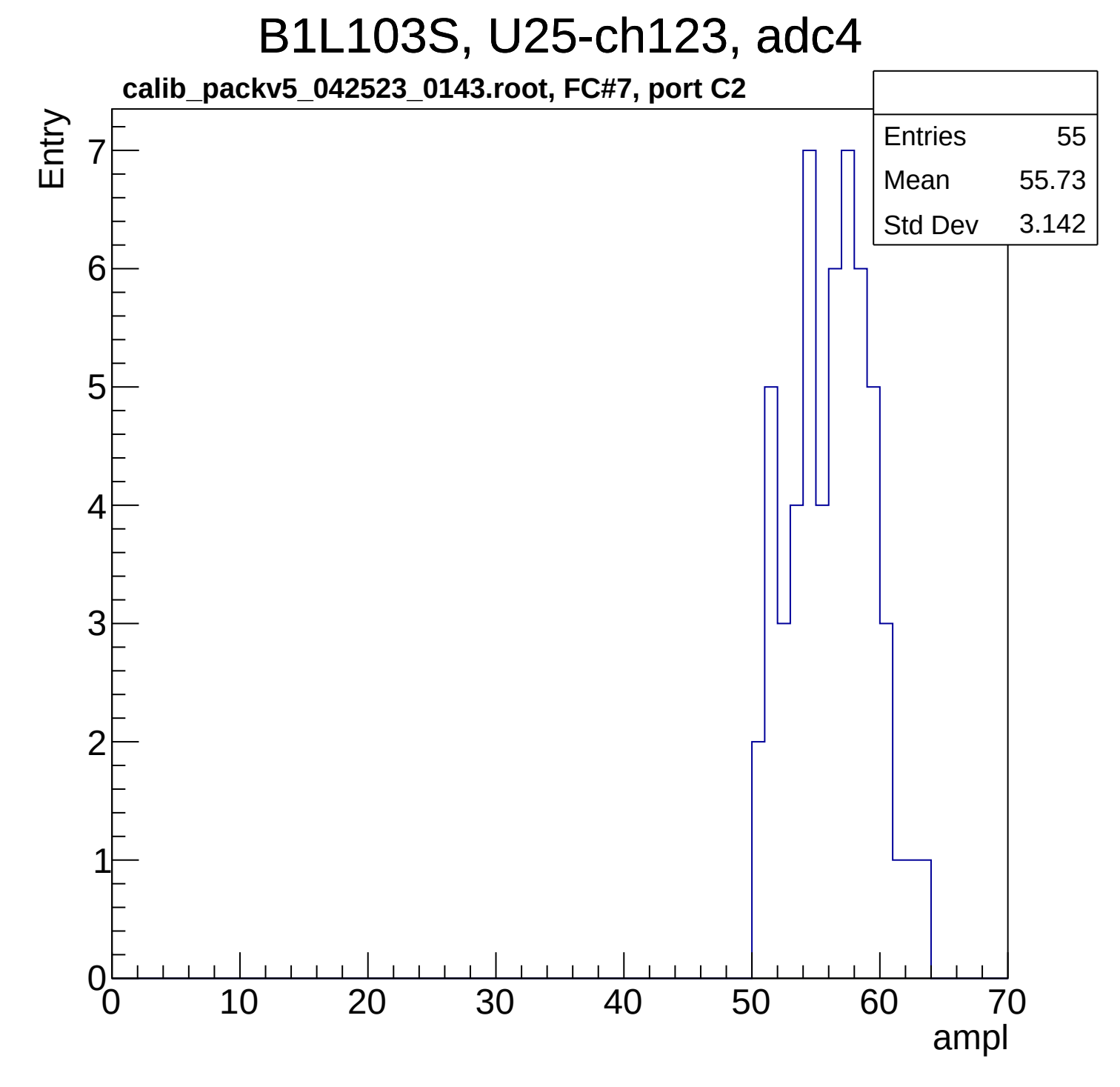
70
ampl


B1L103S, U25-ch123, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 55
Mean 55.73
Std Dev 3.142
Entry
ampl
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60
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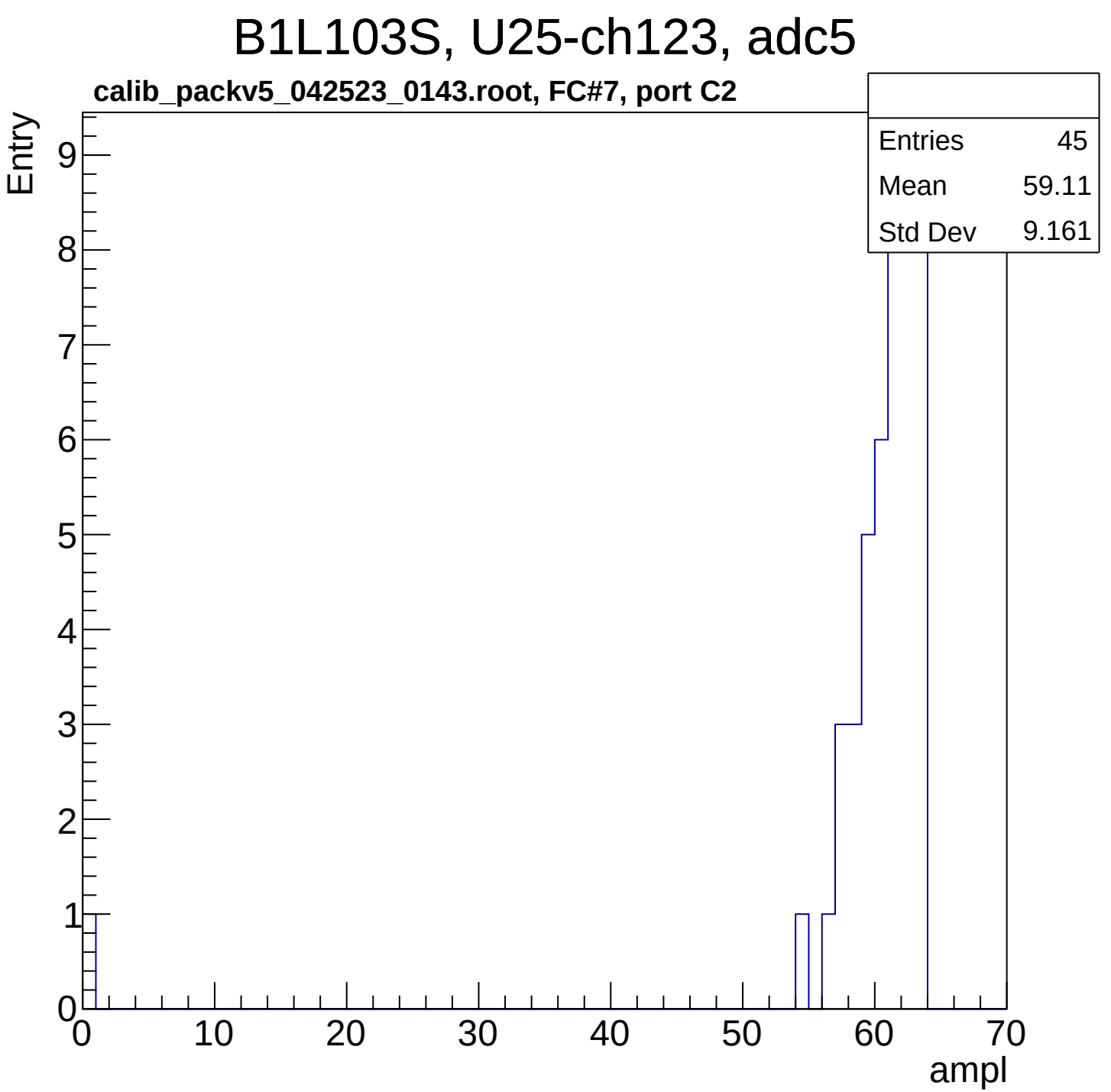
B1L103S, U25-ch123, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 59.11
Std Dev 9.161
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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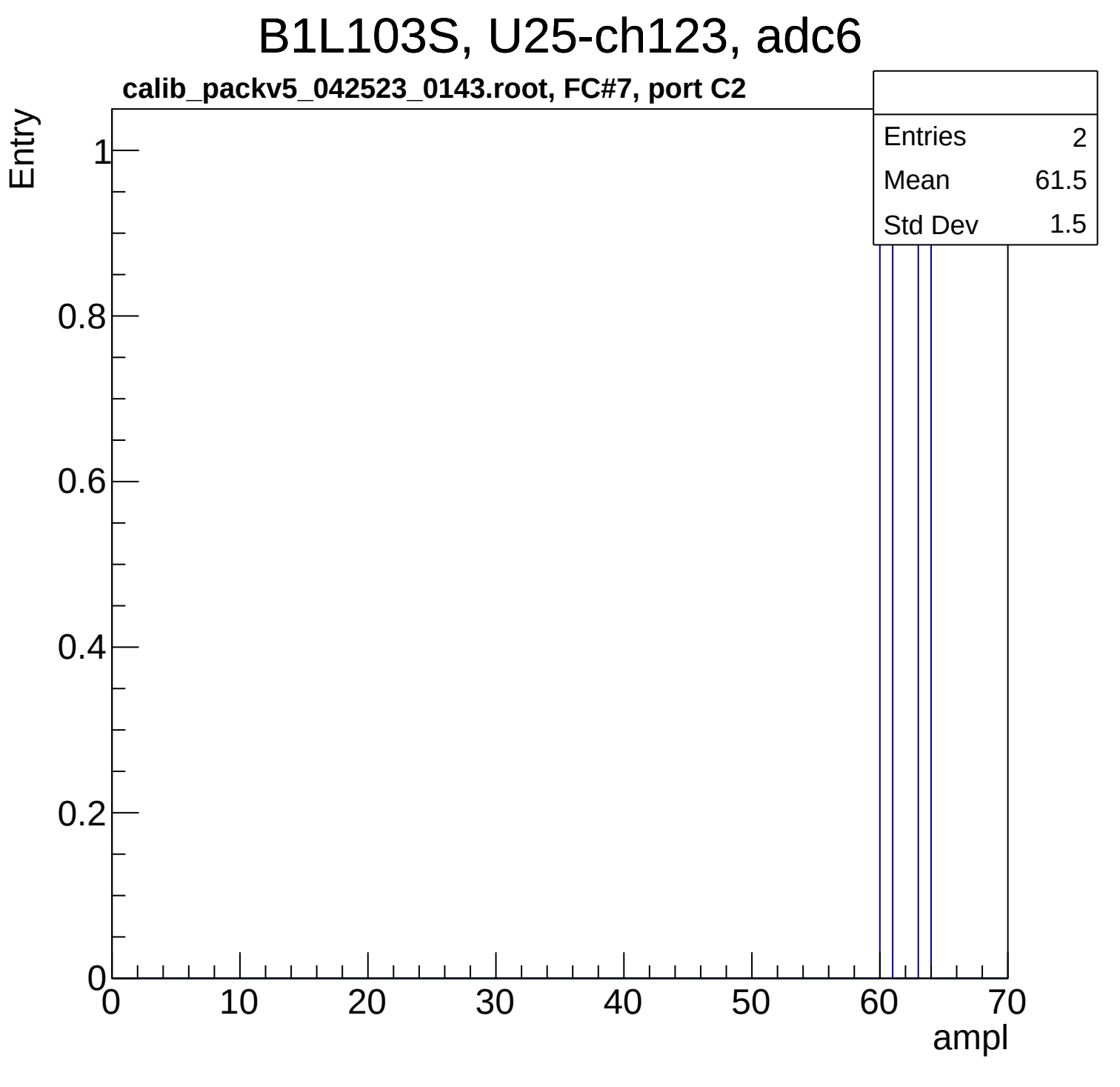
B1L103S, U25-ch123, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
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20
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60
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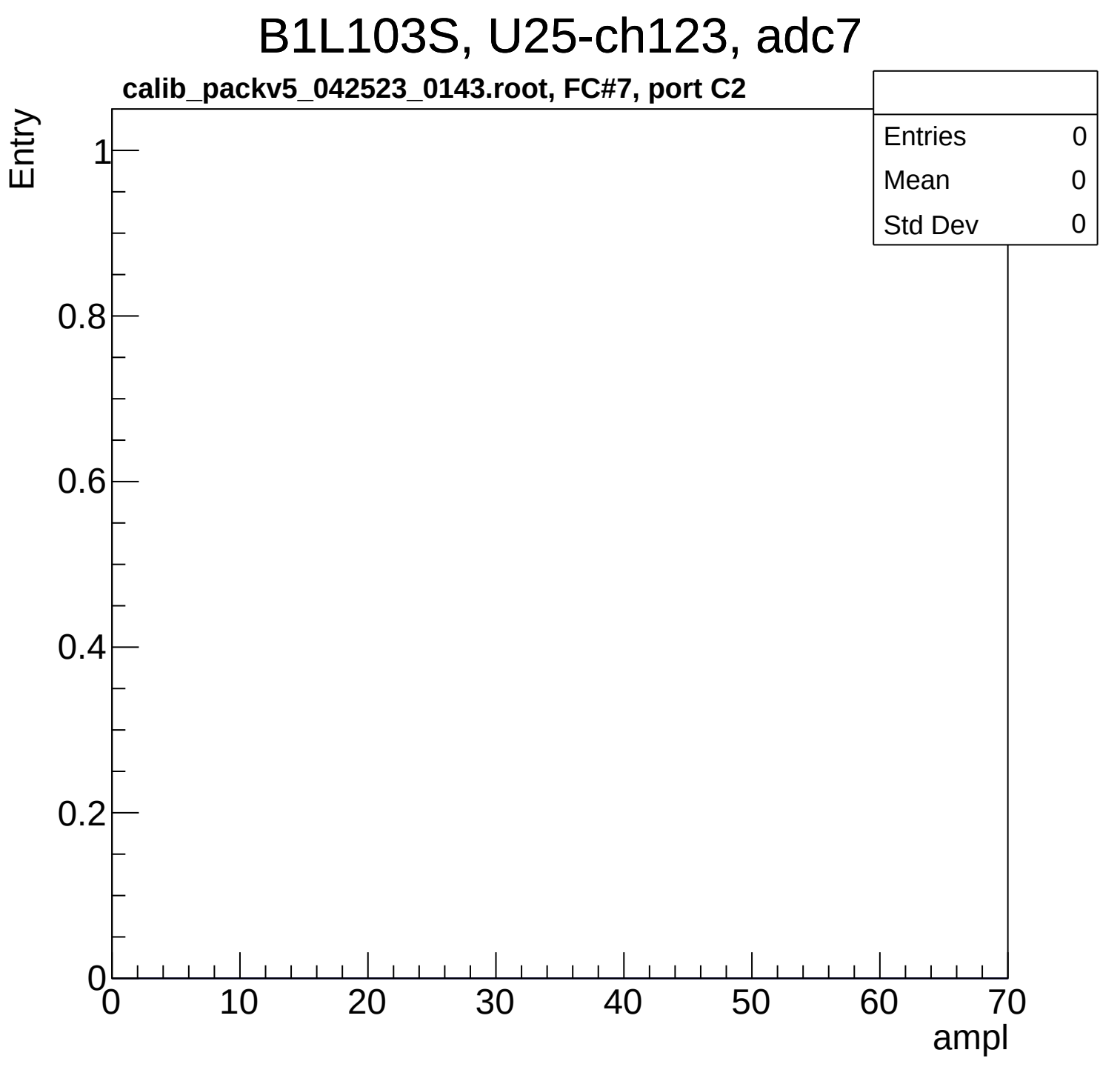
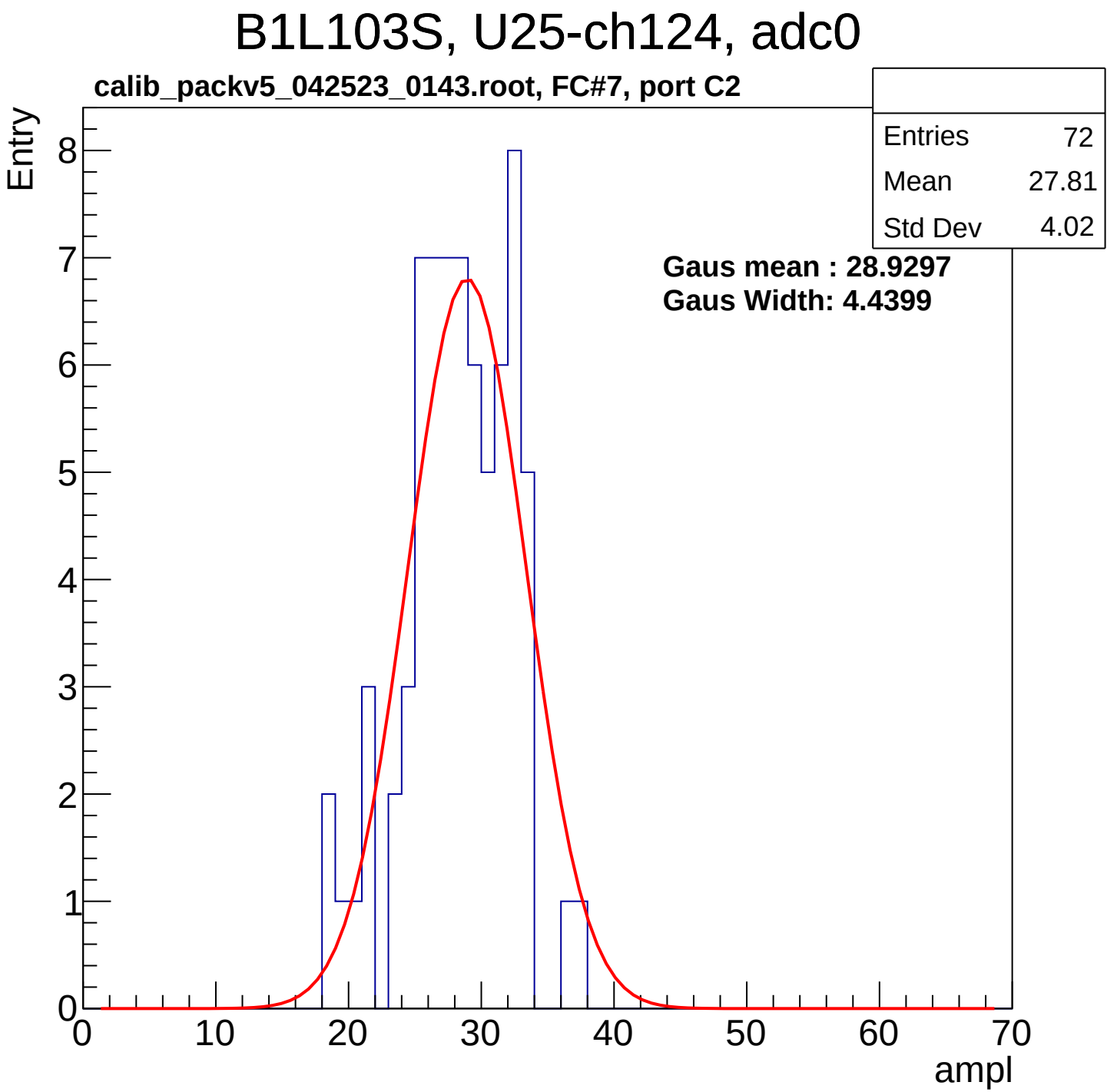

B1L103S, U25-ch123, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch124, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 72
Mean 27.81
Std Dev 4.02
Gaus mean : 28.9297
Gaus Width: 4.4399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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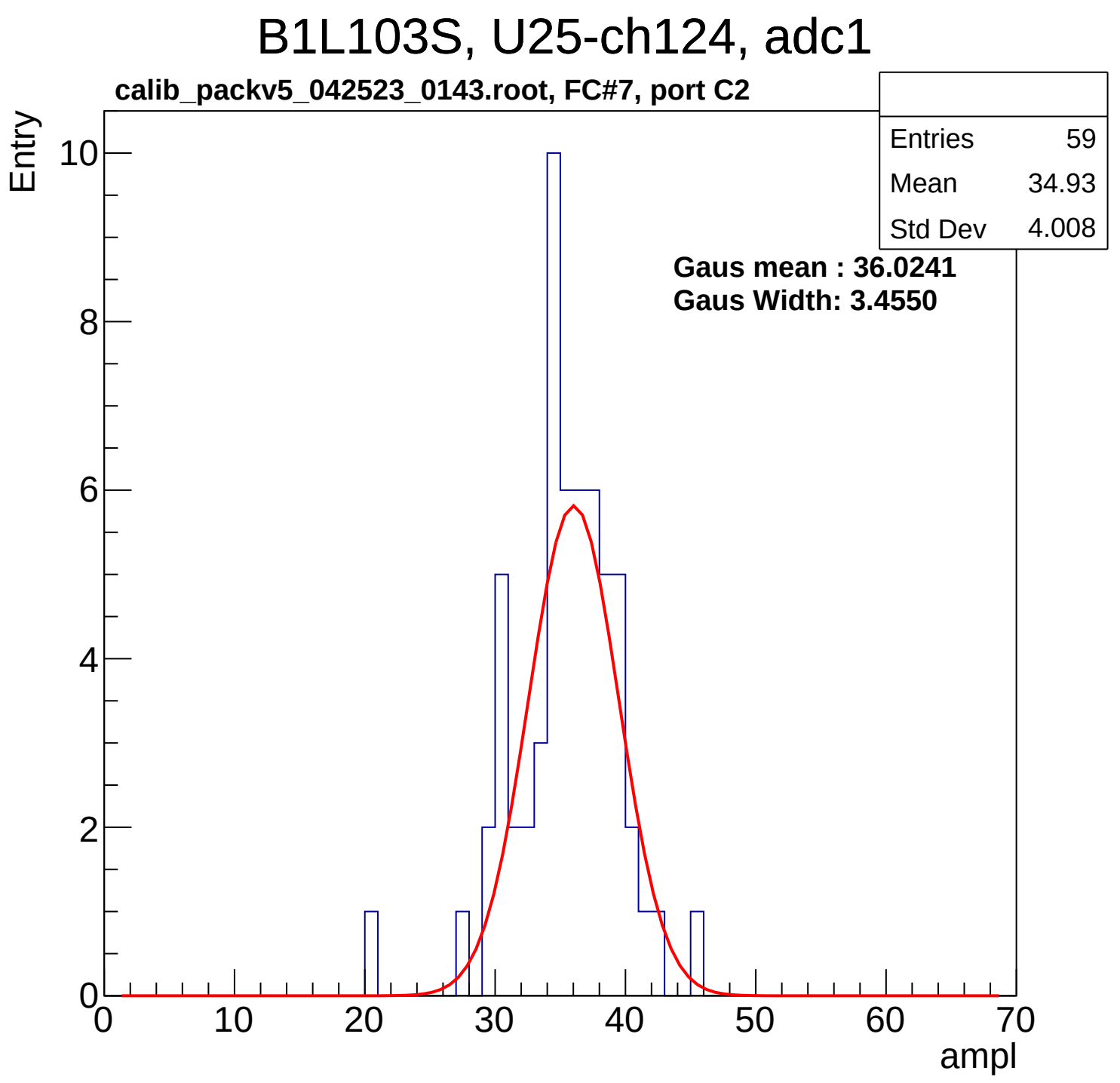
B1L103S, U25-ch124, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 59
Mean 34.93
Std Dev 4.008
Gaus mean : 36.0241
Gaus Width: 3.4550
Entry
ampl
0
2
4
6
8
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0
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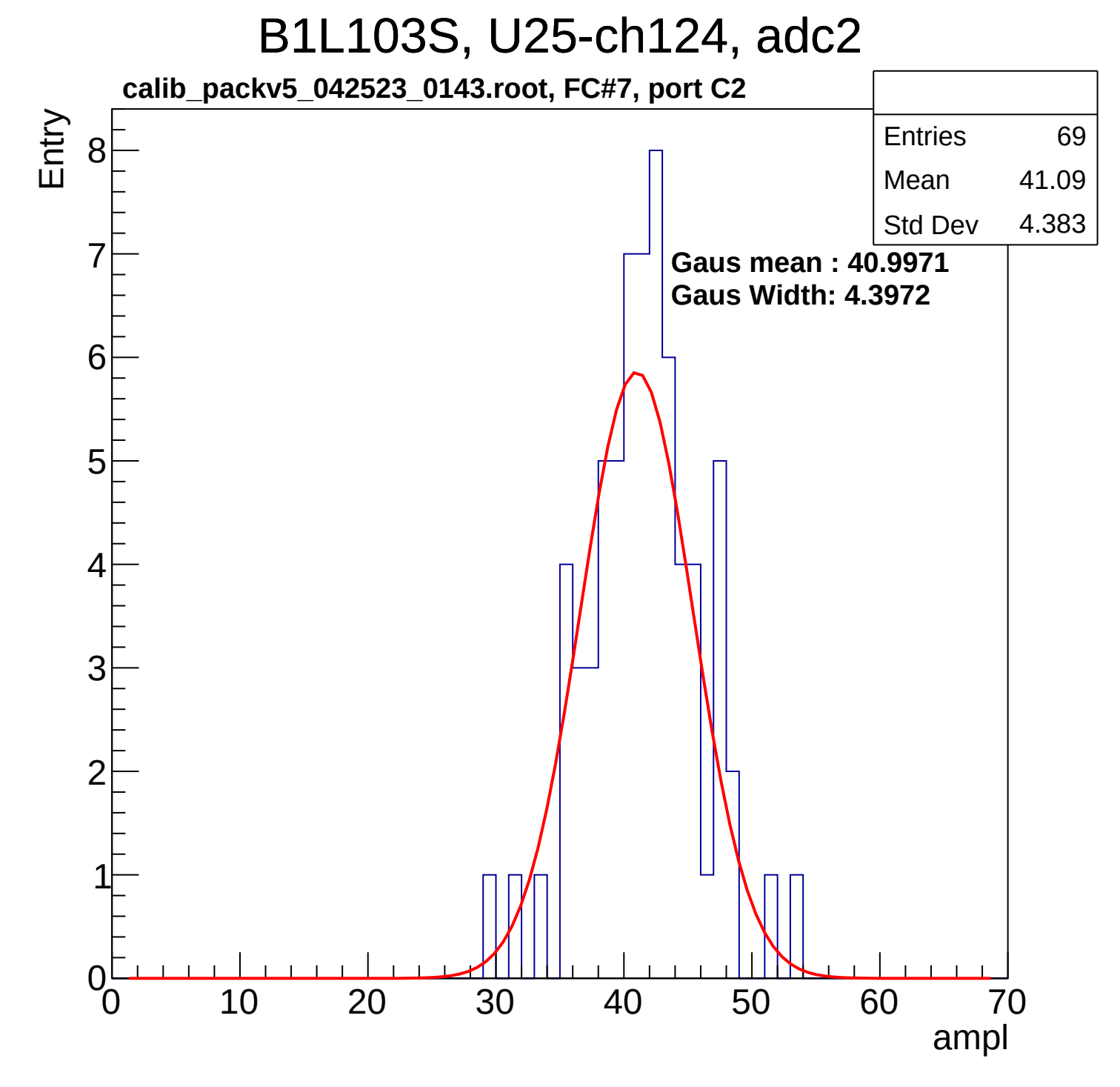
B1L103S, U25-ch124, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 69
Mean 41.09
Std Dev 4.383
Gaus mean : 40.9971
Gaus Width: 4.3972
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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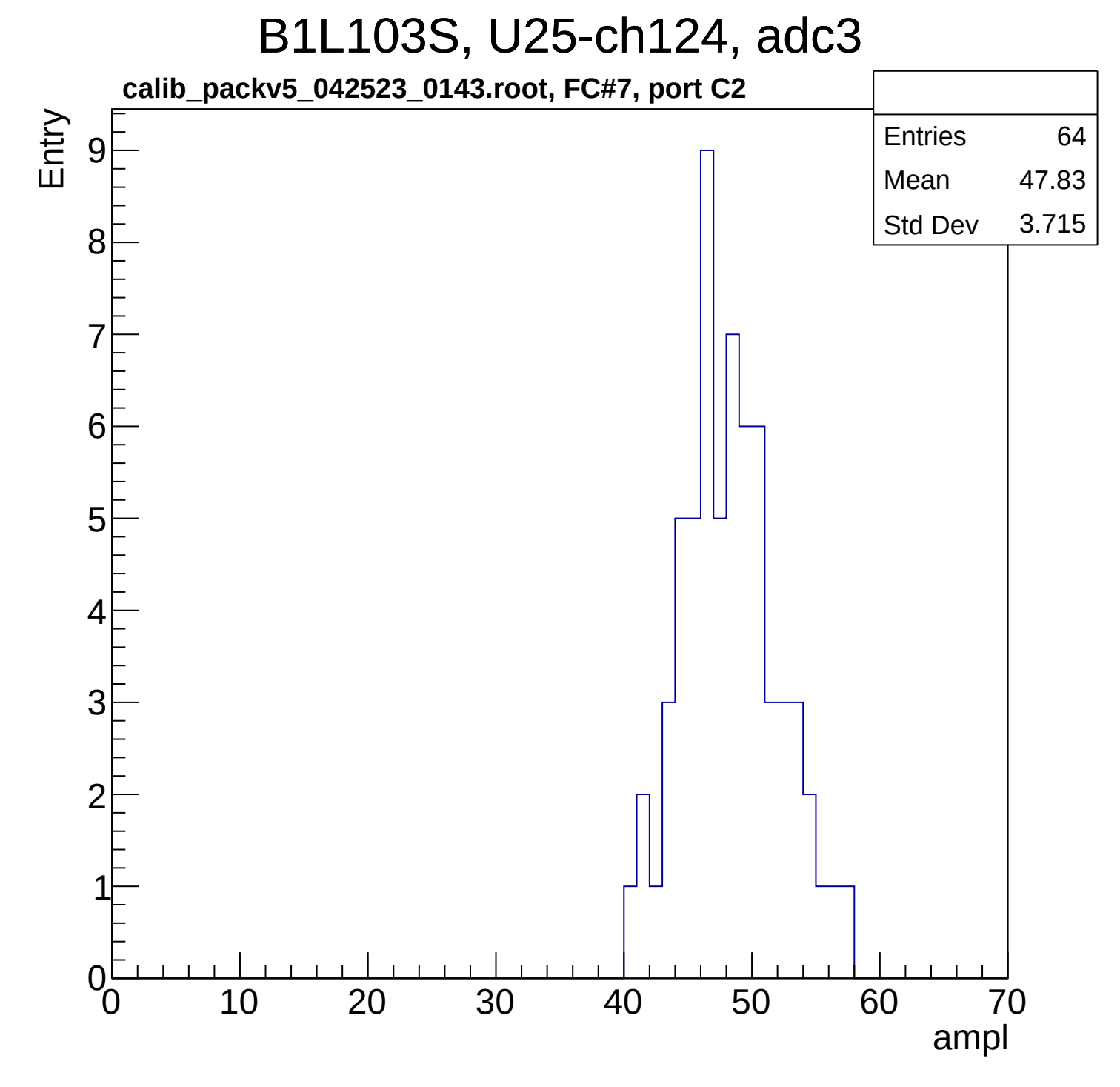
B1L103S, U25-ch124, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 47.83
Std Dev 3.715
Entry
ampl
0
1
2
3
4
5
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0
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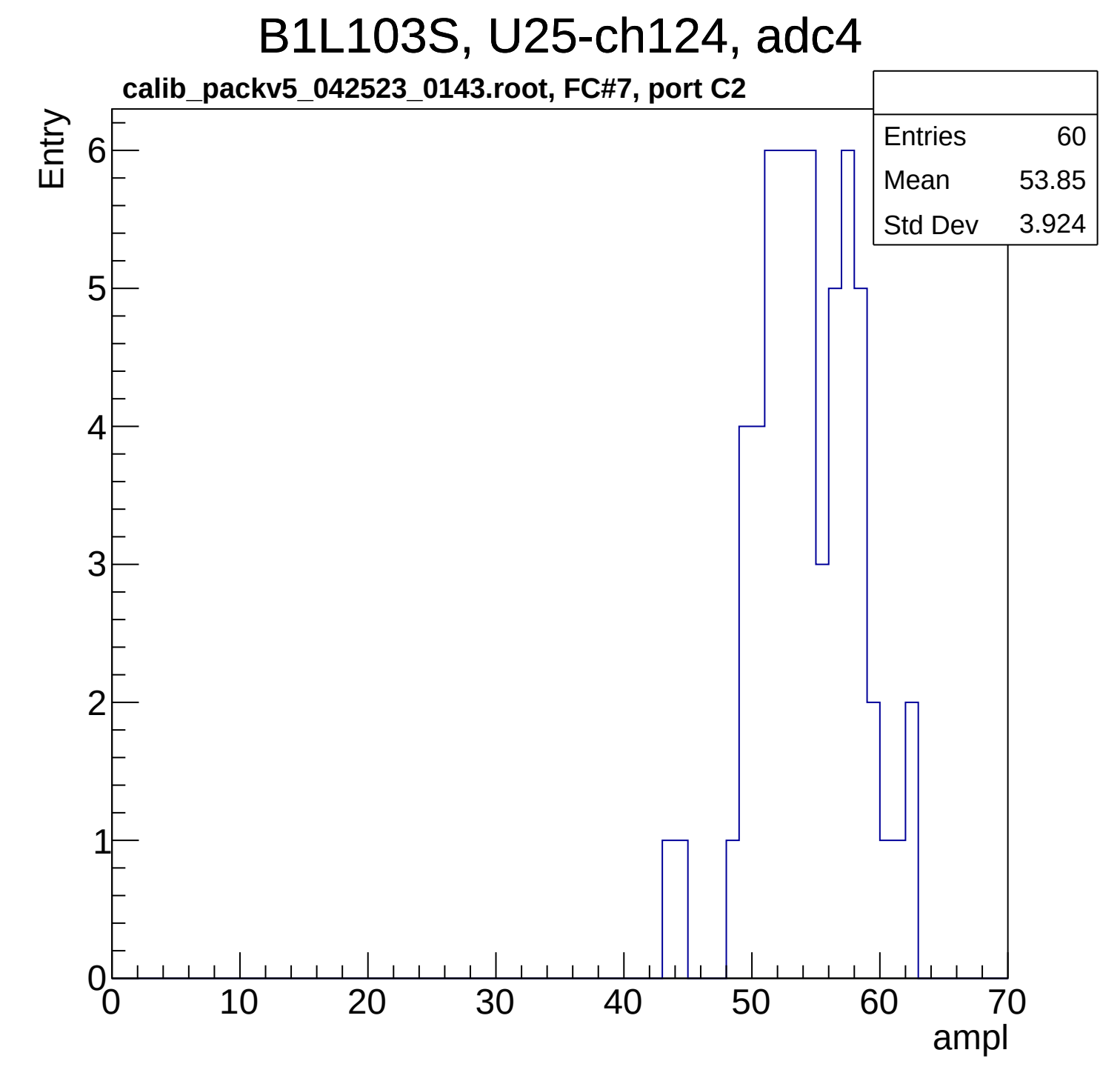
B1L103S, U25-ch124, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 60
Mean 53.85
Std Dev 3.924
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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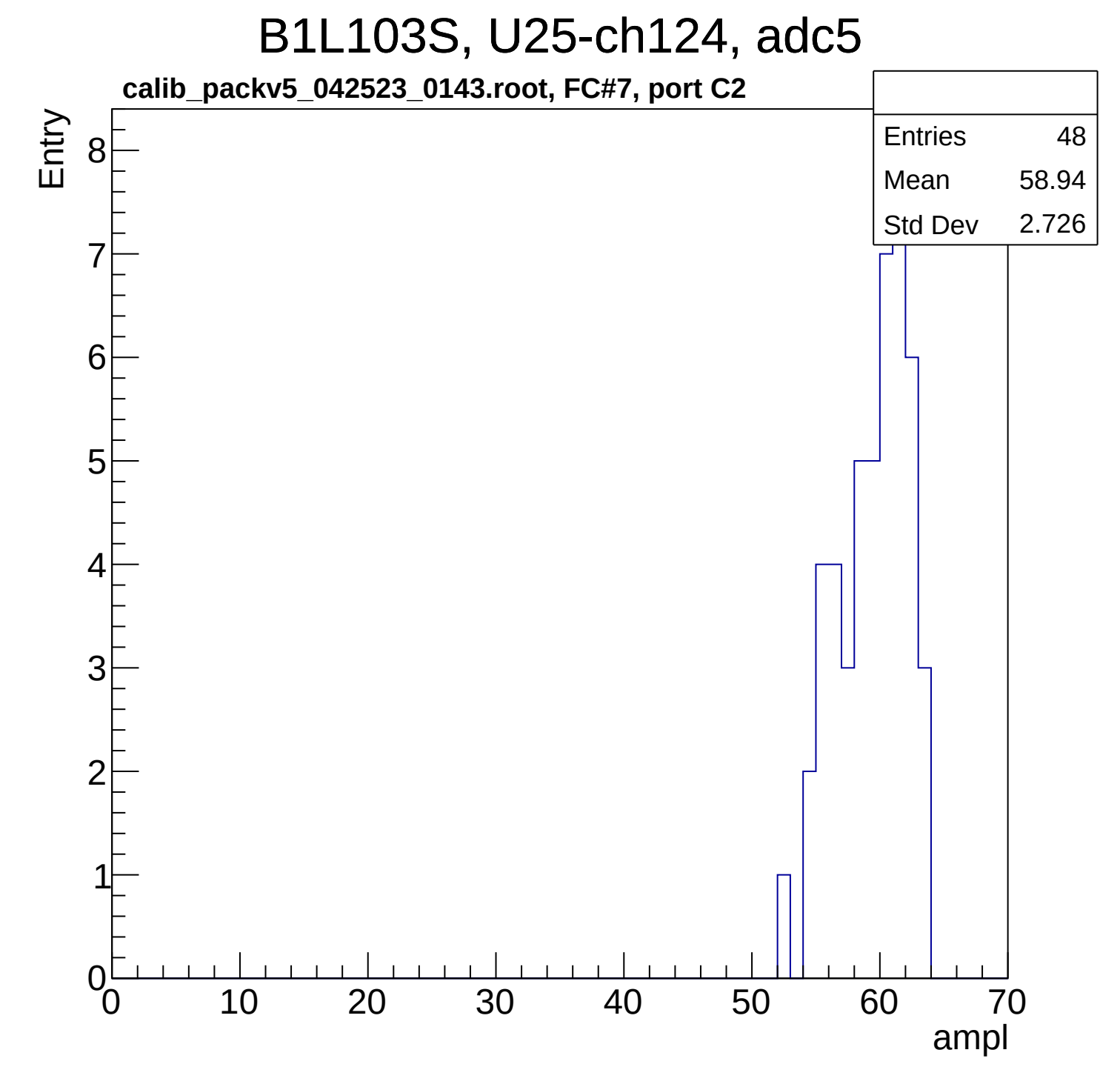
B1L103S, U25-ch124, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 58.94
Std Dev 2.726
Entry
ampl
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1
2
3
4
5
6
7
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0
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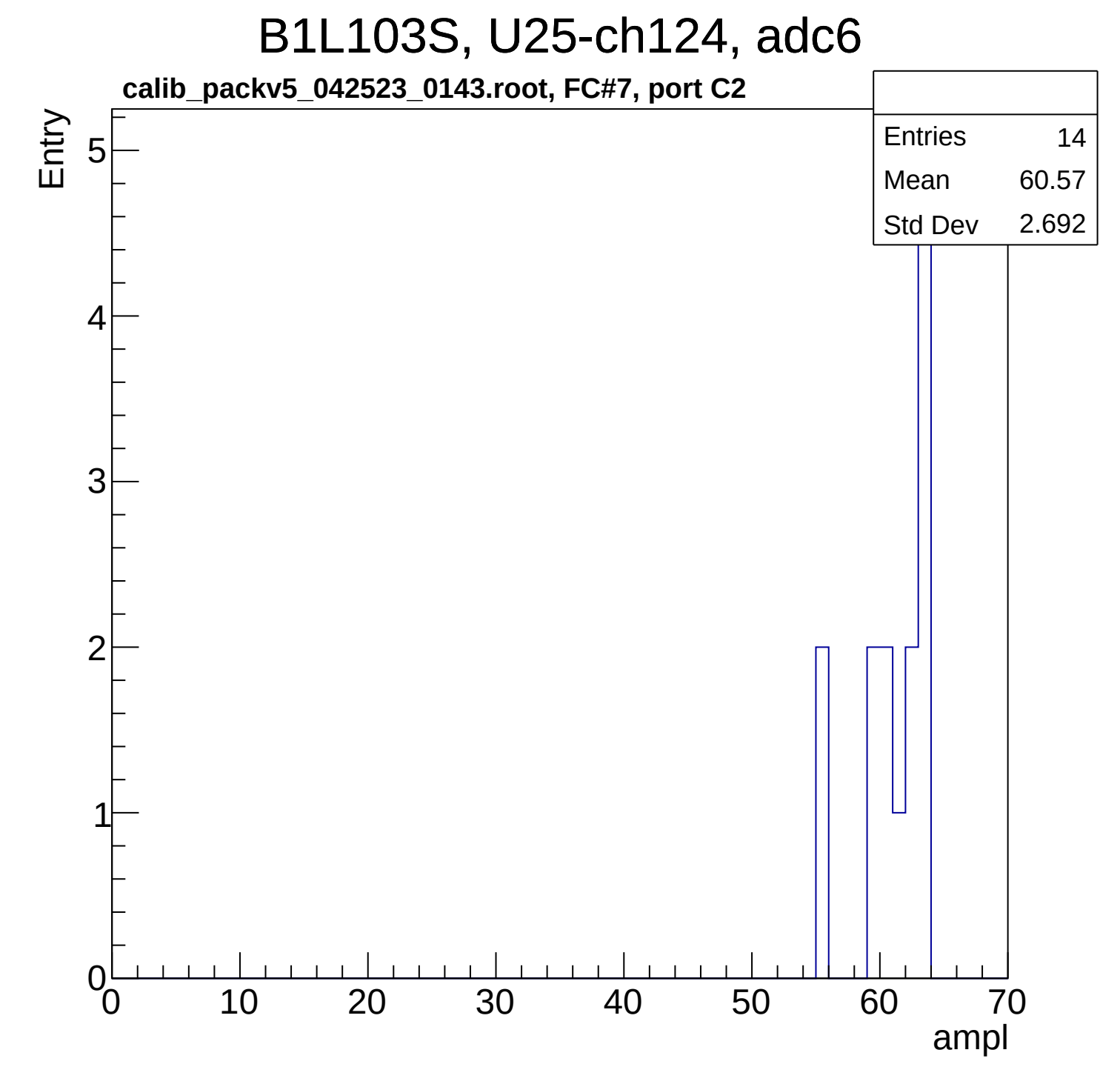
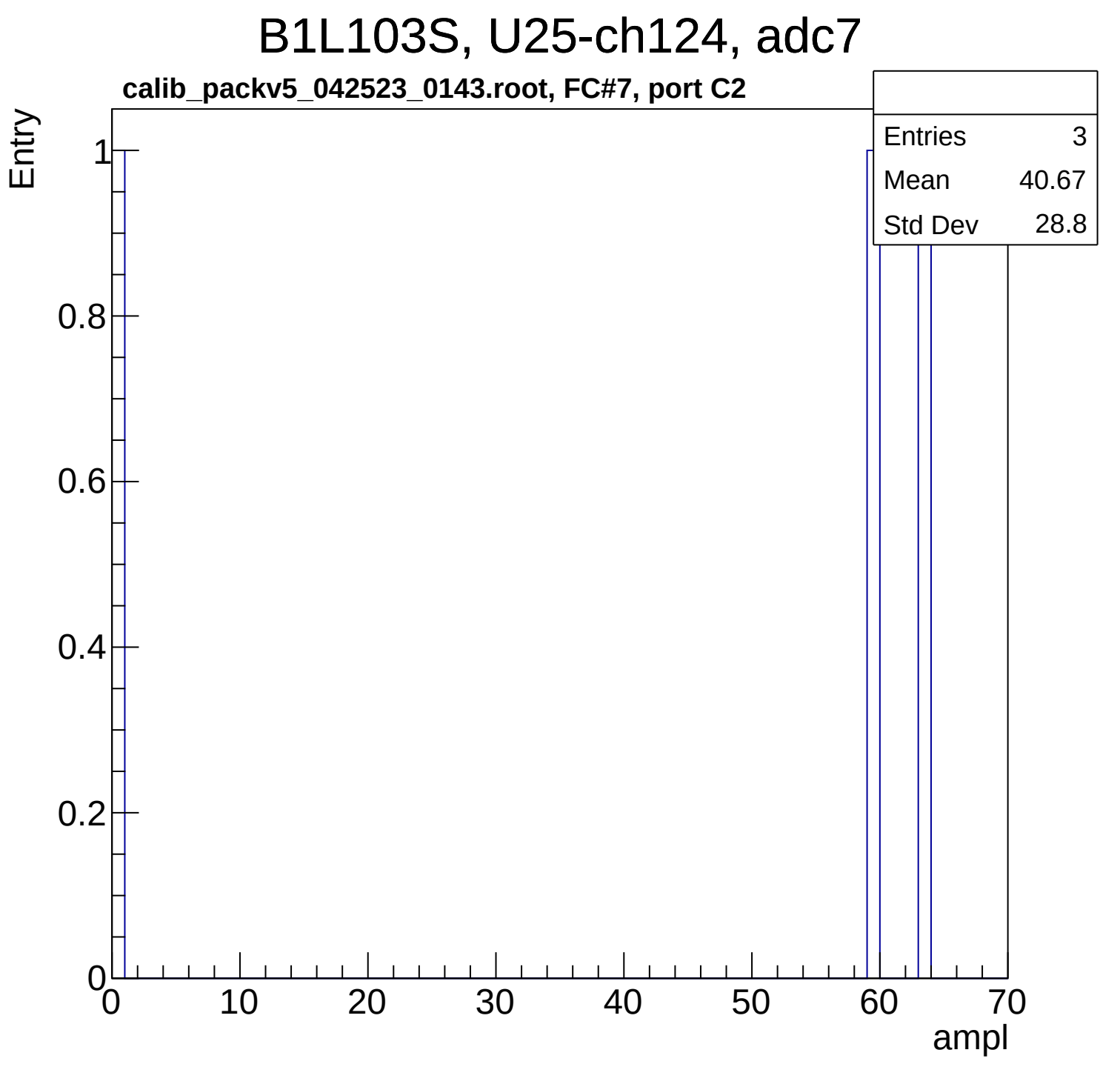
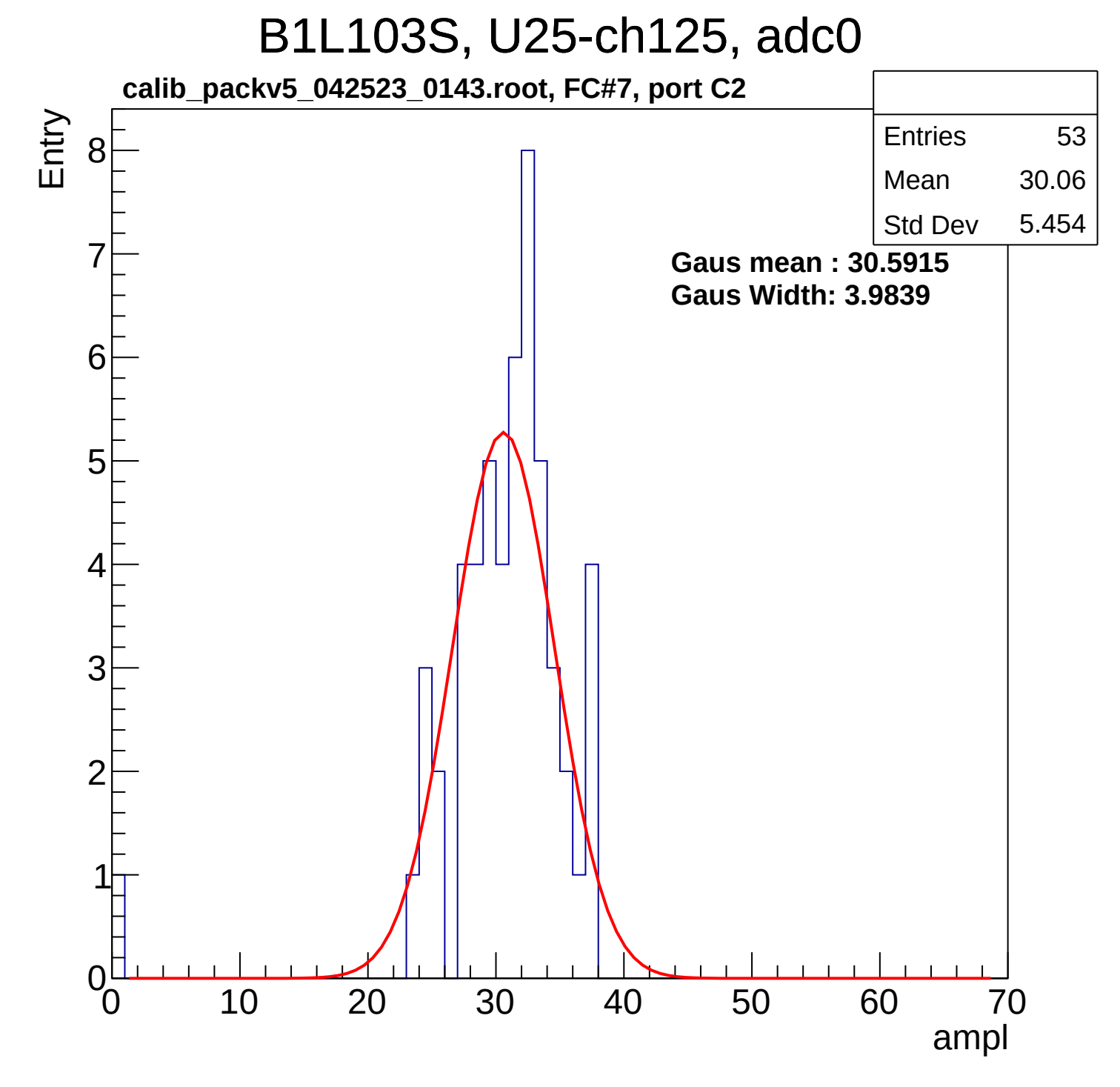

B1L103S, U25-ch124, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 14
Mean 60.57
Std Dev 2.692
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U25-ch124, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 3
Mean 40.67
Std Dev 28.8
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L103S, U25-ch125, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 53
Mean 30.06
Std Dev 5.454
Gaus mean : 30.5915
Gaus Width: 3.9839
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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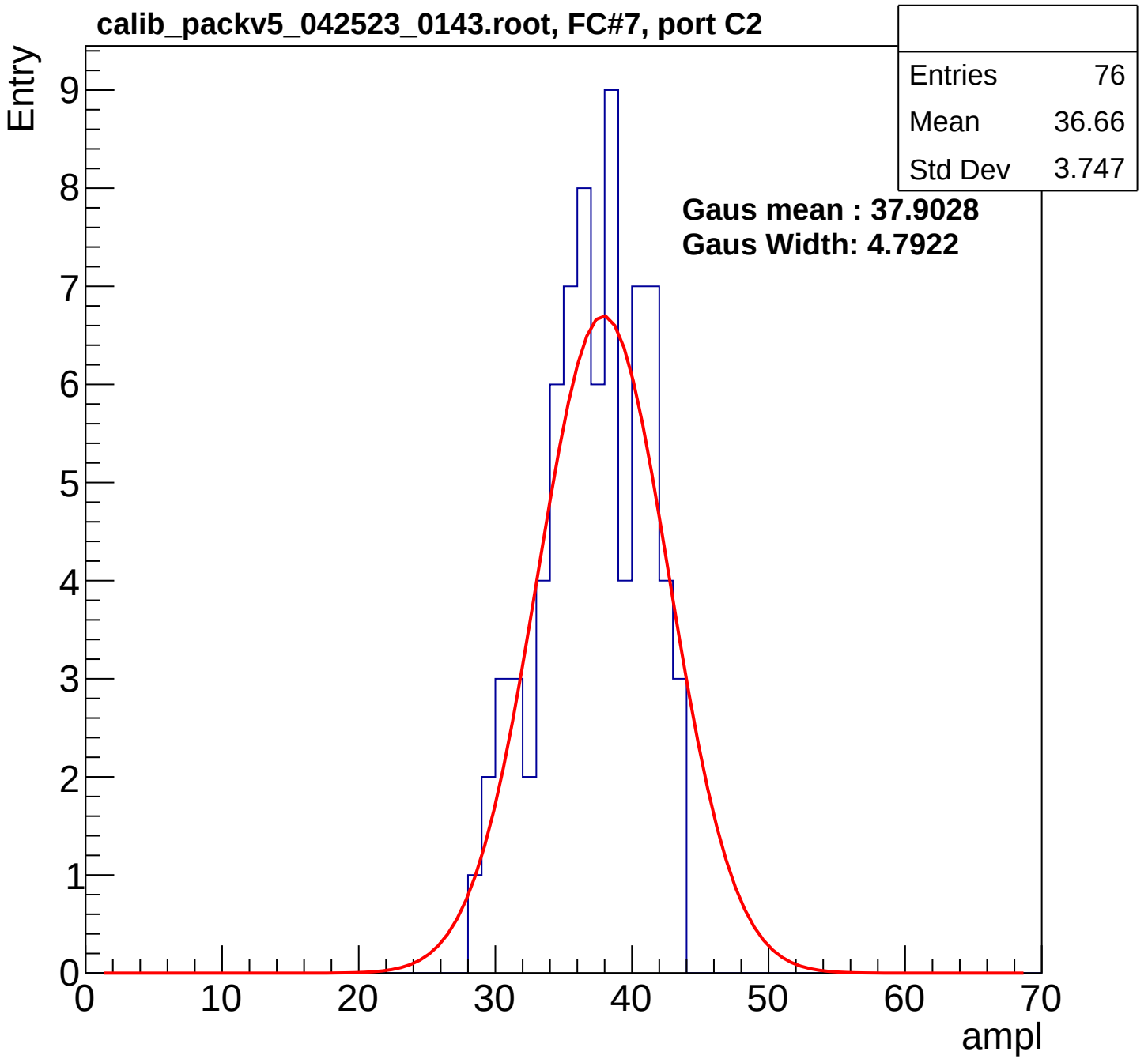
B1L103S, U25-ch125, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 76
Mean 36.66
Std Dev 3.747
Gaus mean : 37.9028
Gaus Width: 4.7922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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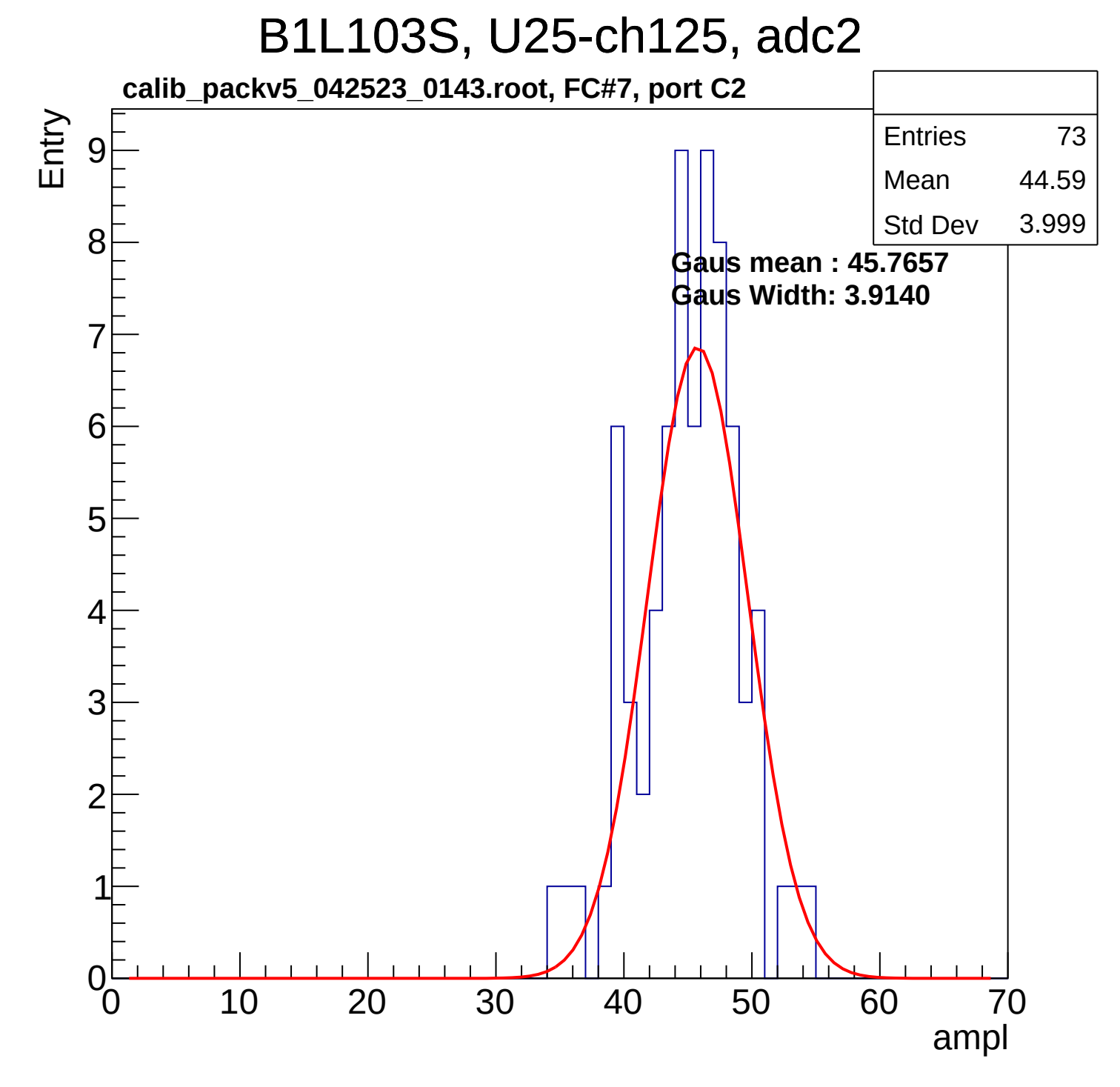
B1L103S, U25-ch125, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 73
Mean 44.59
Std Dev 3.999
Gaus mean : 45.7657
Gaus Width: 3.9140
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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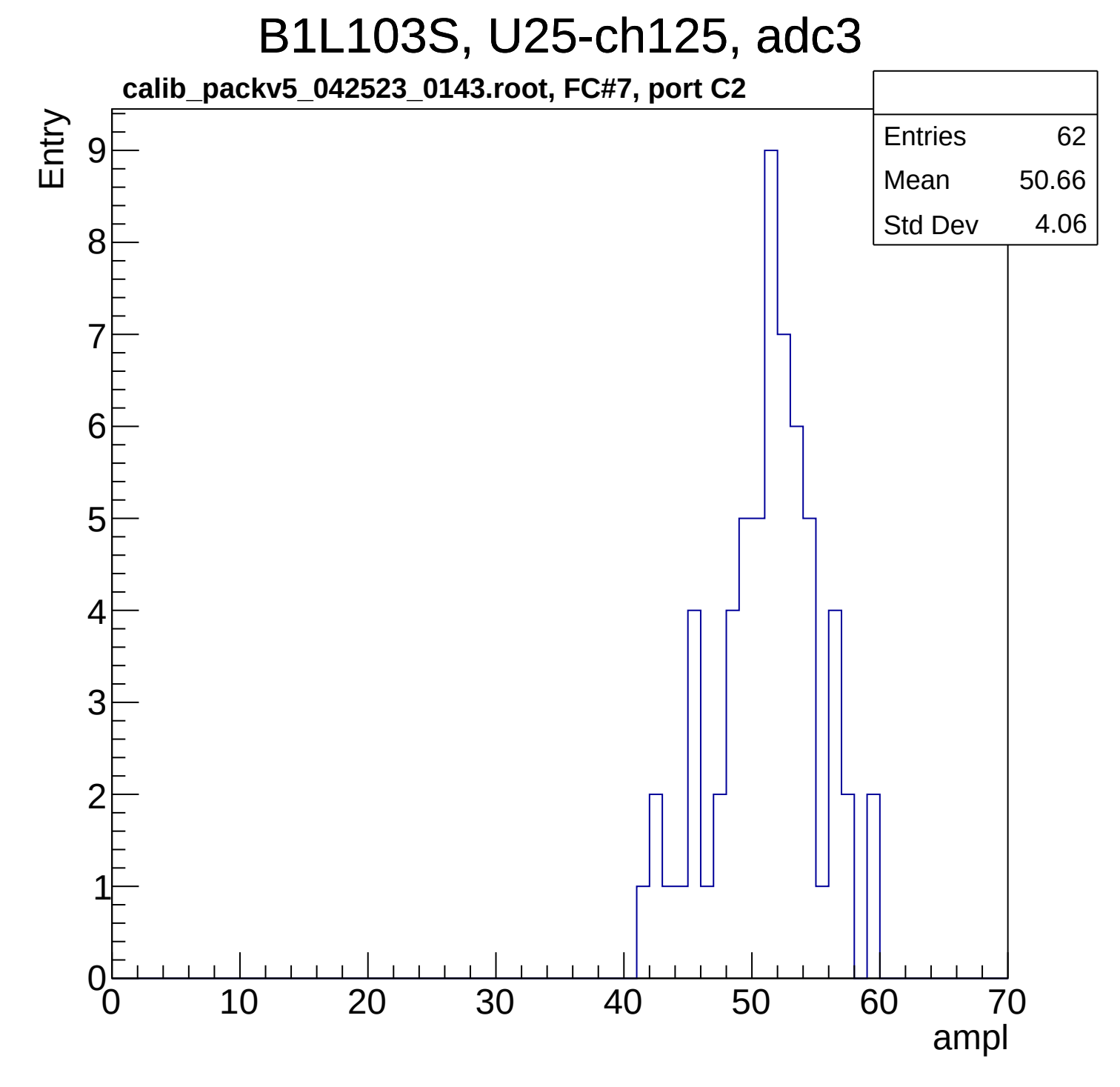
B1L103S, U25-ch125, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 62
Mean 50.66
Std Dev 4.06
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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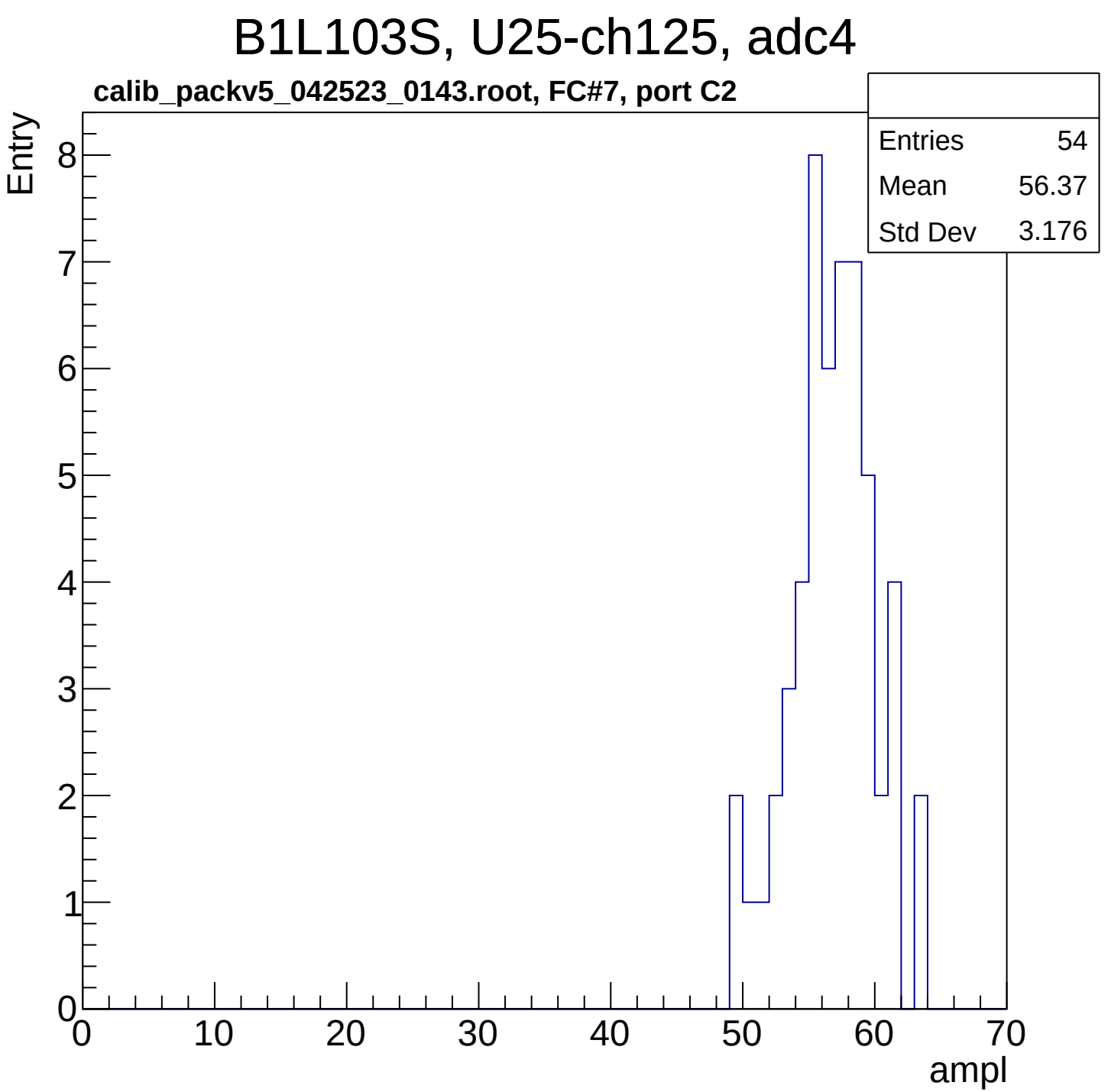
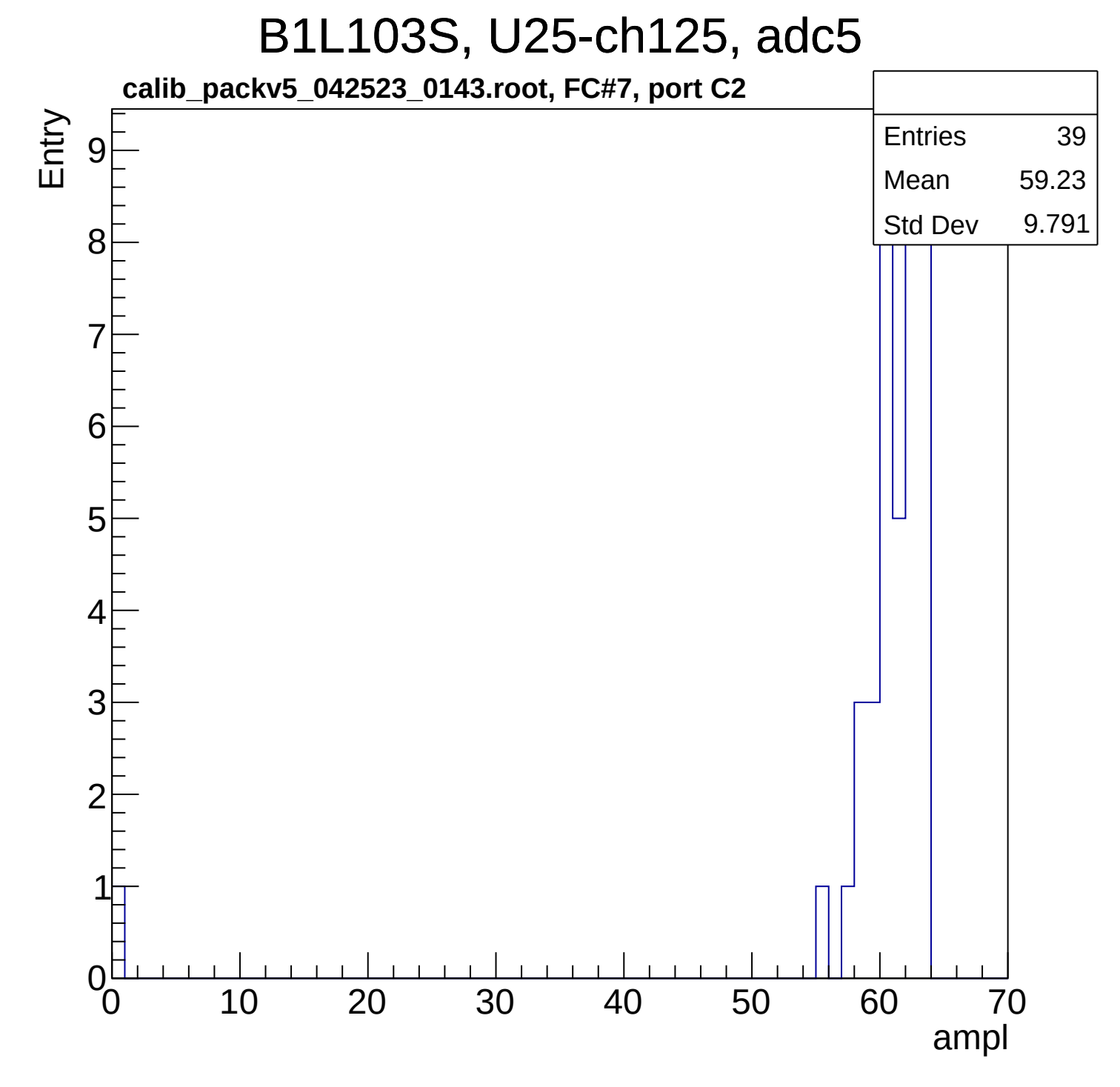
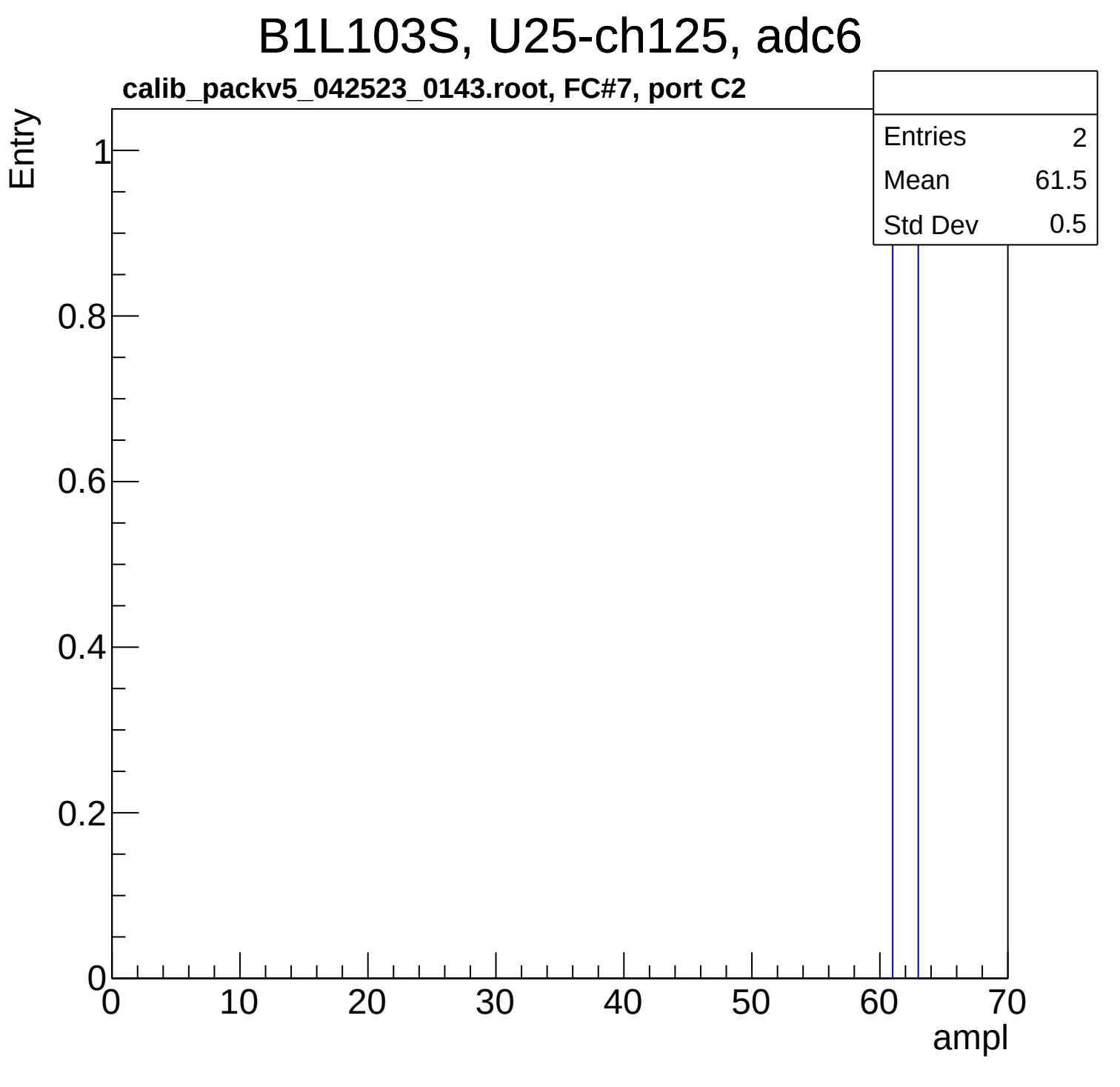
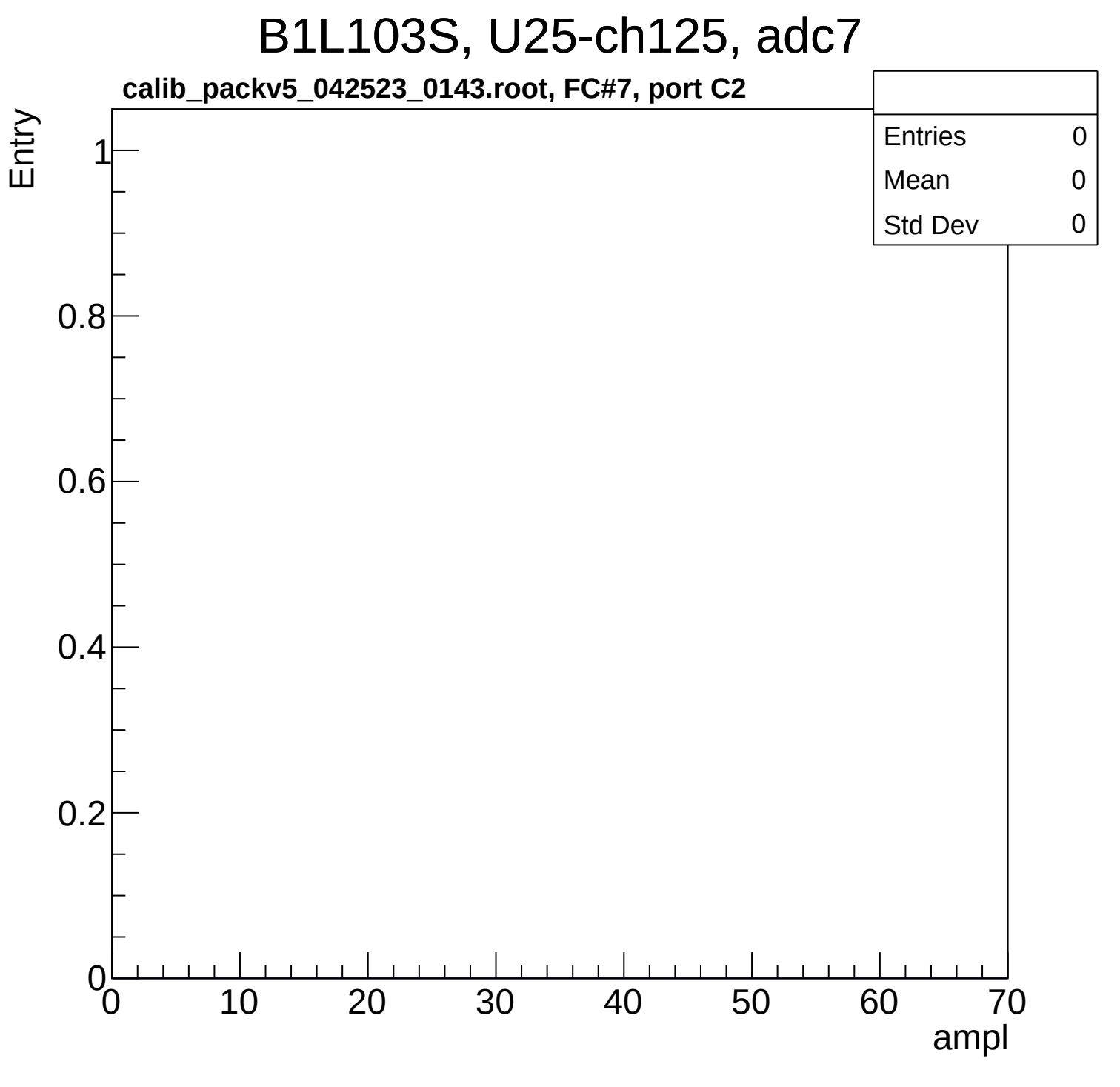
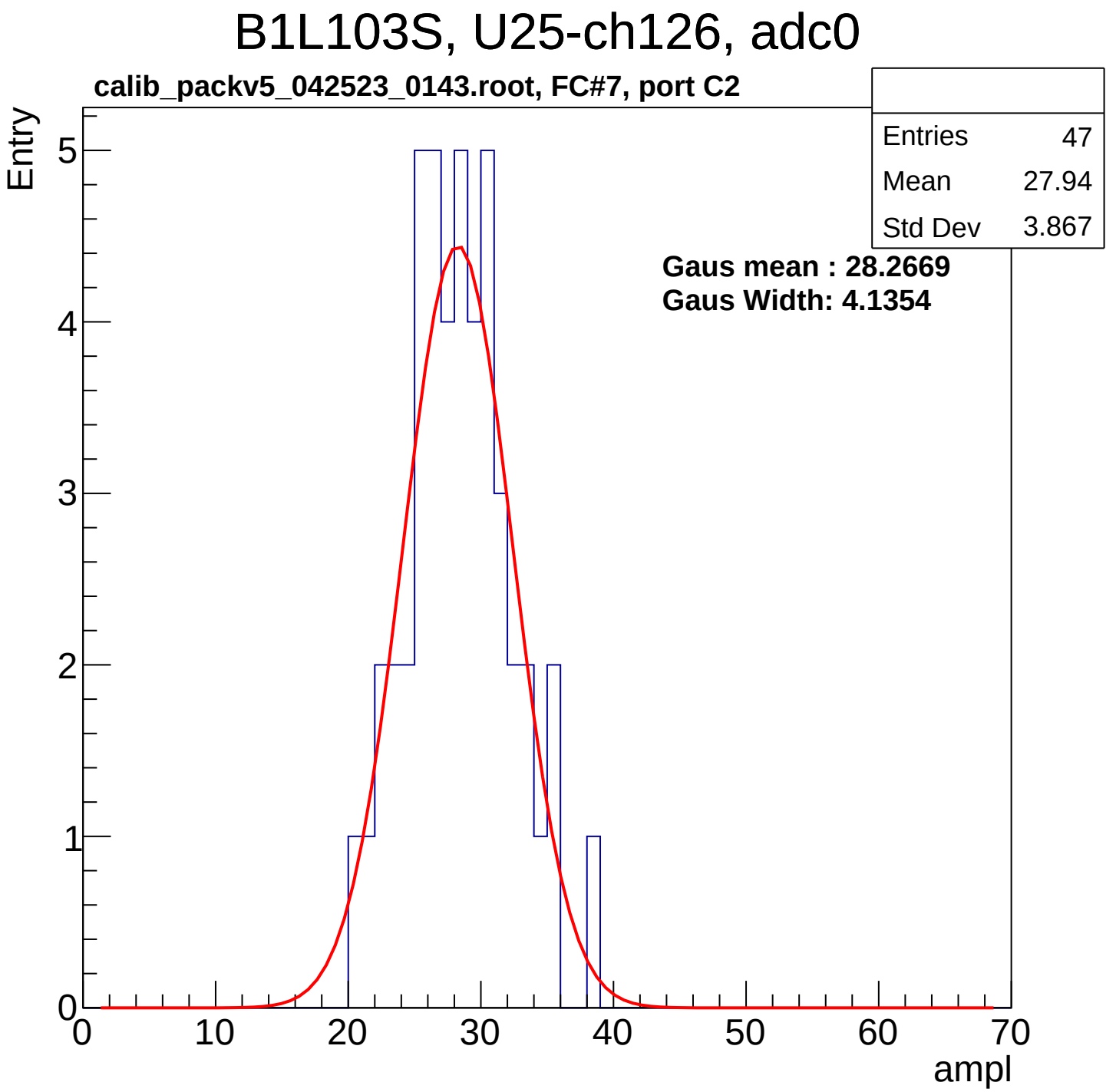
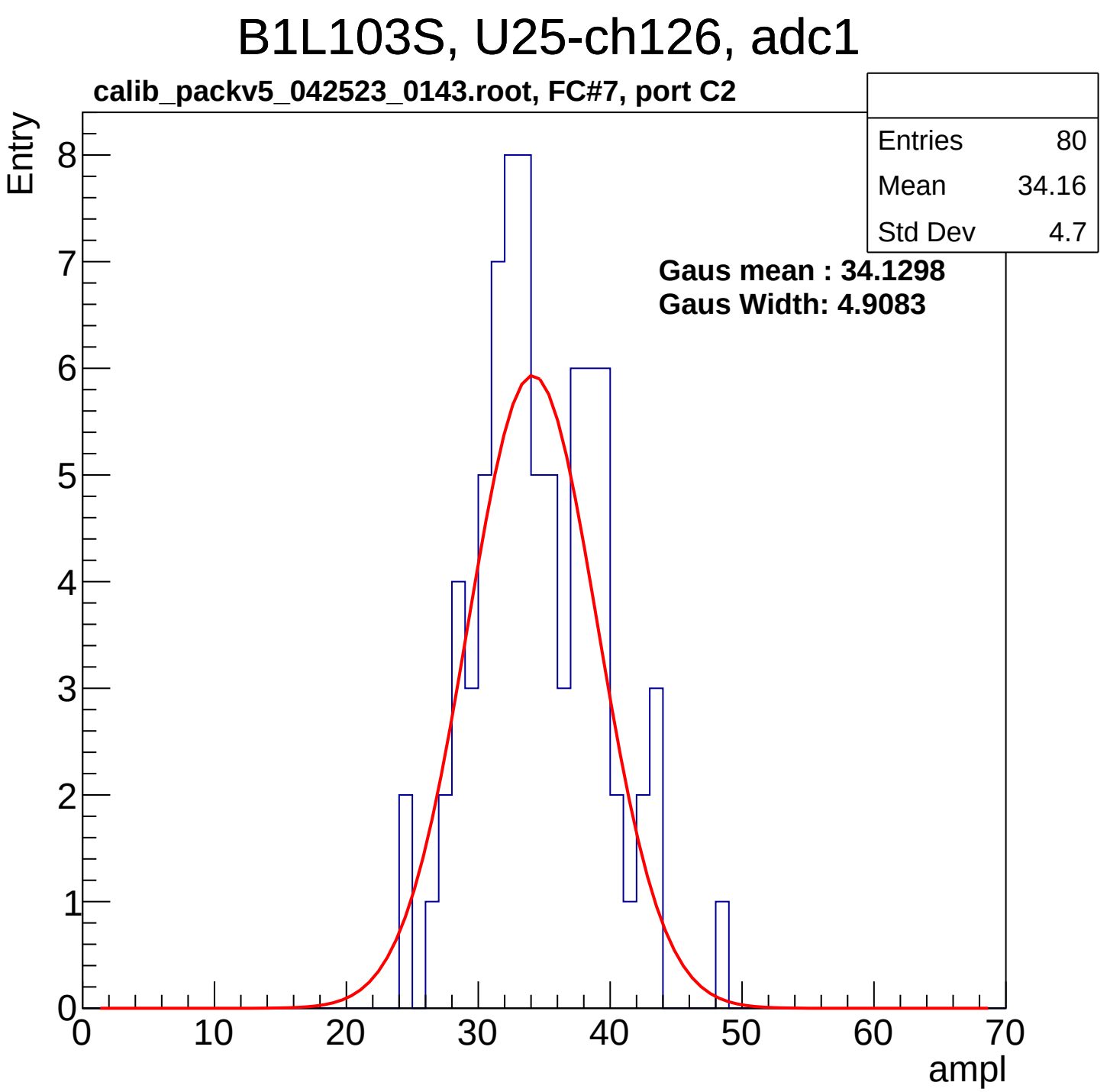

B1L103S, U25-ch125, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 54
Mean 56.37
Std Dev 3.176
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

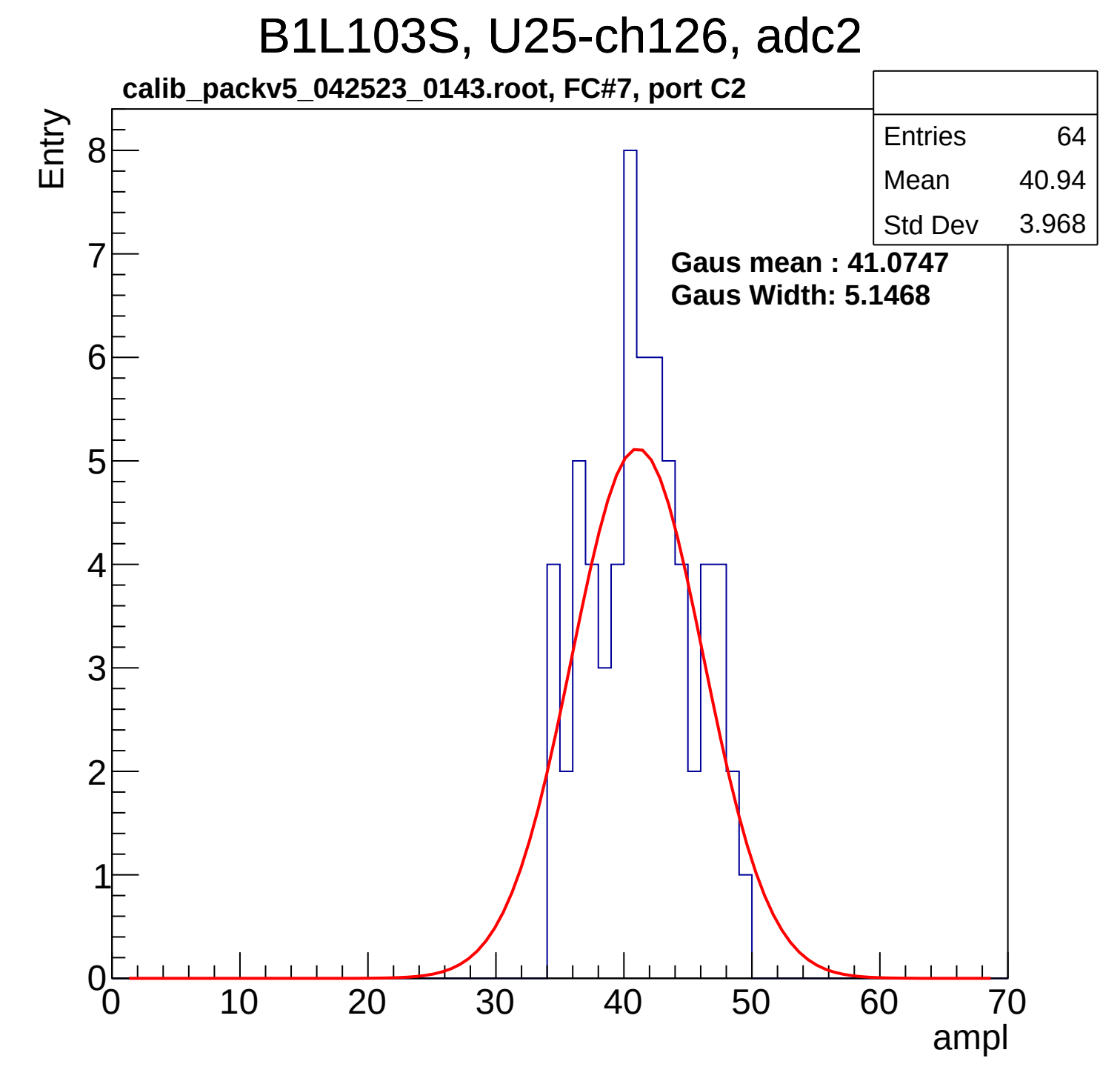

B1L103S, U25-ch125, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 39
Mean 59.23
Std Dev 9.791
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch125, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch125, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch126, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 47
Mean 27.94
Std Dev 3.867
Gaus mean : 28.2669
Gaus Width: 4.1354
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L103S, U25-ch126, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 80
Mean 34.16
Std Dev 4.7
Gaus mean : 34.1298
Gaus Width: 4.9083
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch126, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 40.94
Std Dev 3.968
Gaus mean : 41.0747
Gaus Width: 5.1468
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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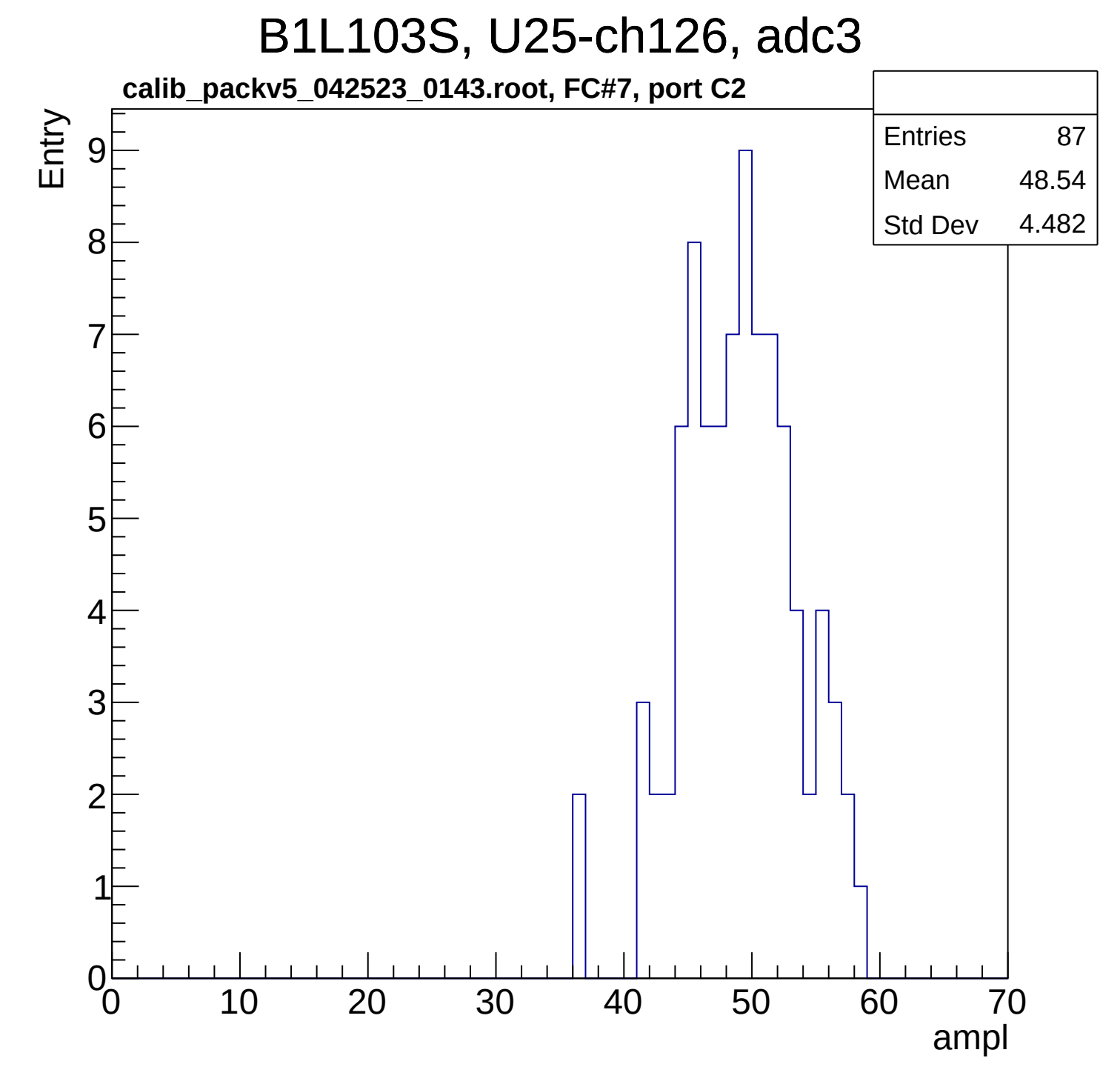
B1L103S, U25-ch126, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 87
Mean 48.54
Std Dev 4.482
Entry
ampl
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2
3
4
5
6
7
8
9
0
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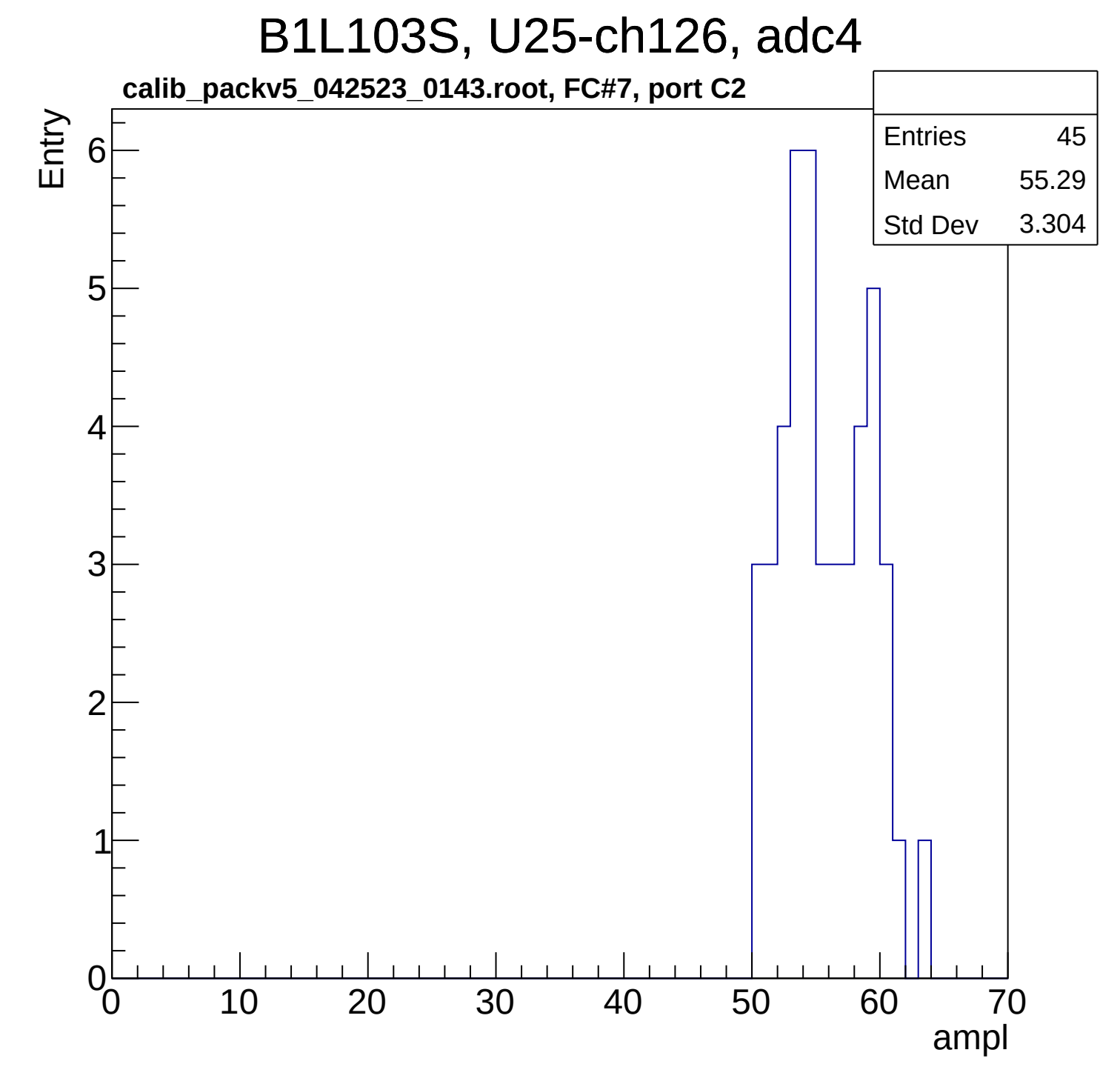
50
60
70


B1L103S, U25-ch126, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 45
Mean 55.29
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
50
60
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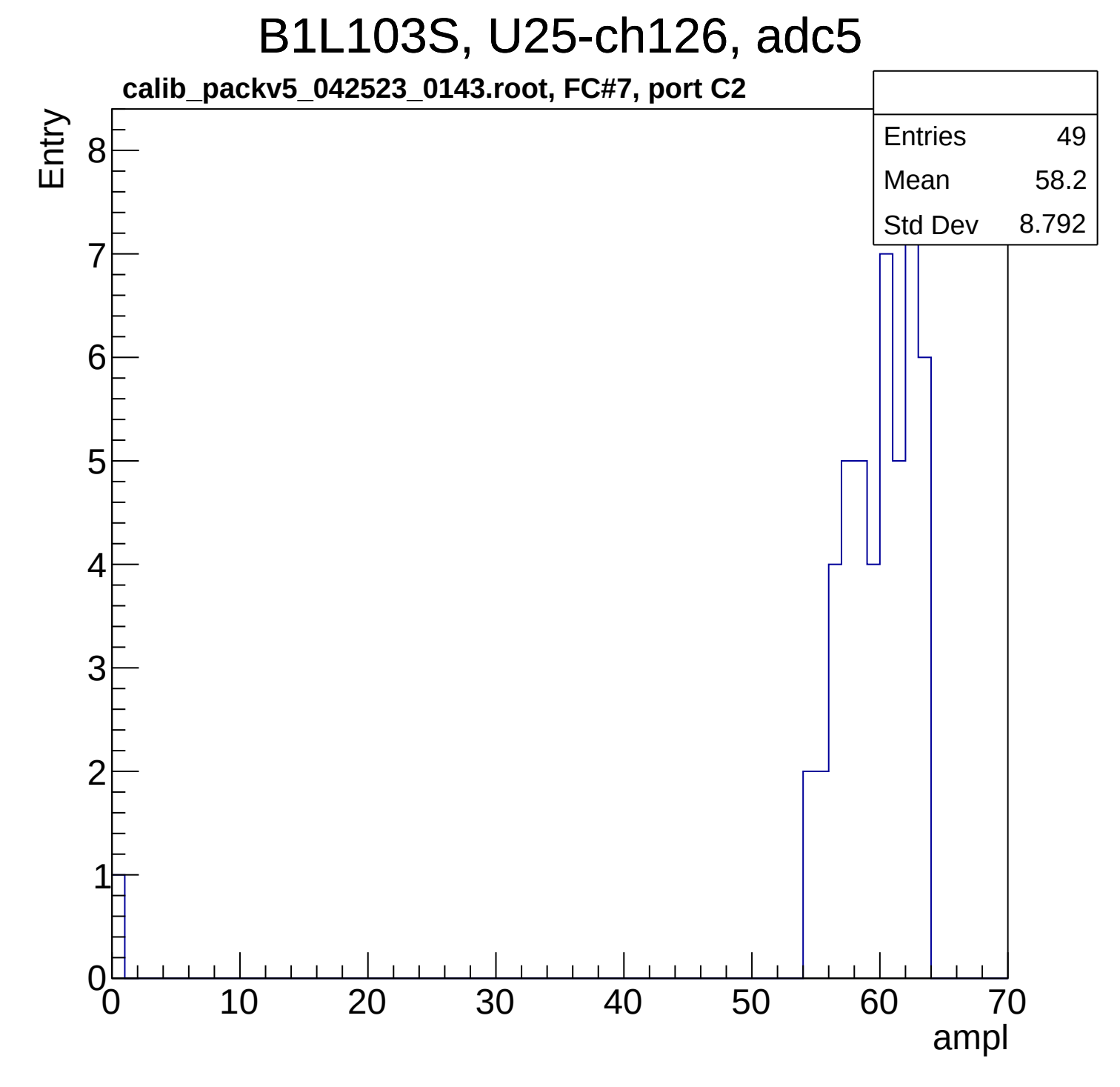
B1L103S, U25-ch126, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 49
Mean 58.2
Std Dev 8.792
Entry
ampl
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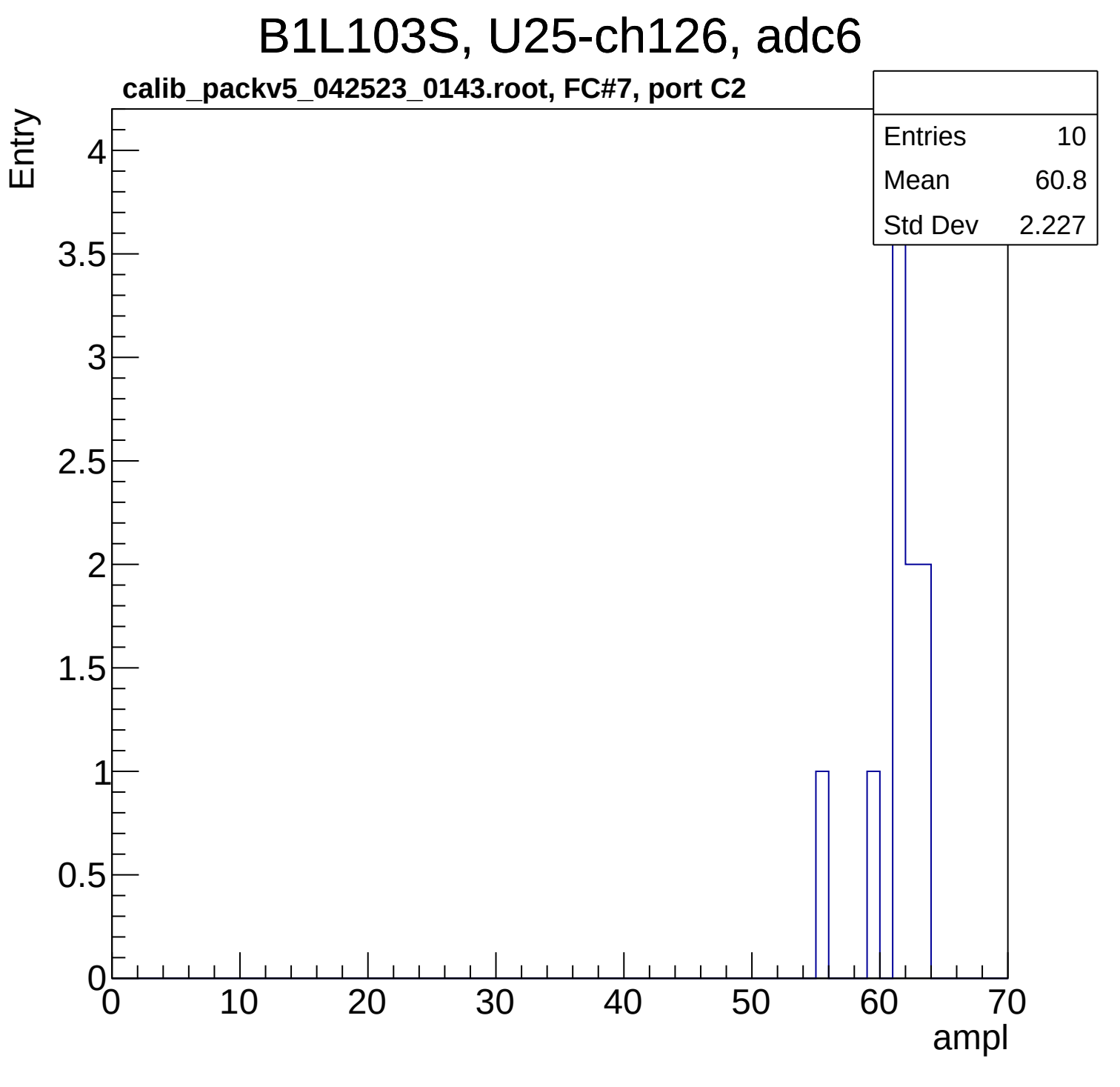
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch126, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 10
Mean 60.8
Std Dev 2.227
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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60
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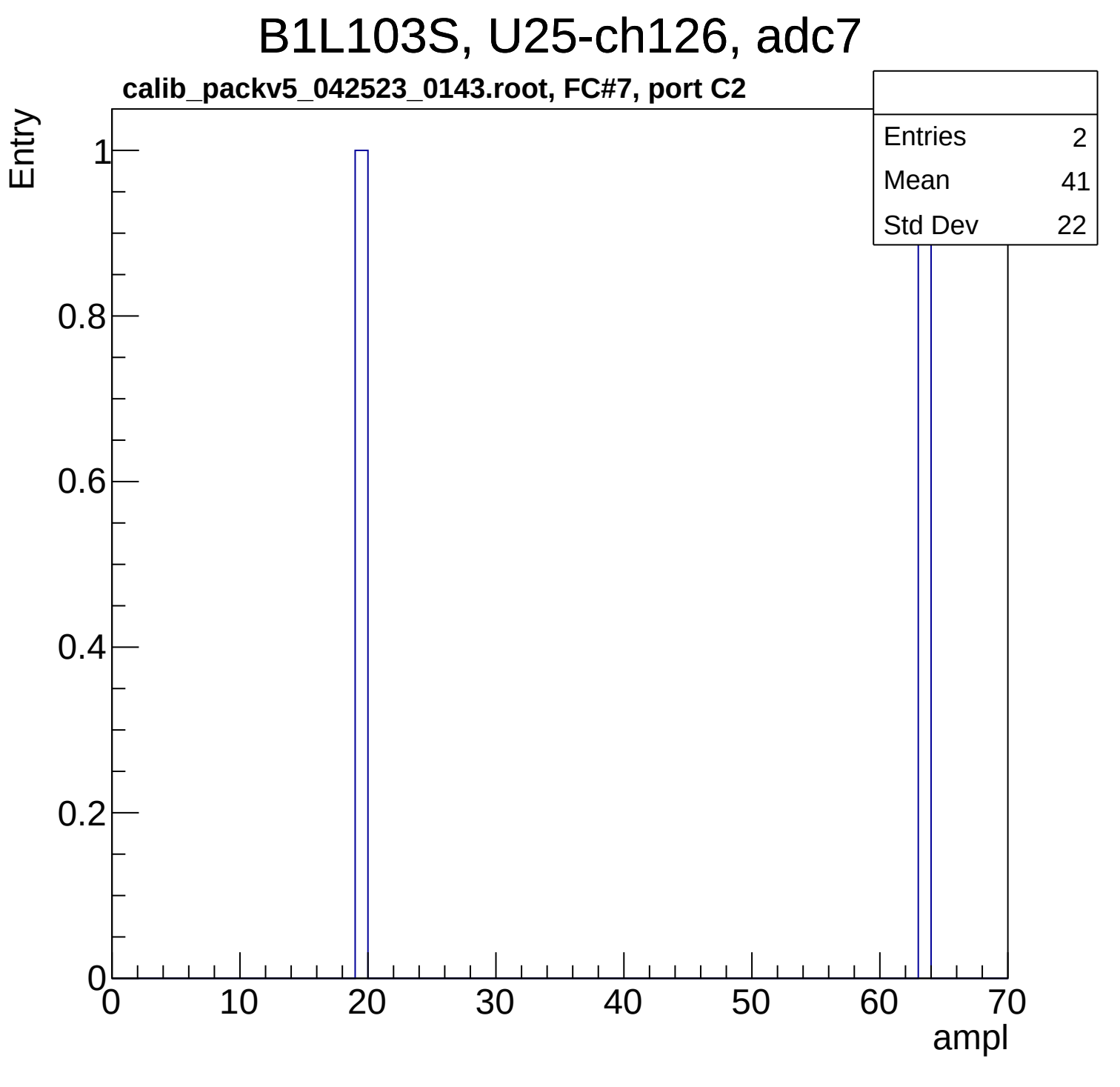
B1L103S, U25-ch126, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 41
Std Dev 22
Entry
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0.6
0.4
0.2
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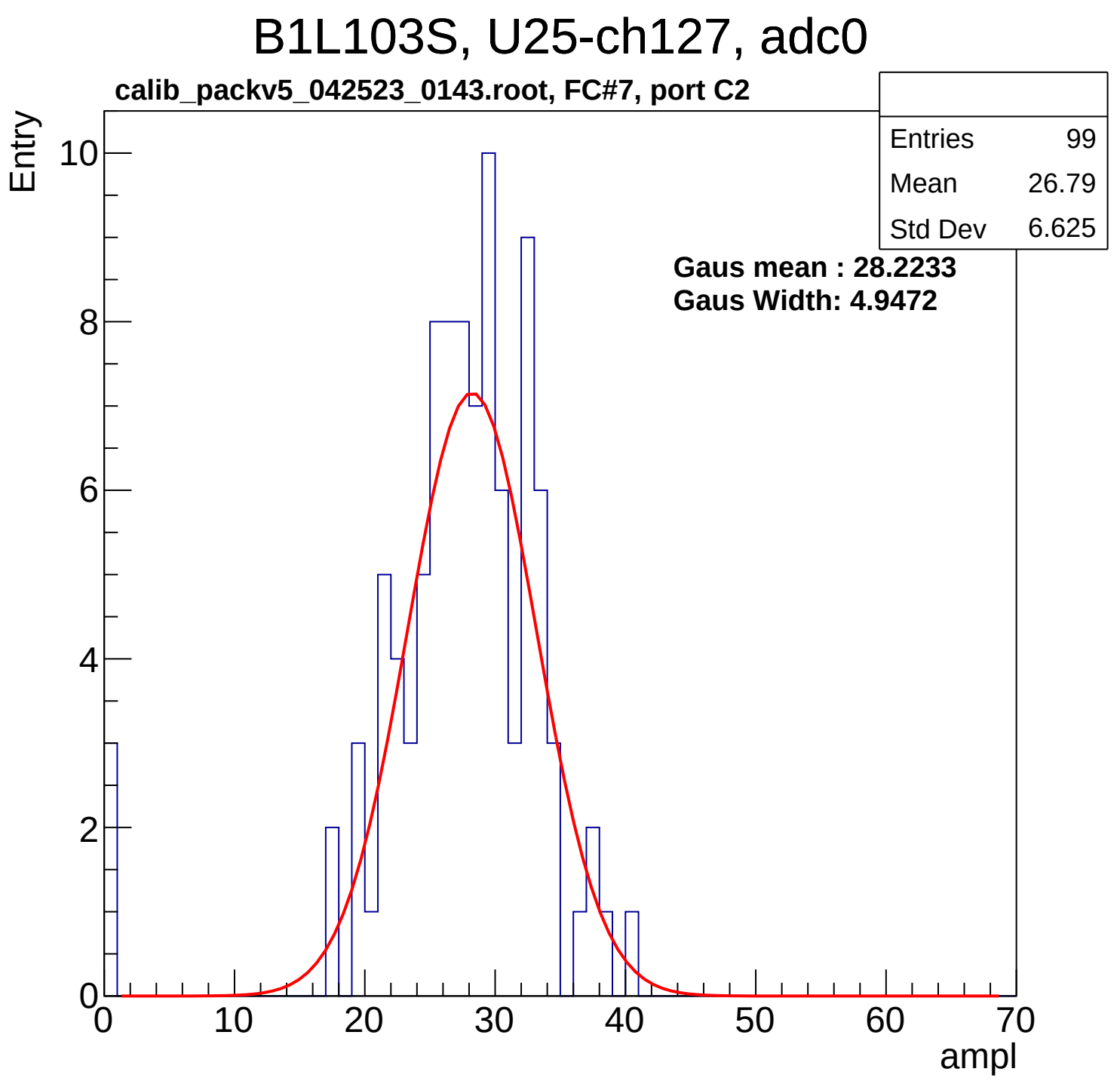
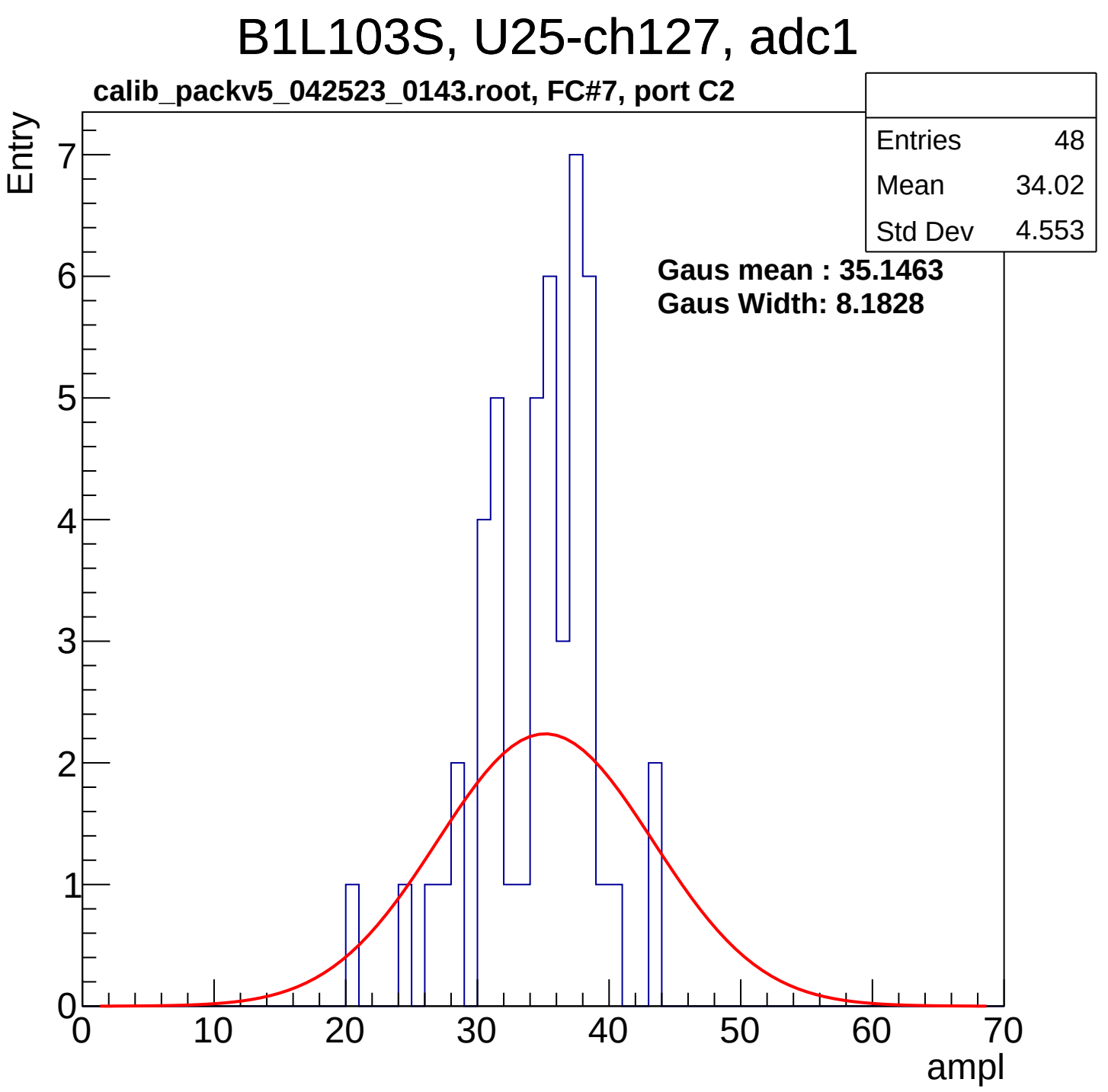
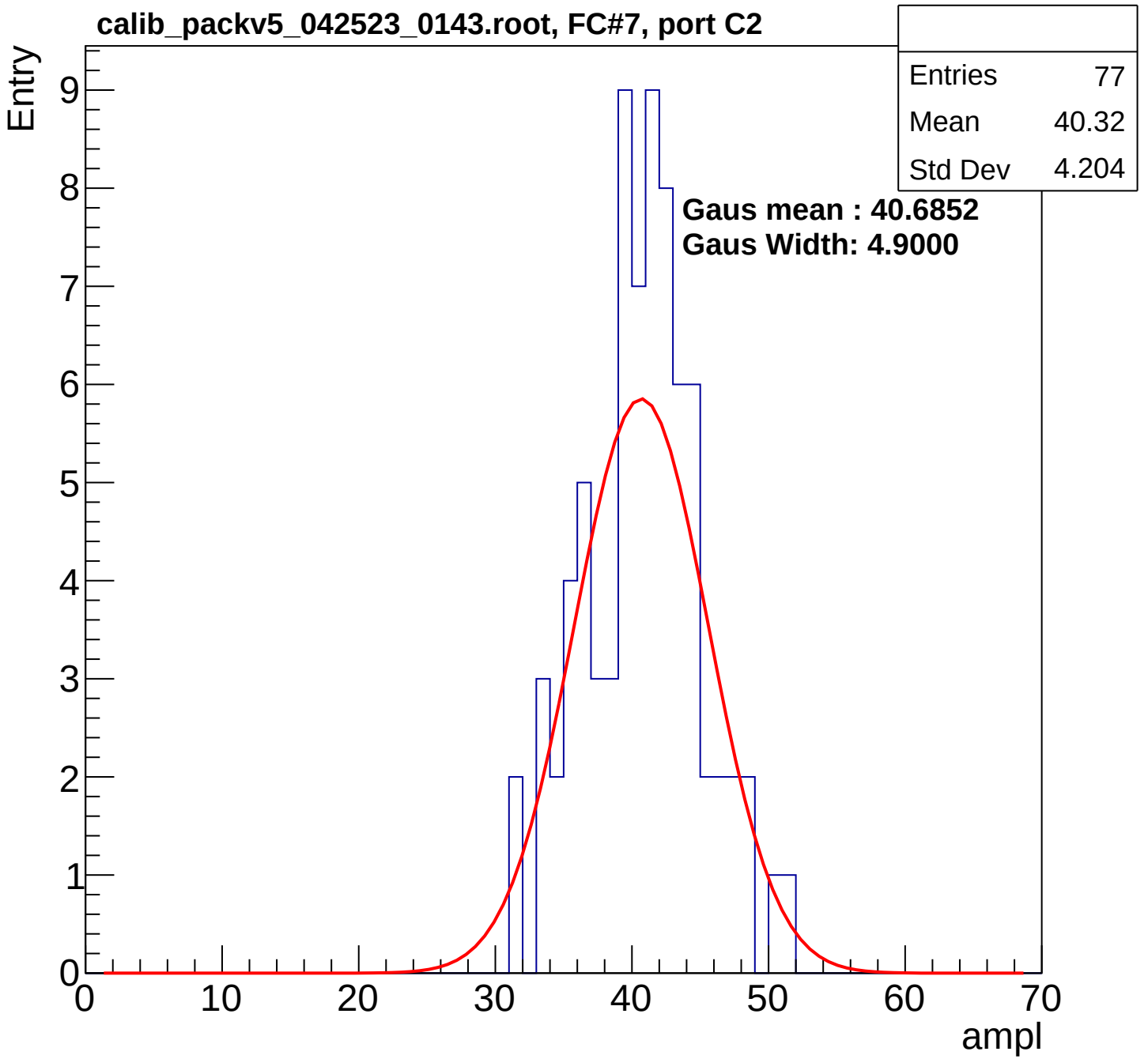
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch127, adc0
calib_packv5_042523_0143.root, FC#7, port C2
Entries 99
Mean 26.79
Std Dev 6.625
Gaus mean : 28.2233
Gaus Width: 4.9472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc1
calib_packv5_042523_0143.root, FC#7, port C2
Entries 48
Mean 34.02
Std Dev 4.553
Gaus mean : 35.1463
Gaus Width: 8.1828
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc2
calib_packv5_042523_0143.root, FC#7, port C2
Entries 77
Mean 40.32
Std Dev 4.204
Gaus mean : 40.6852
Gaus Width: 4.9000
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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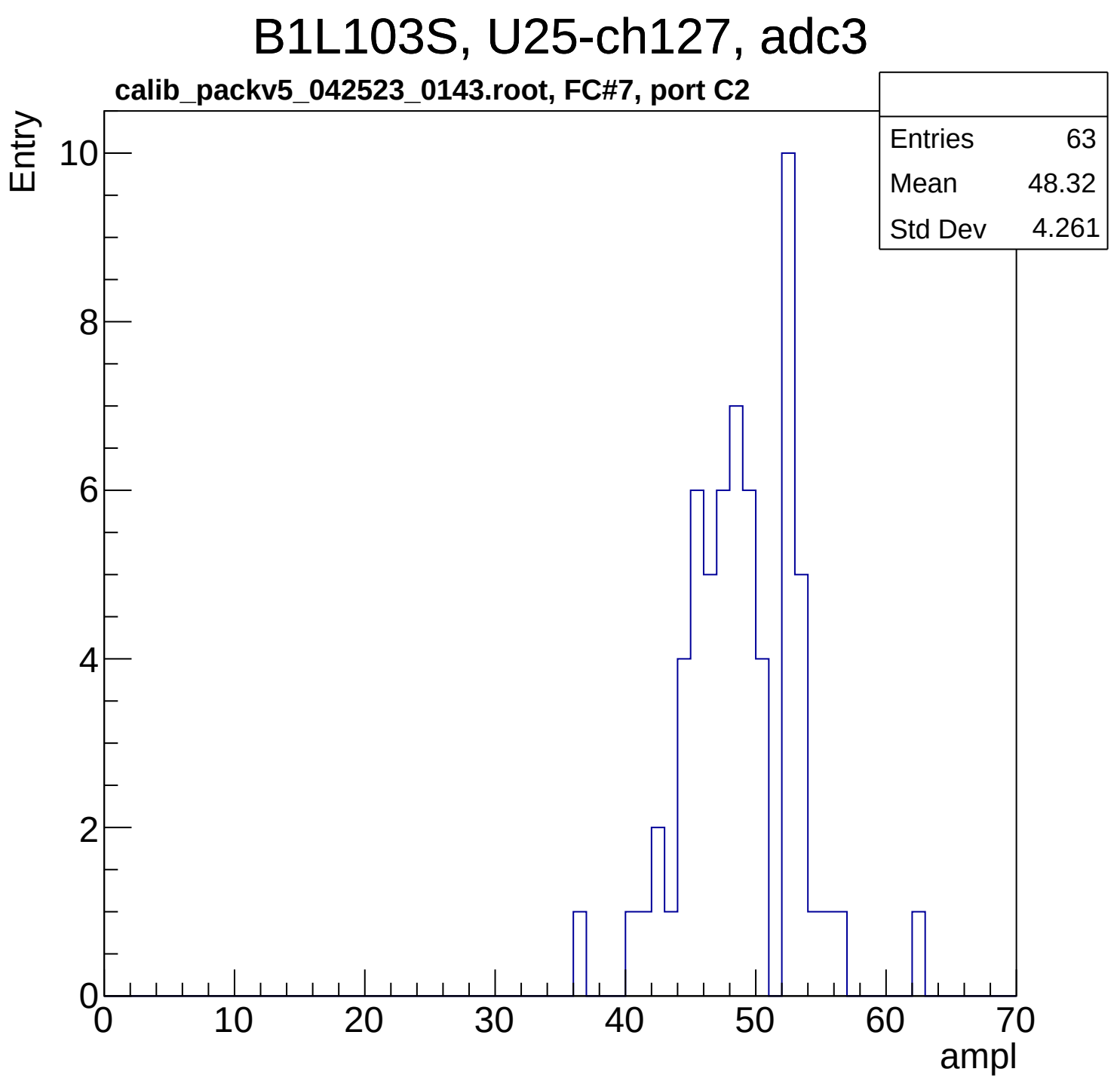
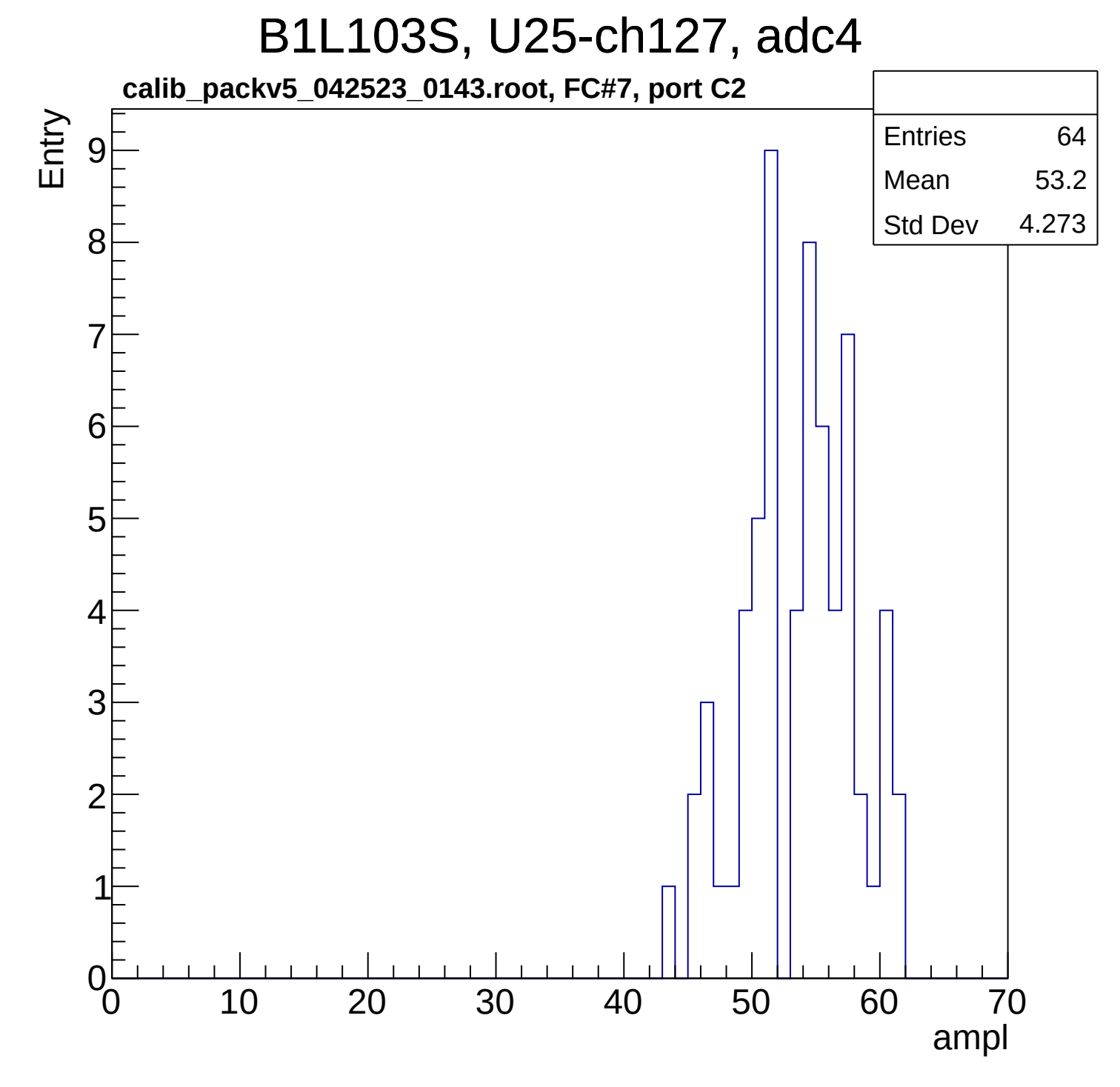
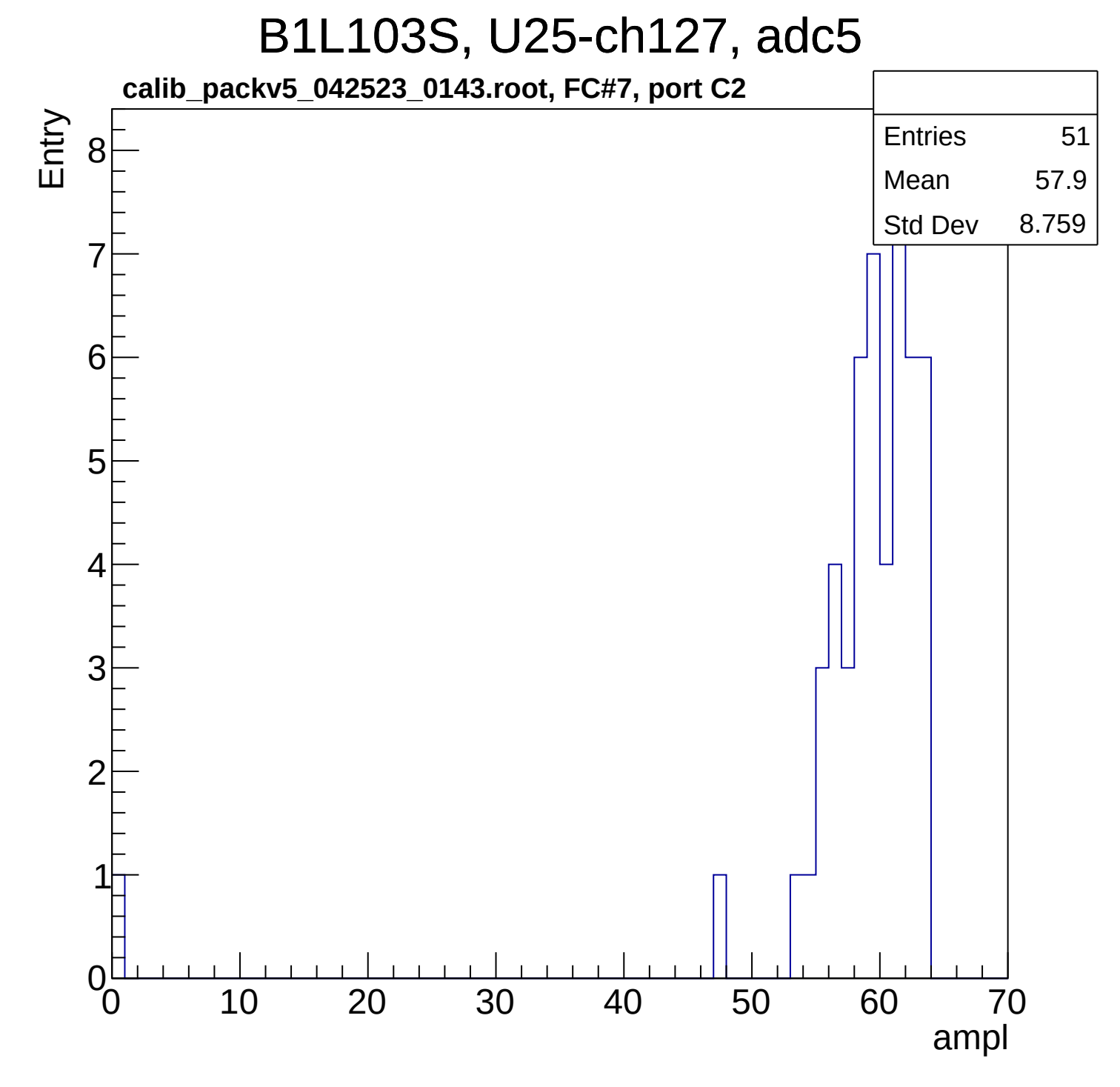
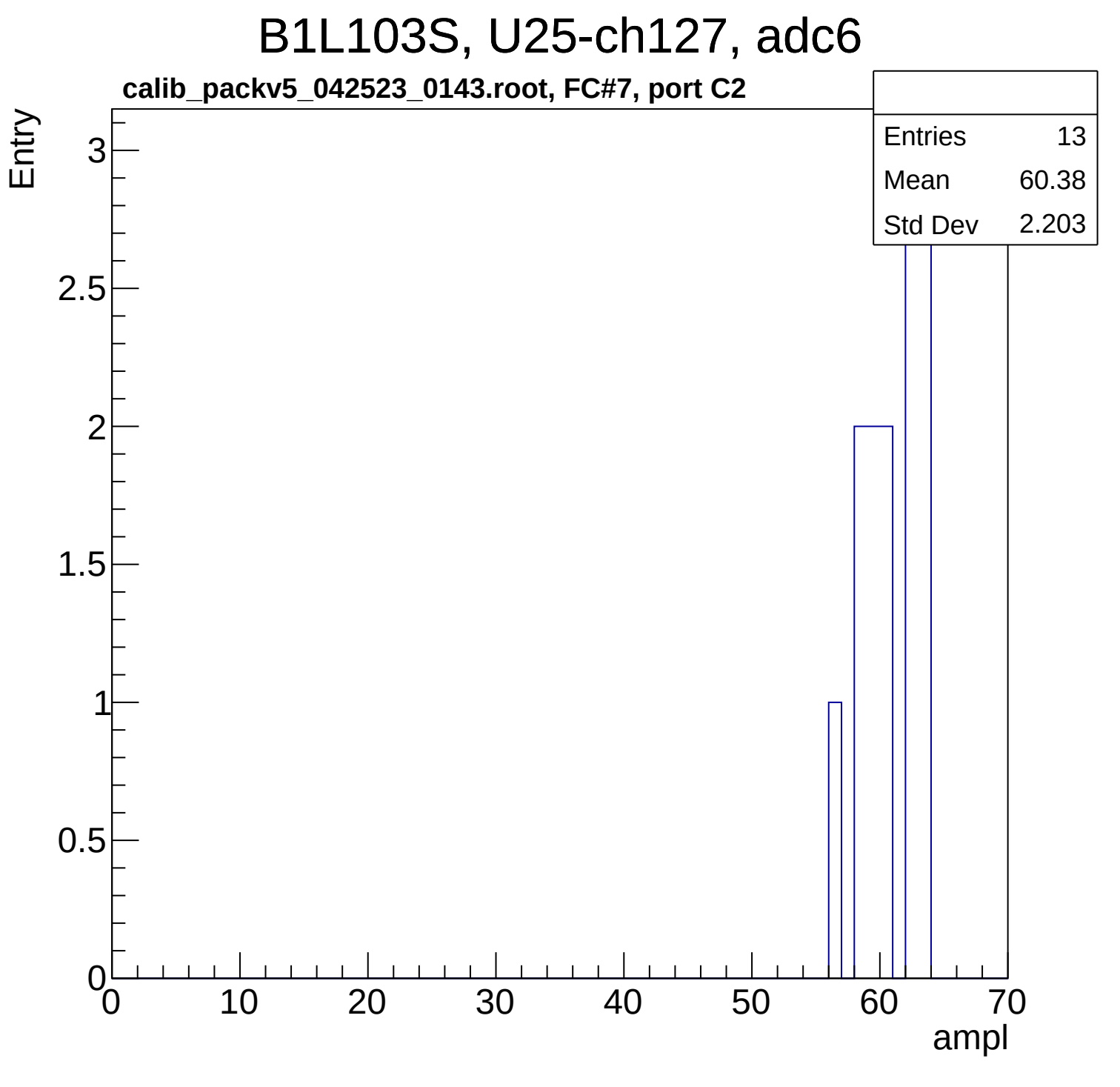
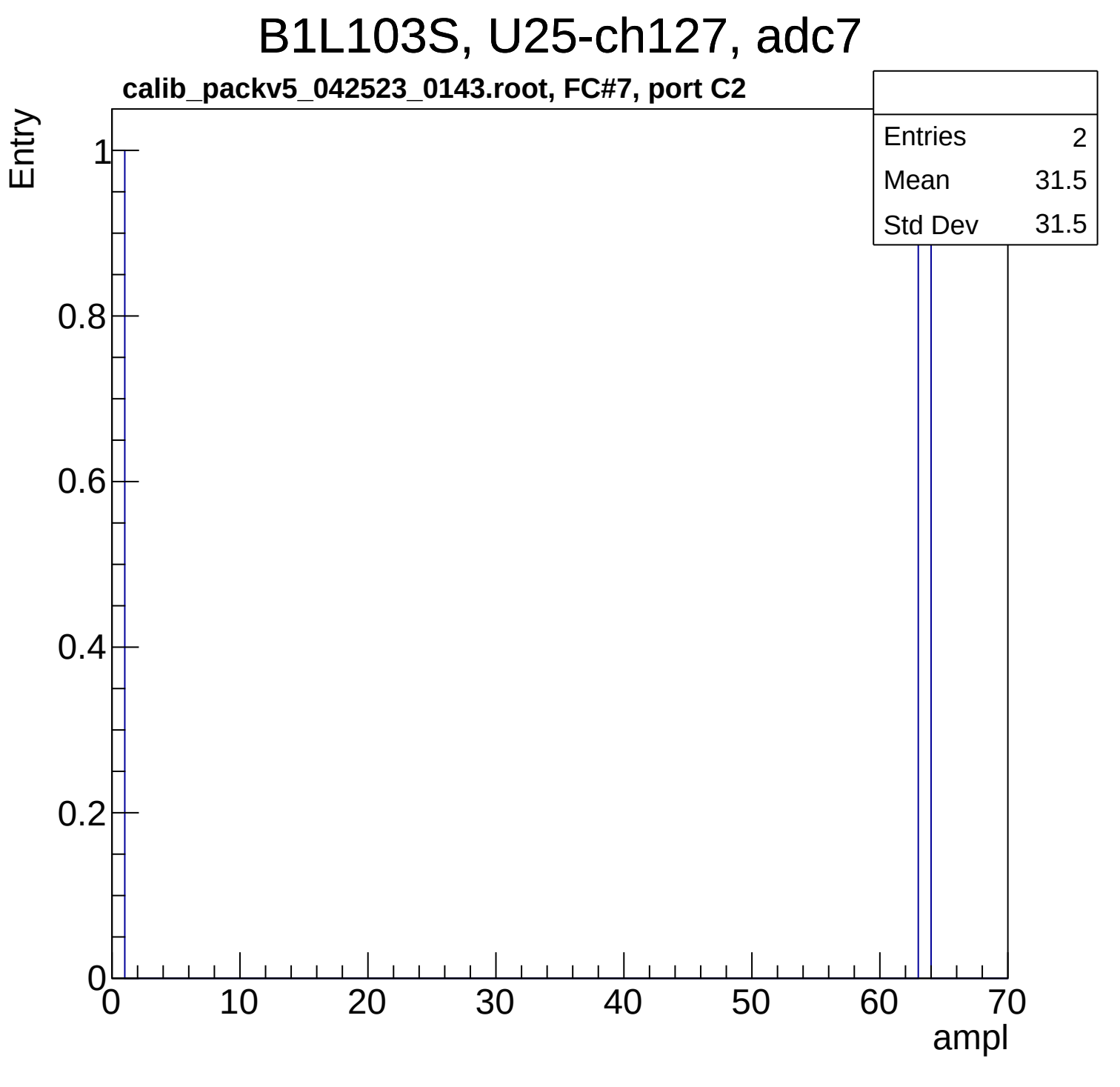
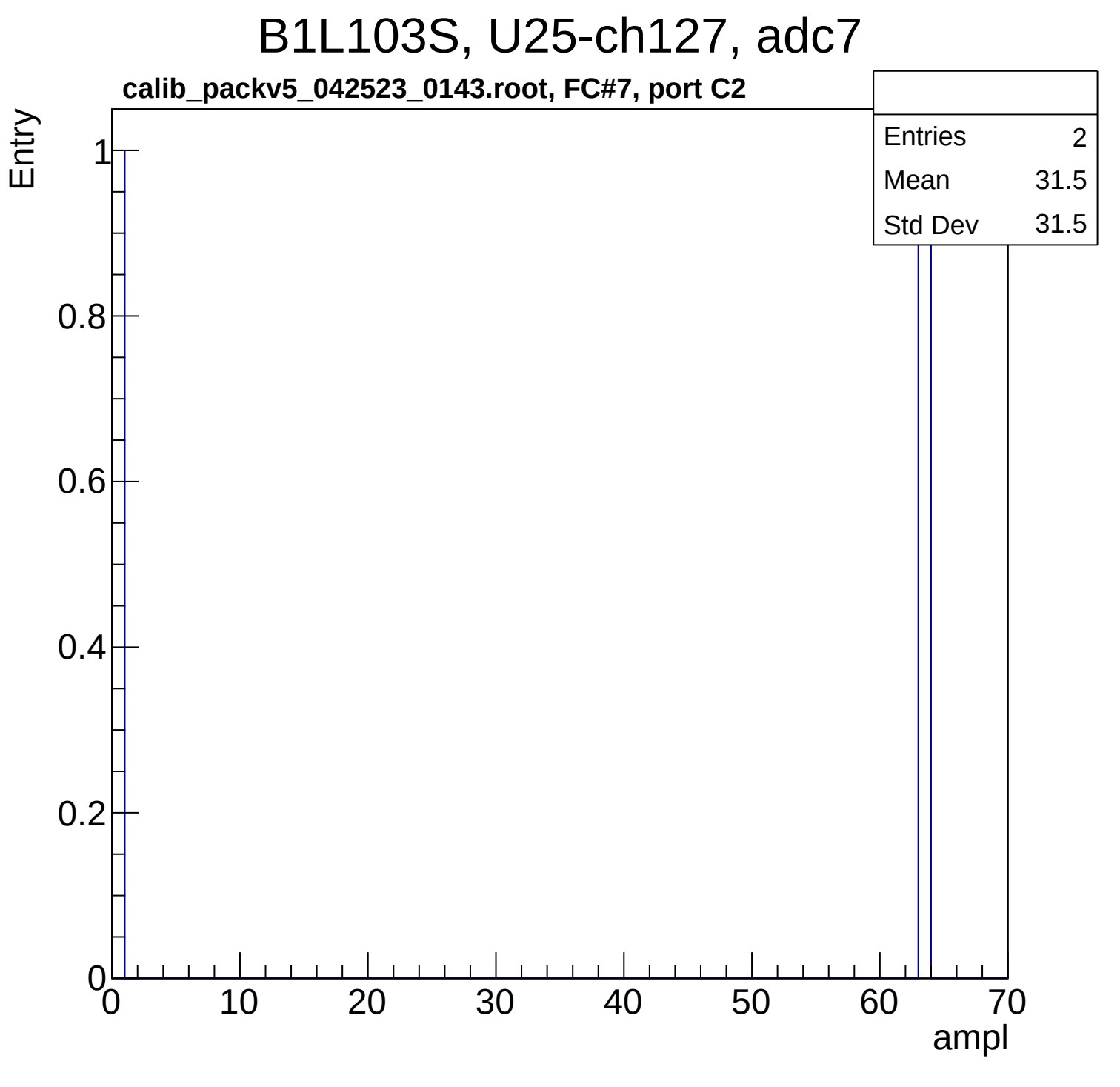

B1L103S, U25-ch127, adc3
calib_packv5_042523_0143.root, FC#7, port C2
Entries 63
Mean 48.32
Std Dev 4.261
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc4
calib_packv5_042523_0143.root, FC#7, port C2
Entries 64
Mean 53.2
Std Dev 4.273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc5
calib_packv5_042523_0143.root, FC#7, port C2
Entries 51
Mean 57.9
Std Dev 8.759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc6
calib_packv5_042523_0143.root, FC#7, port C2
Entries 13
Mean 60.38
Std Dev 2.203
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L103S, U25-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L103S, U25-ch127, adc7
calib_packv5_042523_0143.root, FC#7, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl