

B1L003S, U6-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.92
Std Dev	11.21

Turn on : 28.0956

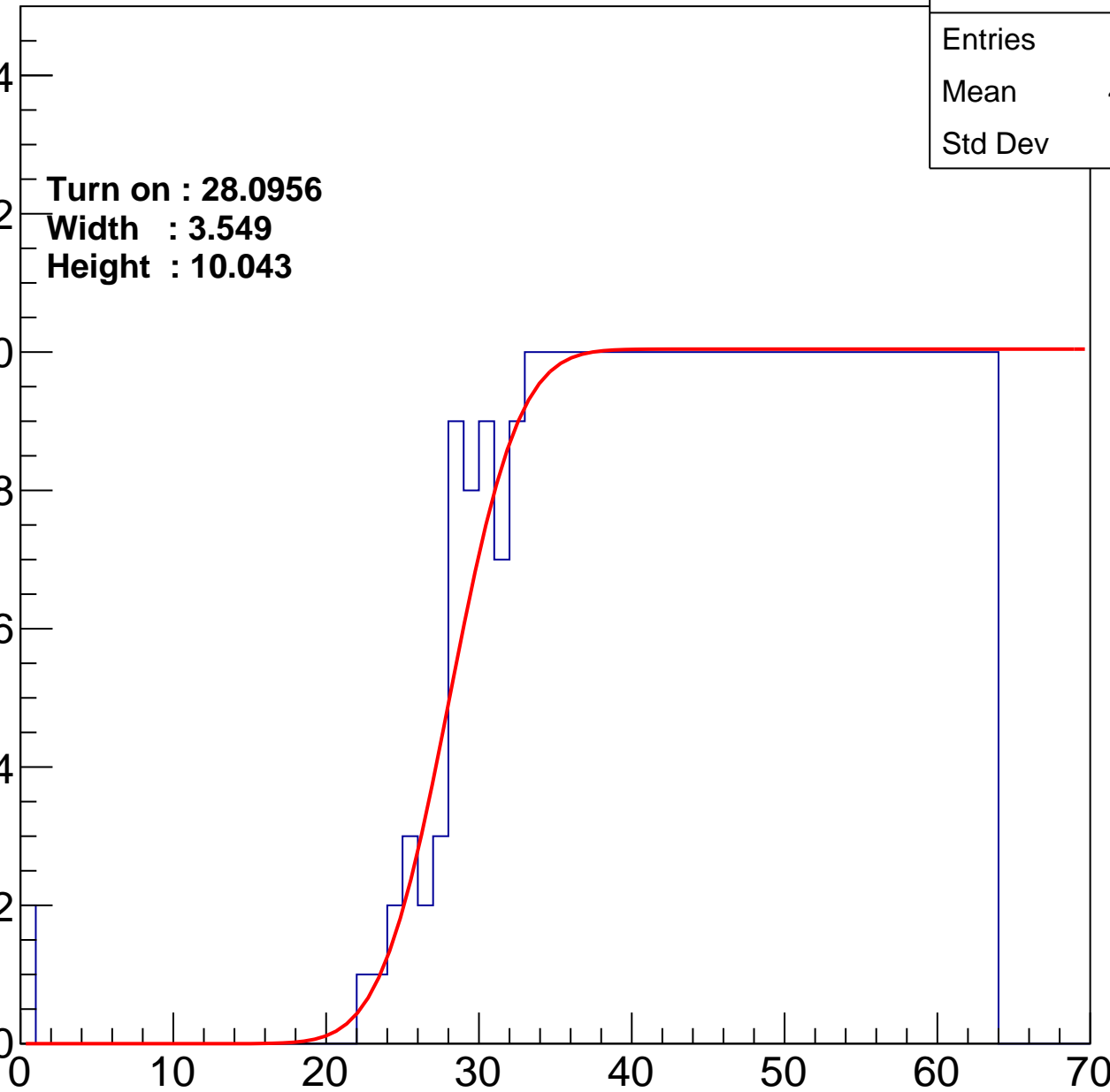
Width : 3.549

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch1

calib_packv5_042523_0143.root, FC#13, port D2

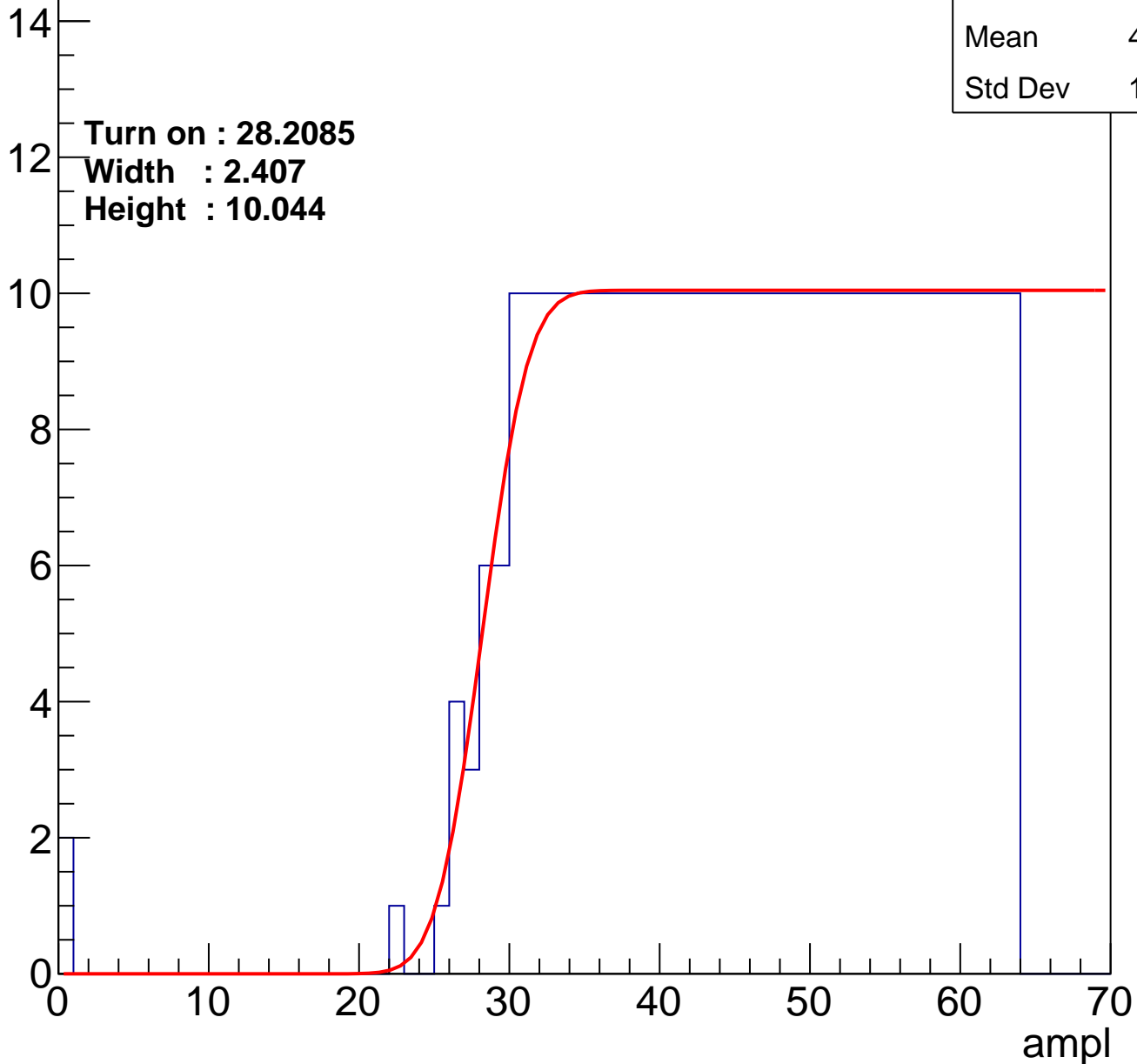
Entry

Entries	363
Mean	45.13
Std Dev	11.03

Turn on : 28.2085

Width : 2.407

Height : 10.044



B1L003S, U6-ch2

calib_packv5_042523_0143.root, FC#13, port D2

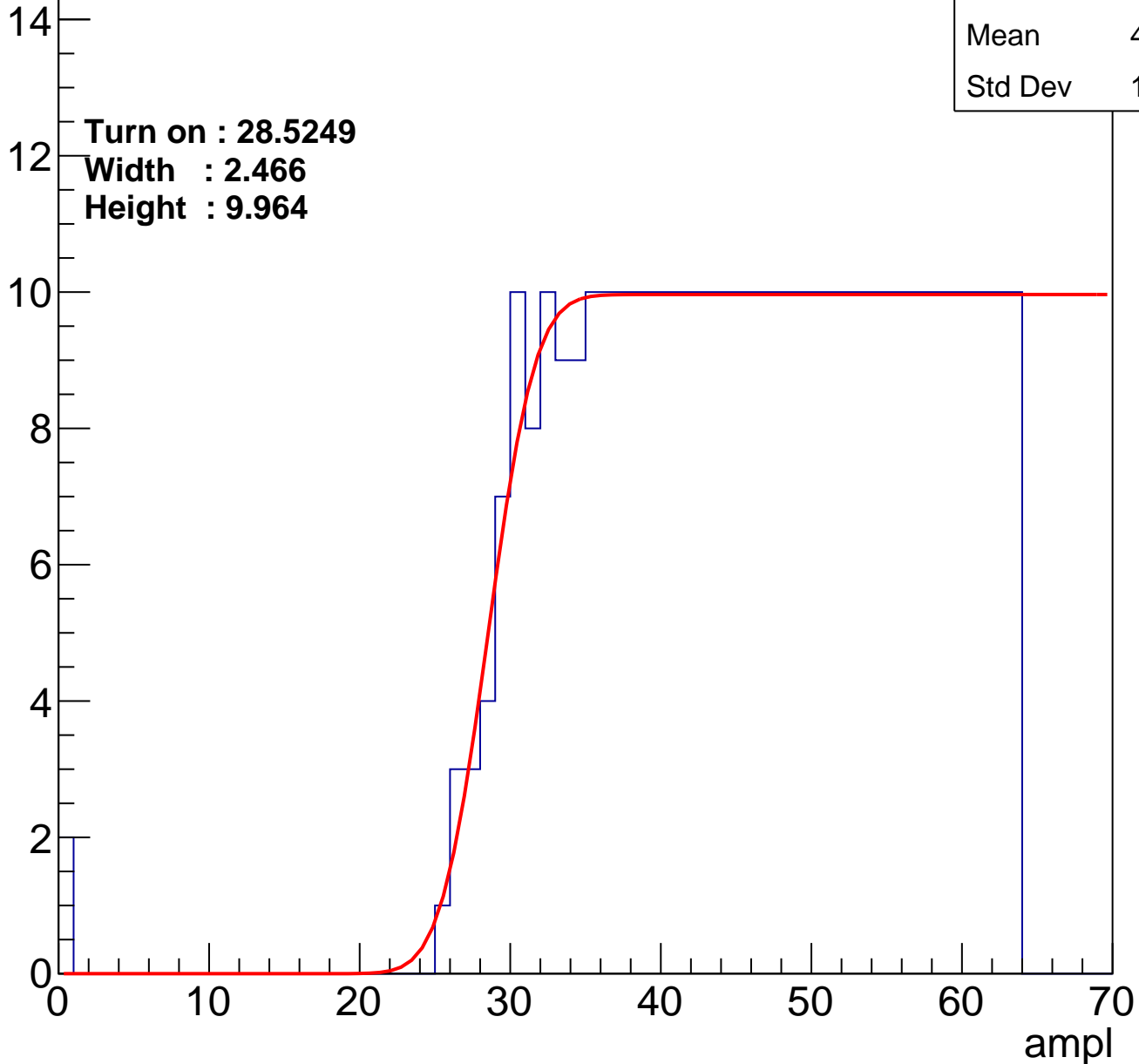
Entries	356
Mean	45.45
Std Dev	10.89

Turn on : 28.5249

Width : 2.466

Height : 9.964

Entry



B1L003S, U6-ch3

calib_packv5_042523_0143.root, FC#13, port D2

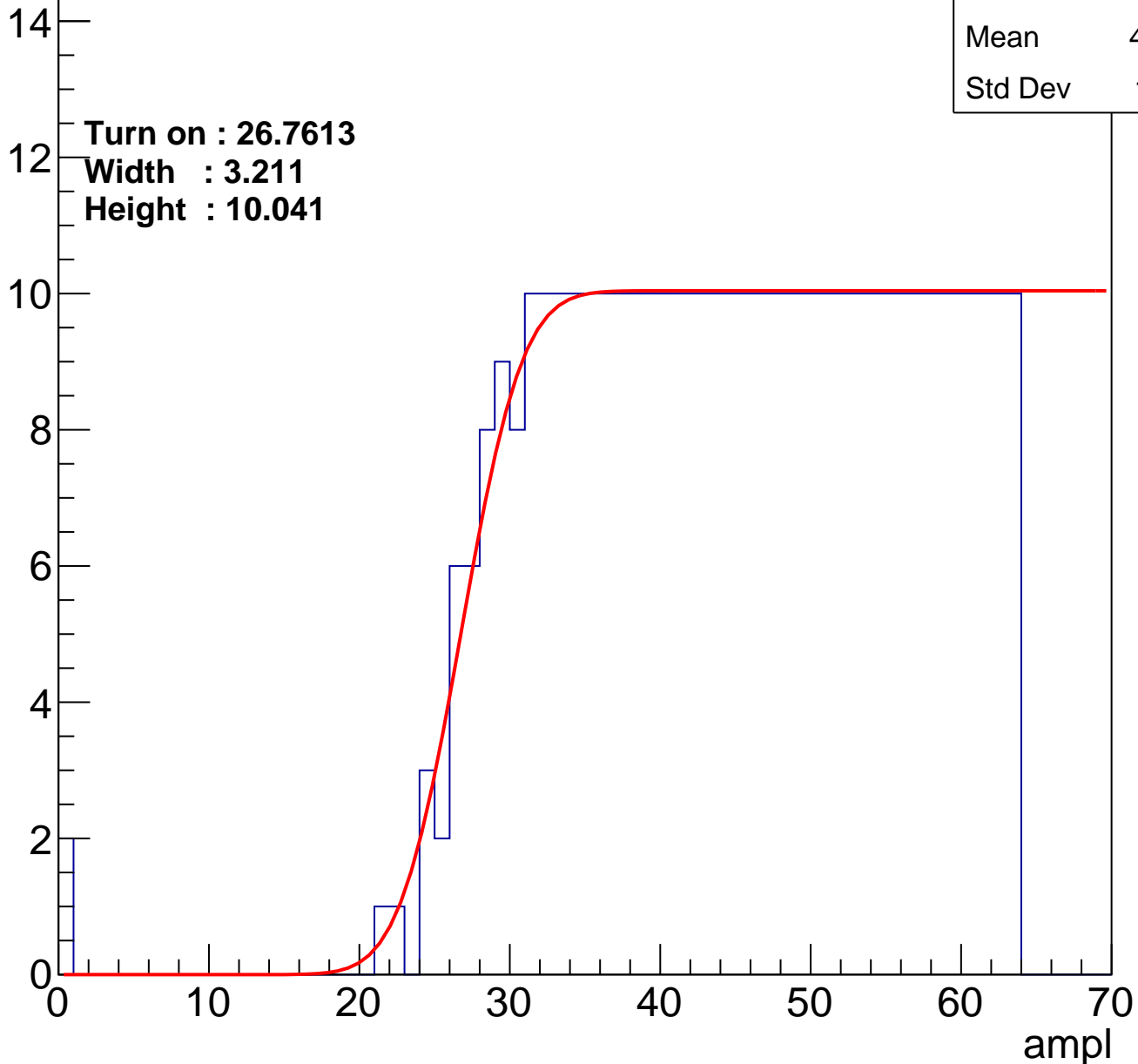
Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.7613

Width : 3.211

Height : 10.041

Entry



B1L003S, U6-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.77
Std Dev	10.7

Turn on : 28.9243

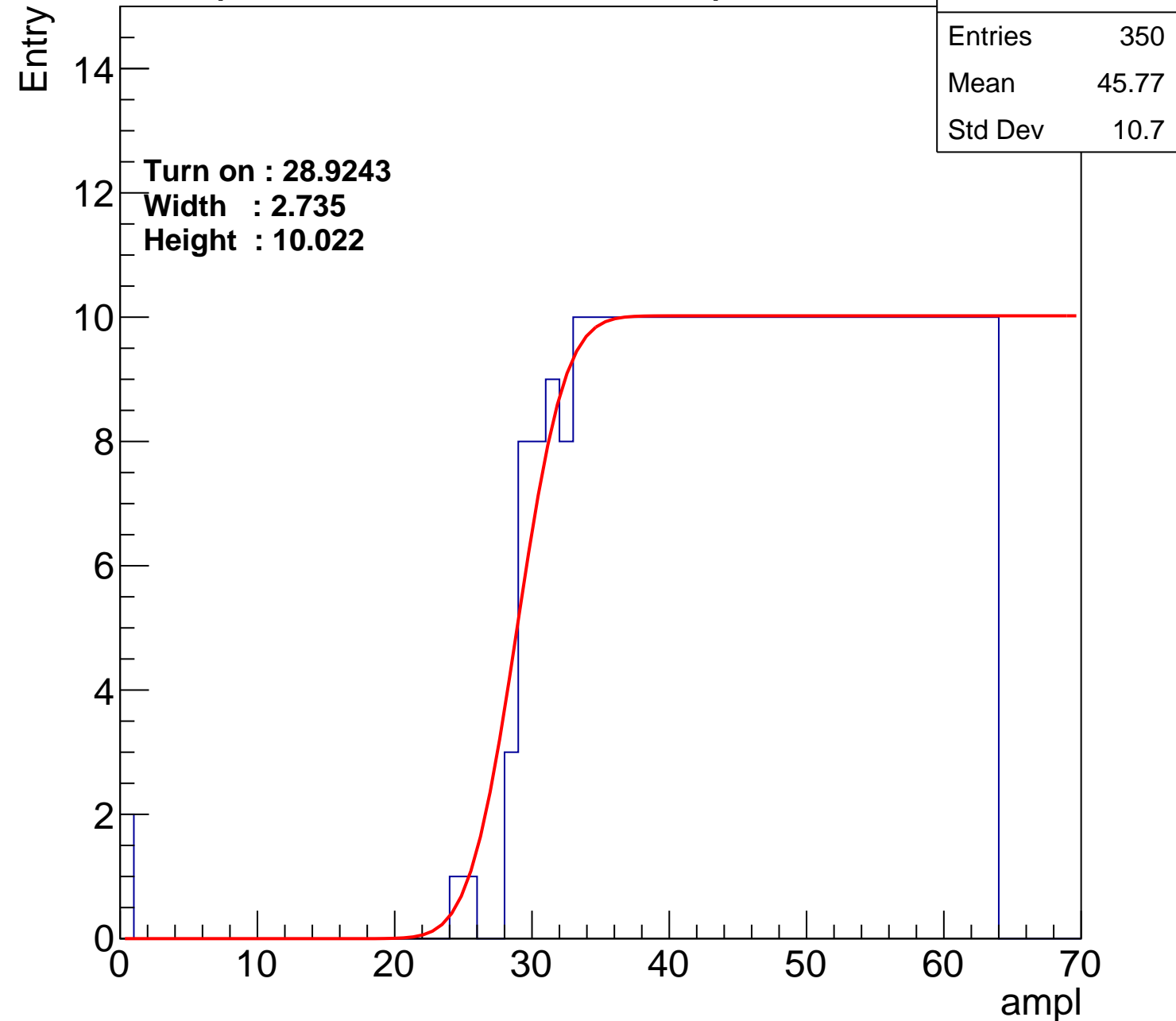
Width : 2.735

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
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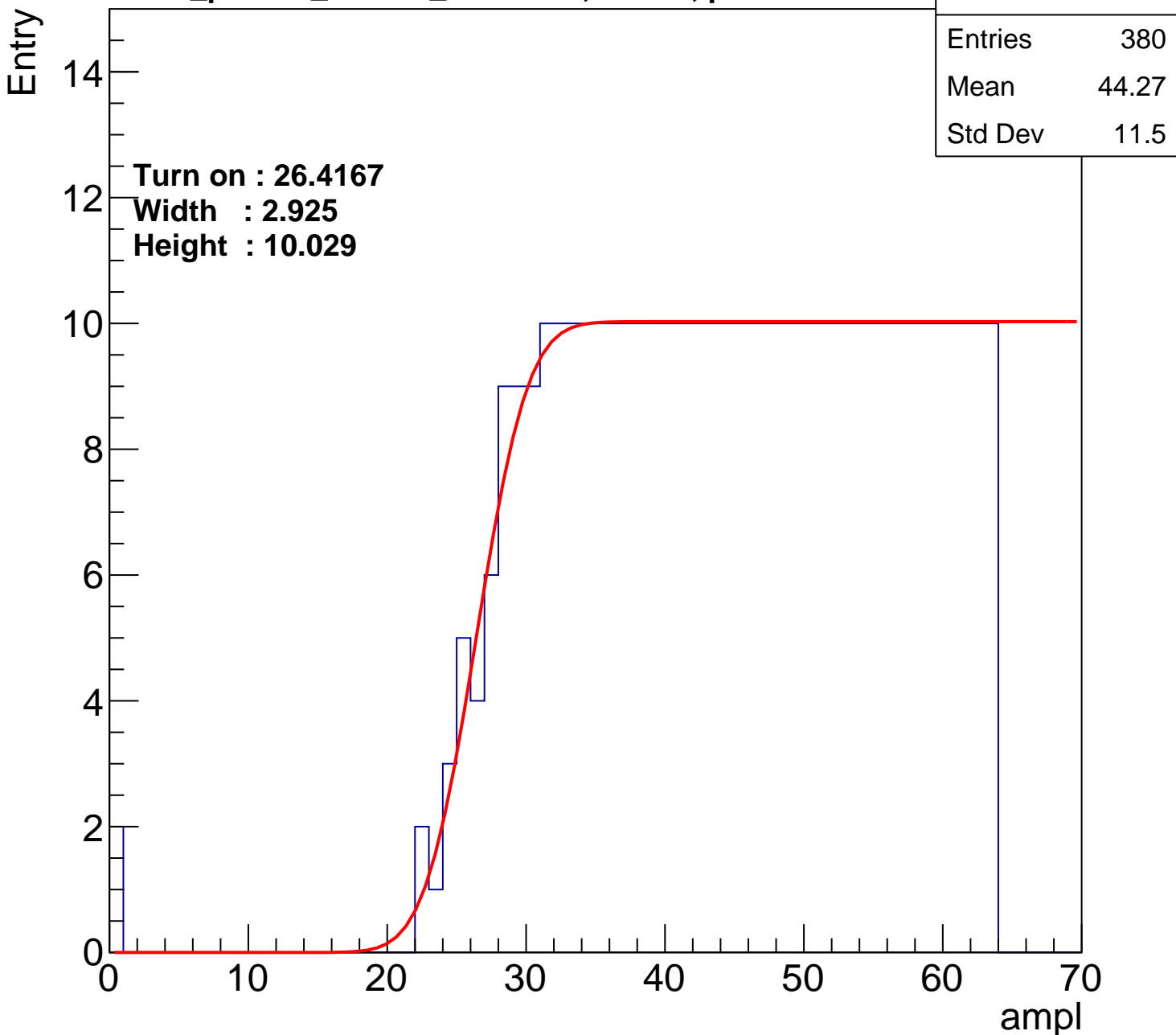
Mean	44.27
------	-------

Std Dev	11.5
---------	------

Turn on : 26.4167

Width : 2.925

Height : 10.029



B1L003S, U6-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.33
Std Dev	10.93

Turn on : 28.4179

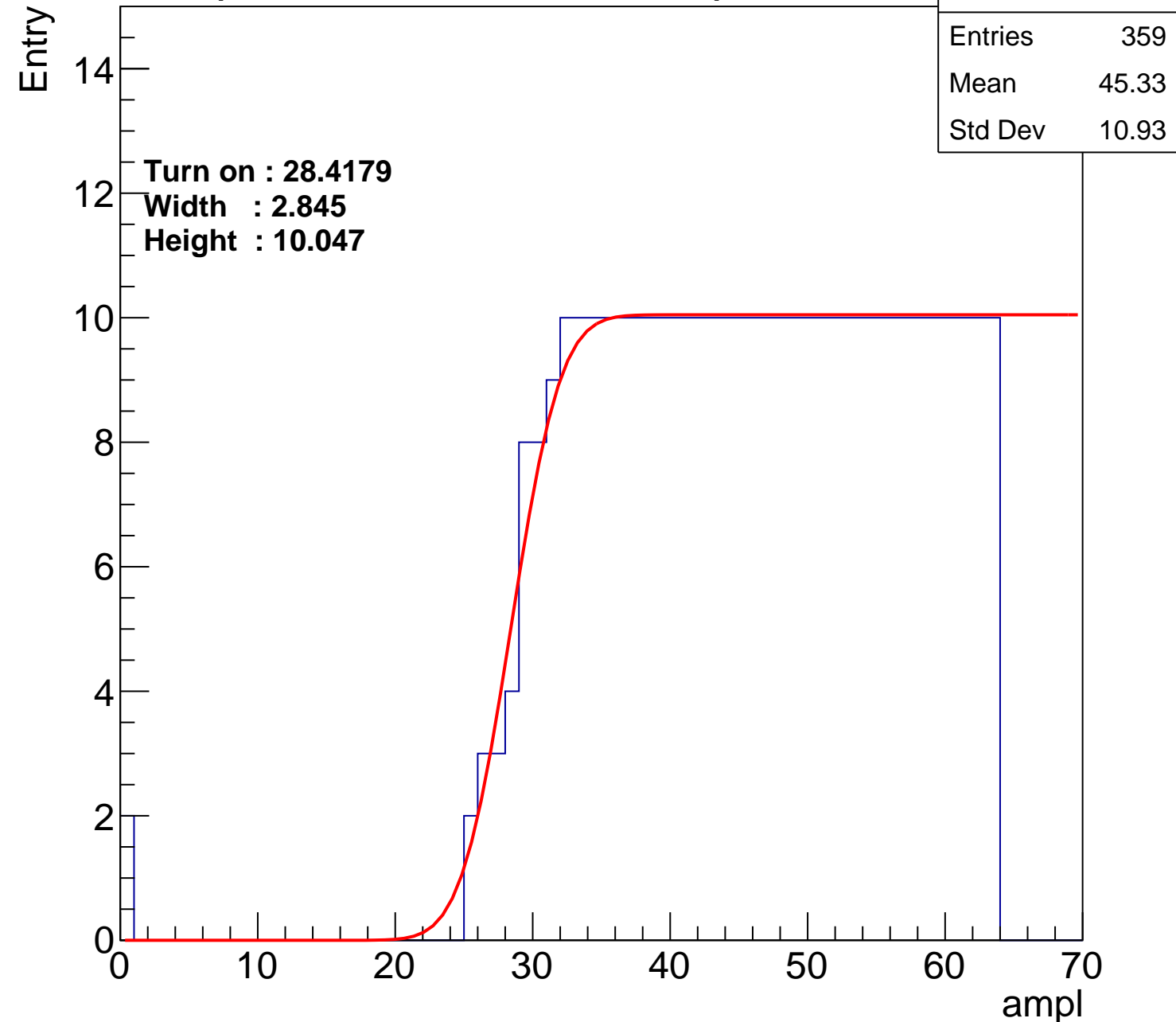
Width : 2.845

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch7

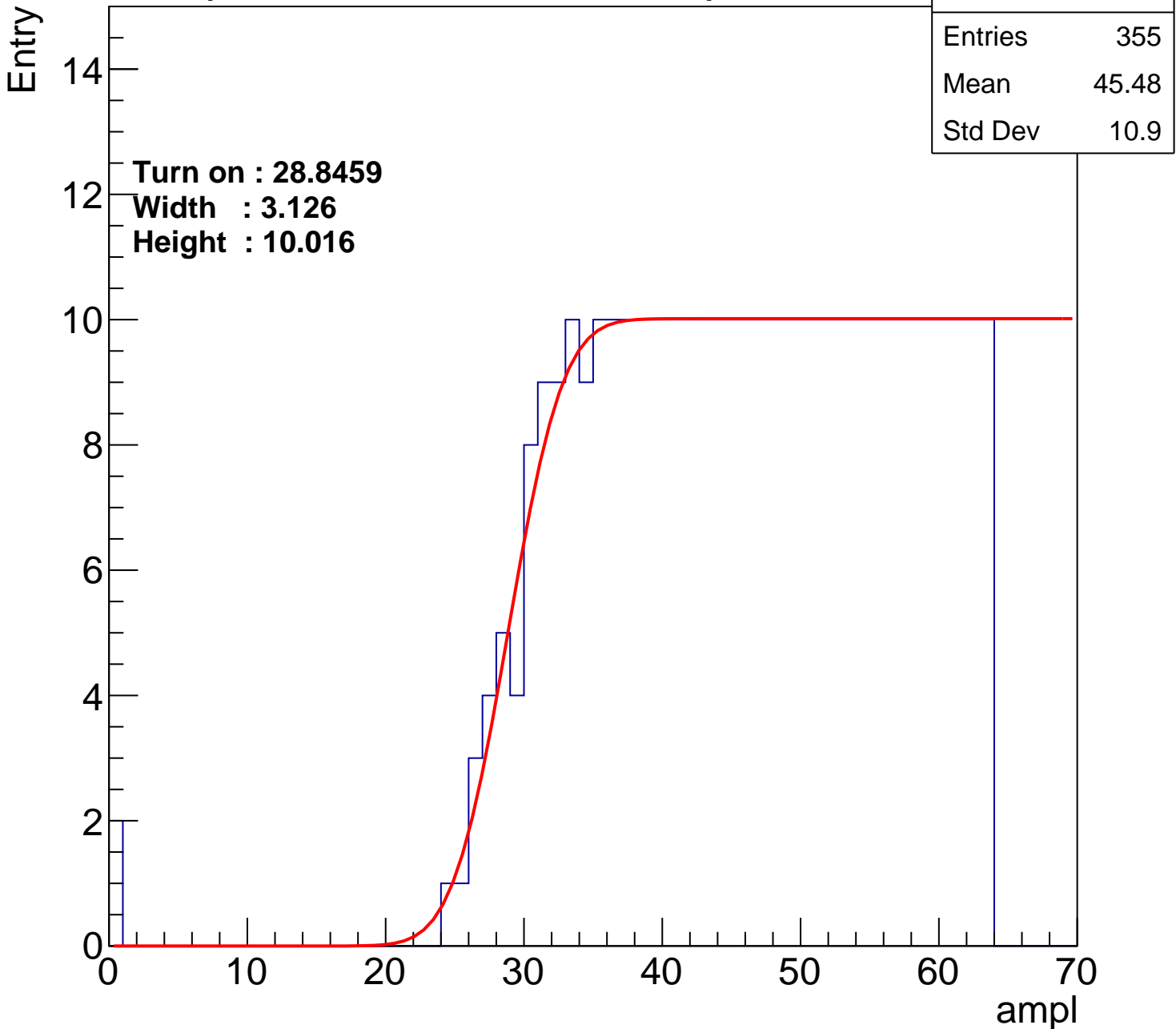
calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.48
Std Dev	10.9

Turn on : 28.8459

Width : 3.126

Height : 10.016



B1L003S, U6-ch8

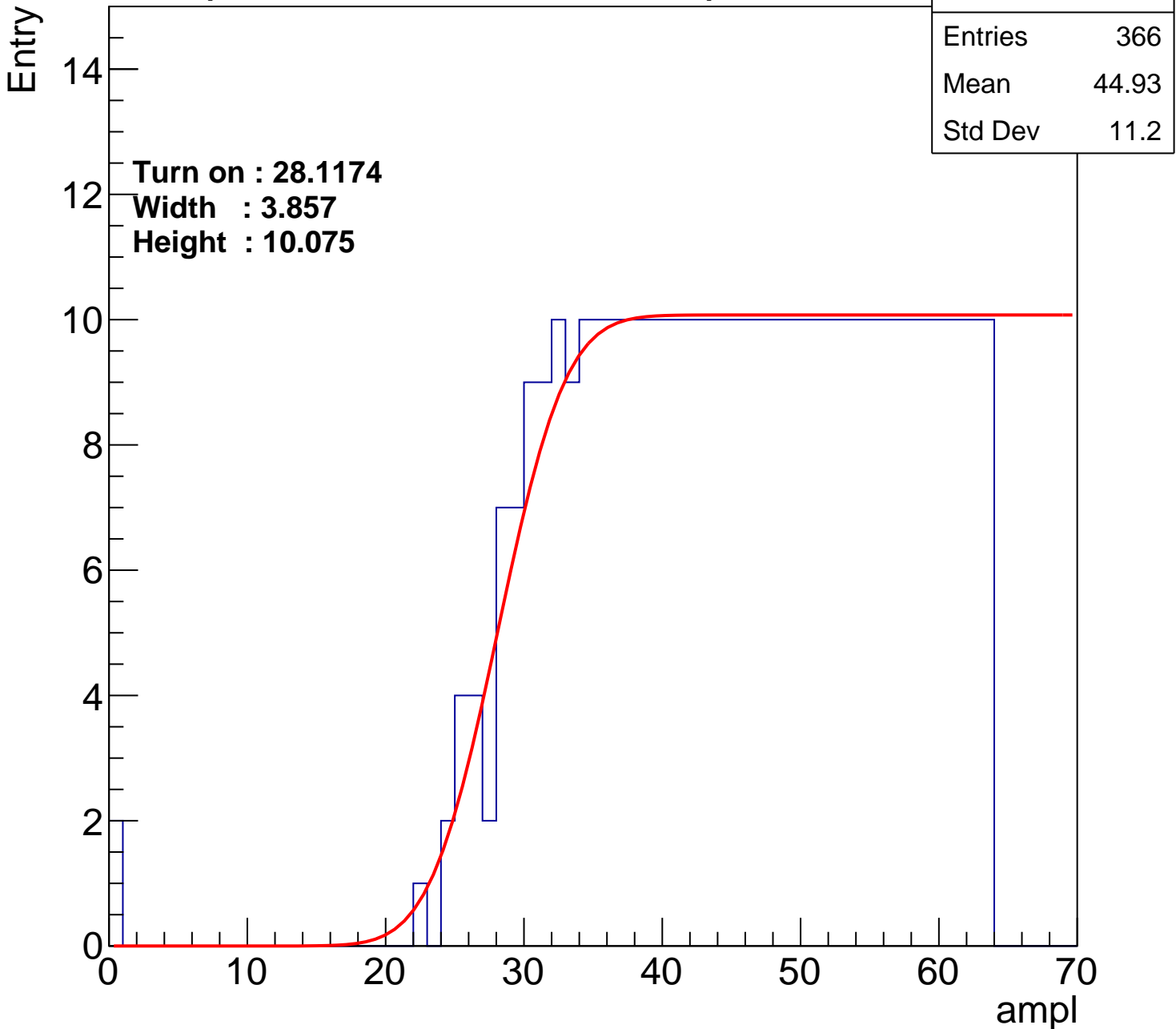
calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.93
Std Dev	11.2

Turn on : 28.1174

Width : 3.857

Height : 10.075



B1L003S, U6-ch9

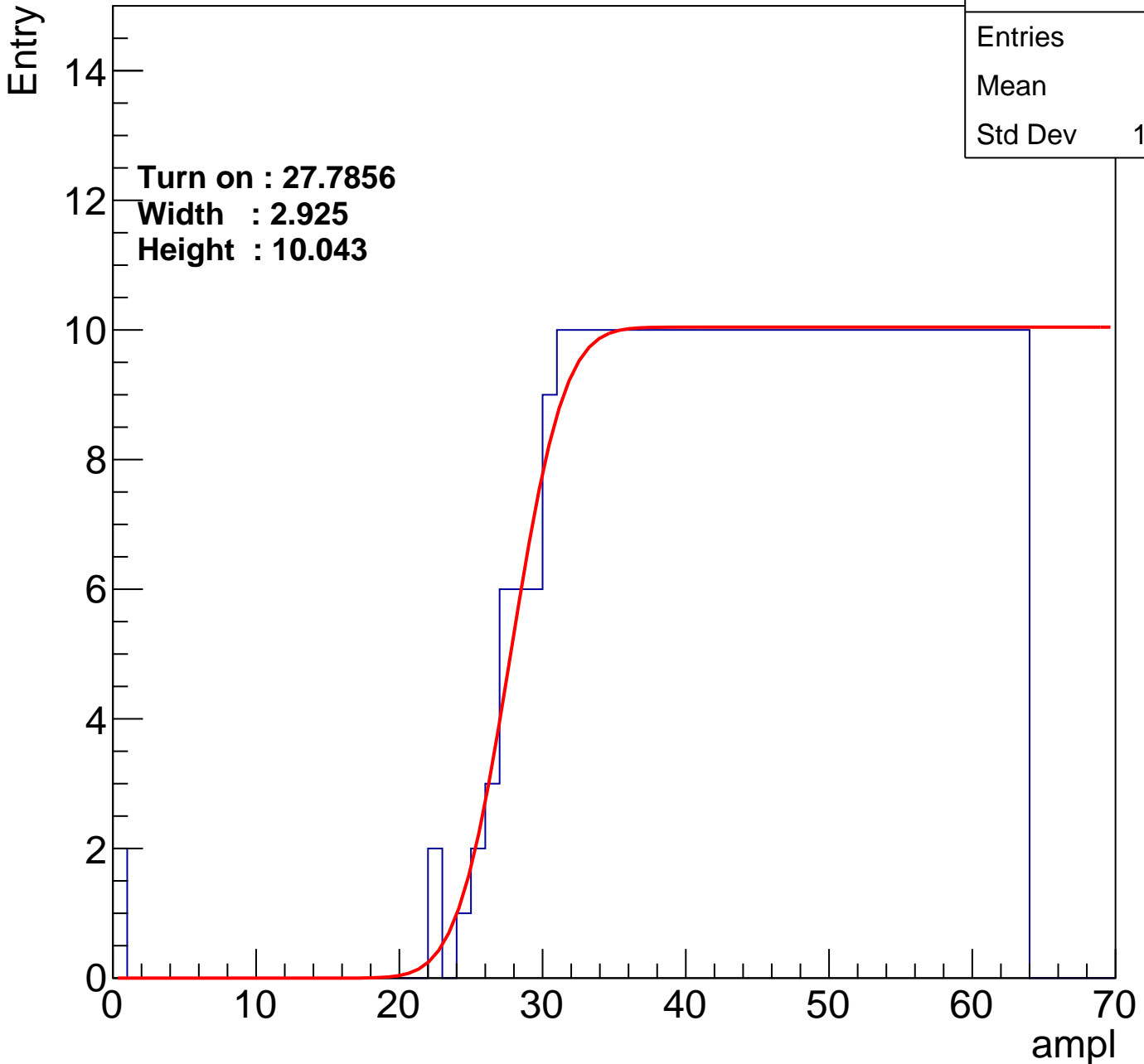
calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.7856

Width : 2.925

Height : 10.043



B1L003S, U6-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.45
Std Dev	10.9

Turn on : 29.6082

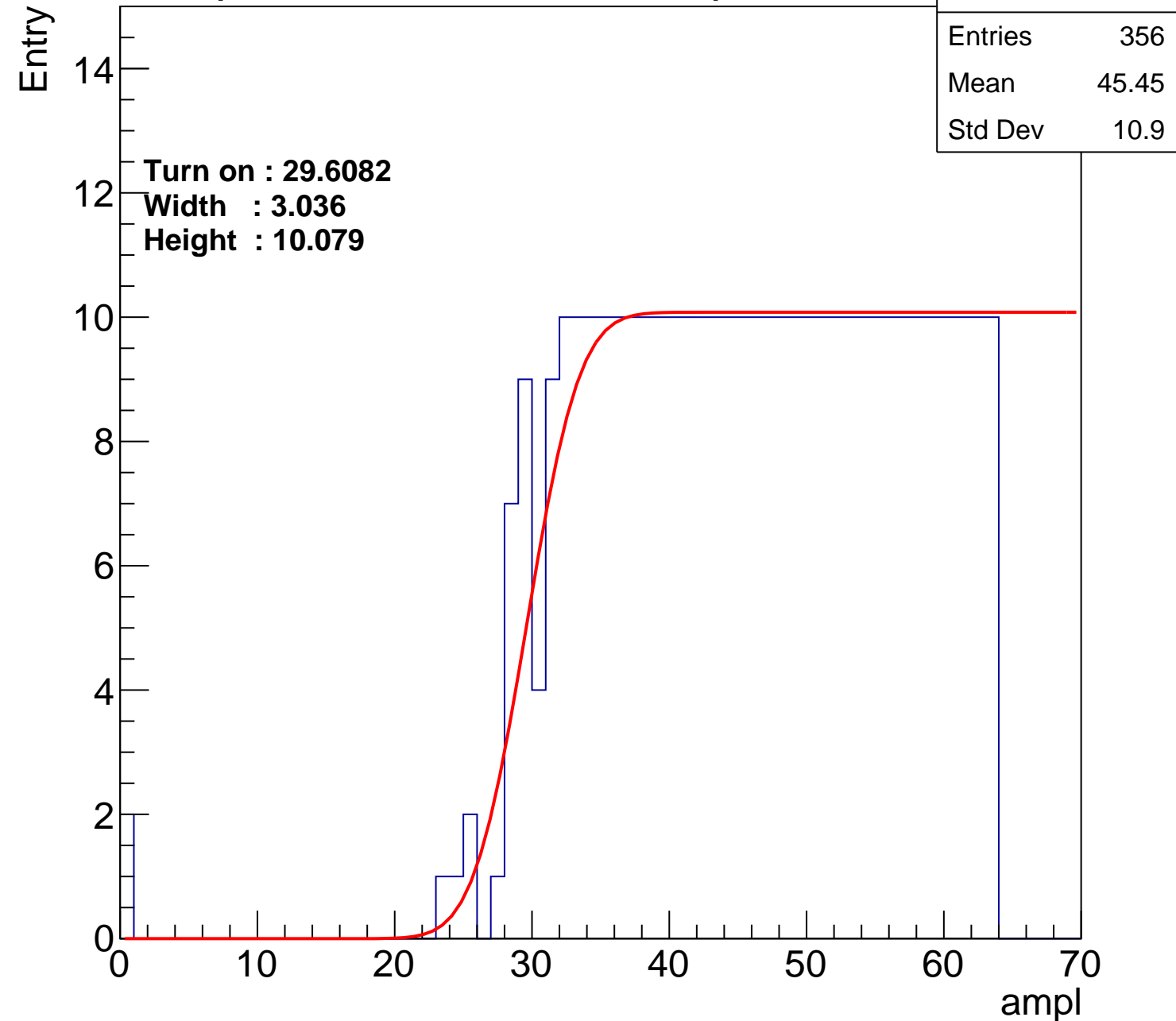
Width : 3.036

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 26.9729

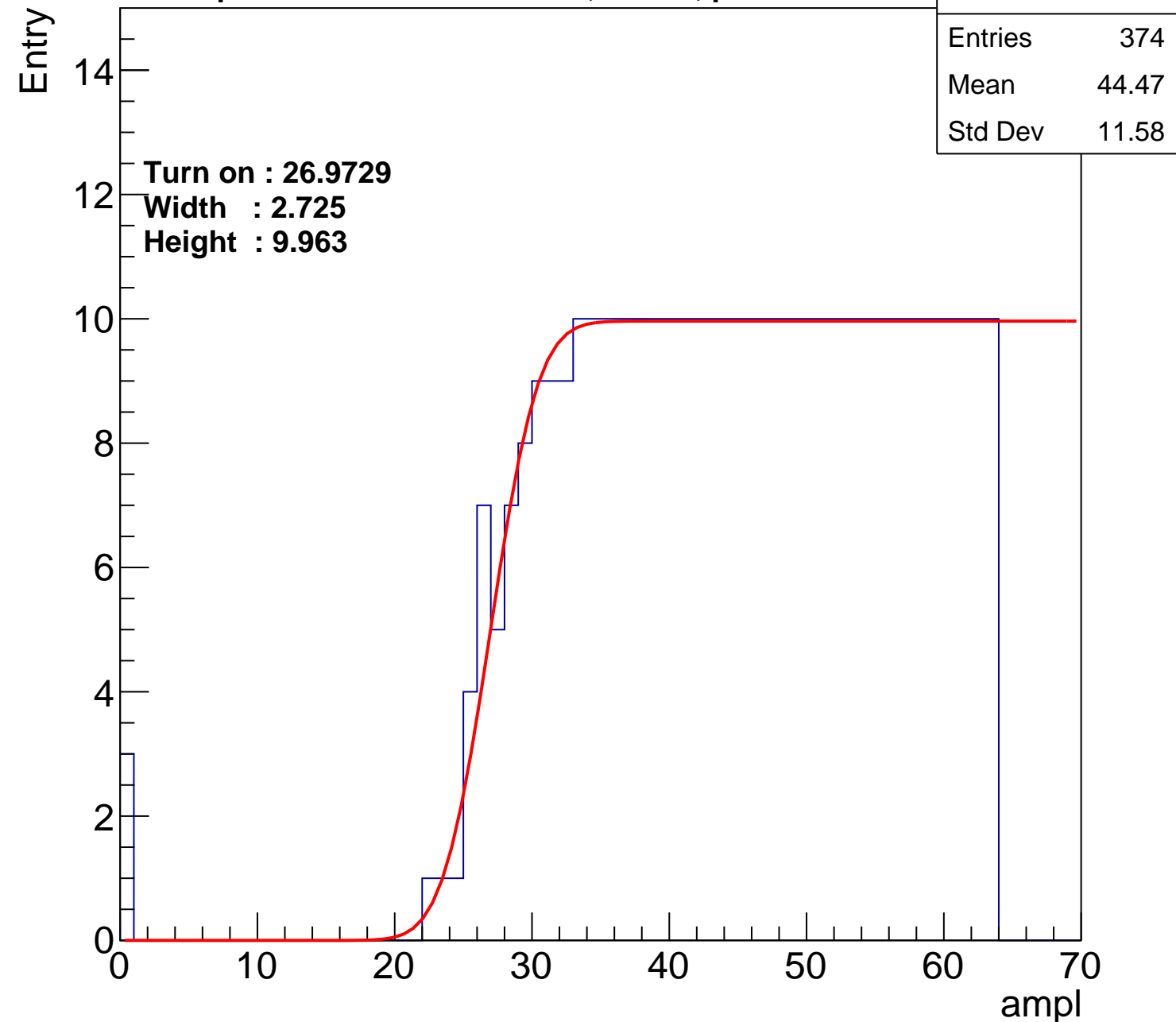
Width : 2.725

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.24
Std Dev	10.97

Turn on : 27.7165

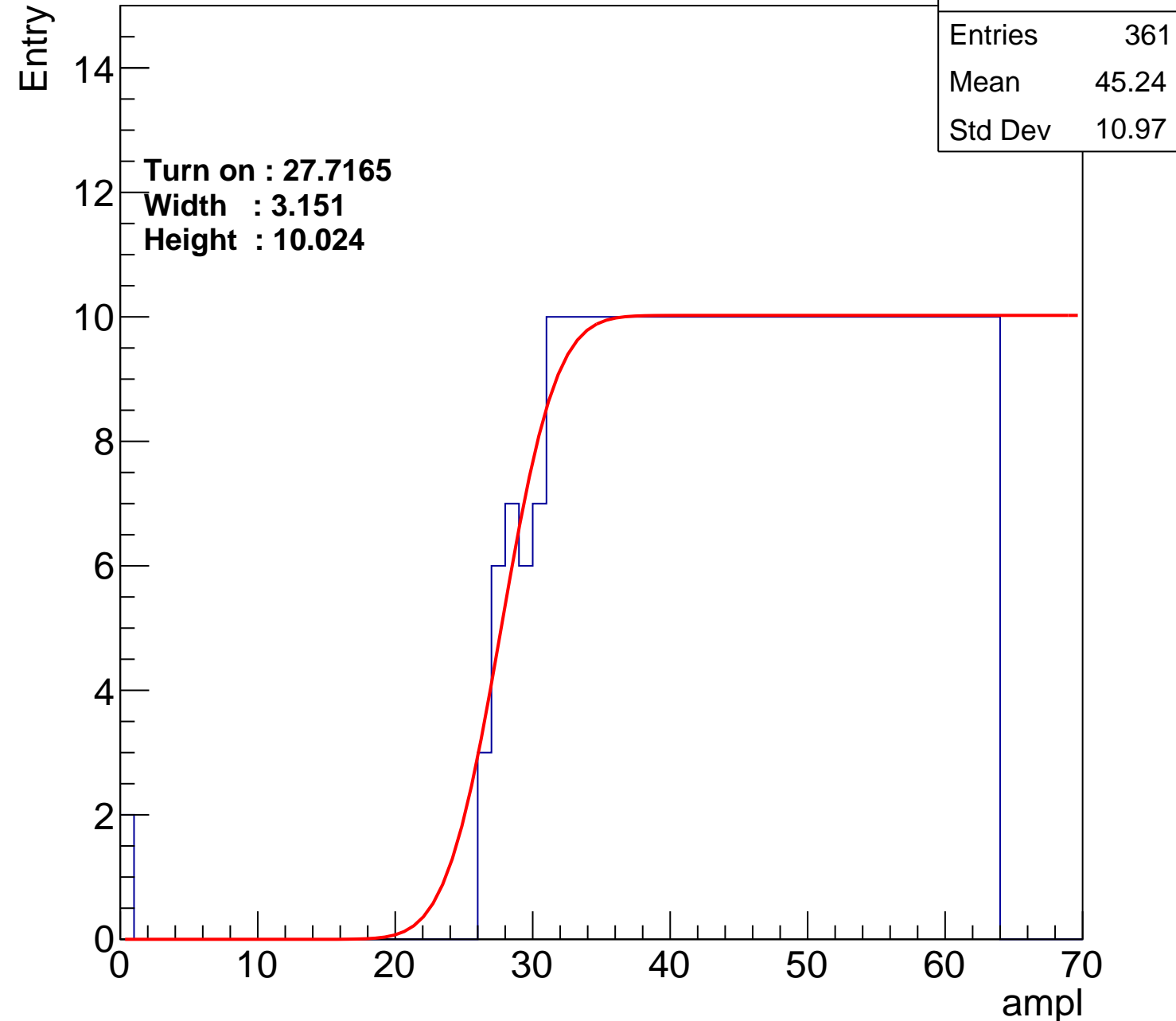
Width : 3.151

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch13

calib_packv5_042523_0143.root, FC#13, port D2

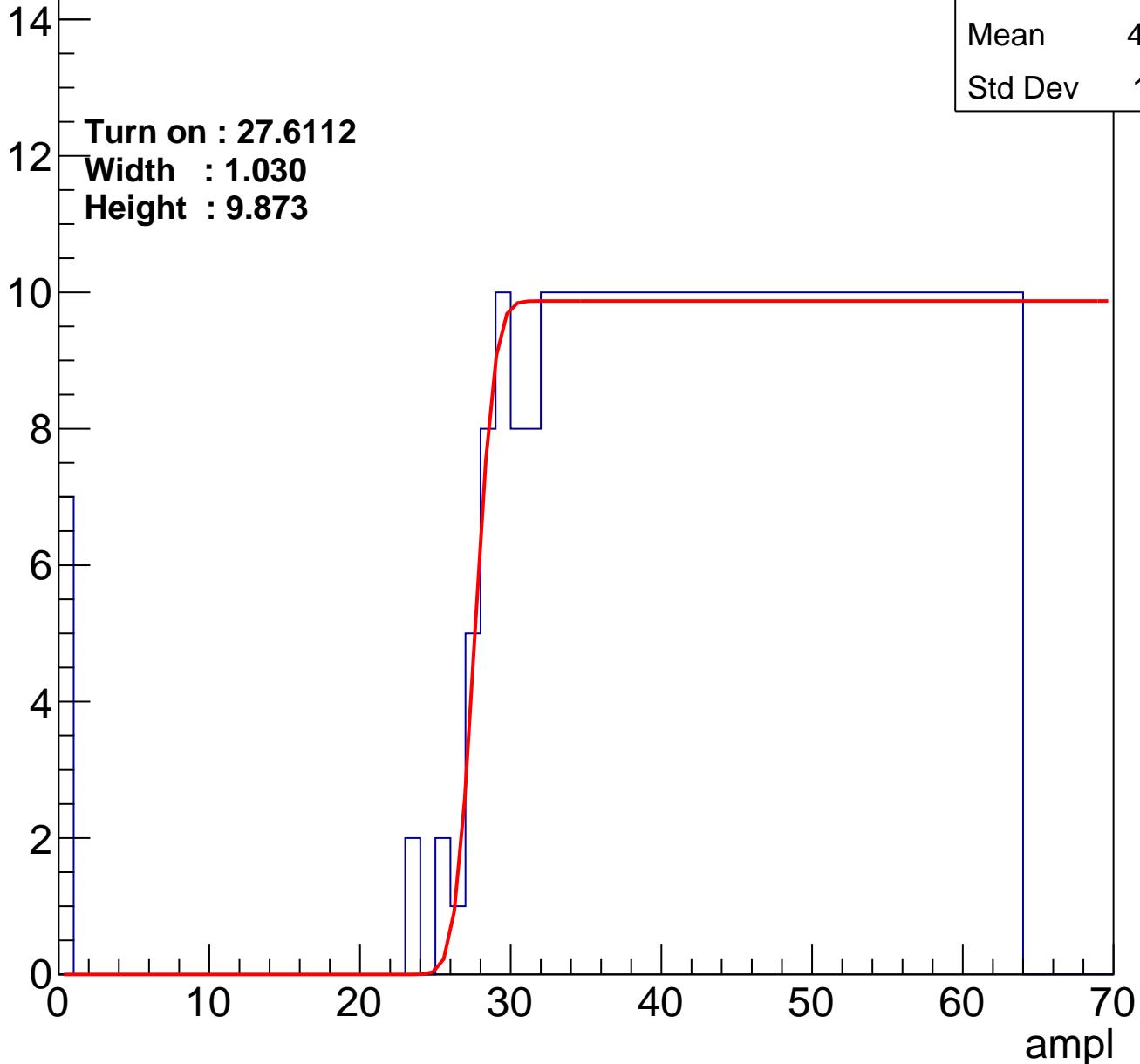
Entries	371
Mean	44.36
Std Dev	12.21

Turn on : 27.6112

Width : 1.030

Height : 9.873

Entry



B1L003S, U6-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.39
Std Dev	11.59

Turn on : 26.5928

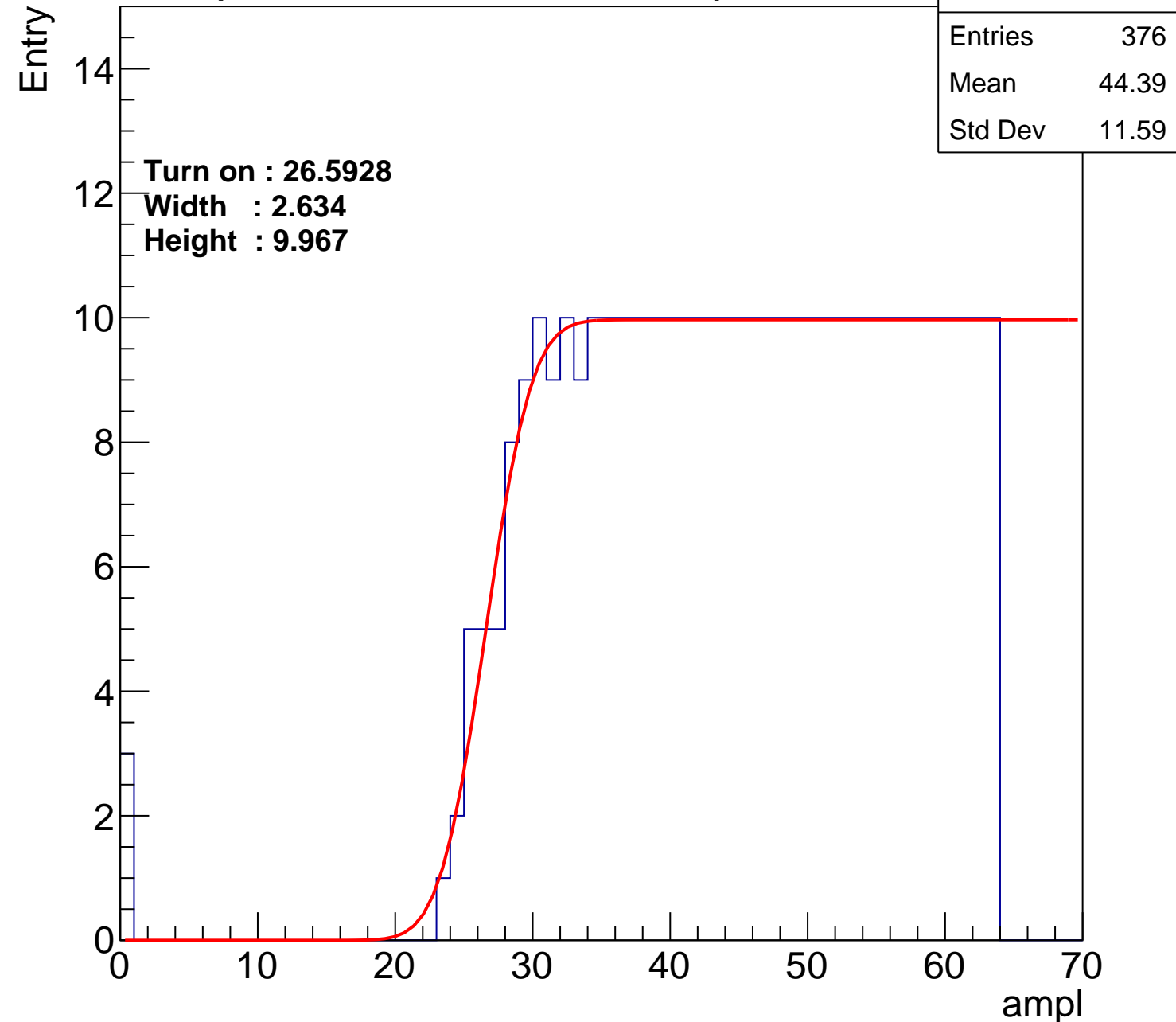
Width : 2.634

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.43
Std Dev	11.72

Turn on : 27.2625

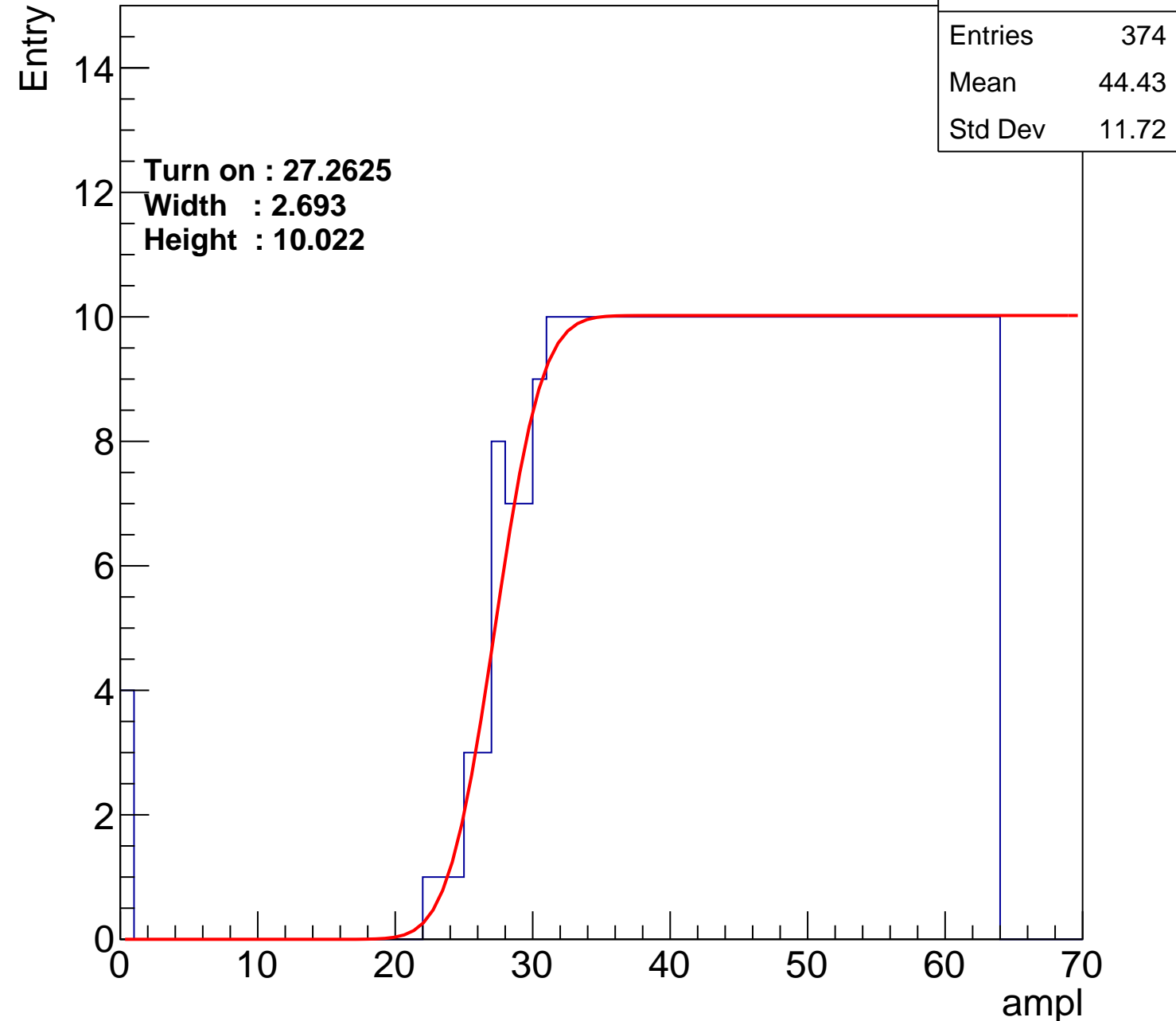
Width : 2.693

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.77
Std Dev	11.75

Turn on : 27.9763

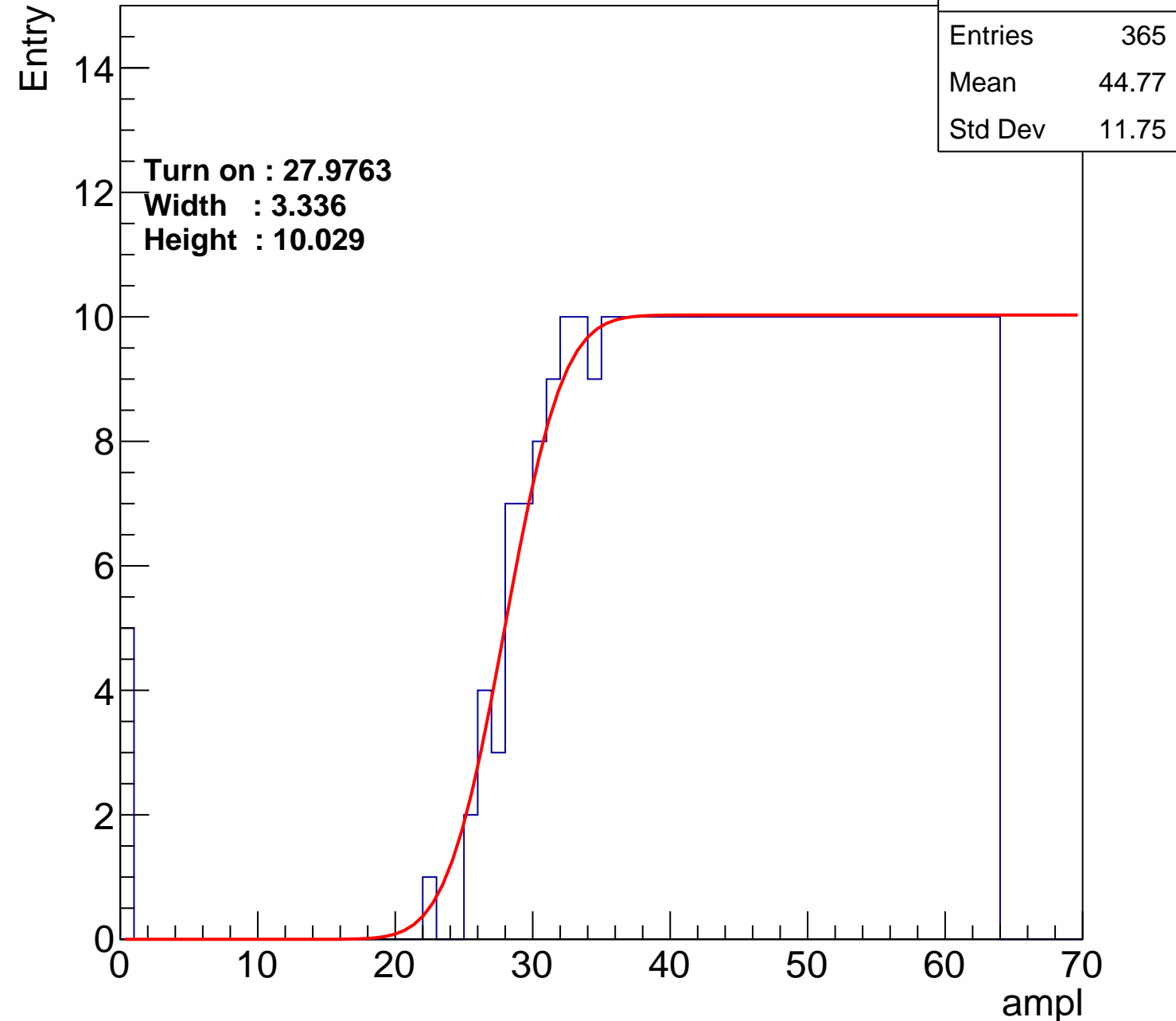
Width : 3.336

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.74
Std Dev	11.55

Turn on : 27.6513

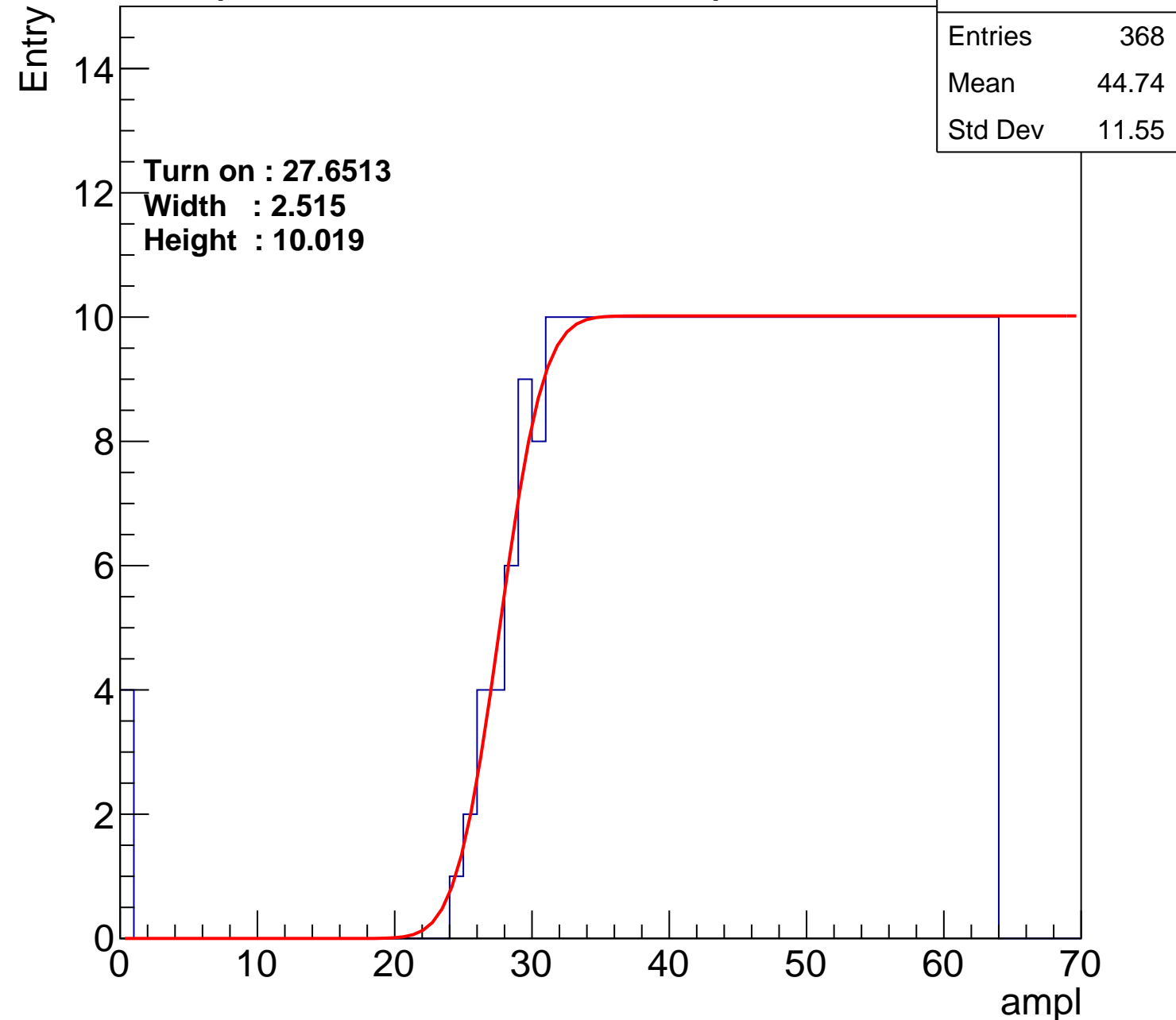
Width : 2.515

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.45
Std Dev	11.57

Turn on : 27.2830

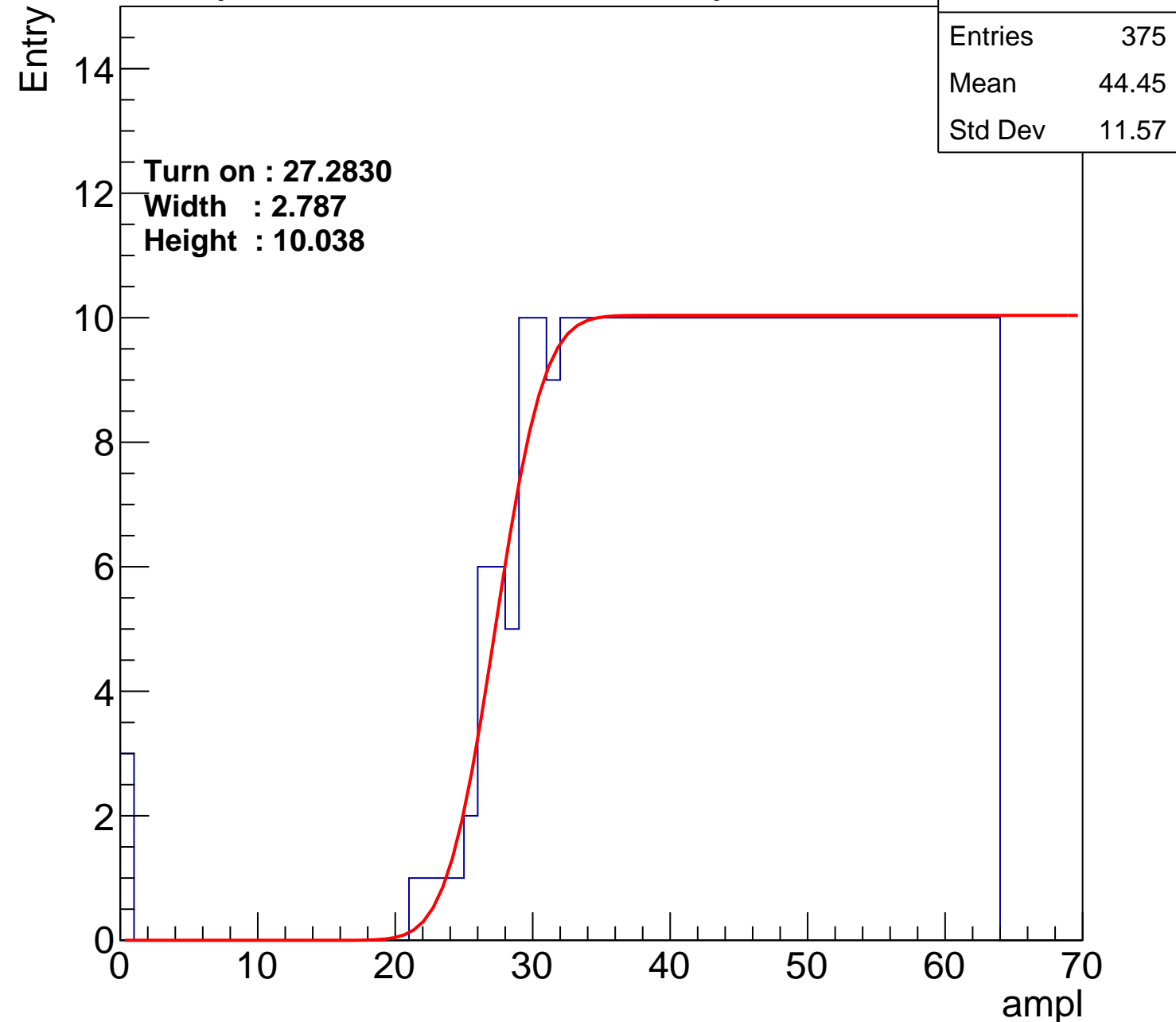
Width : 2.787

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.34
Std Dev	10.97

Turn on : 28.6227

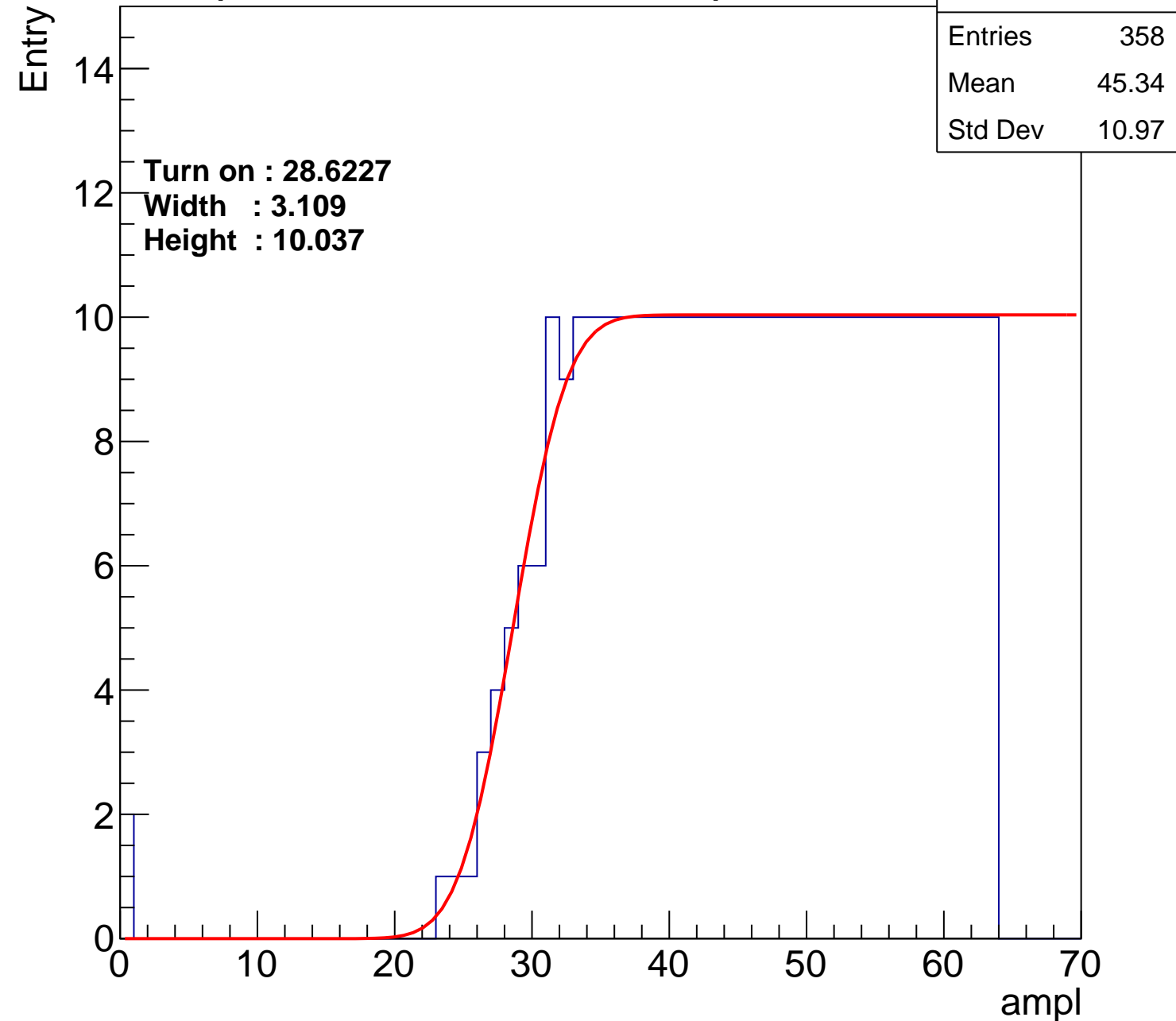
Width : 3.109

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.37
Std Dev	10.76

Turn on : 28.0800

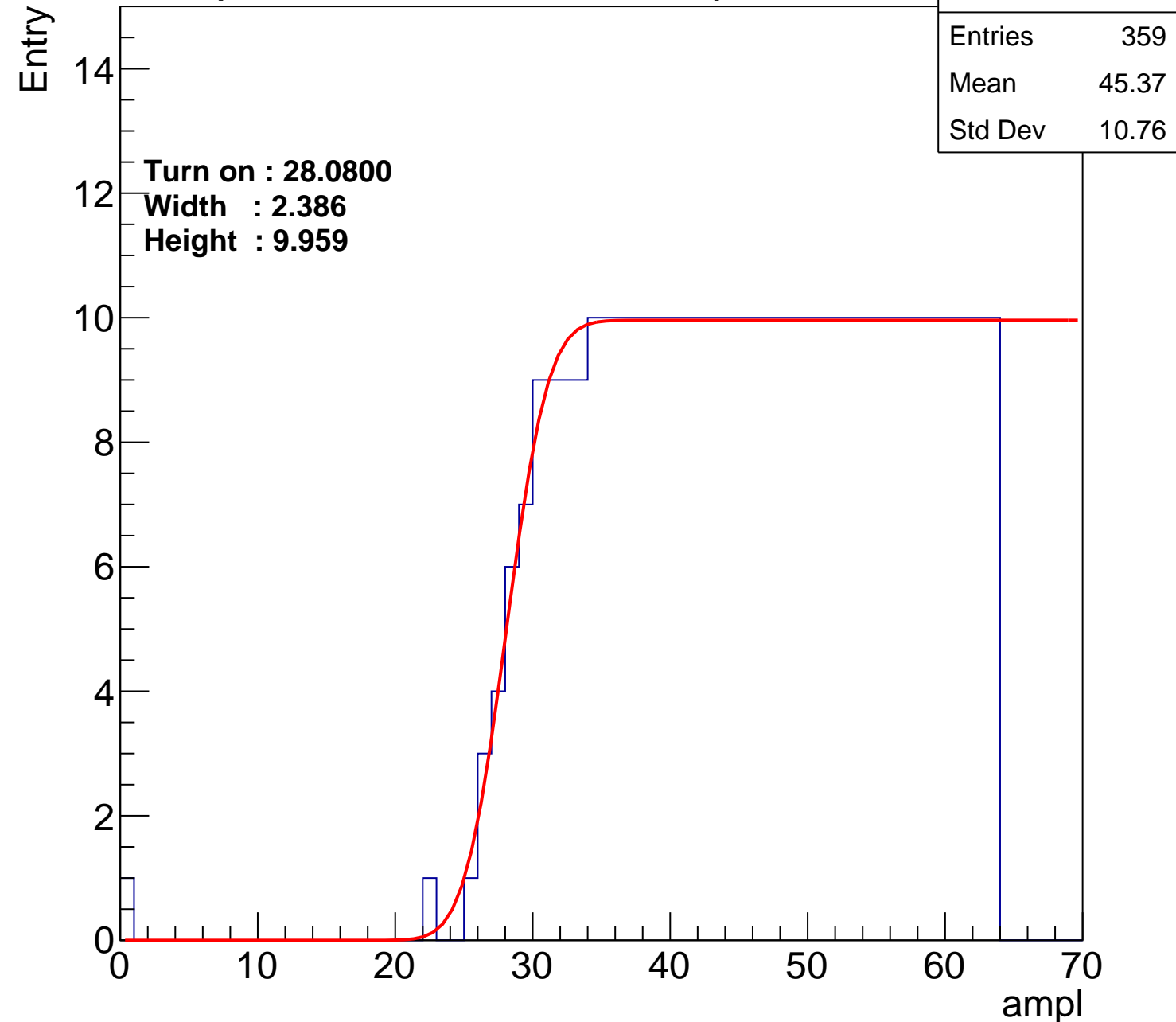
Width : 2.386

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.21
Std Dev	11.01

Turn on : 27.9019

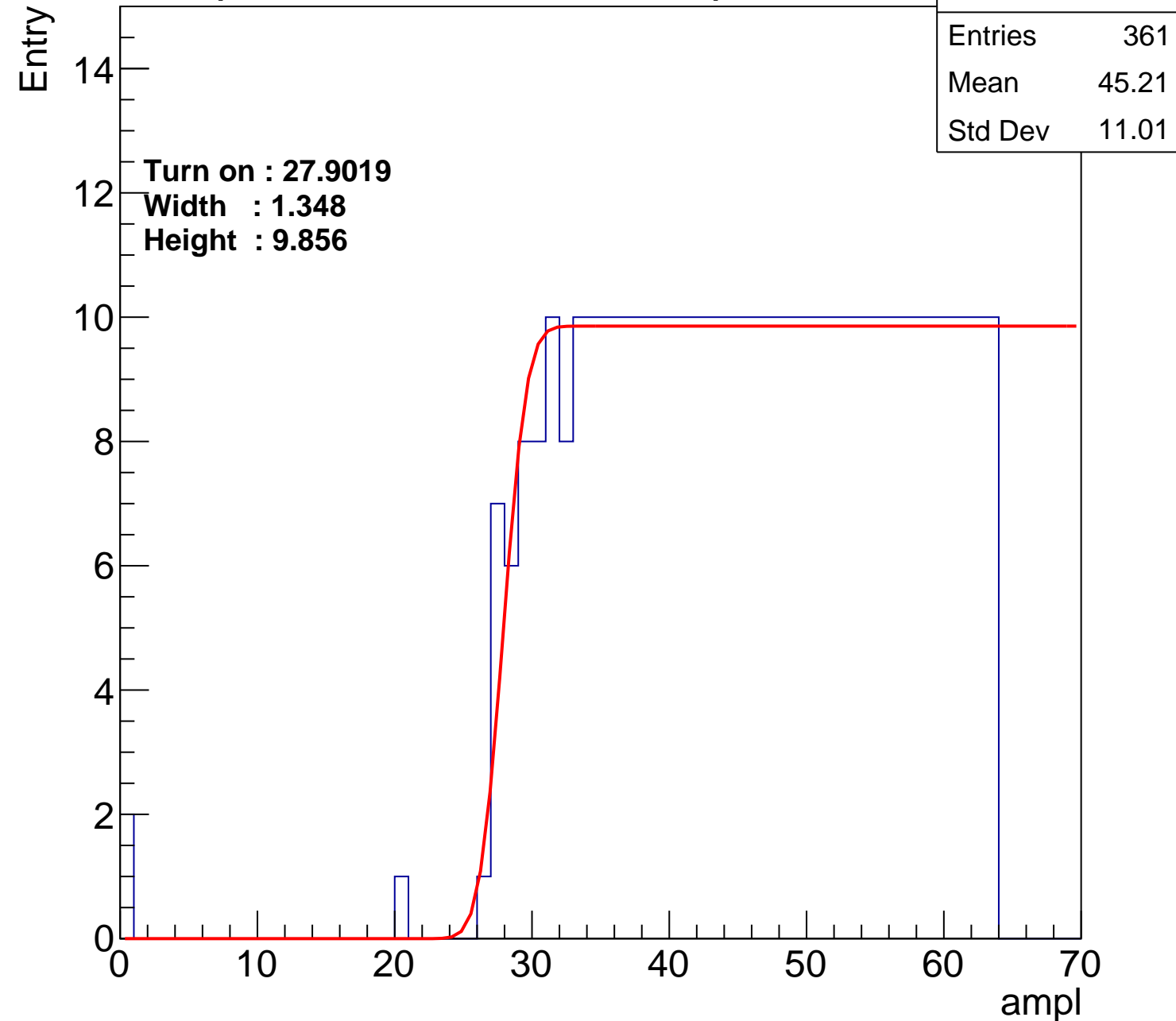
Width : 1.348

Height : 9.856

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.86
Std Dev	11.34

Turn on : 28.2334

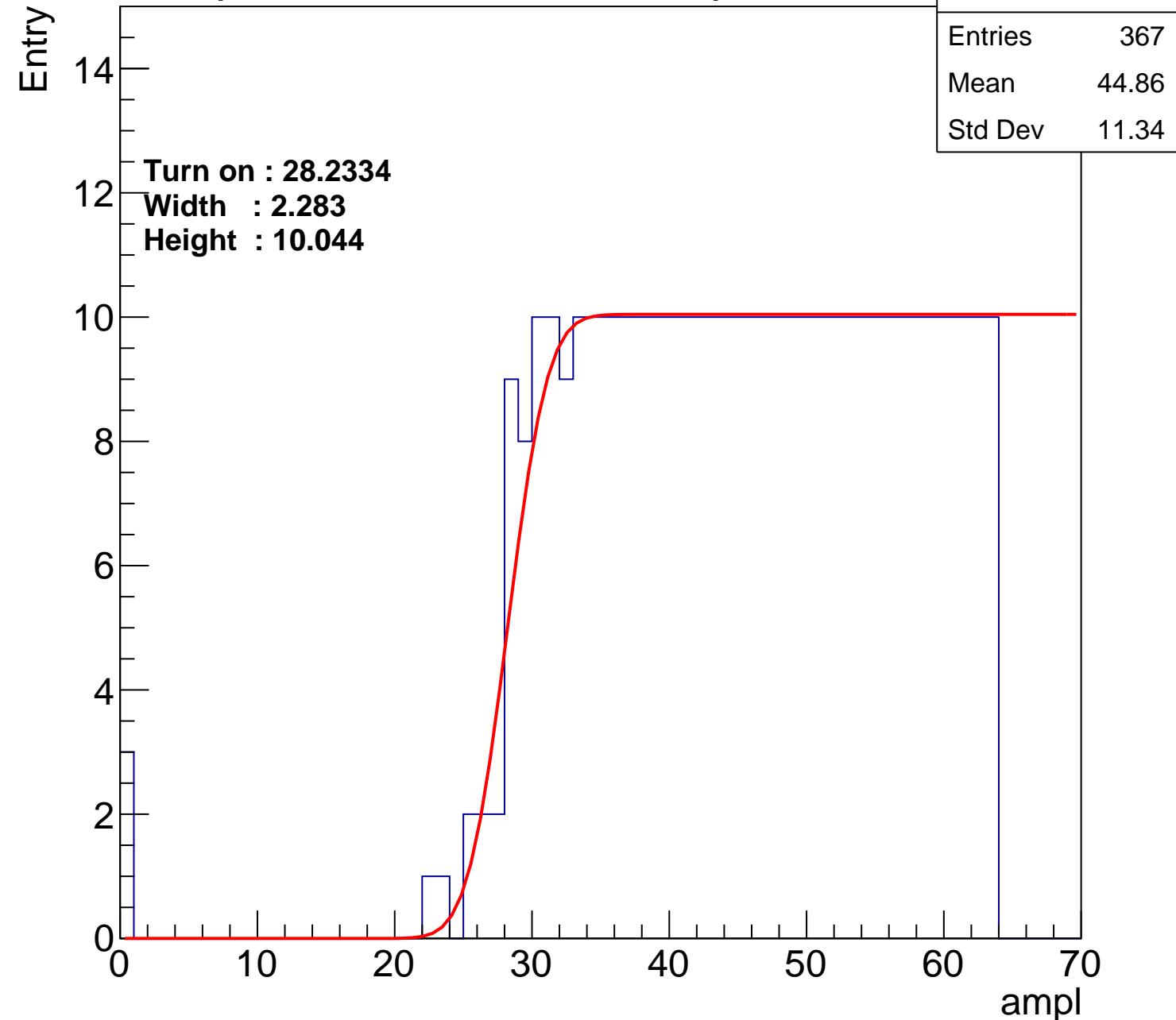
Width : 2.283

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 28.1484

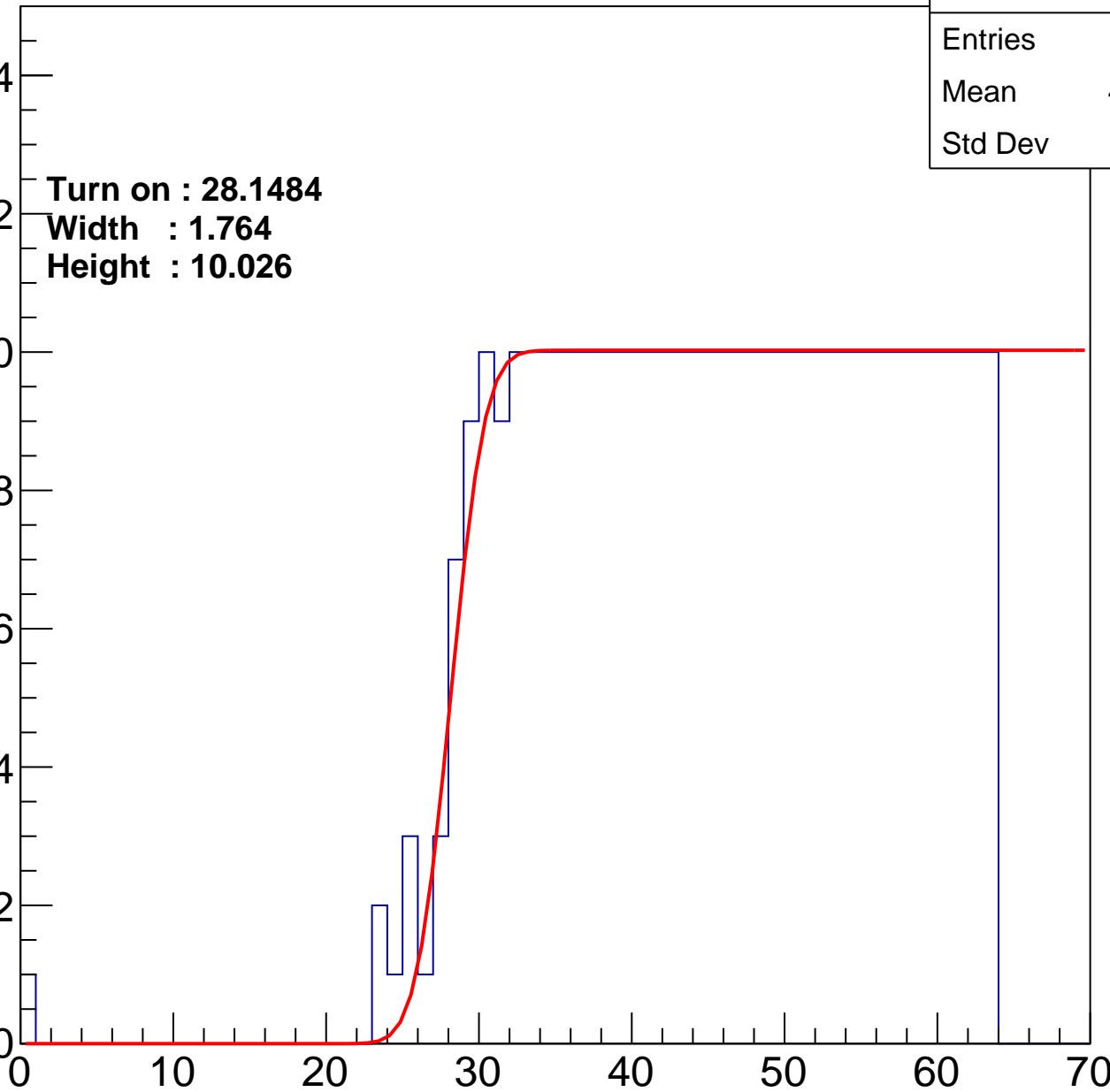
Width : 1.764

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 26.3544

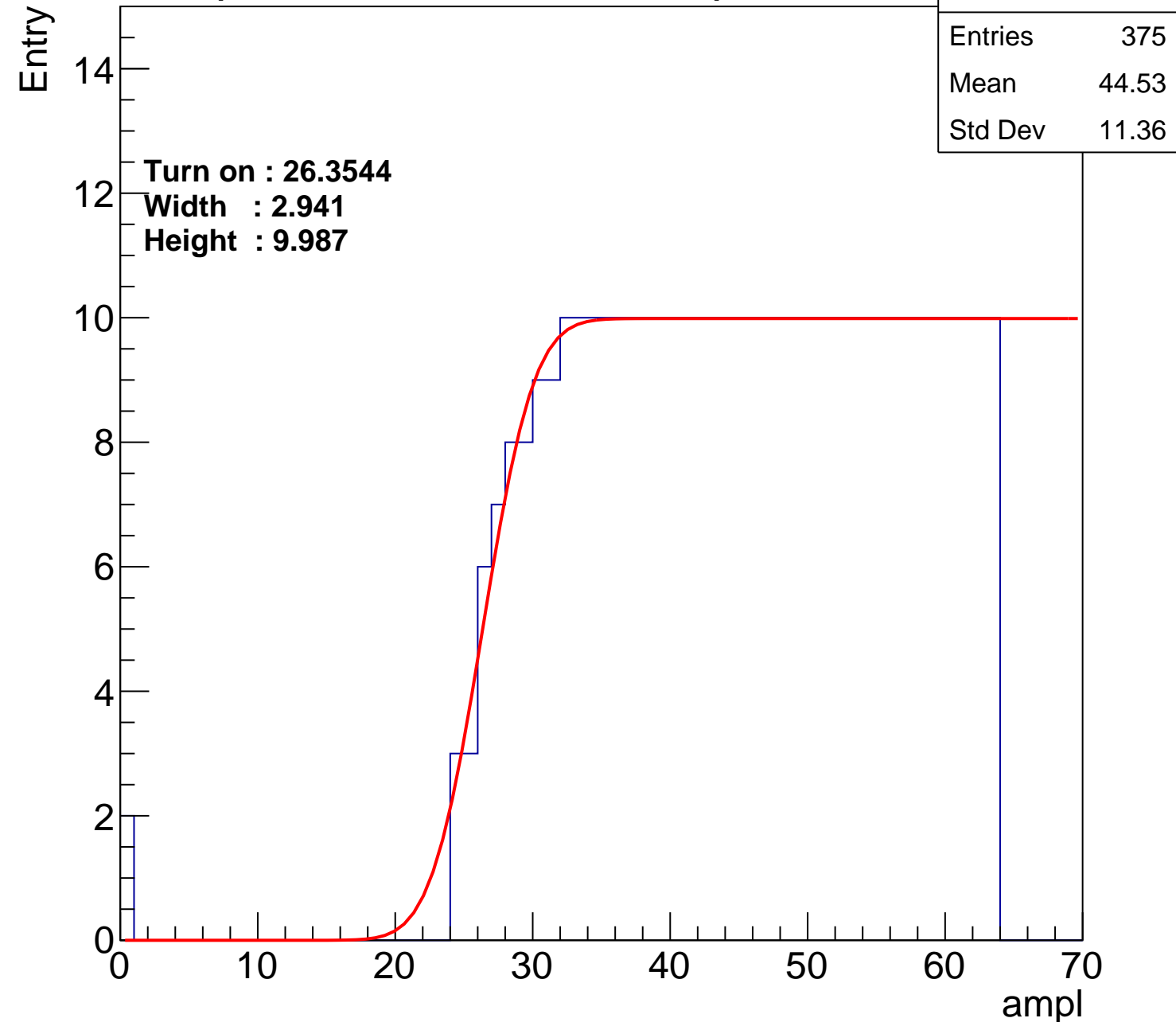
Width : 2.941

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch25

calib_packv5_042523_0143.root, FC#13, port D2

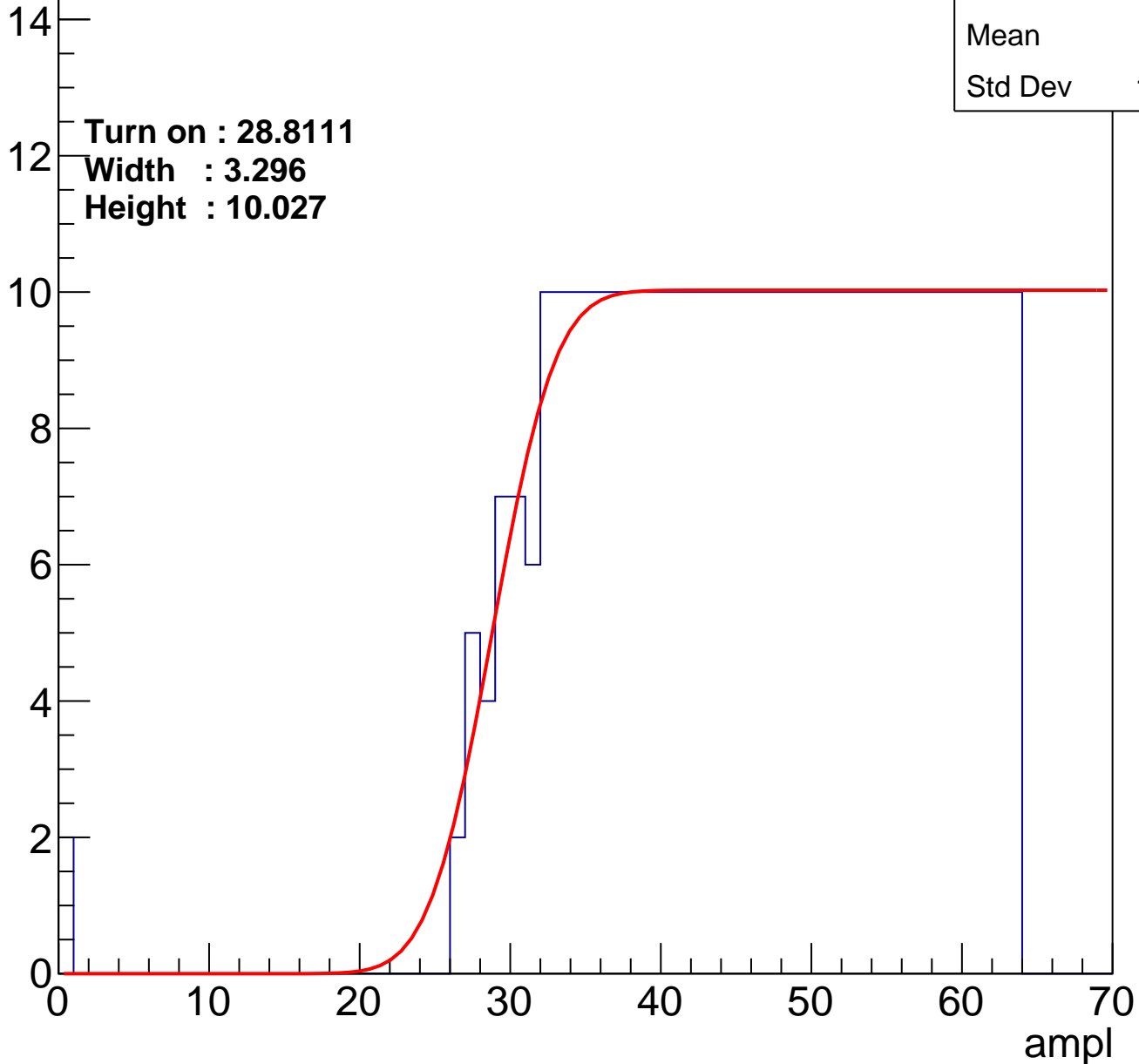
Entries	353
Mean	45.6
Std Dev	10.81

Turn on : 28.8111

Width : 3.296

Height : 10.027

Entry



B1L003S, U6-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 27.1399

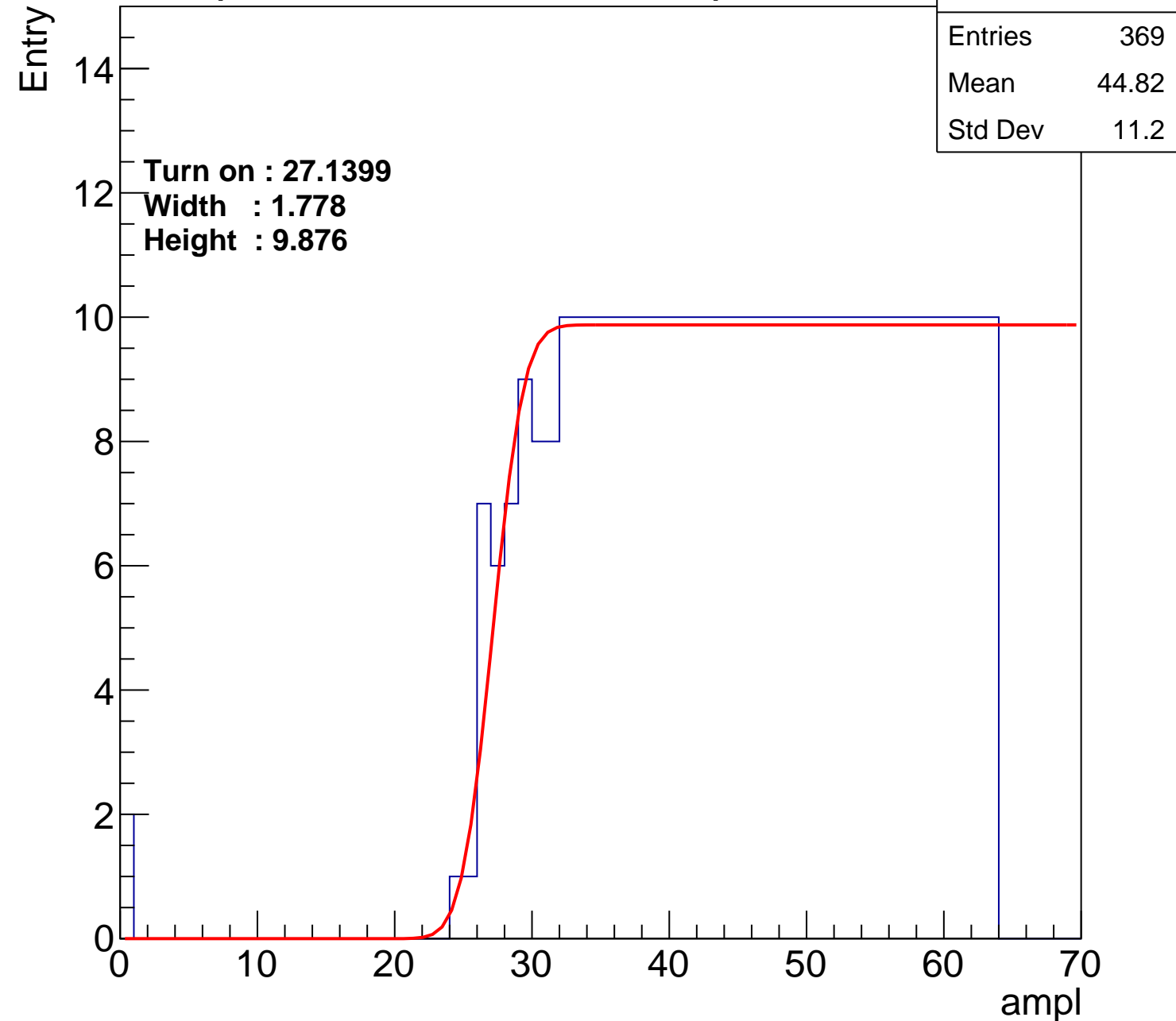
Width : 1.778

Height : 9.876

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.19
Std Dev	10.9

Turn on : 28.1799

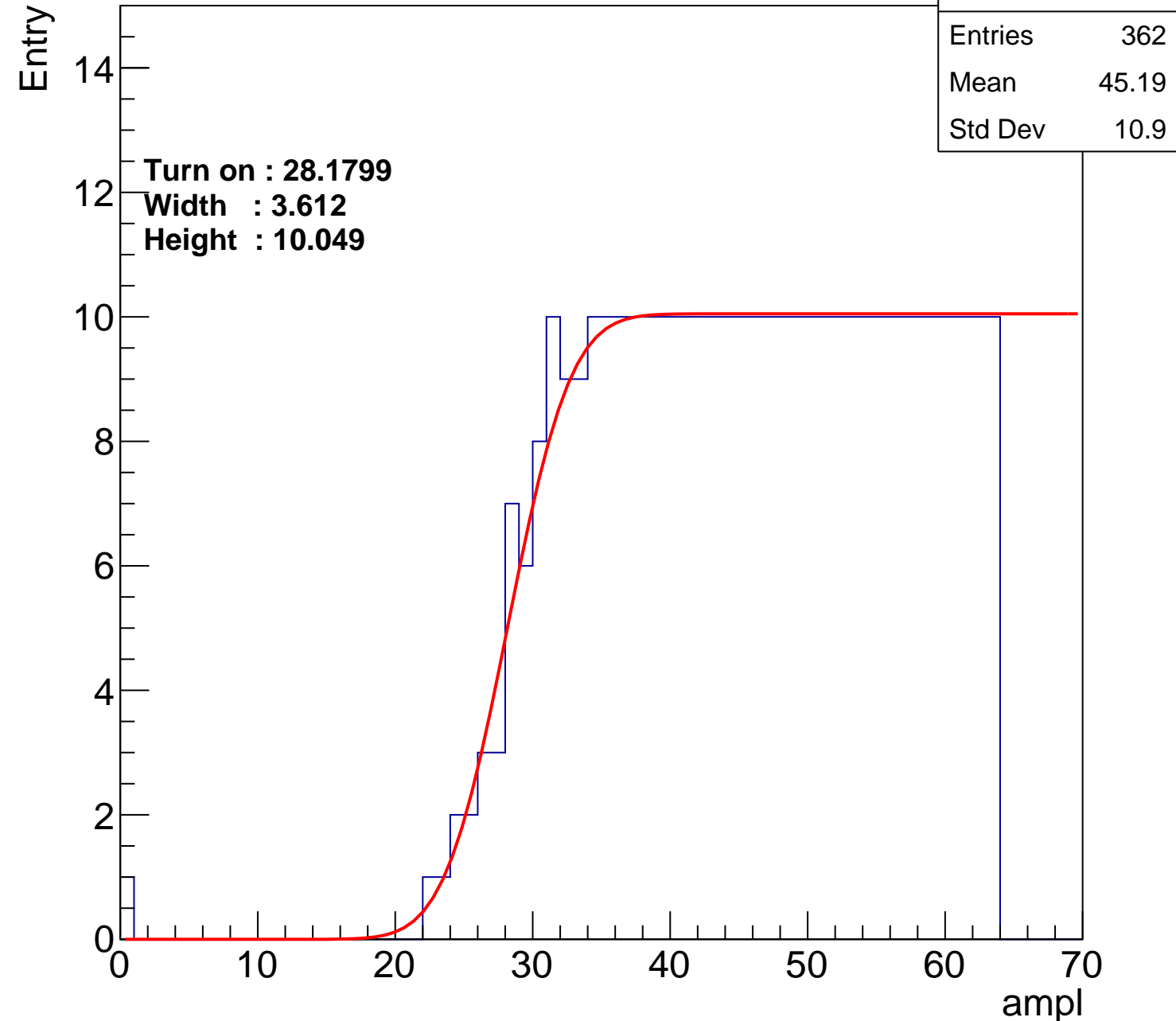
Width : 3.612

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 27.4316

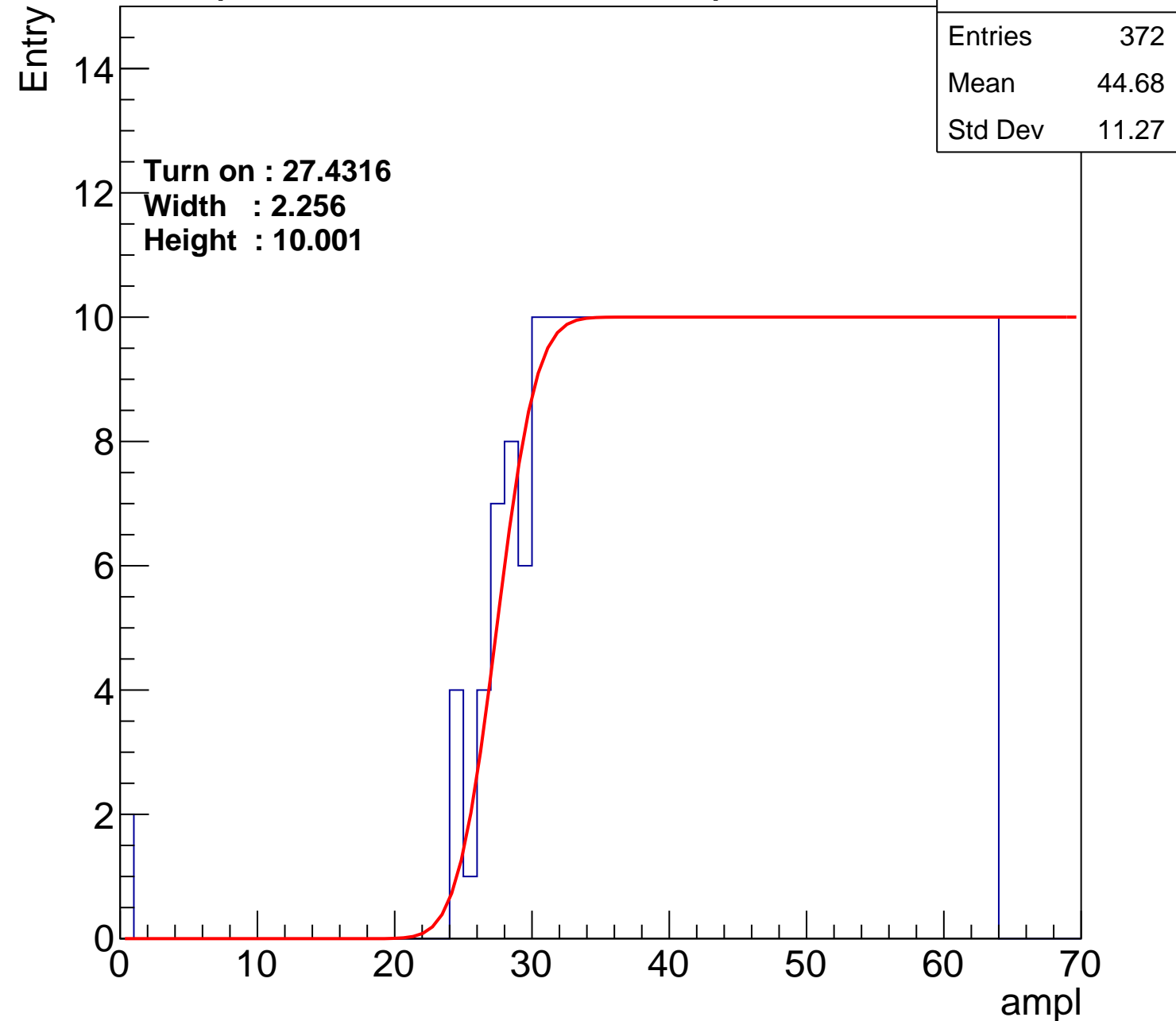
Width : 2.256

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch29

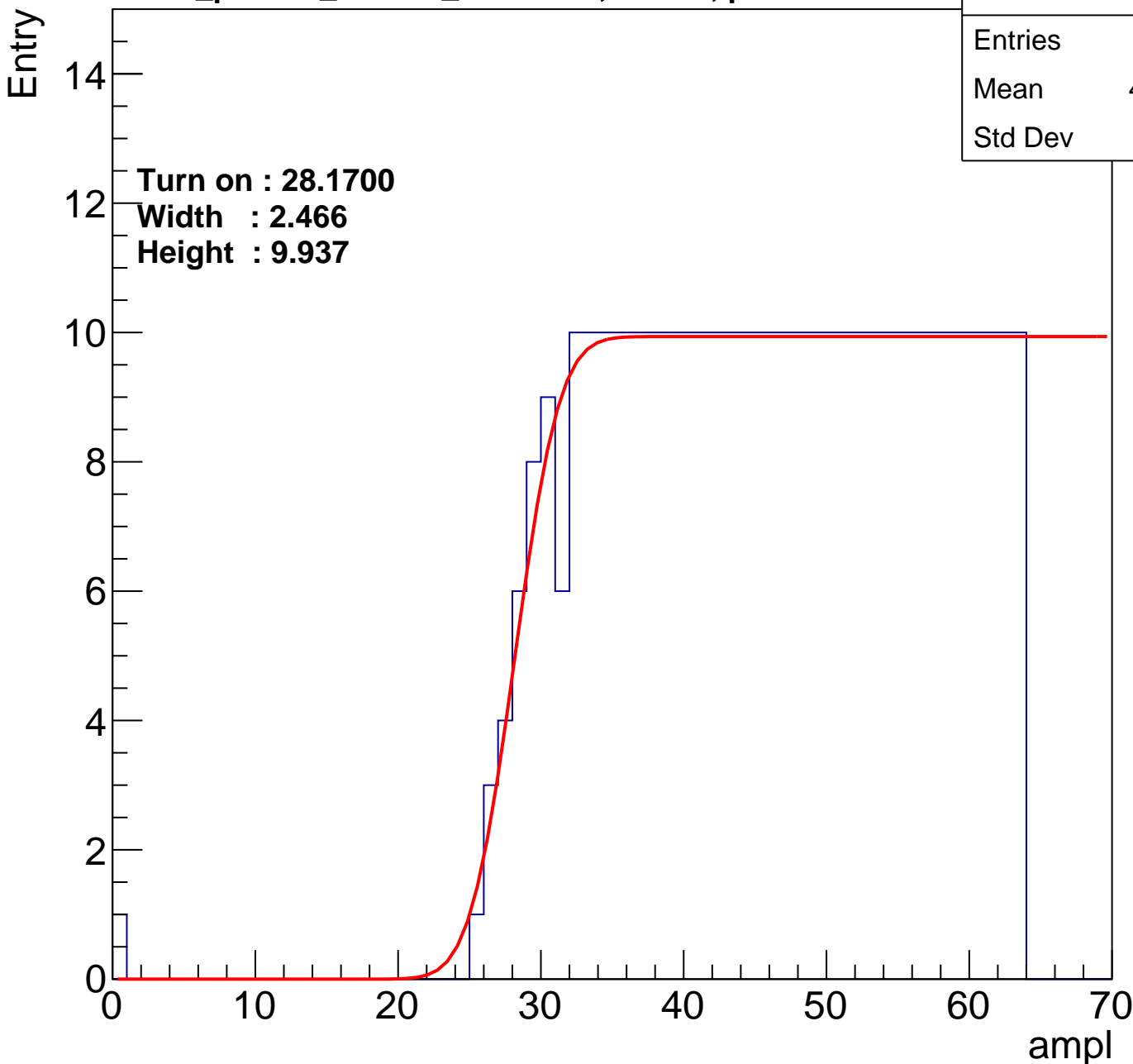
calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.44
Std Dev	10.7

Turn on : 28.1700

Width : 2.466

Height : 9.937



B1L003S, U6-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.82
Std Dev	11.4

Turn on : 27.5412

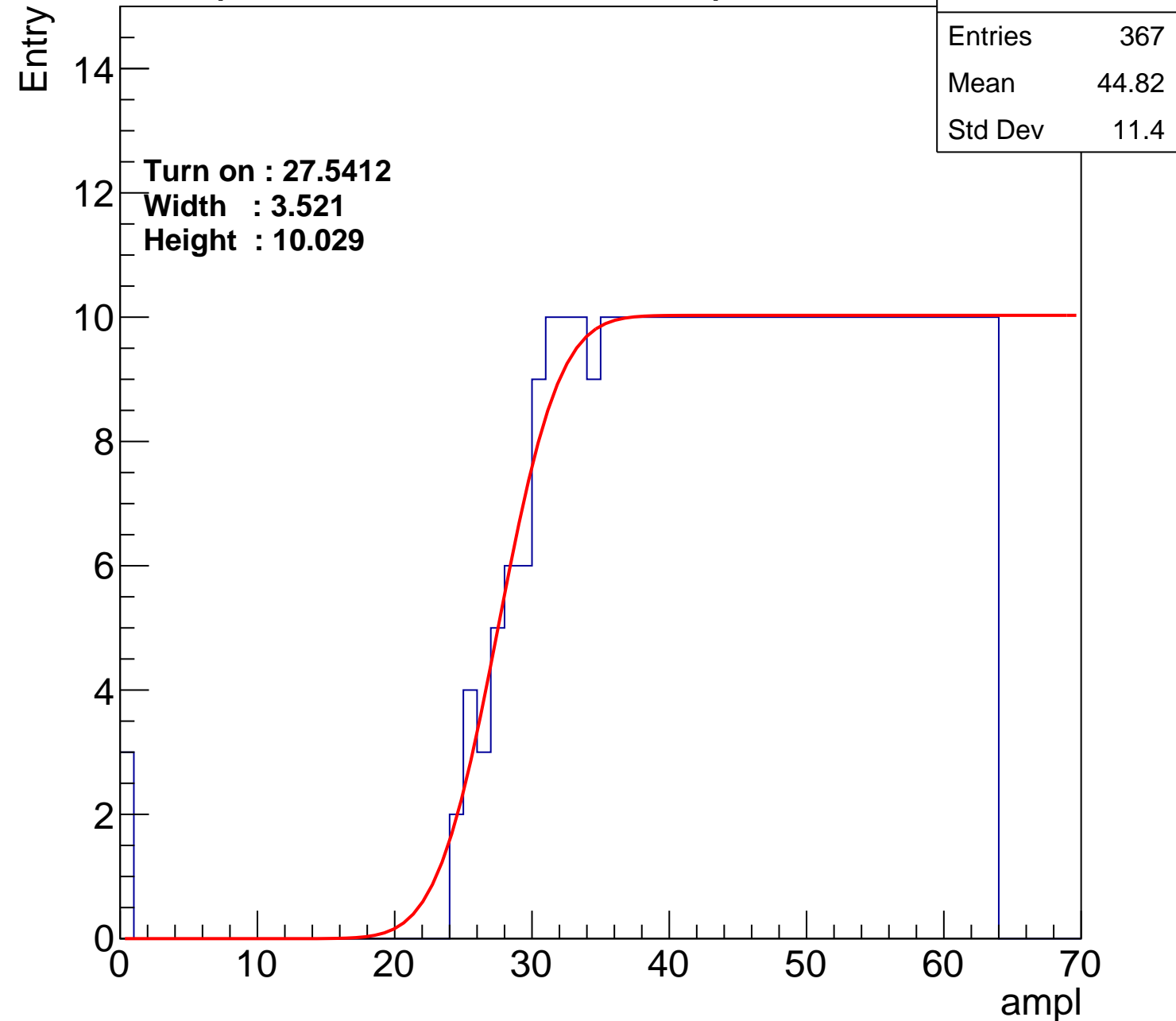
Width : 3.521

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.51
Std Dev	11.65

Turn on : 27.2560

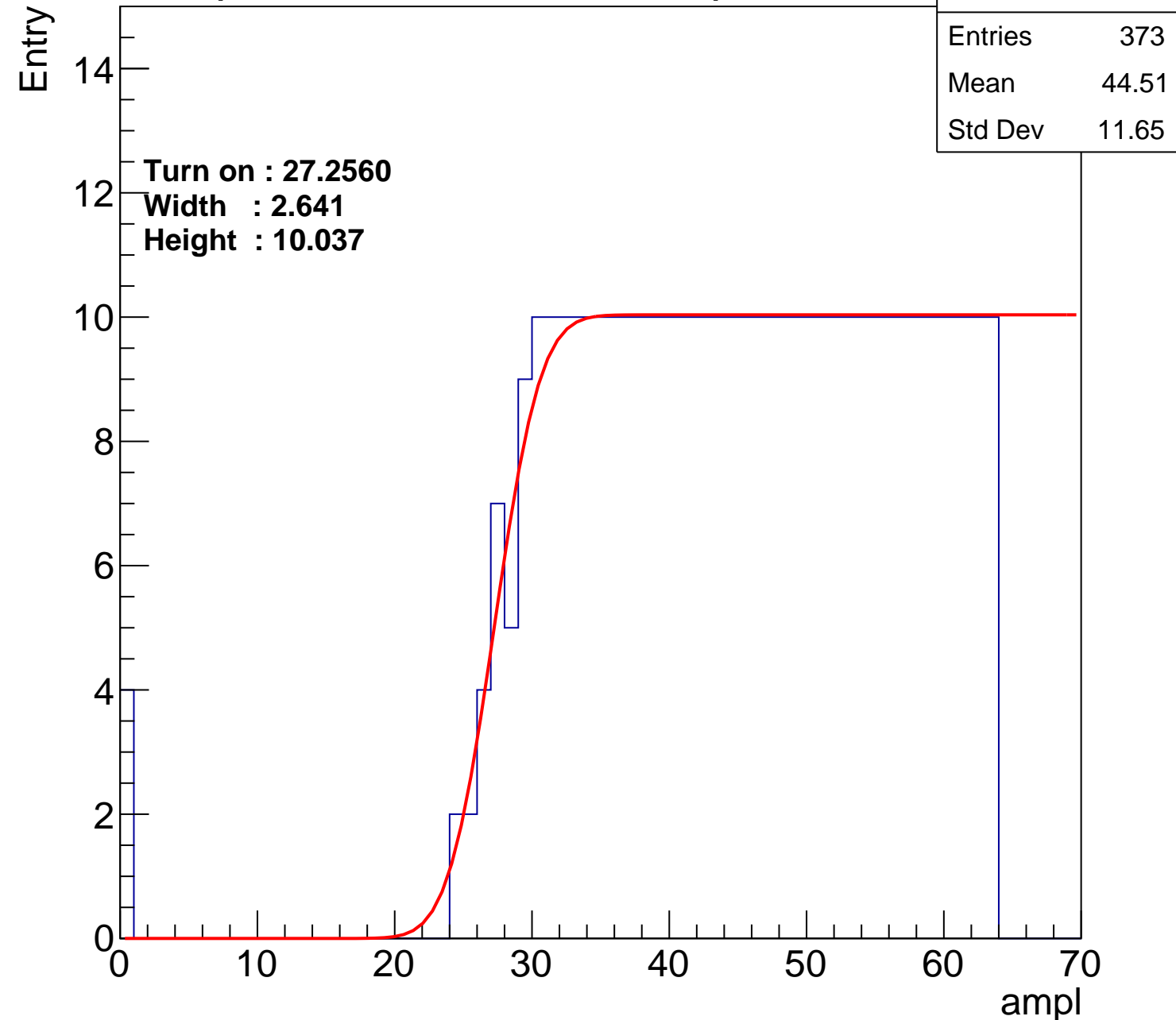
Width : 2.641

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 27.0746

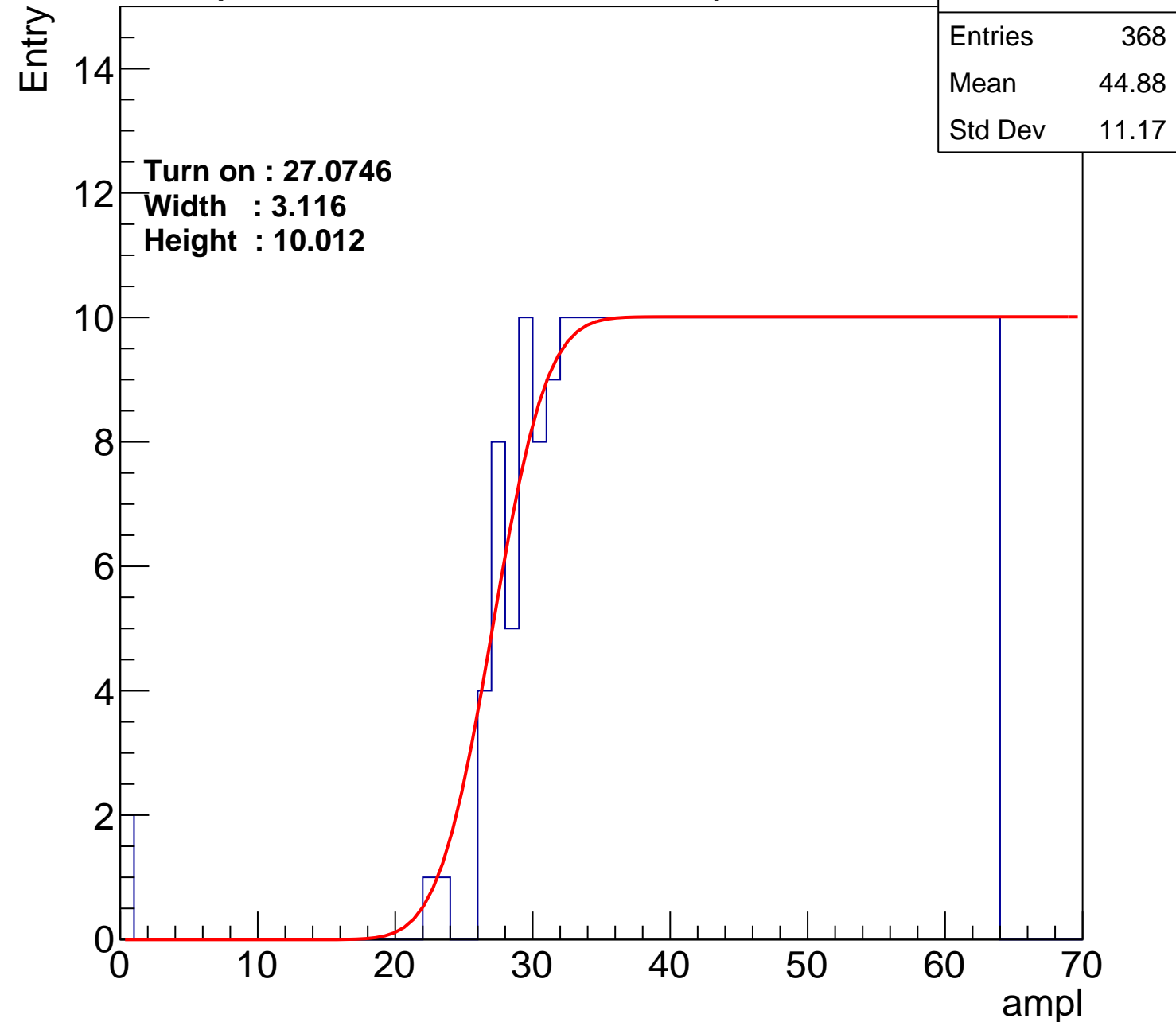
Width : 3.116

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.1
Std Dev	10.89

Turn on : 27.7905

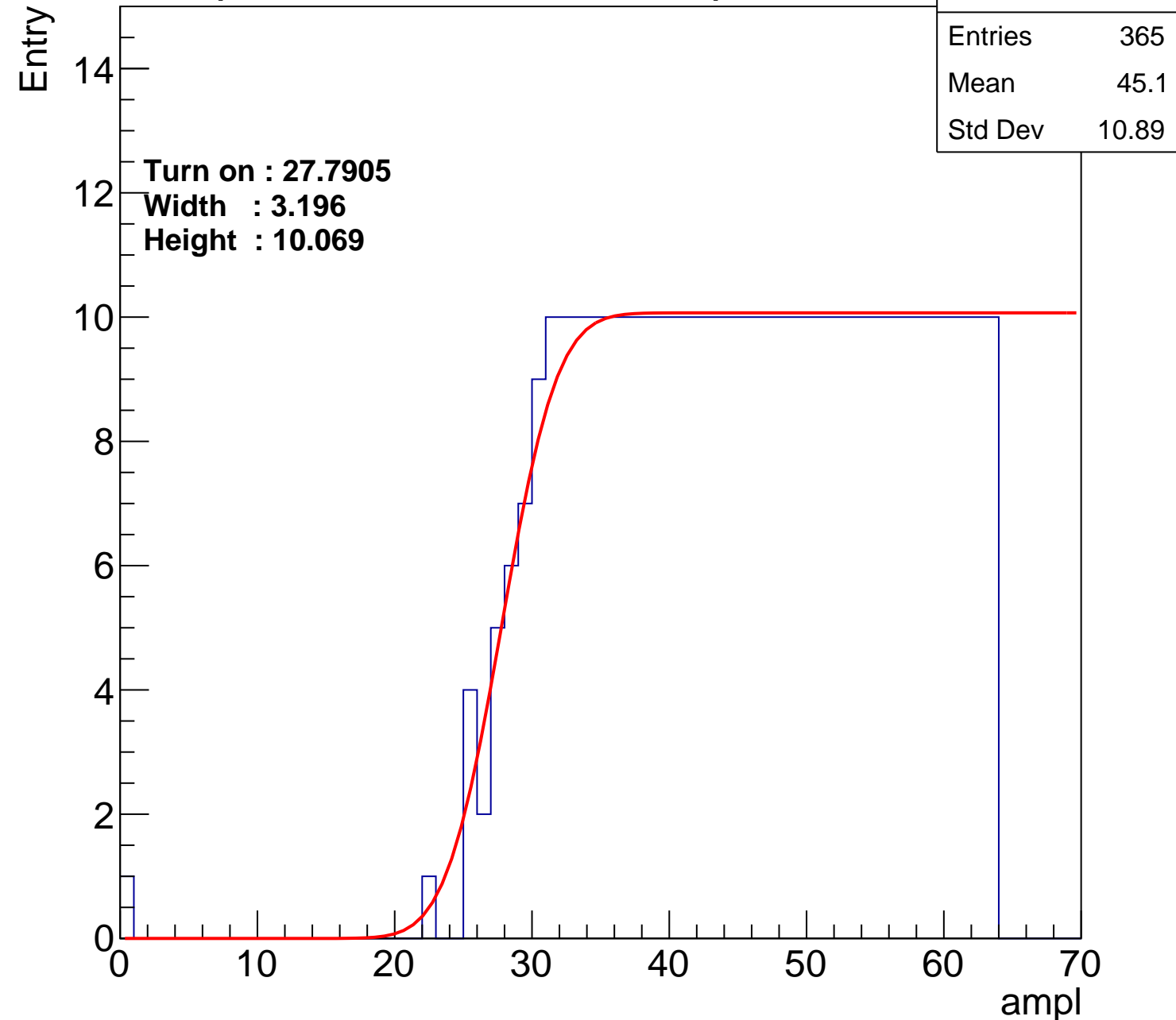
Width : 3.196

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.0664

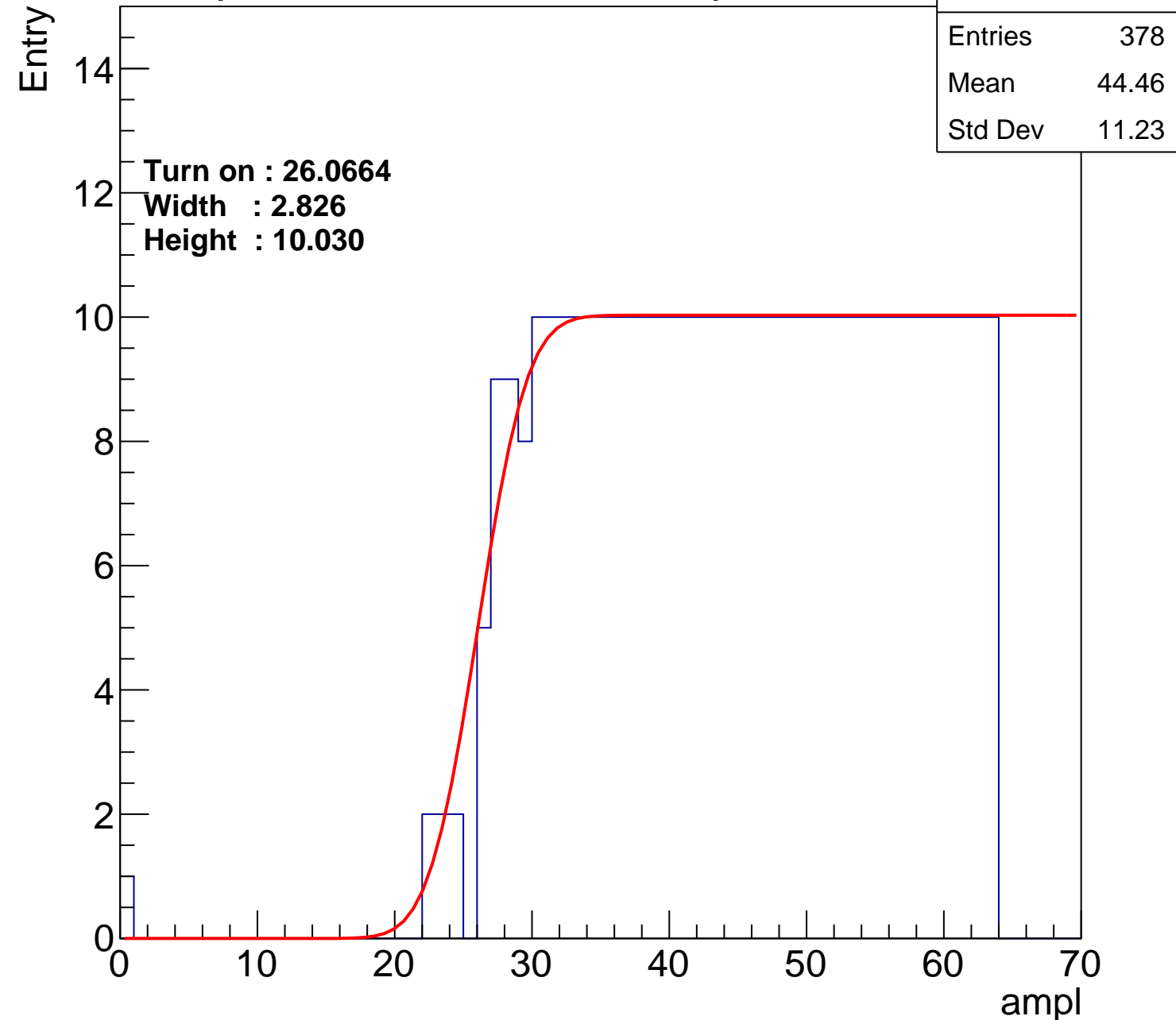
Width : 2.826

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.72
Std Dev	11.6

Turn on : 27.7980

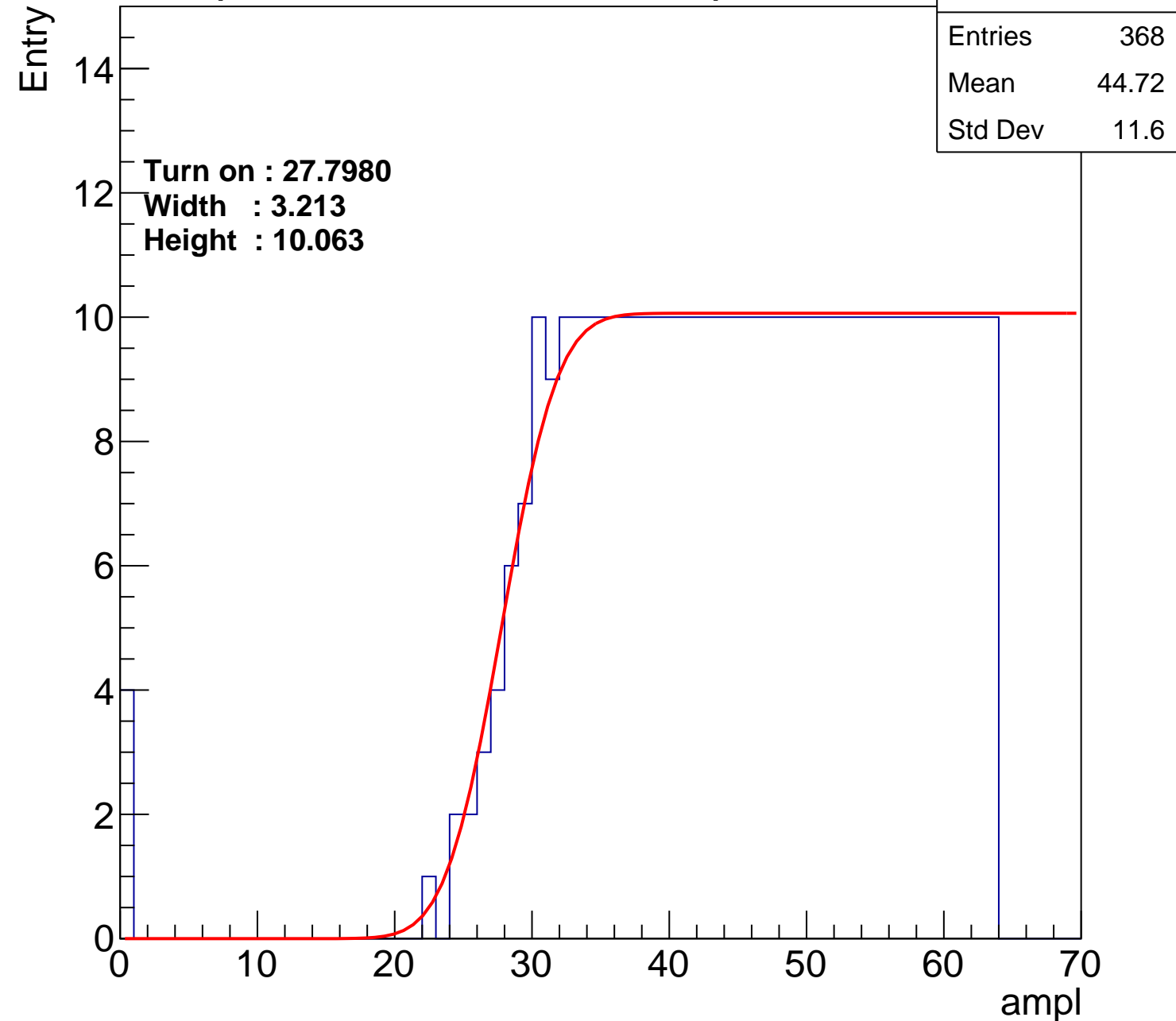
Width : 3.213

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 28.0789

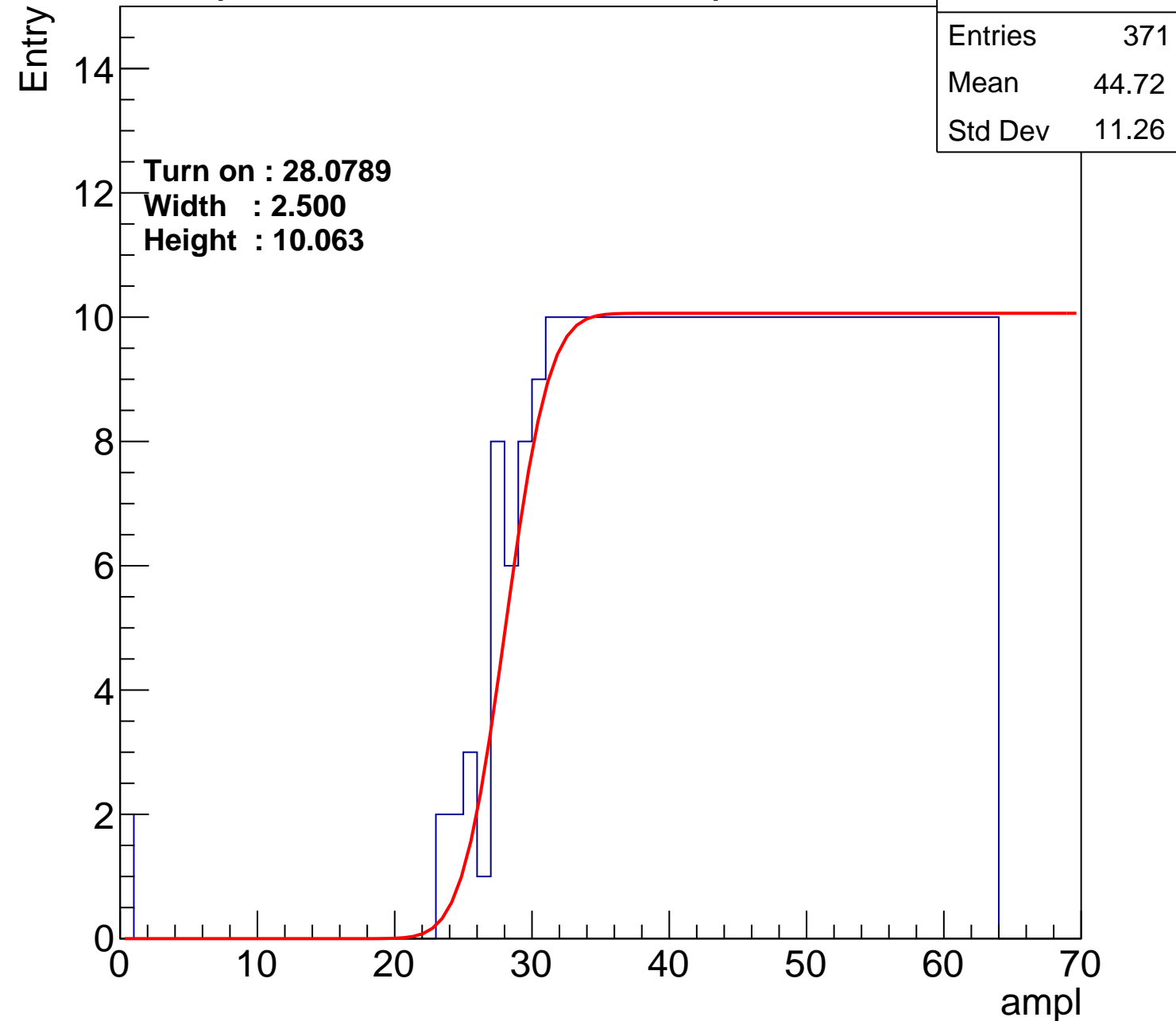
Width : 2.500

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.85
Std Dev	11.26

Turn on : 27.5836

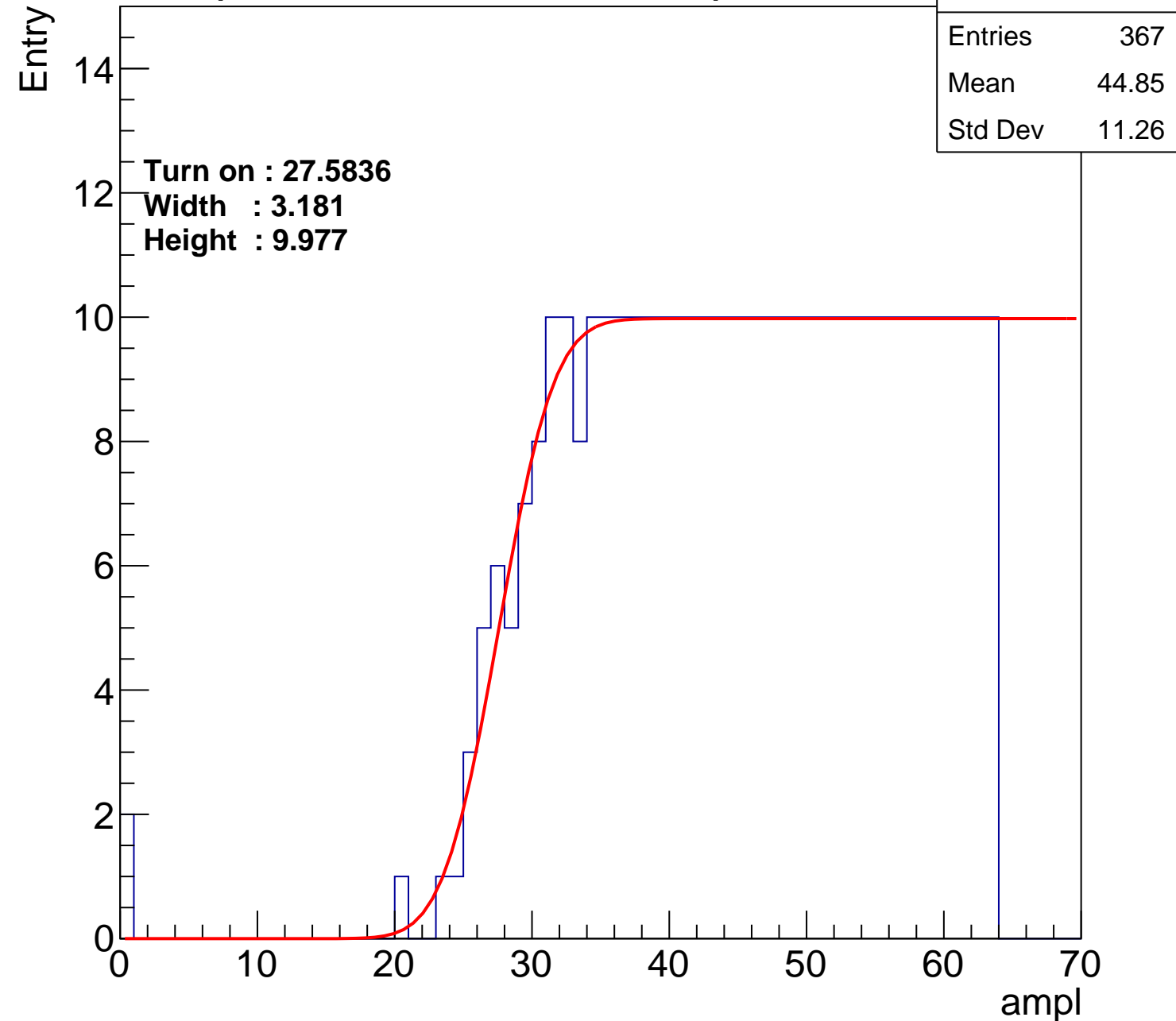
Width : 3.181

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch38

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.91
Std Dev	11.17

Turn on : 27.7511

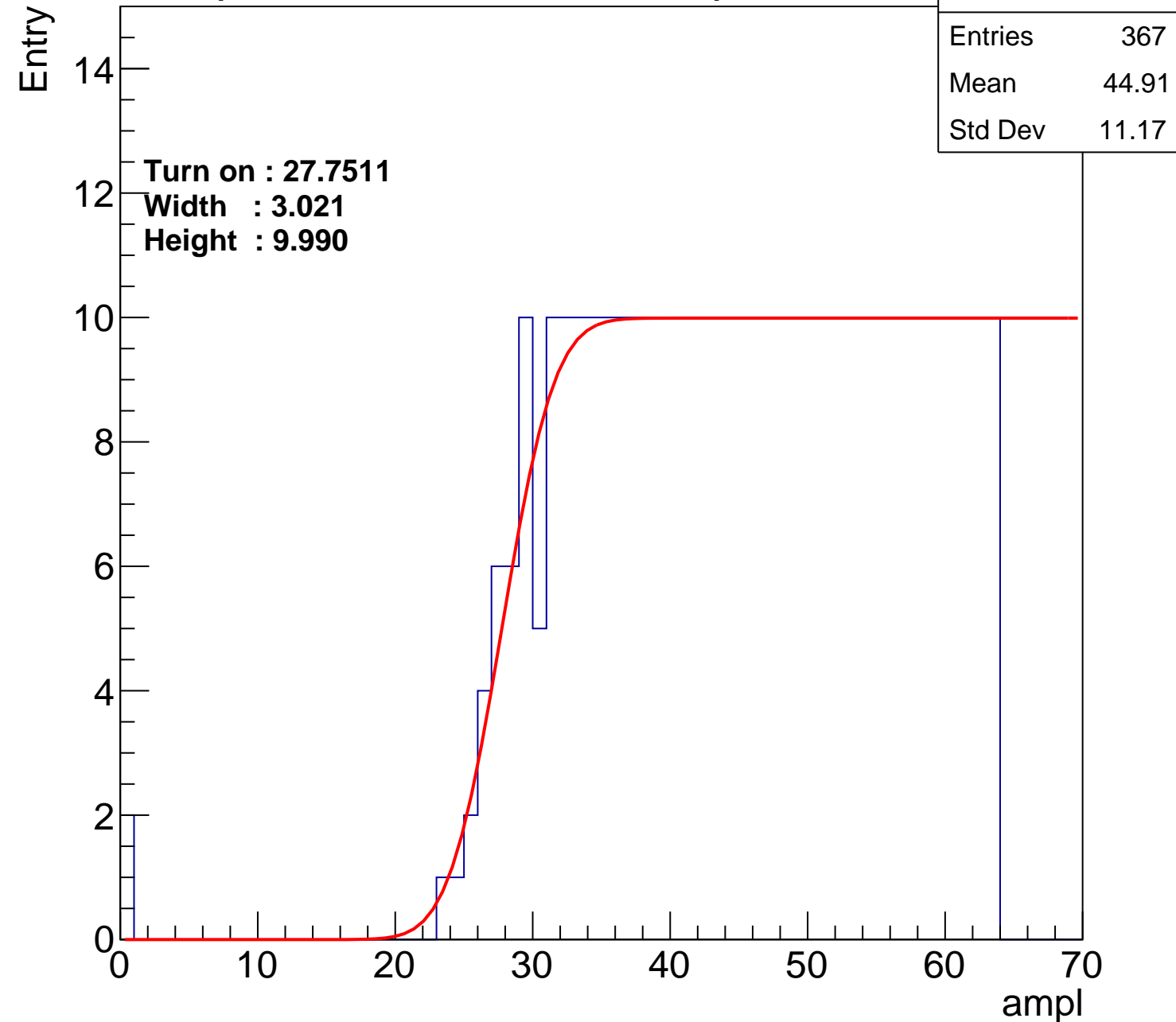
Width : 3.021

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch39

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.7
Std Dev	12.02

Turn on : 25.3933

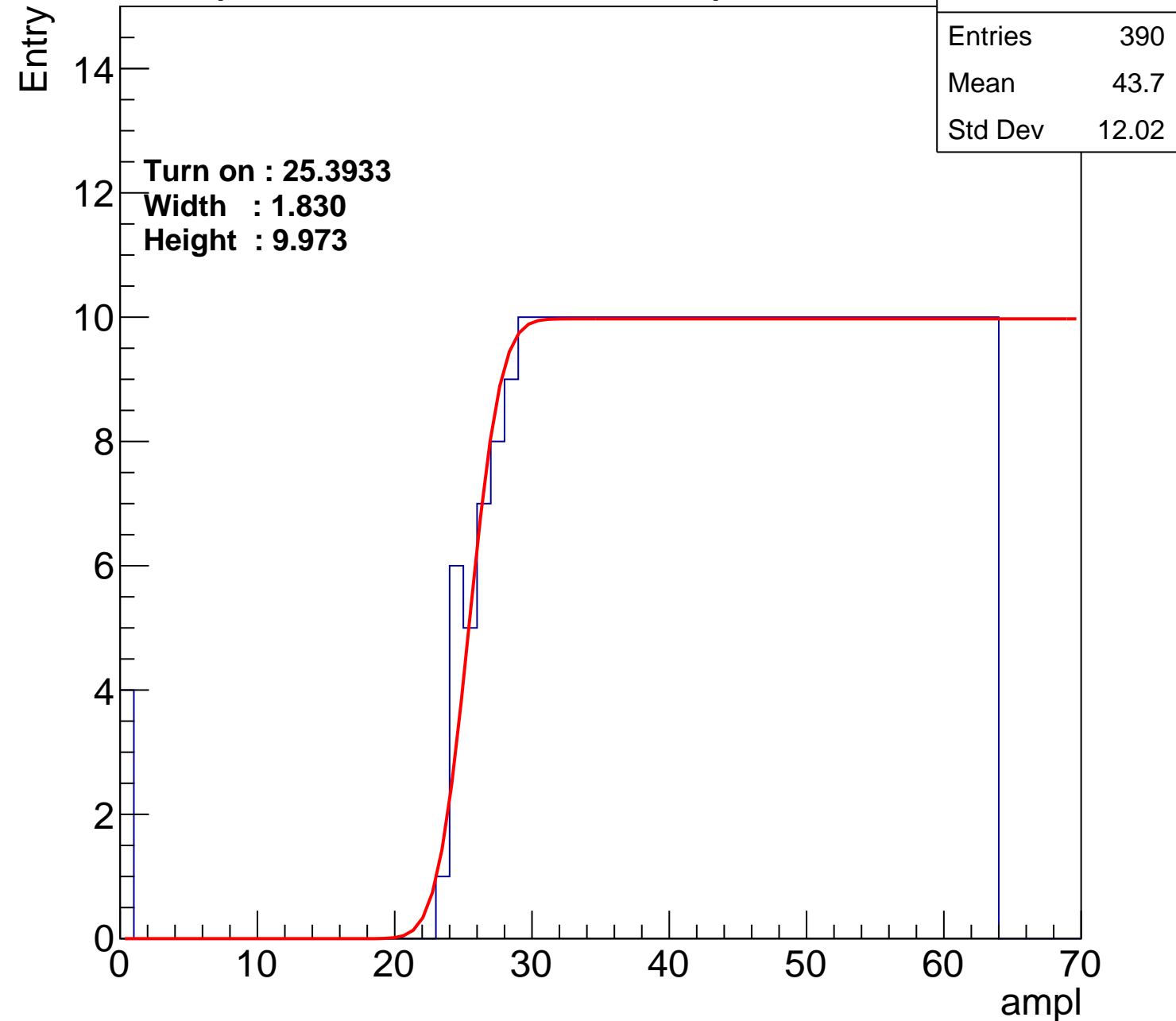
Width : 1.830

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.58
Std Dev	11.53

Turn on : 27.7690

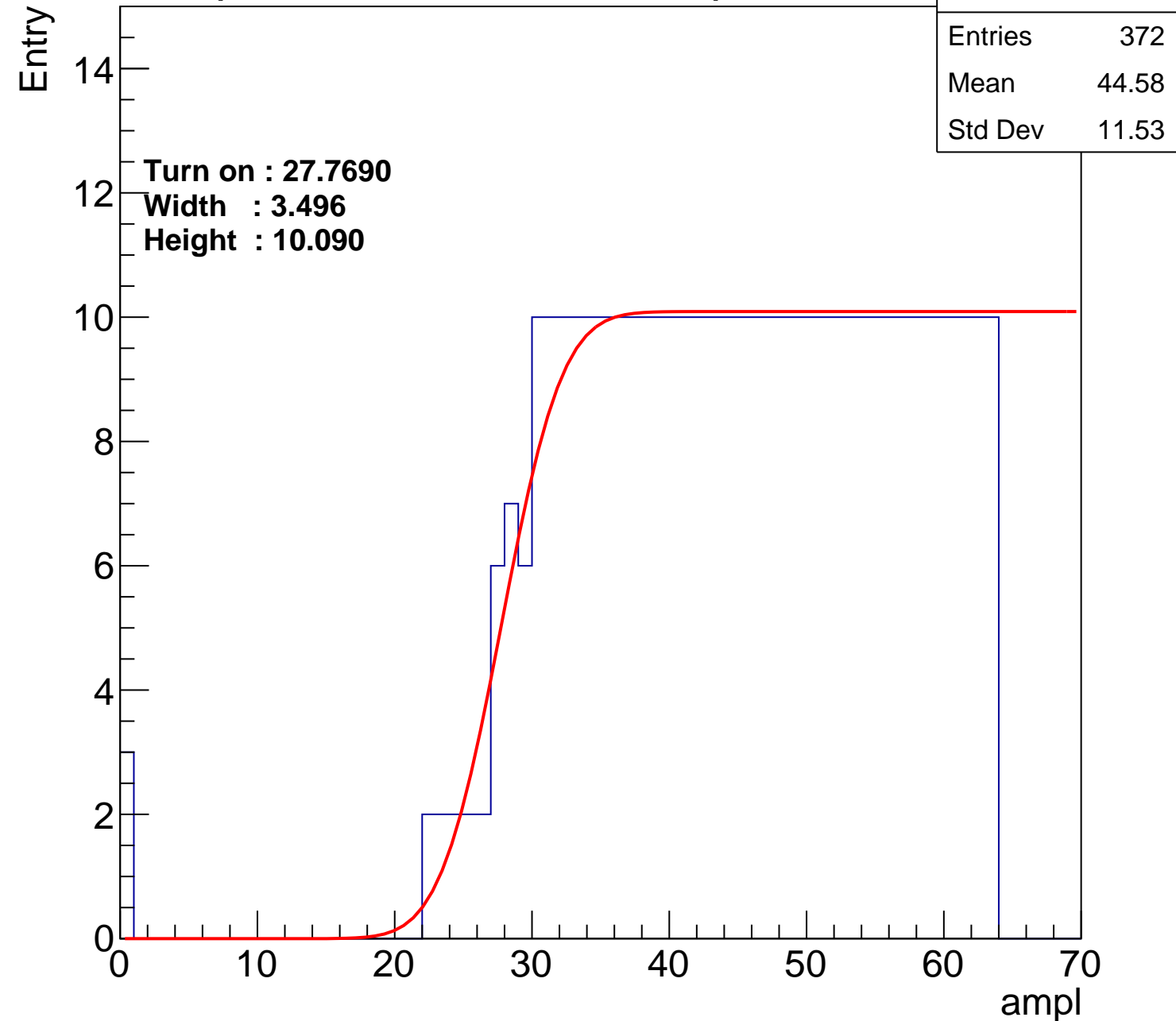
Width : 3.496

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.21
Std Dev	10.8

Turn on : 28.1385

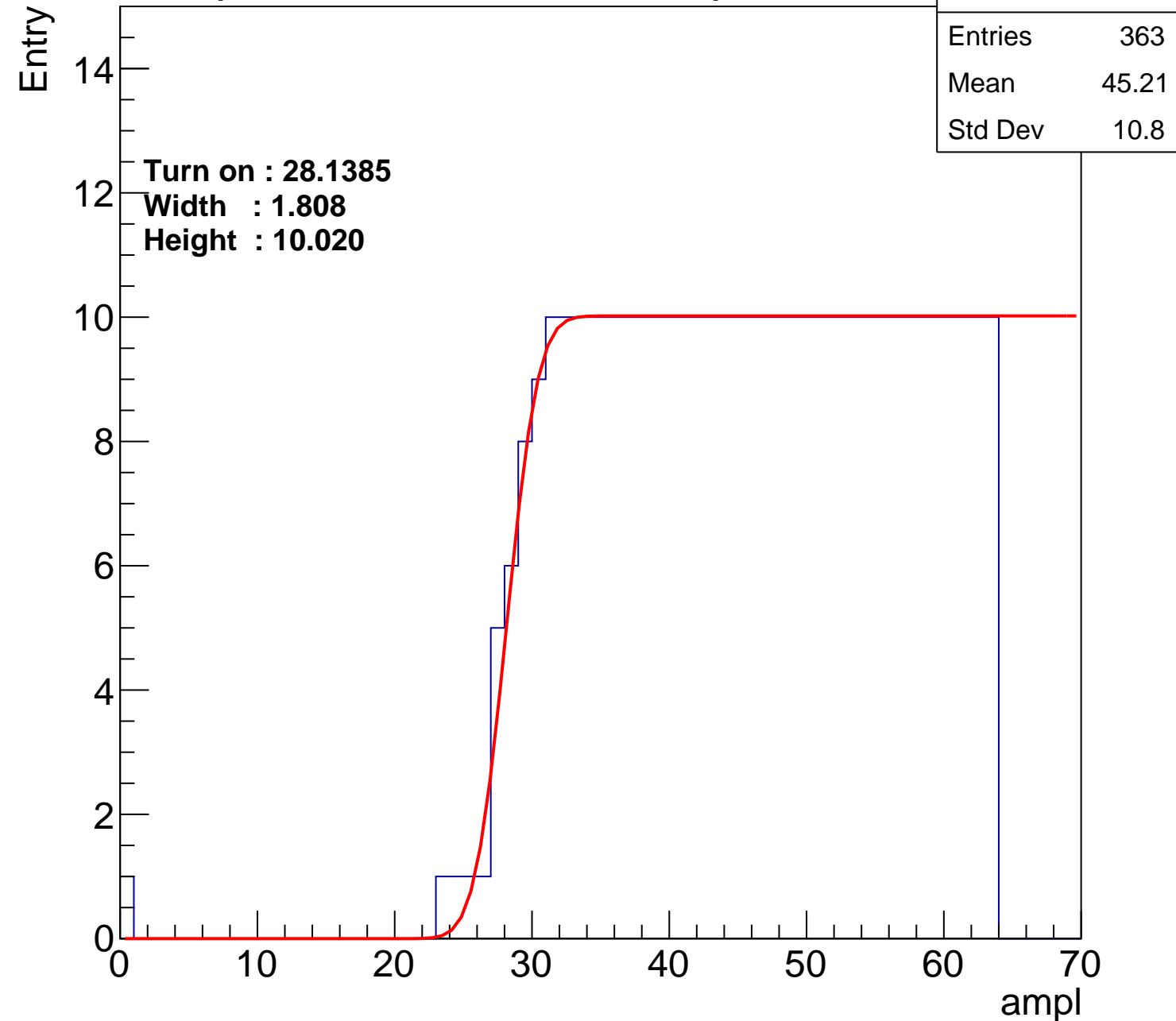
Width : 1.808

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.5390

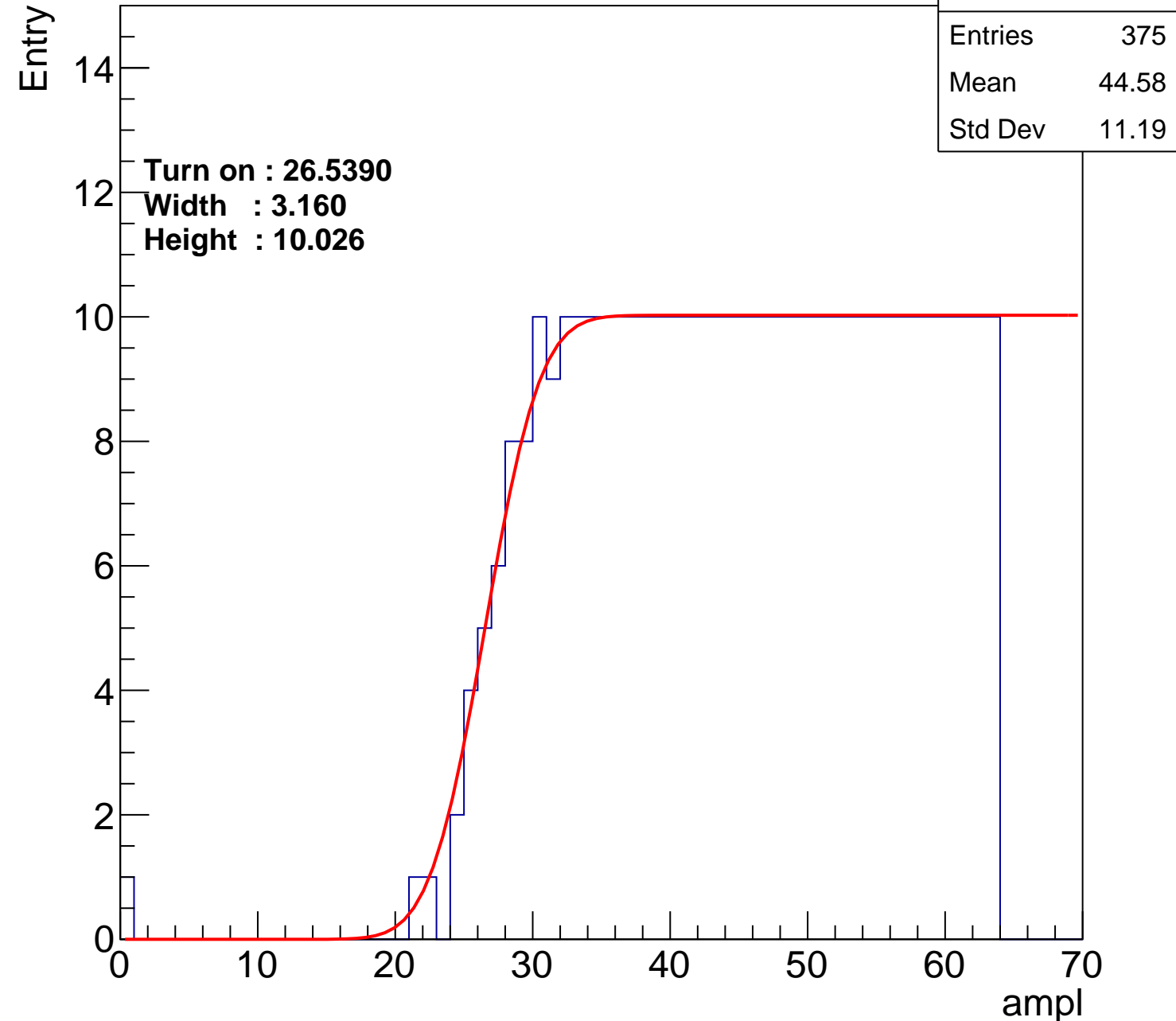
Width : 3.160

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.87
Std Dev	11.57

Turn on : 28.1641

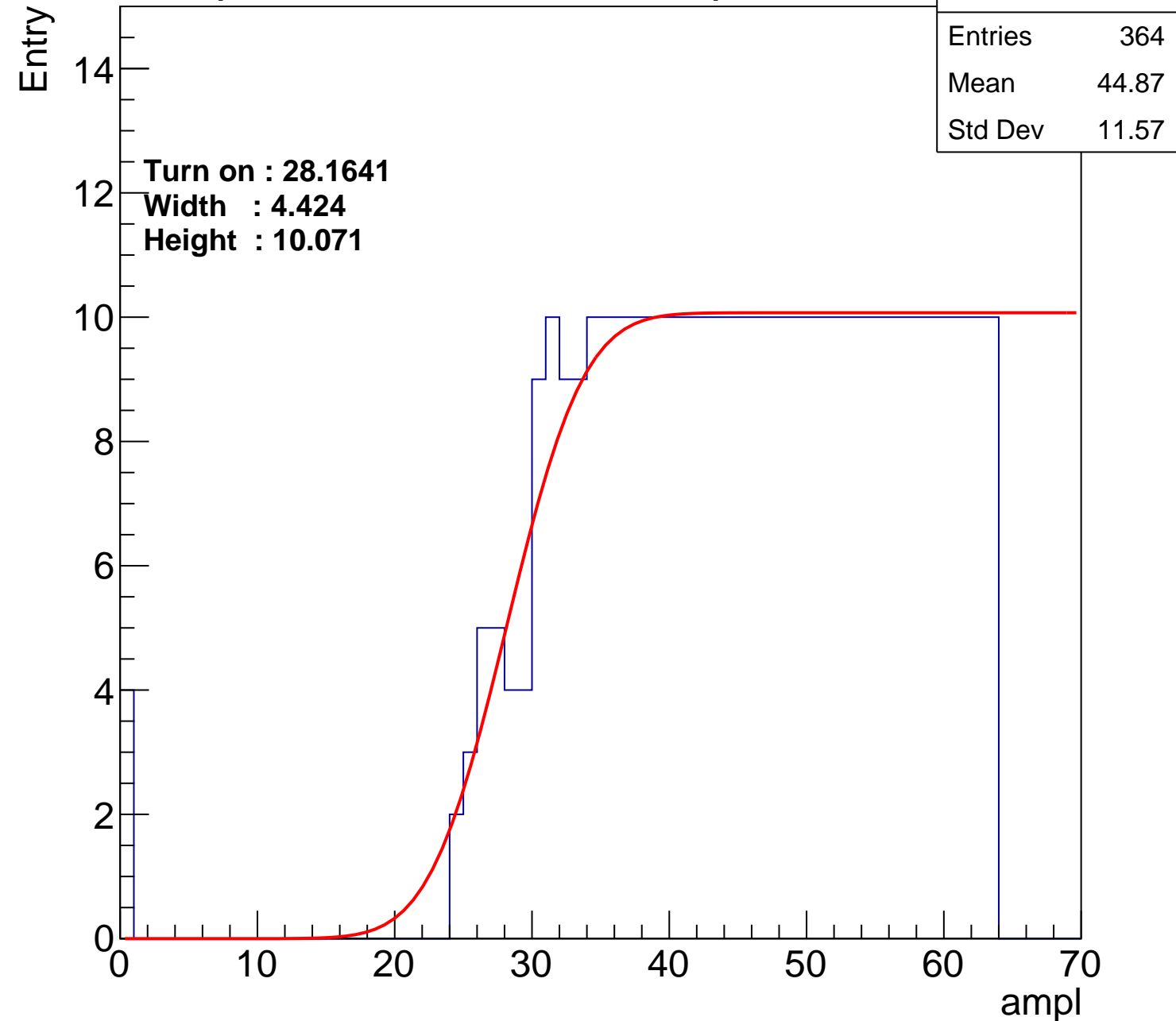
Width : 4.424

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.67
Std Dev	11.82

Turn on : 25.0420

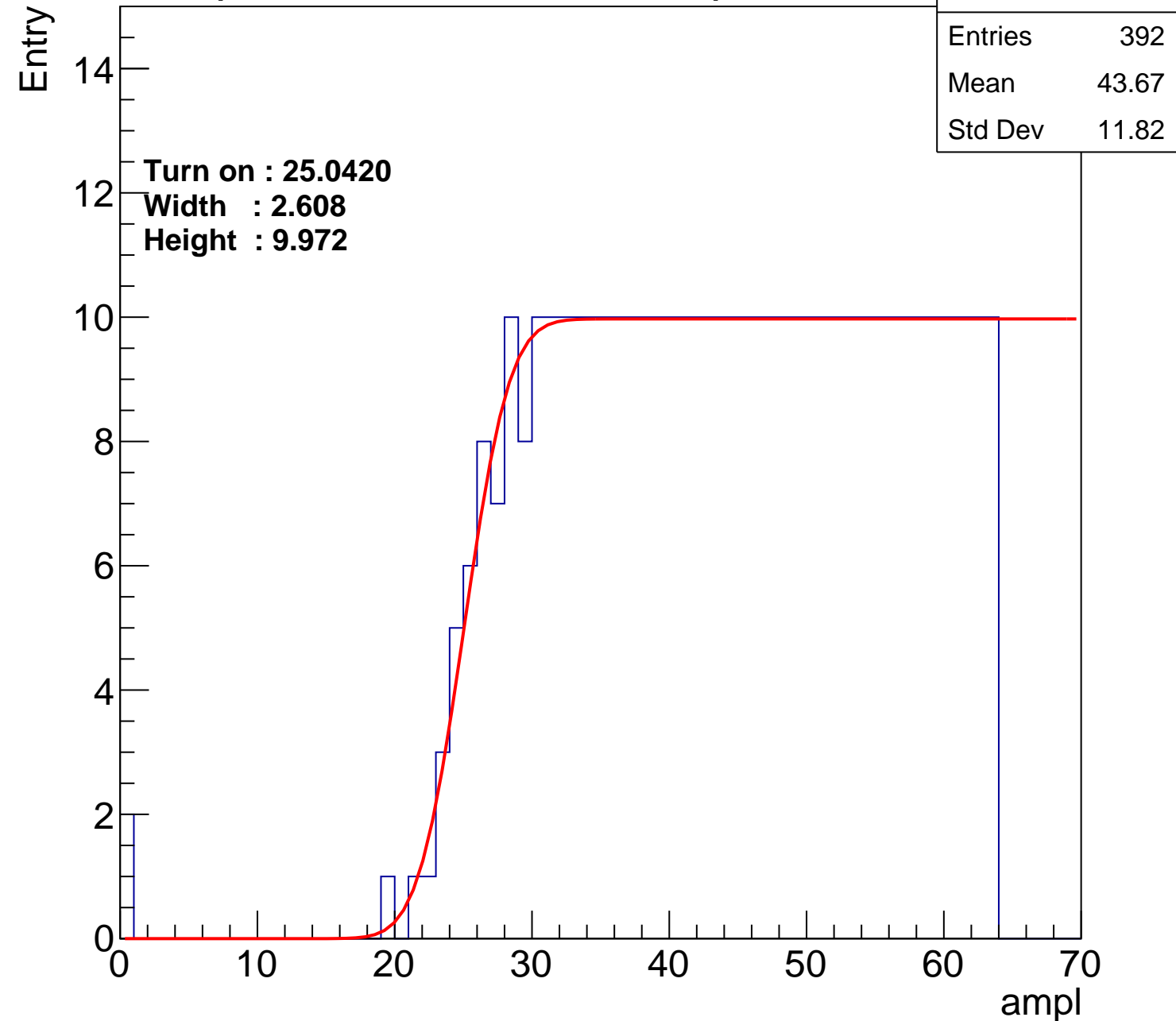
Width : 2.608

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch45

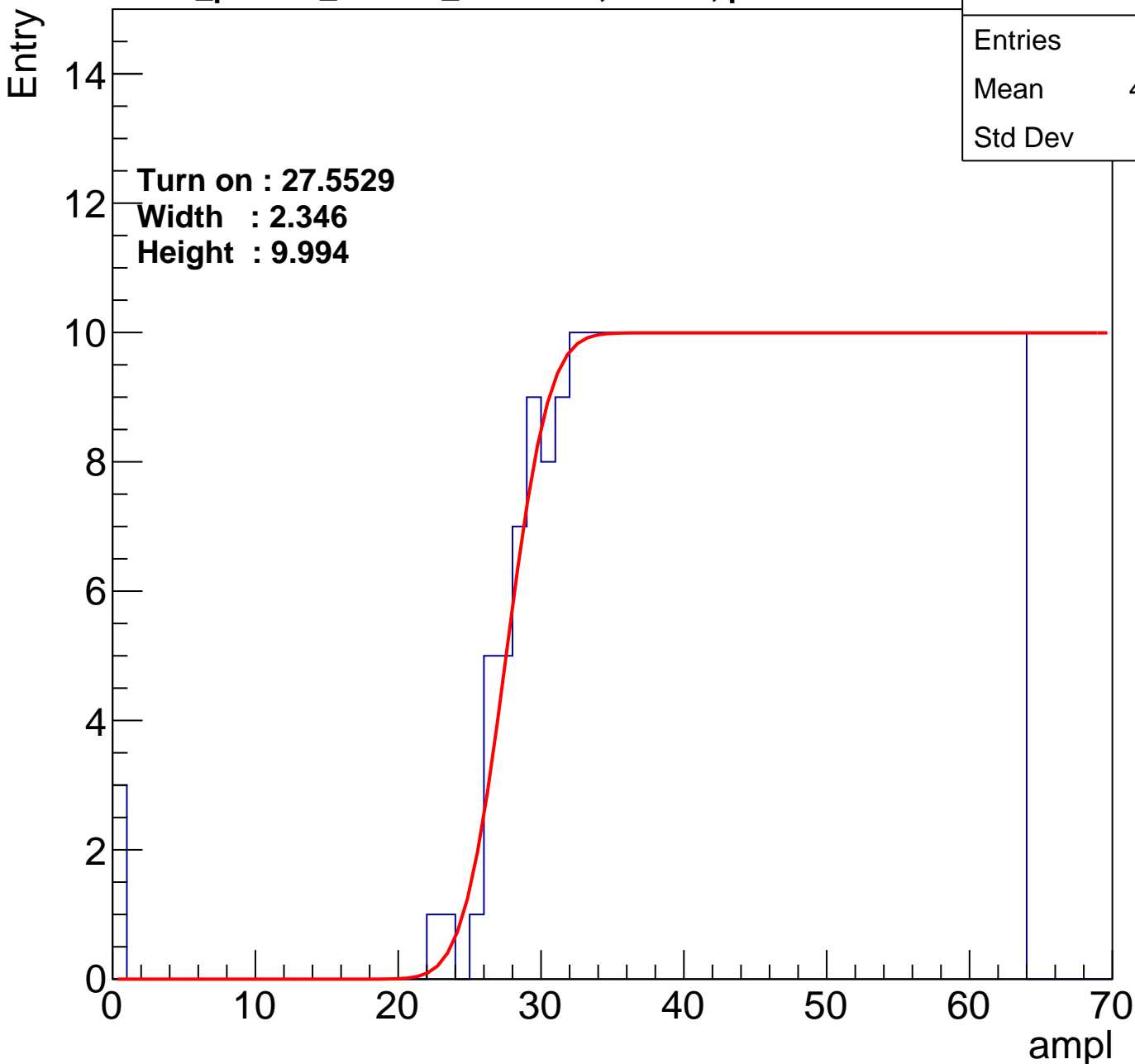
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.5529

Width : 2.346

Height : 9.994

Entries	369
Mean	44.75
Std Dev	11.41



B1L003S, U6-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.96
Std Dev	11.48

Turn on : 27.8580

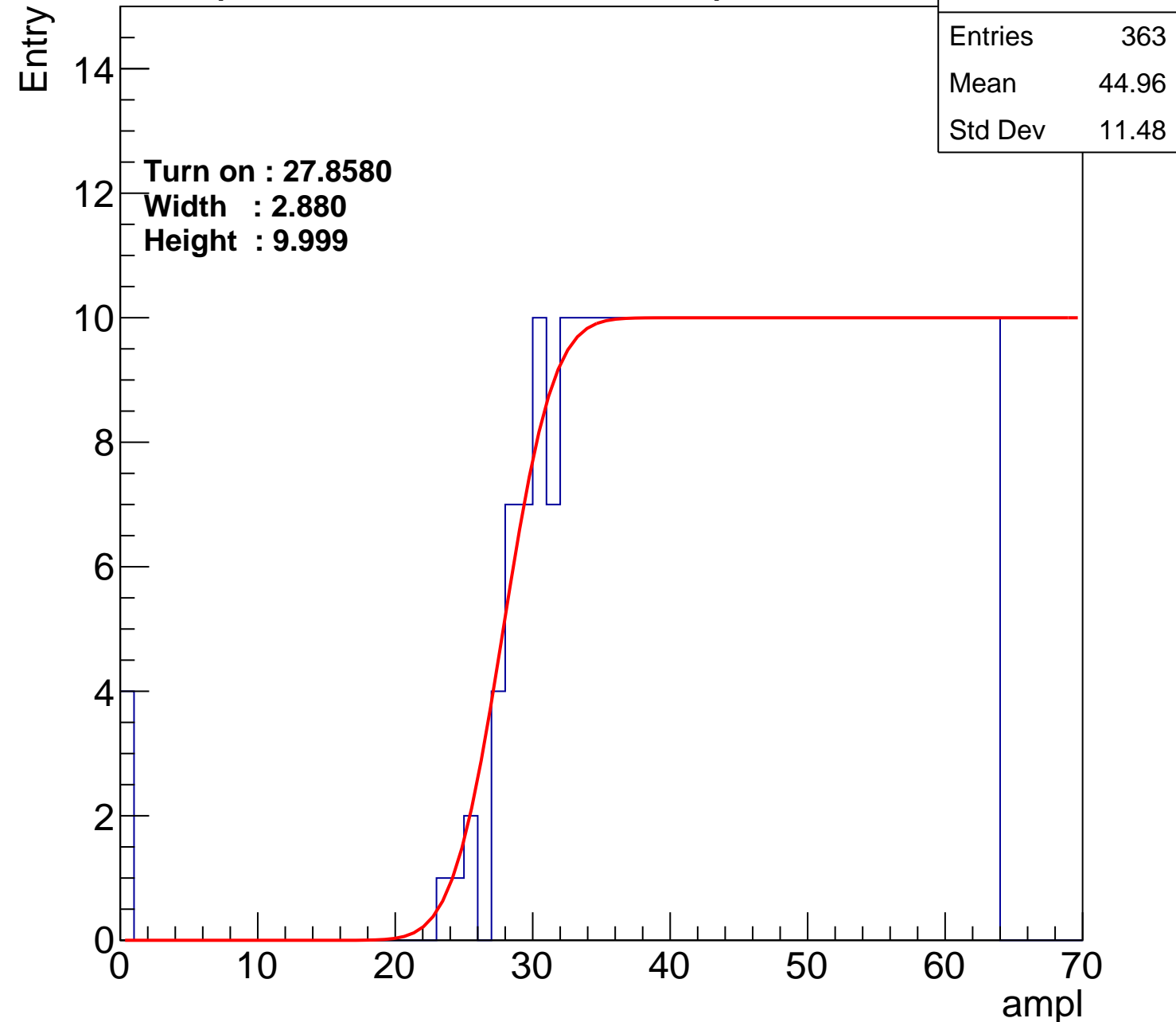
Width : 2.880

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.33
Std Dev	11.54

Turn on : 29.6076

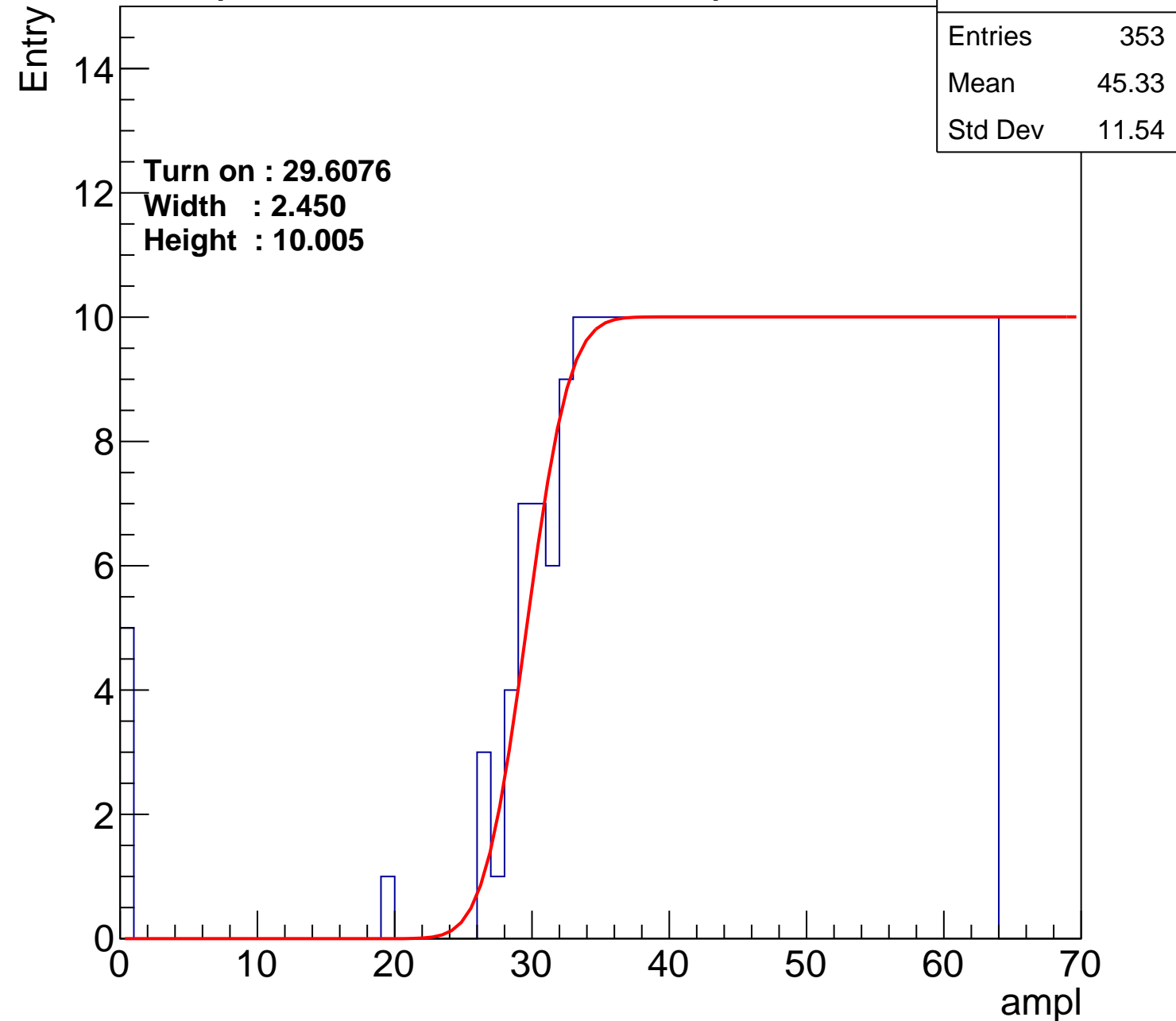
Width : 2.450

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch48

calib_packv5_042523_0143.root, FC#13, port D2

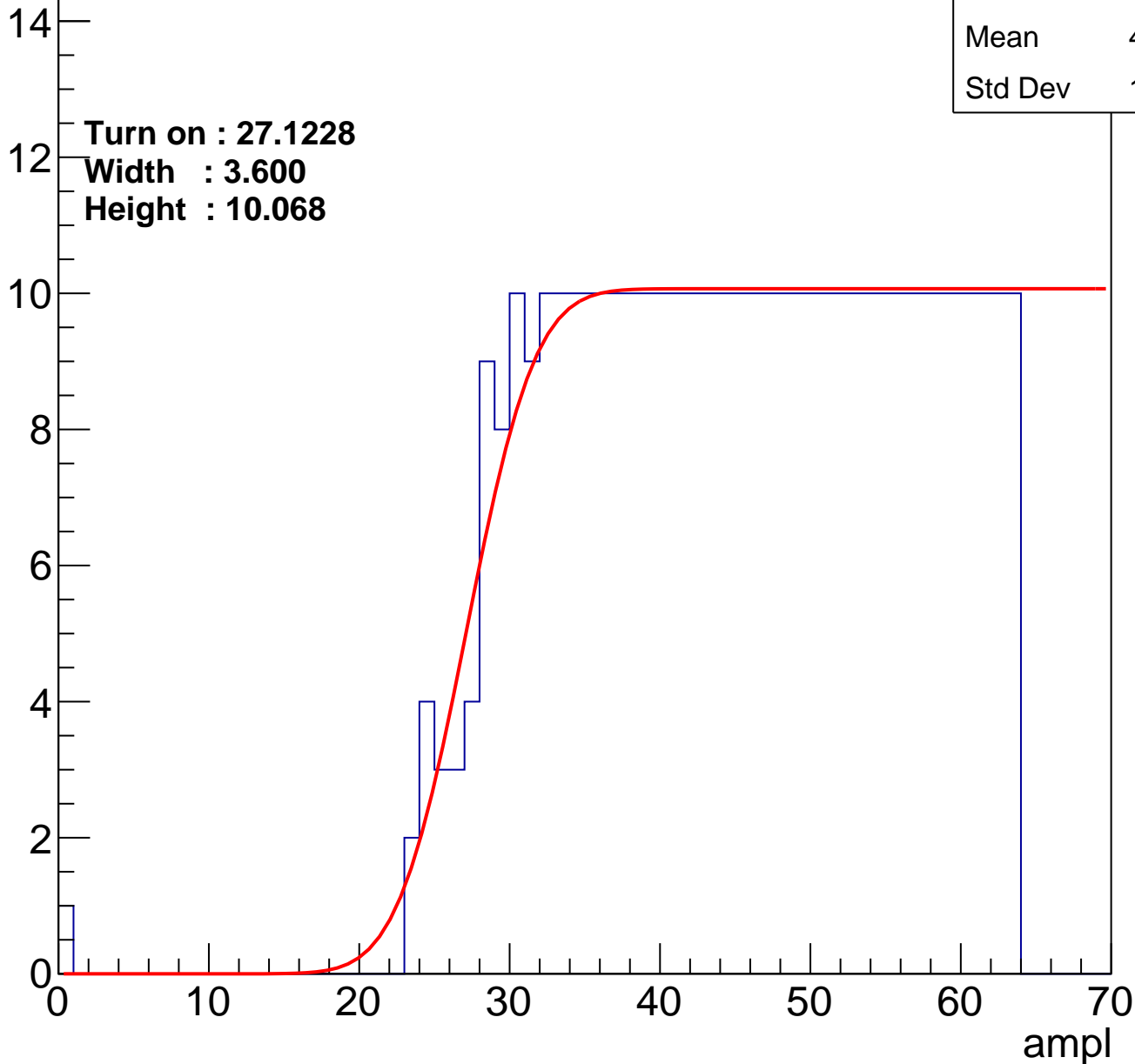
Entry

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 27.1228

Width : 3.600

Height : 10.068



B1L003S, U6-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.67
Std Dev	11.76

Turn on : 27.6208

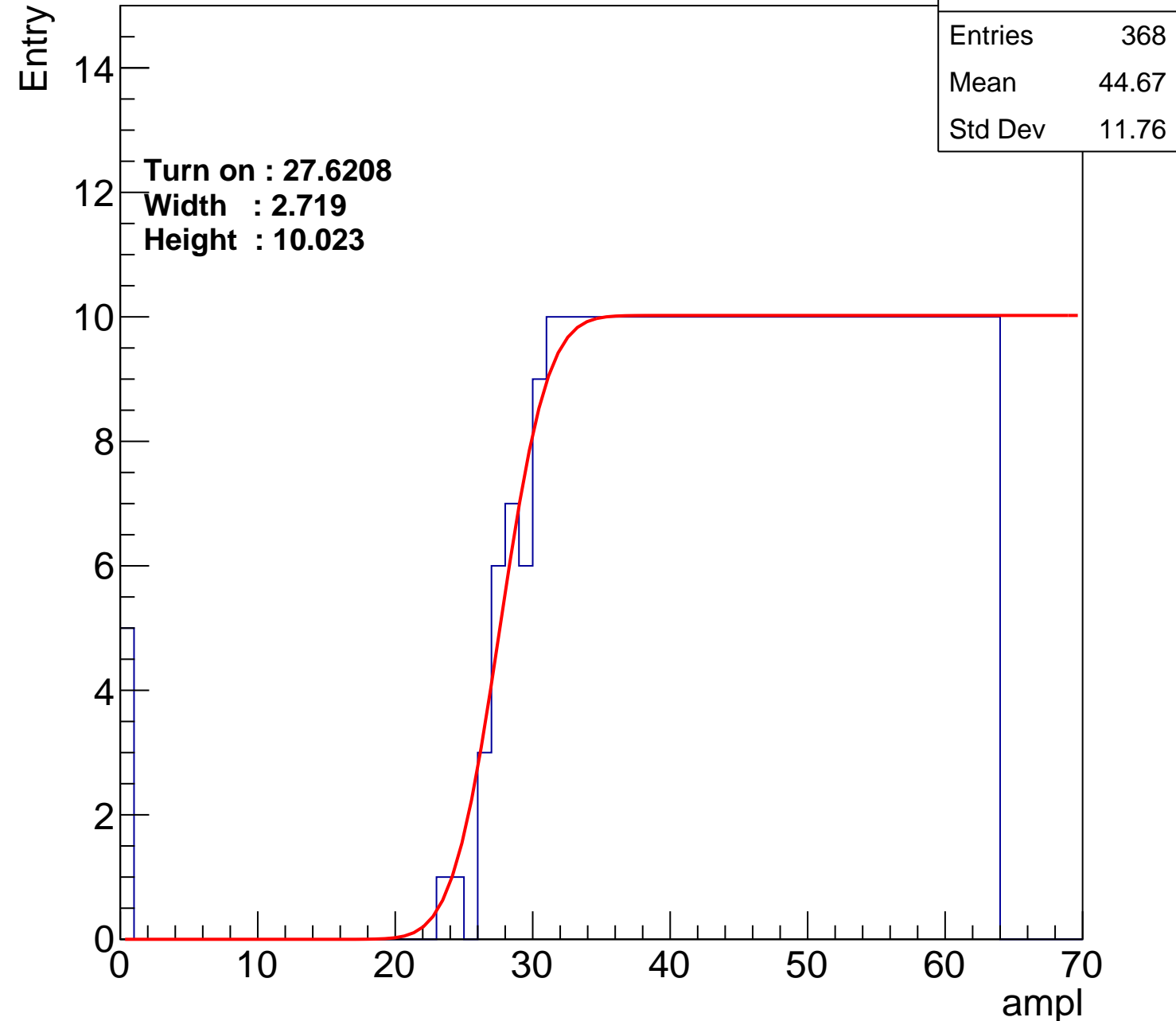
Width : 2.719

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.78
Std Dev	11.14

Turn on : 27.5659

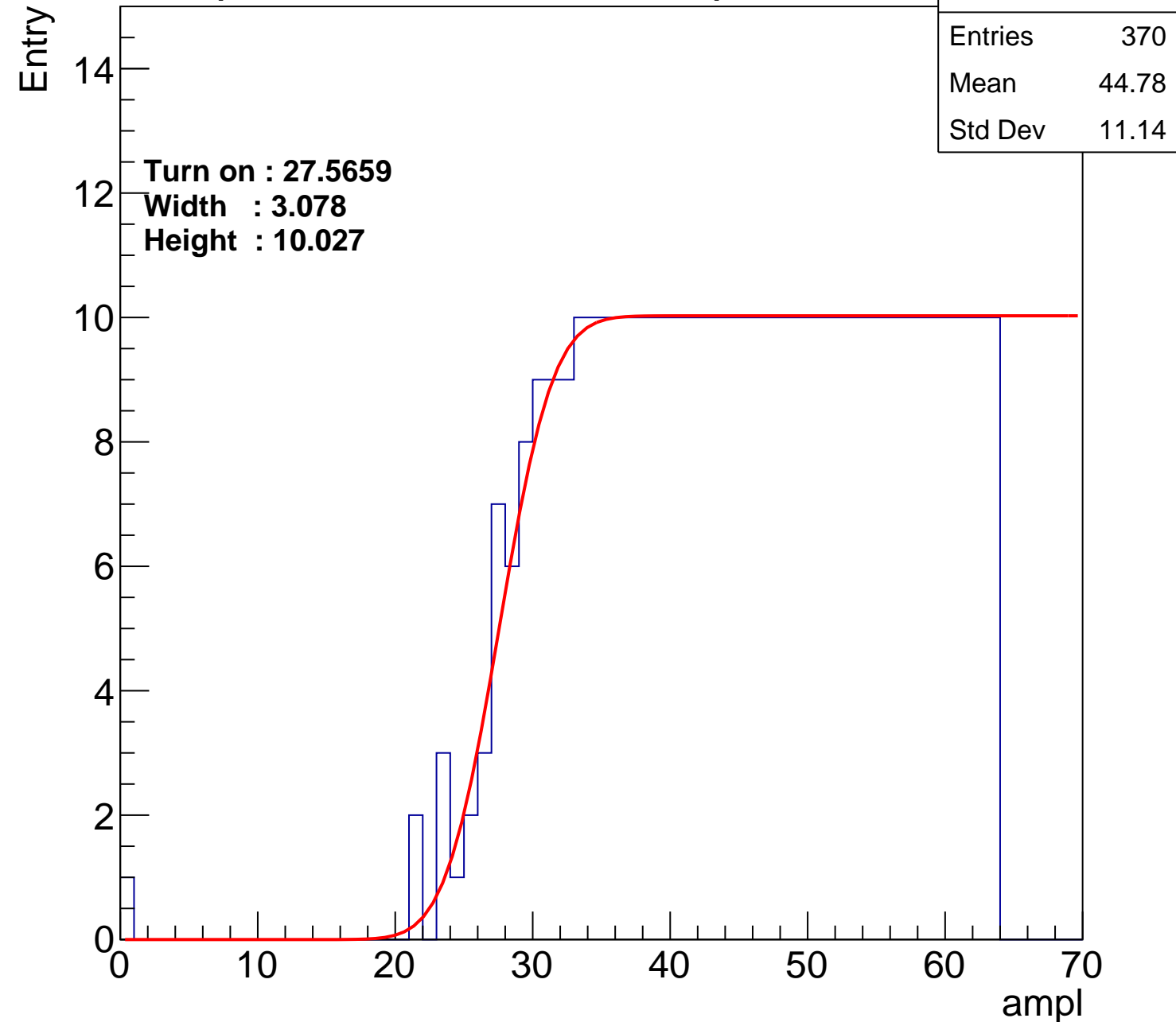
Width : 3.078

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 28.3240

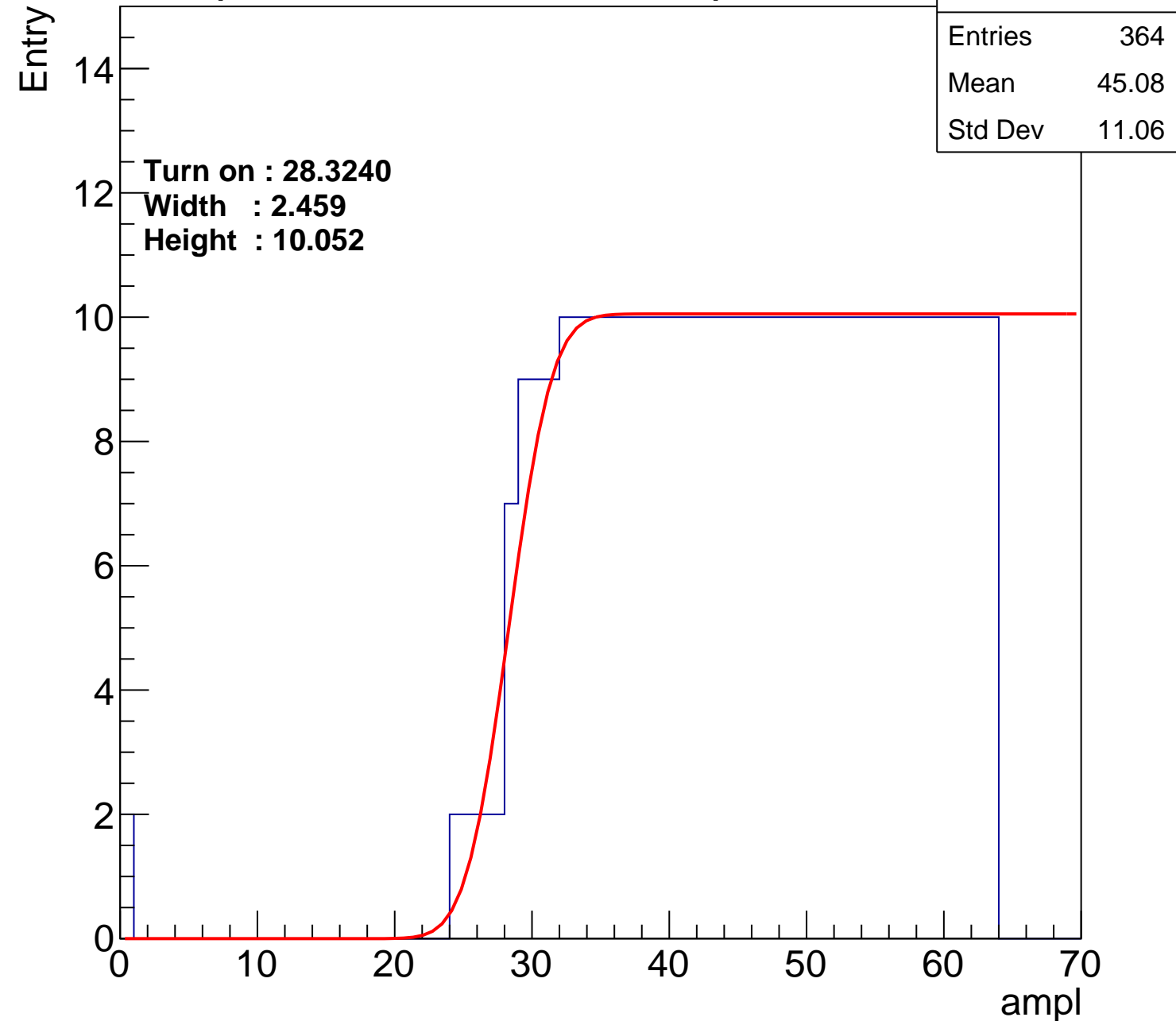
Width : 2.459

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.13
Std Dev	11.45

Turn on : 25.9585

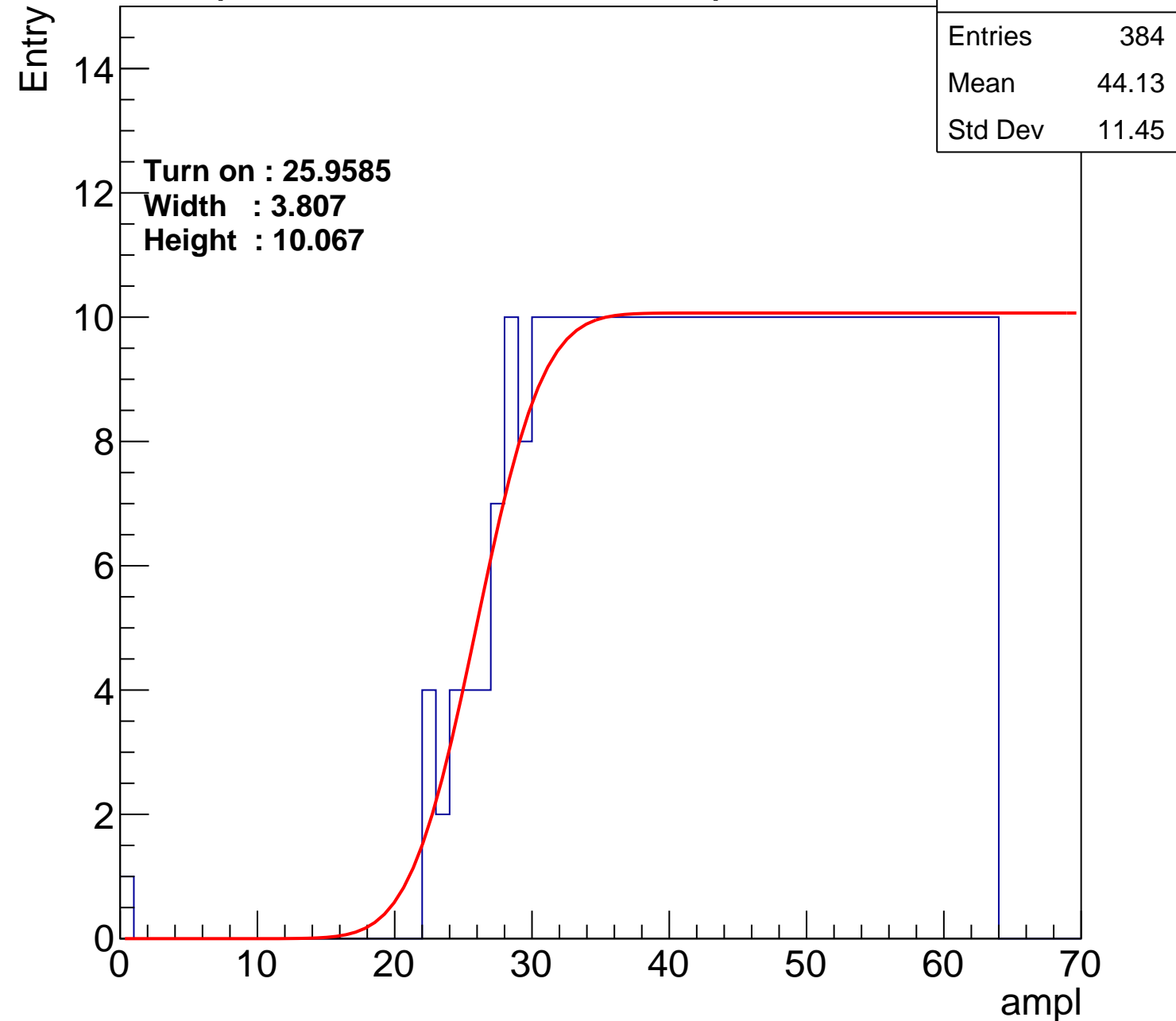
Width : 3.807

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.99
Std Dev	11.01

Turn on : 27.5251

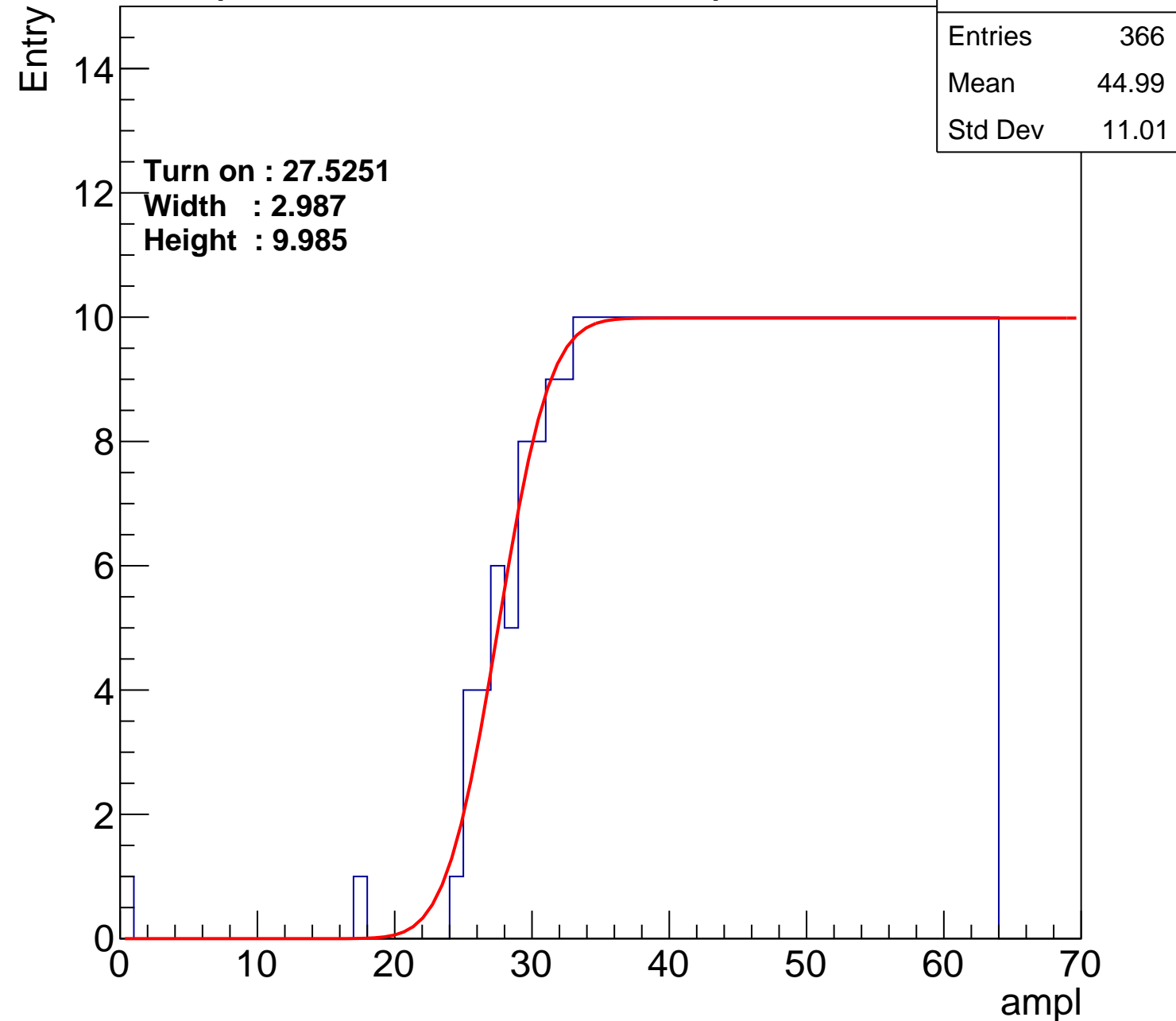
Width : 2.987

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.25
Std Dev	12.15

Turn on : 27.3070

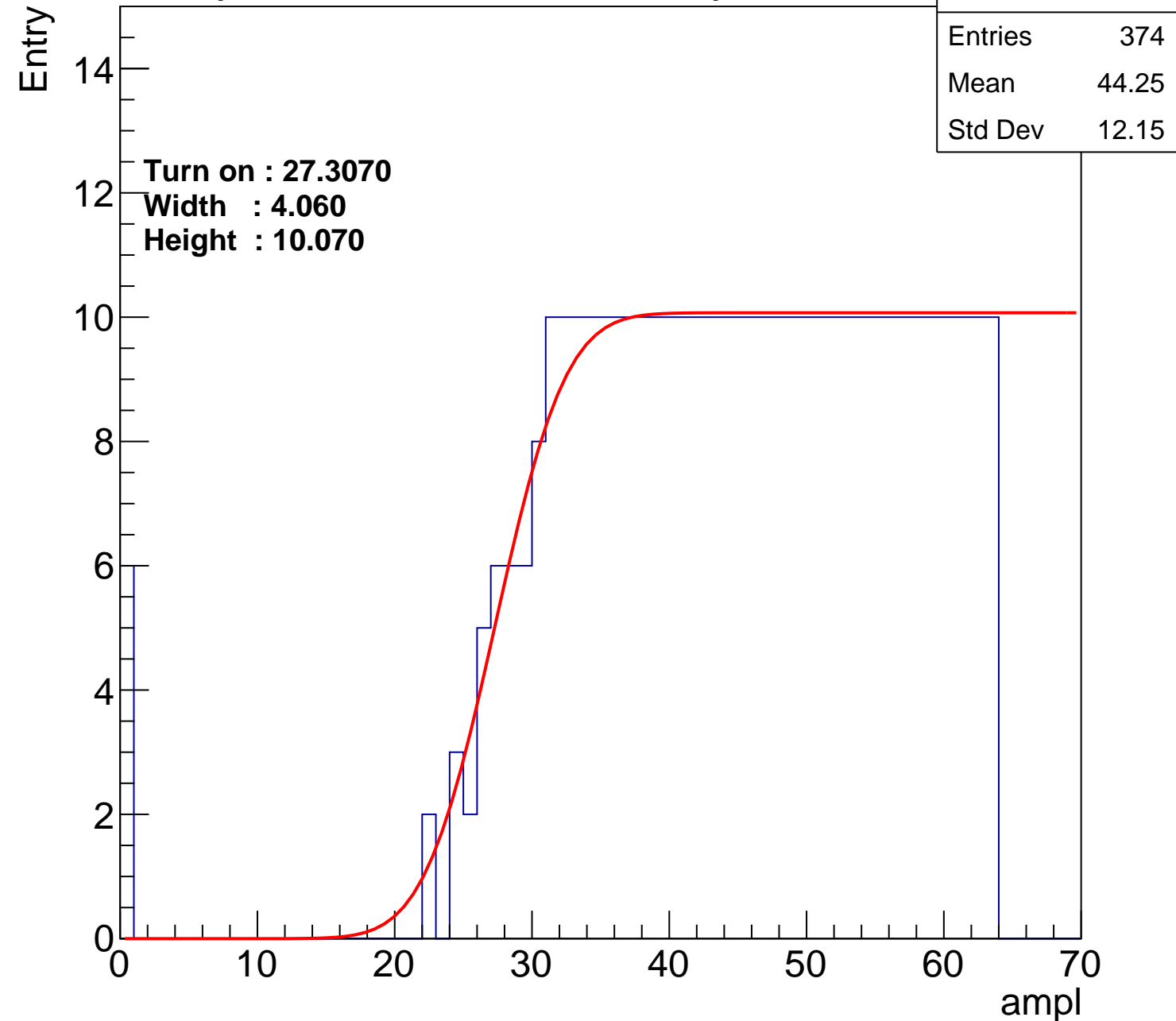
Width : 4.060

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 28.3928

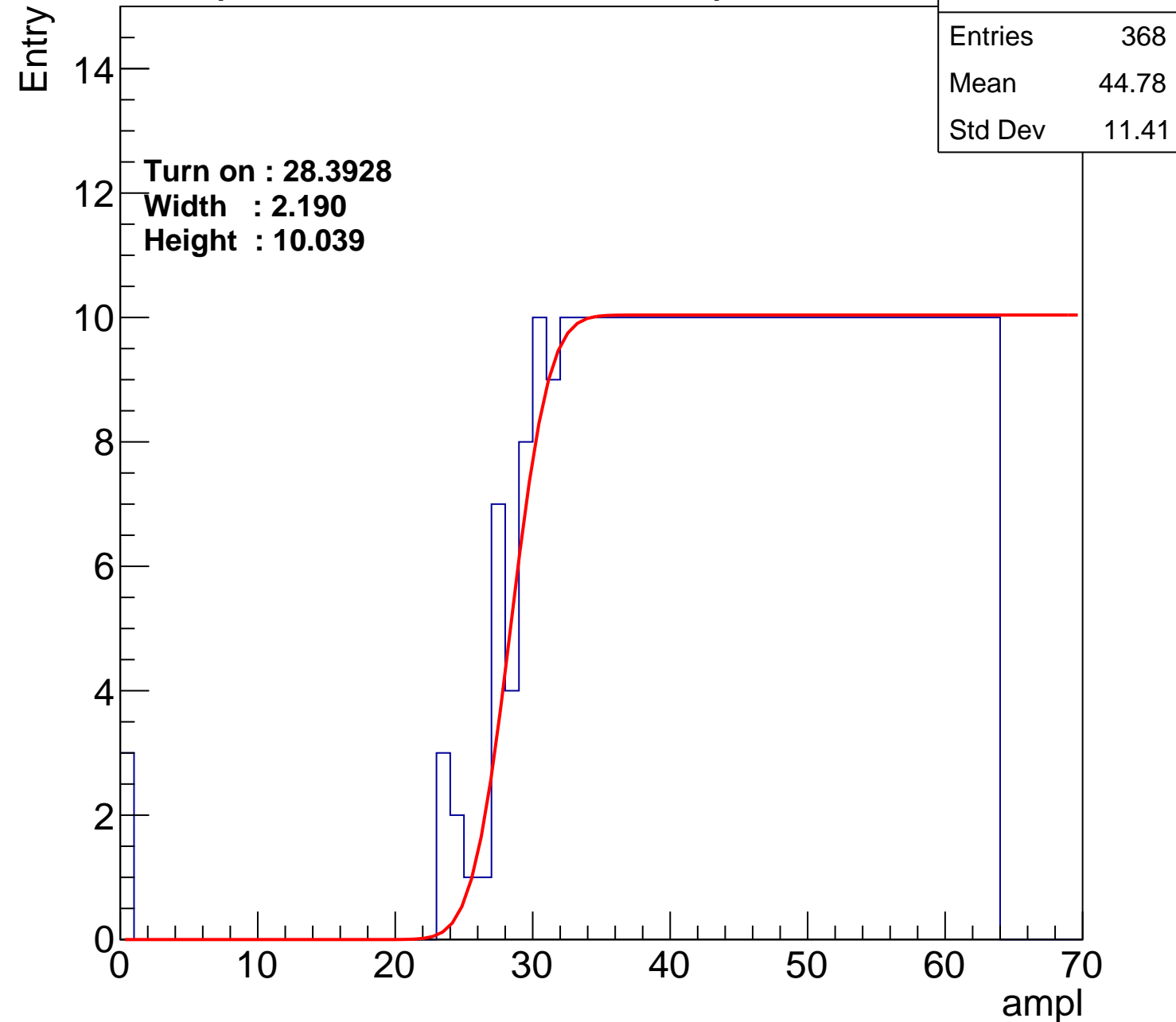
Width : 2.190

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.78
Std Dev	11.26

Turn on : 26.9345

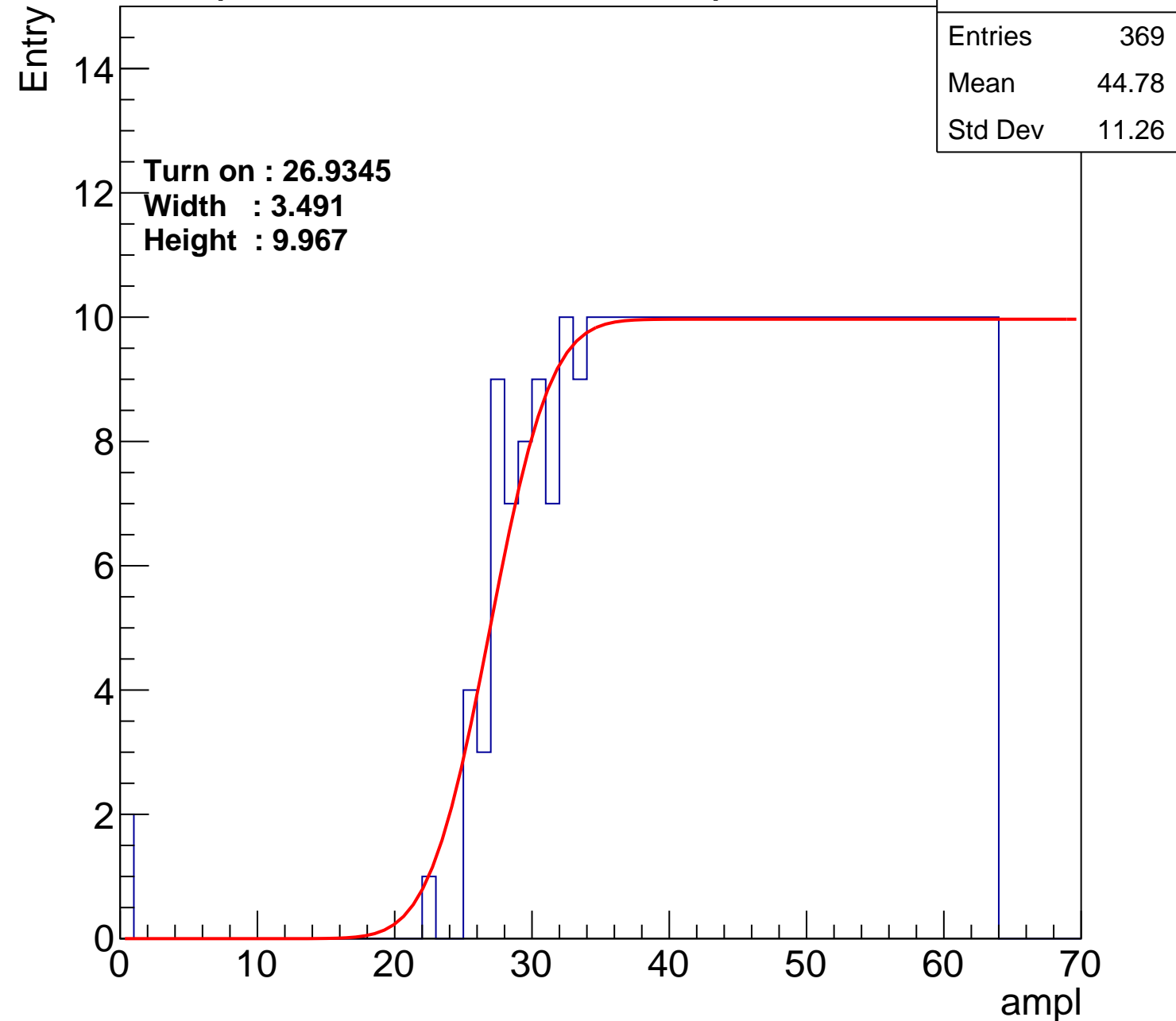
Width : 3.491

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.32
Std Dev	10.79

Turn on : 28.2078

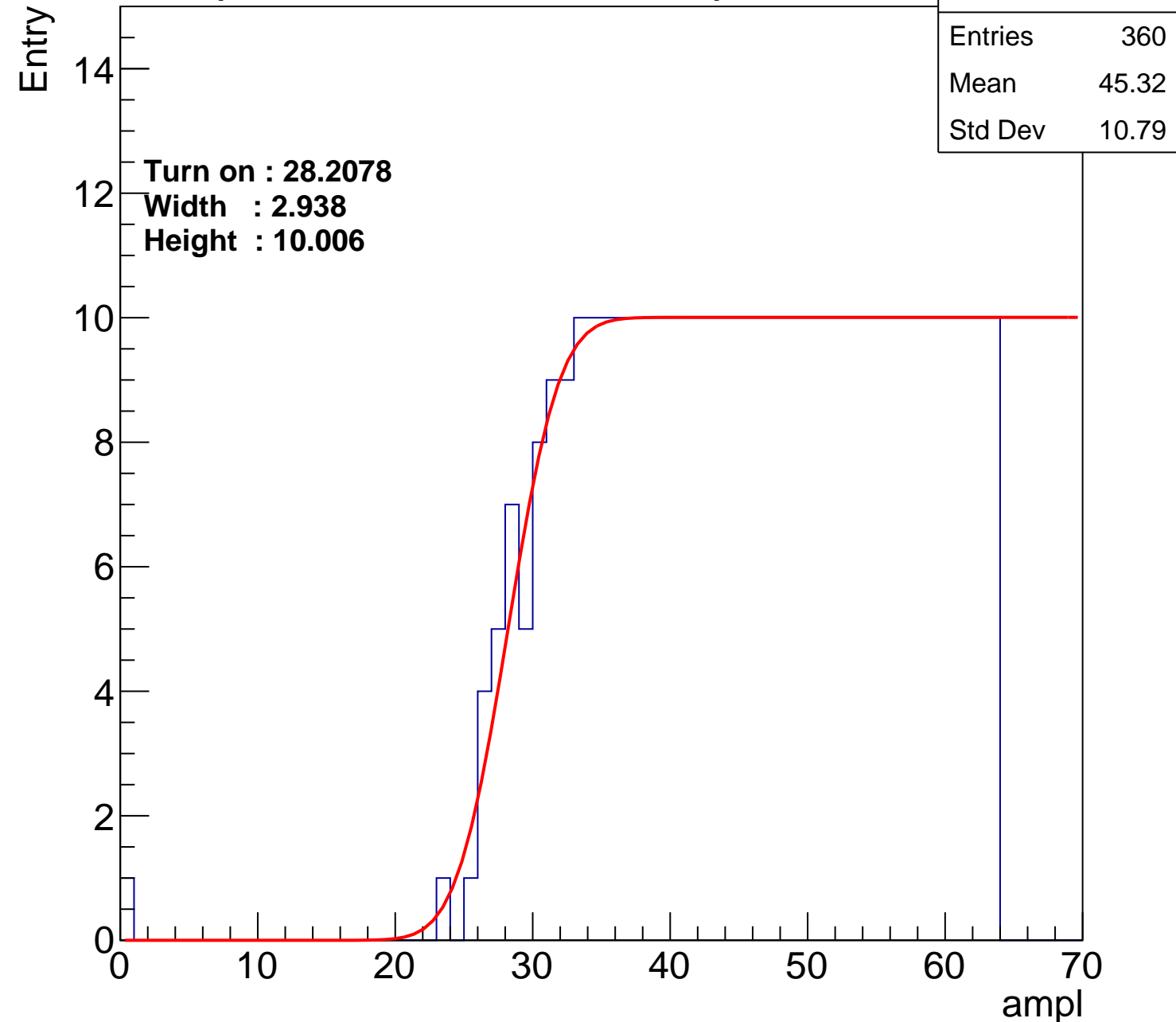
Width : 2.938

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 28.0308

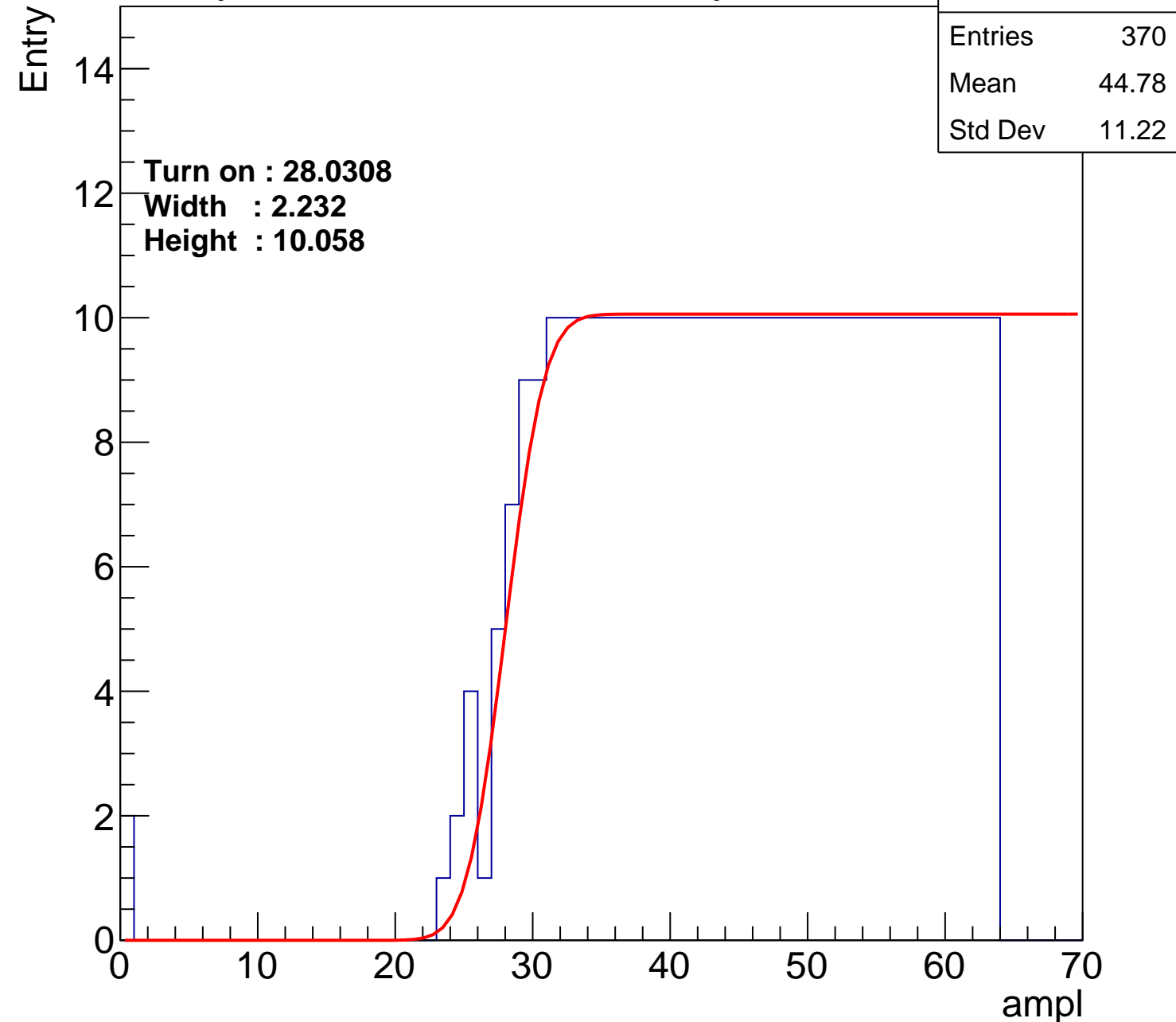
Width : 2.232

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.69
Std Dev	11.44

Turn on : 27.3216

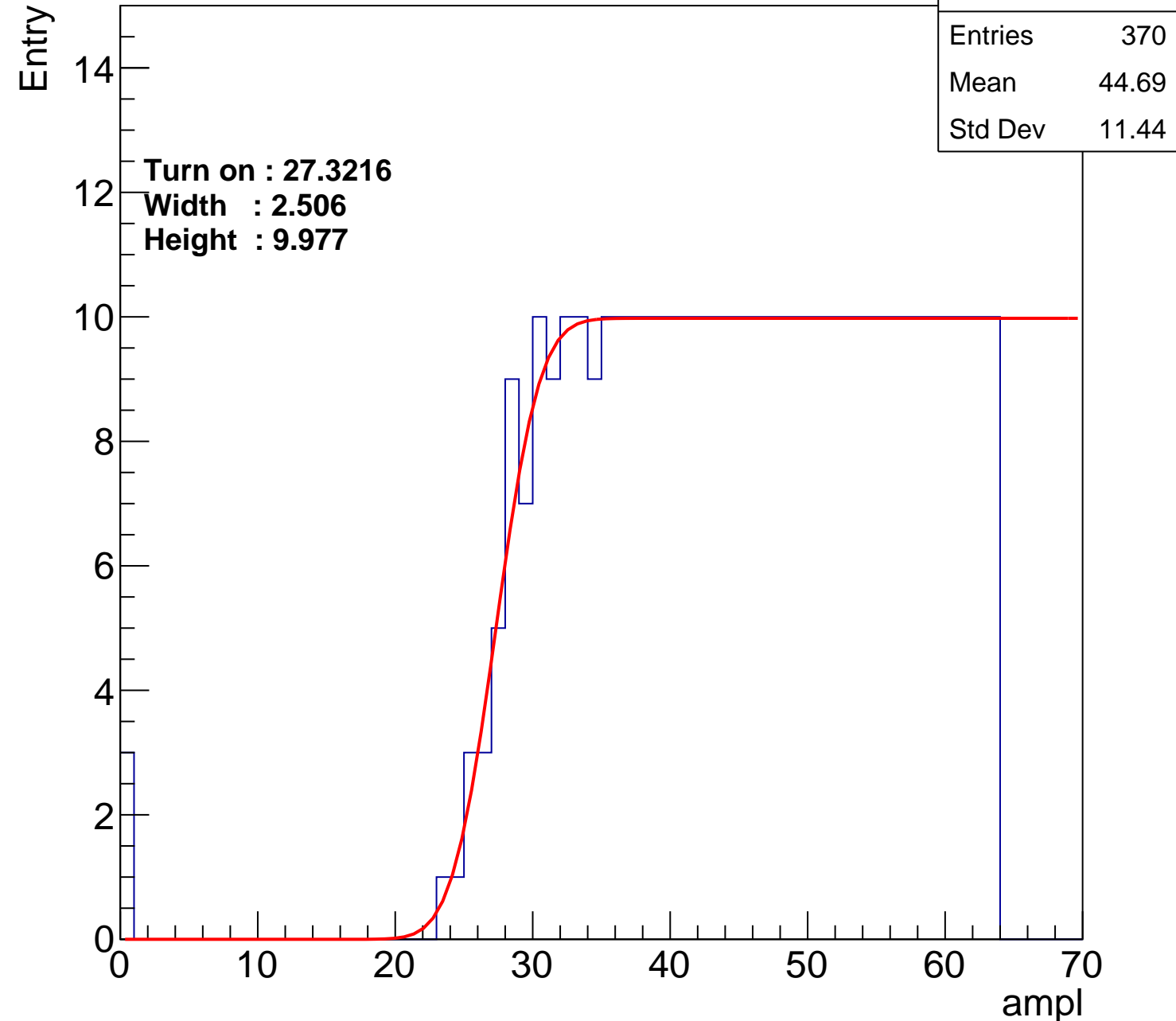
Width : 2.506

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.32
Std Dev	11.67

Turn on : 27.3401

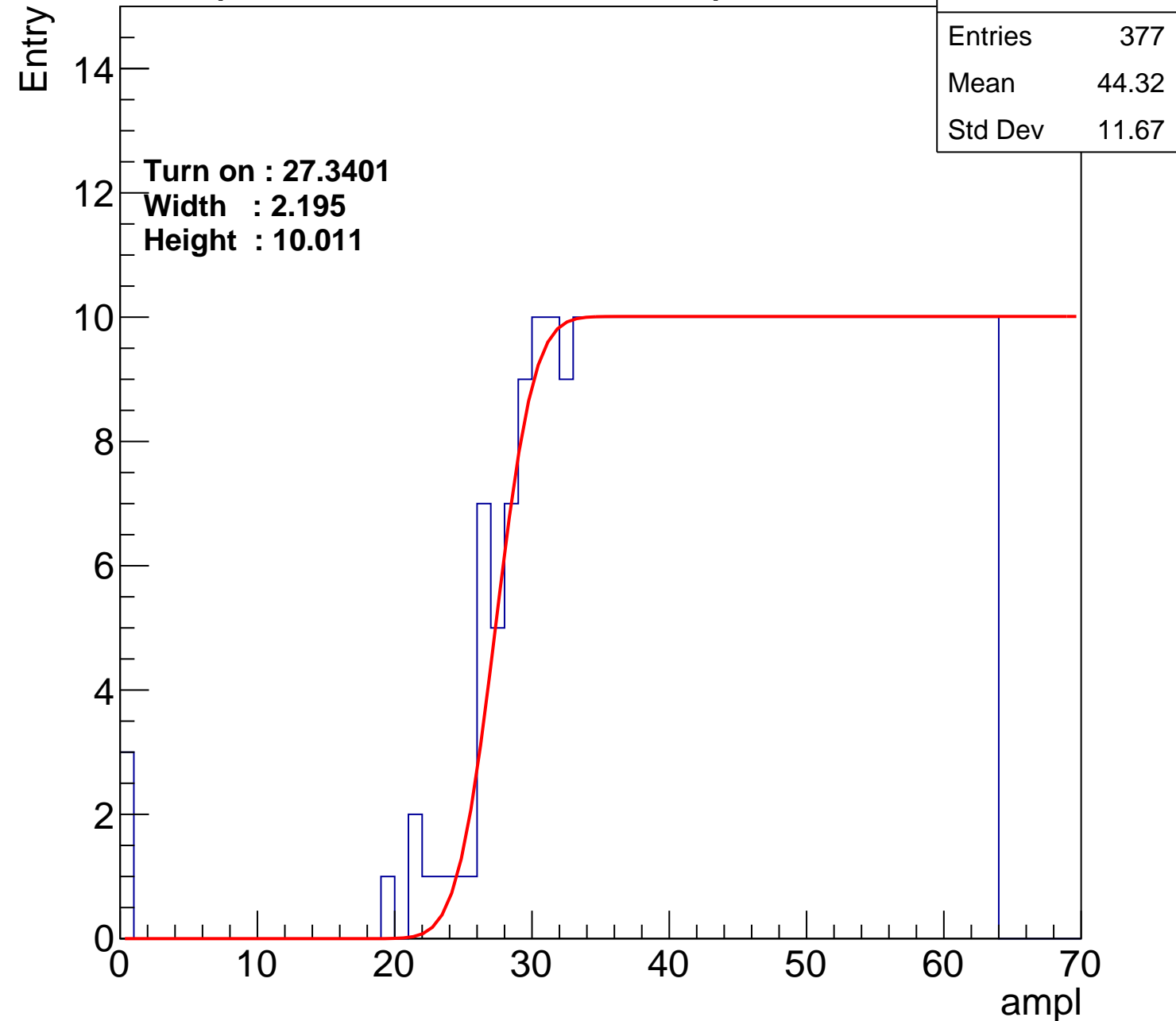
Width : 2.195

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.17
Std Dev	12.16

Turn on : 27.3556

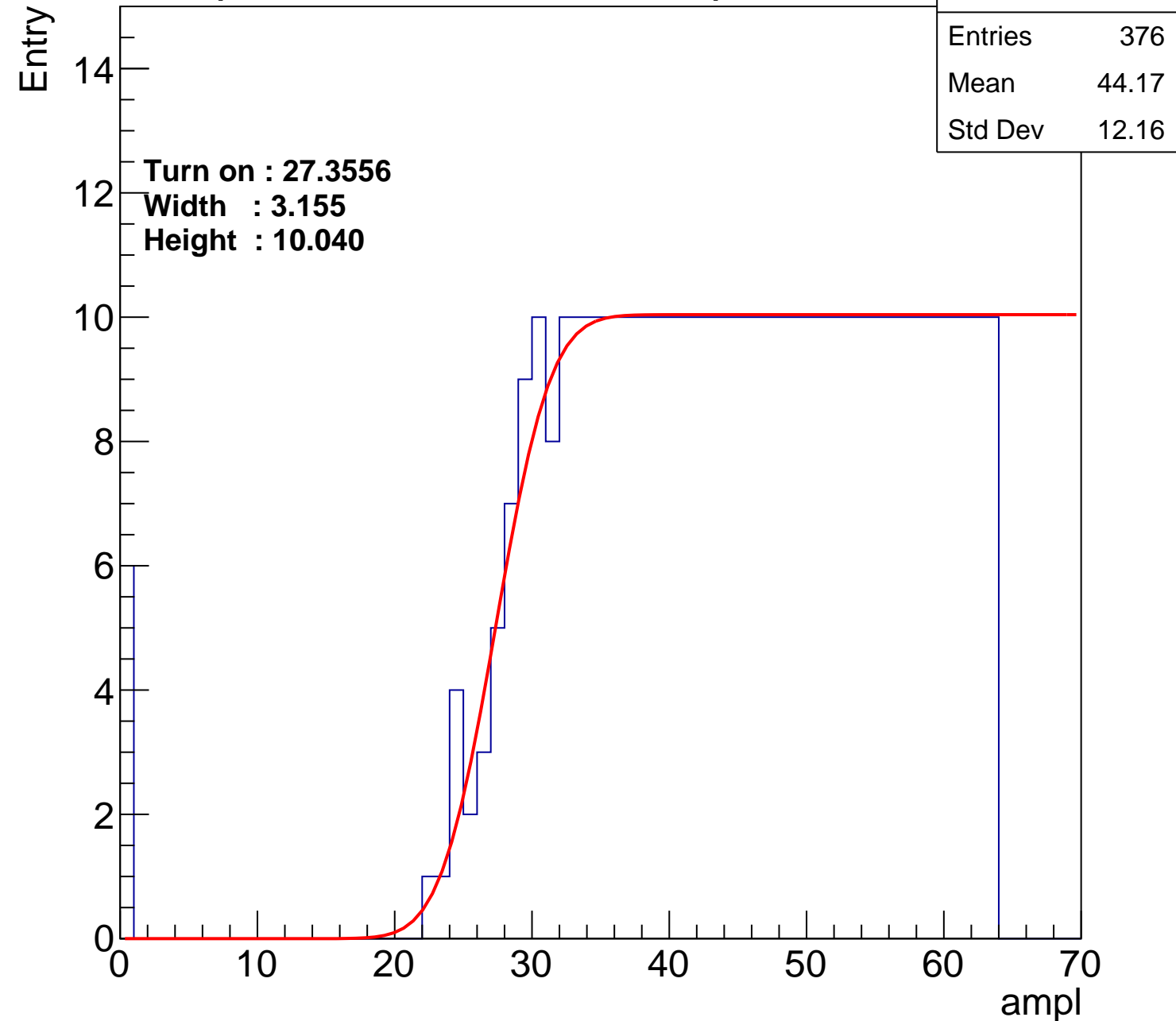
Width : 3.155

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	43.91
Std Dev	12.12

Turn on : 26.5407

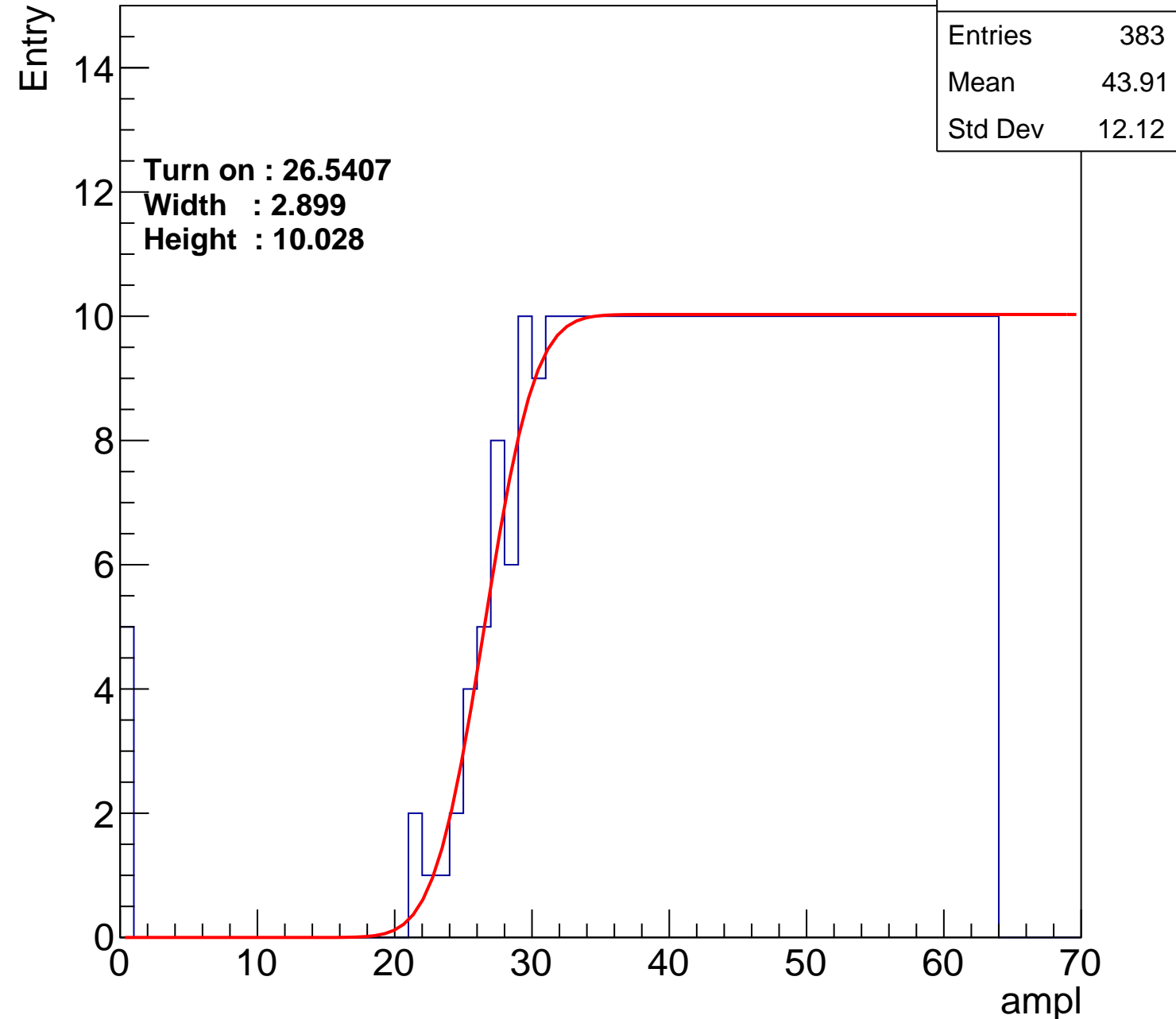
Width : 2.899

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.85
Std Dev	11.71

Turn on : 25.2004

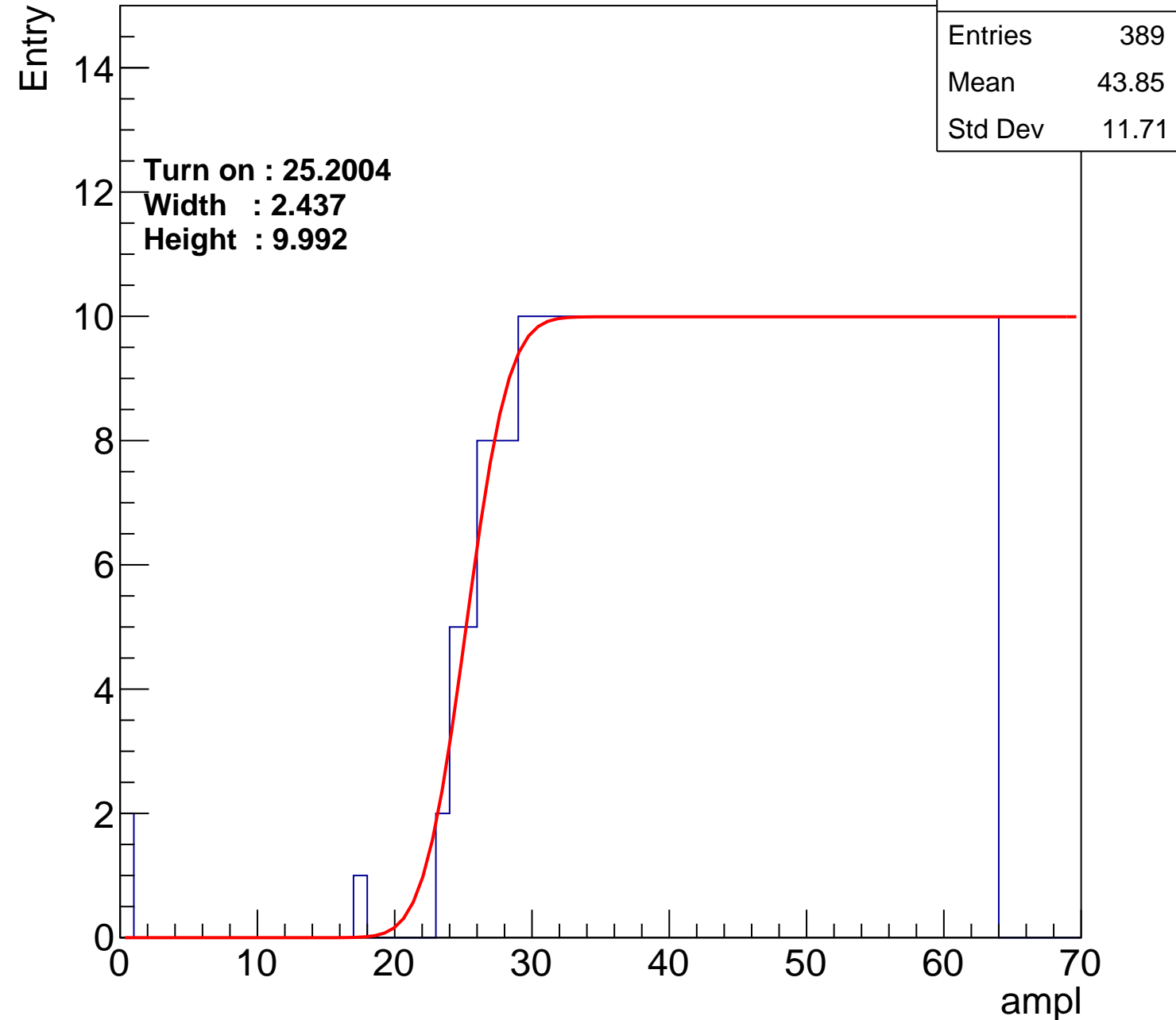
Width : 2.437

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch64

calib_packv5_042523_0143.root, FC#13, port D2

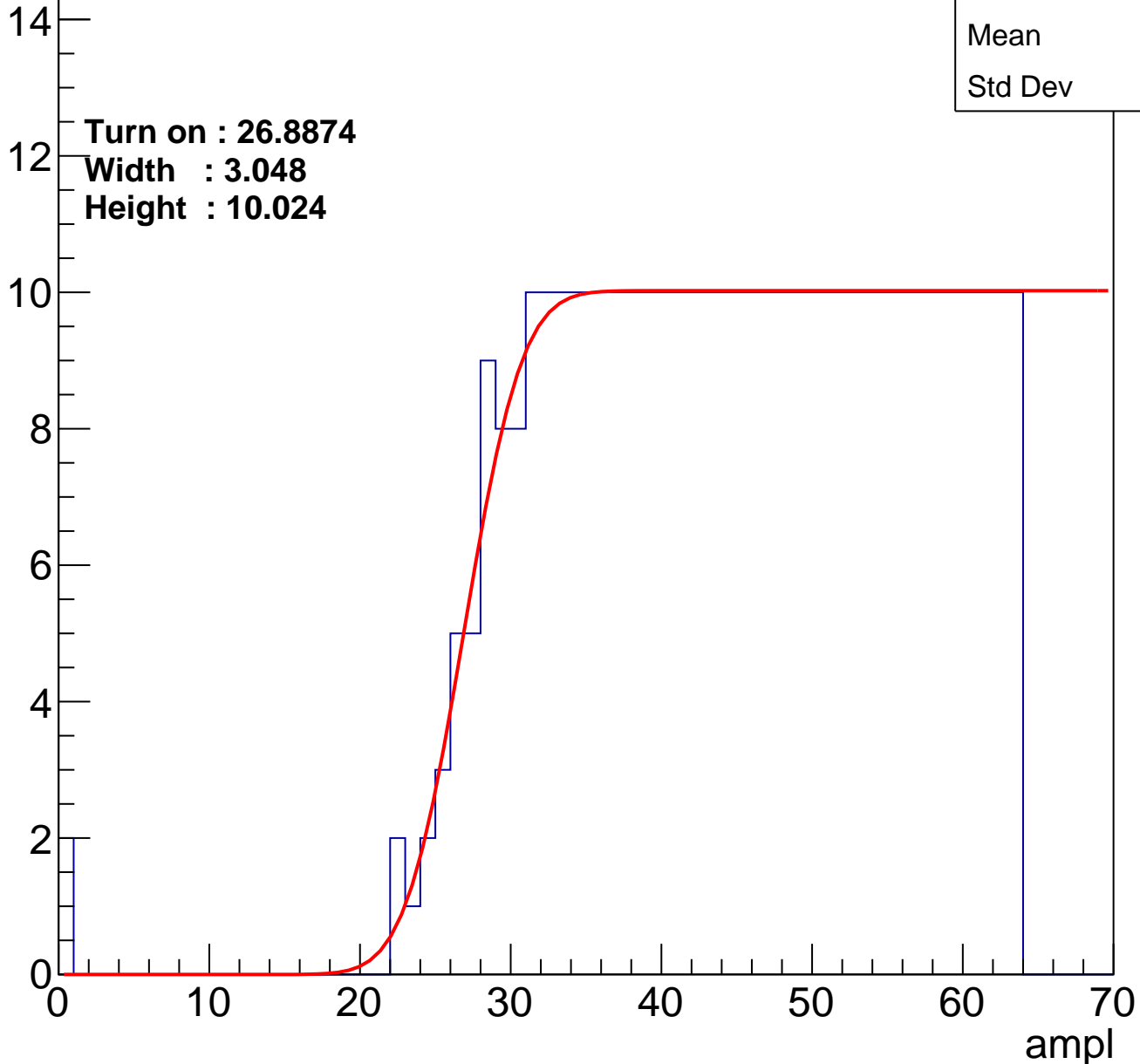
Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 26.8874

Width : 3.048

Height : 10.024

Entry



B1L003S, U6-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.12
Std Dev	11.59

Turn on : 29.2133

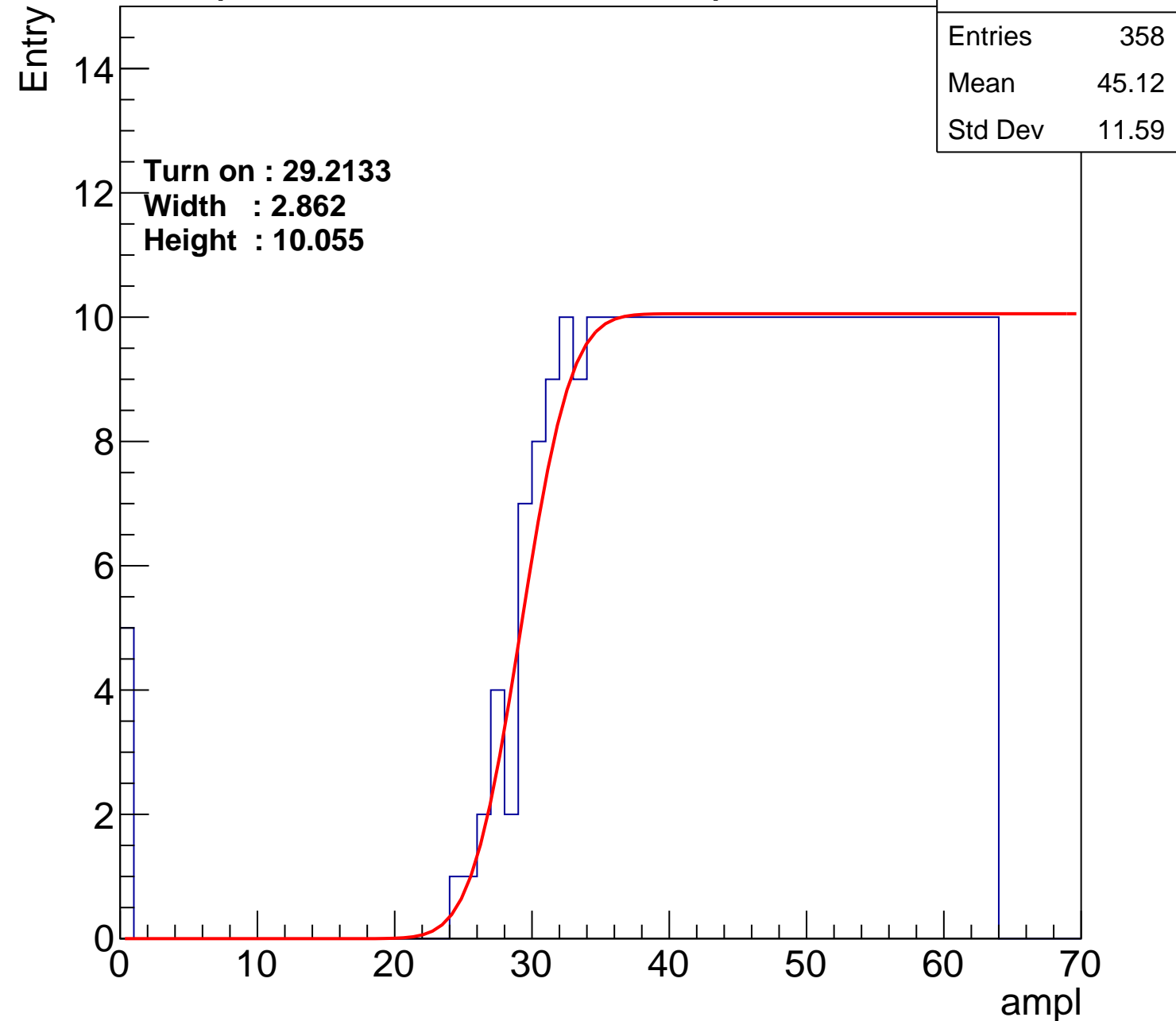
Width : 2.862

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.46
Std Dev	11.73

Turn on : 27.2240

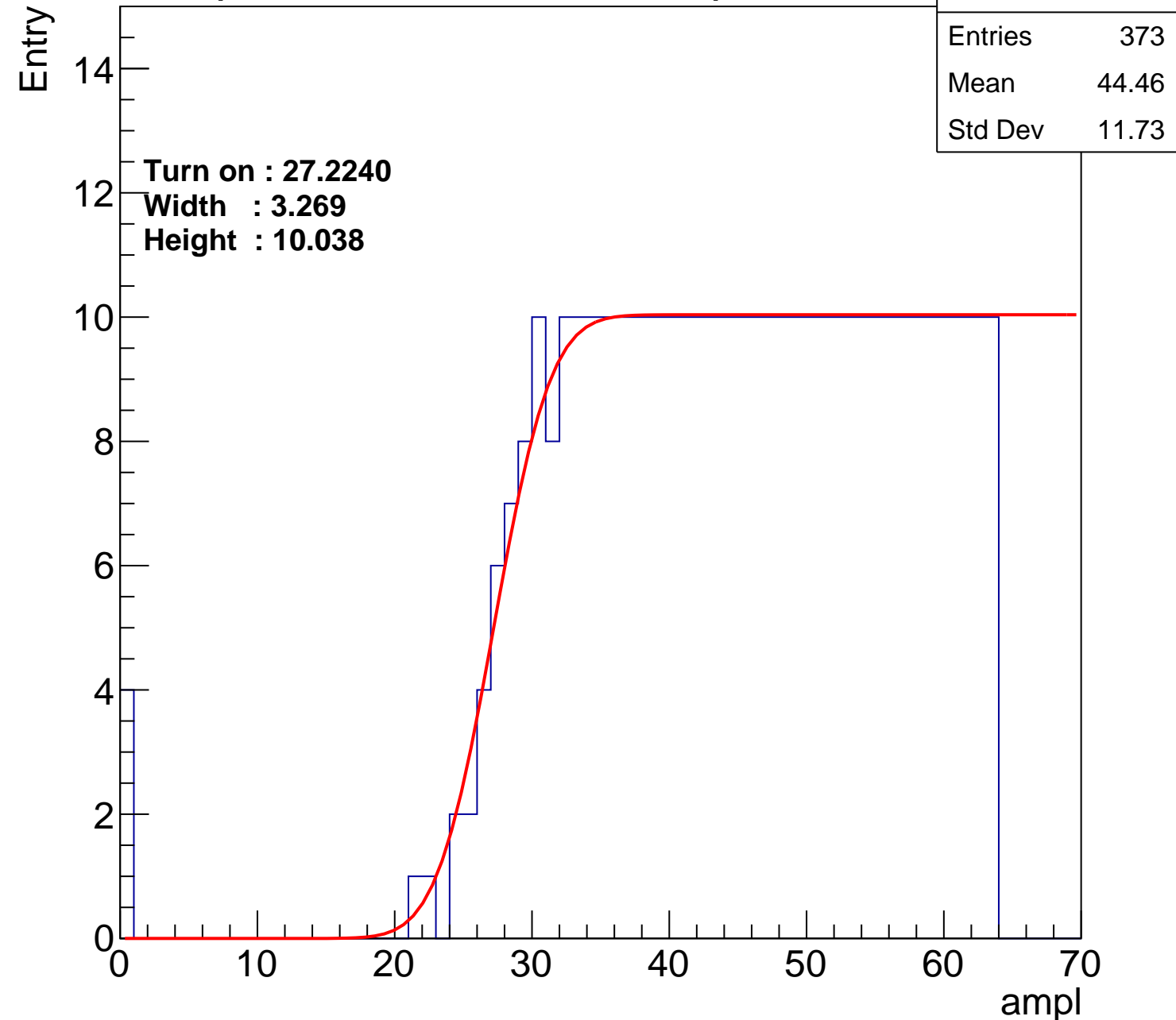
Width : 3.269

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.1
Std Dev	11.86

Turn on : 26.4860

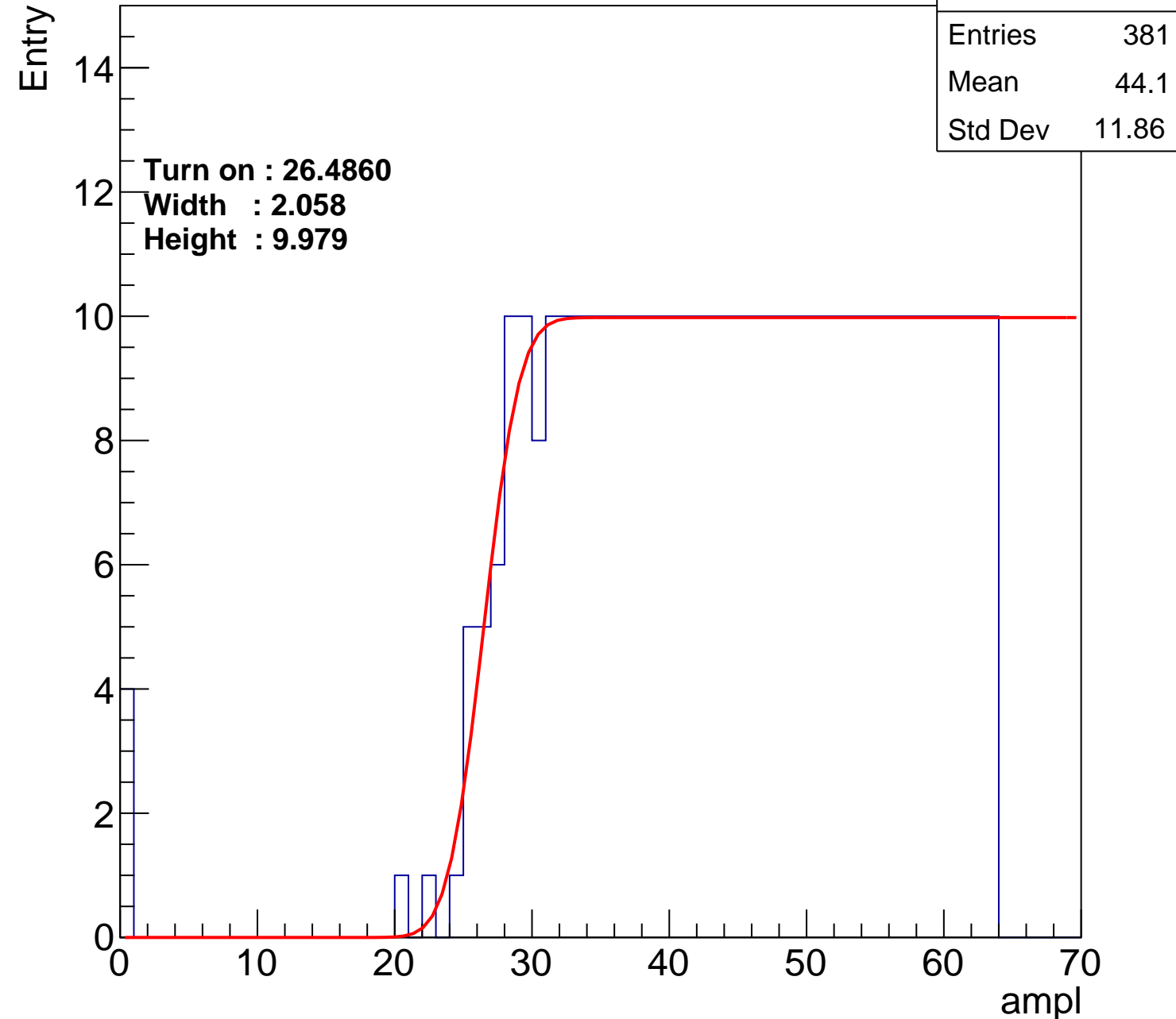
Width : 2.058

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch68

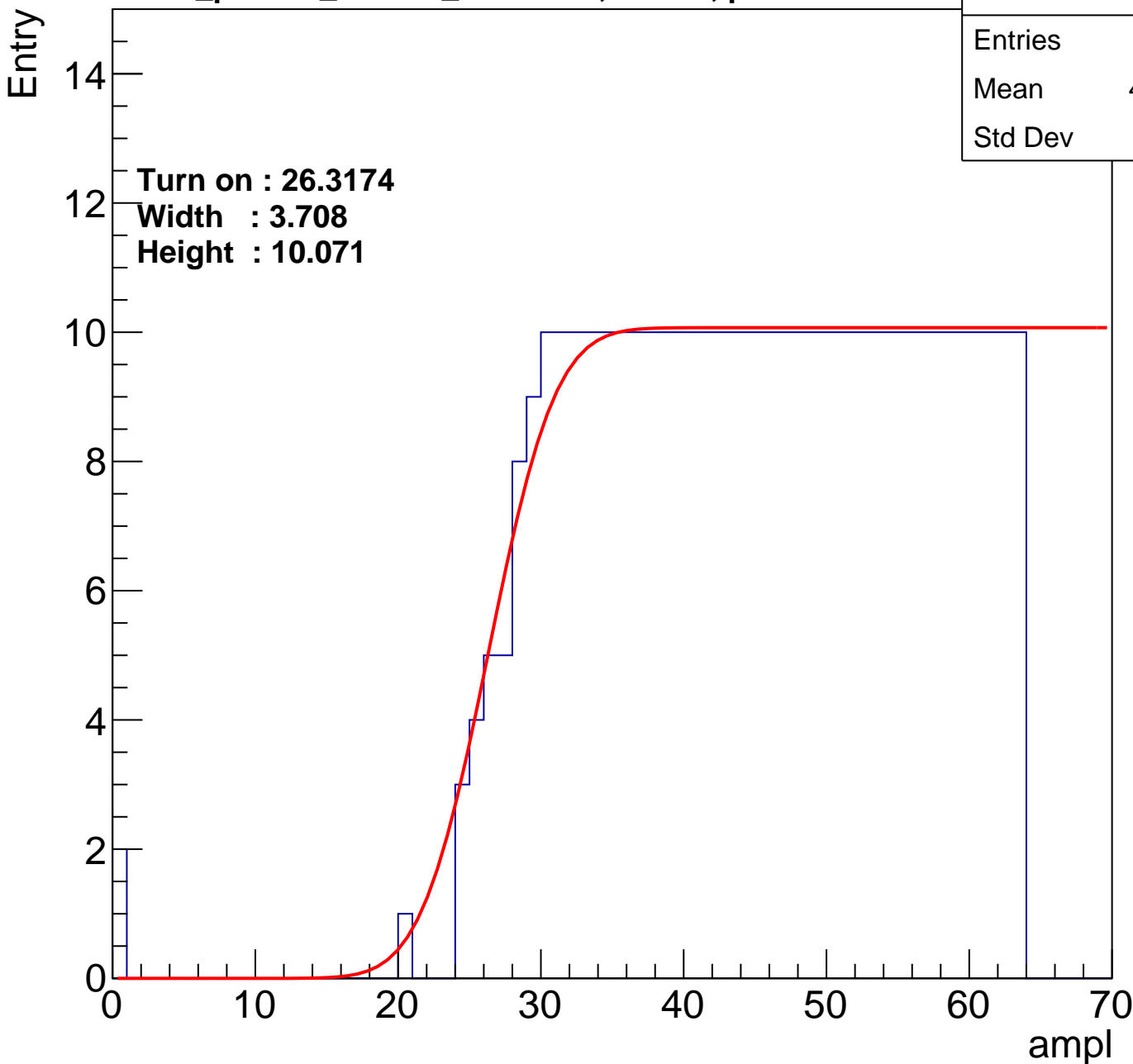
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.3174

Width : 3.708

Height : 10.071

Entries	377
Mean	44.44
Std Dev	11.4



B1L003S, U6-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.29
Std Dev	11.04

Turn on : 28.4717

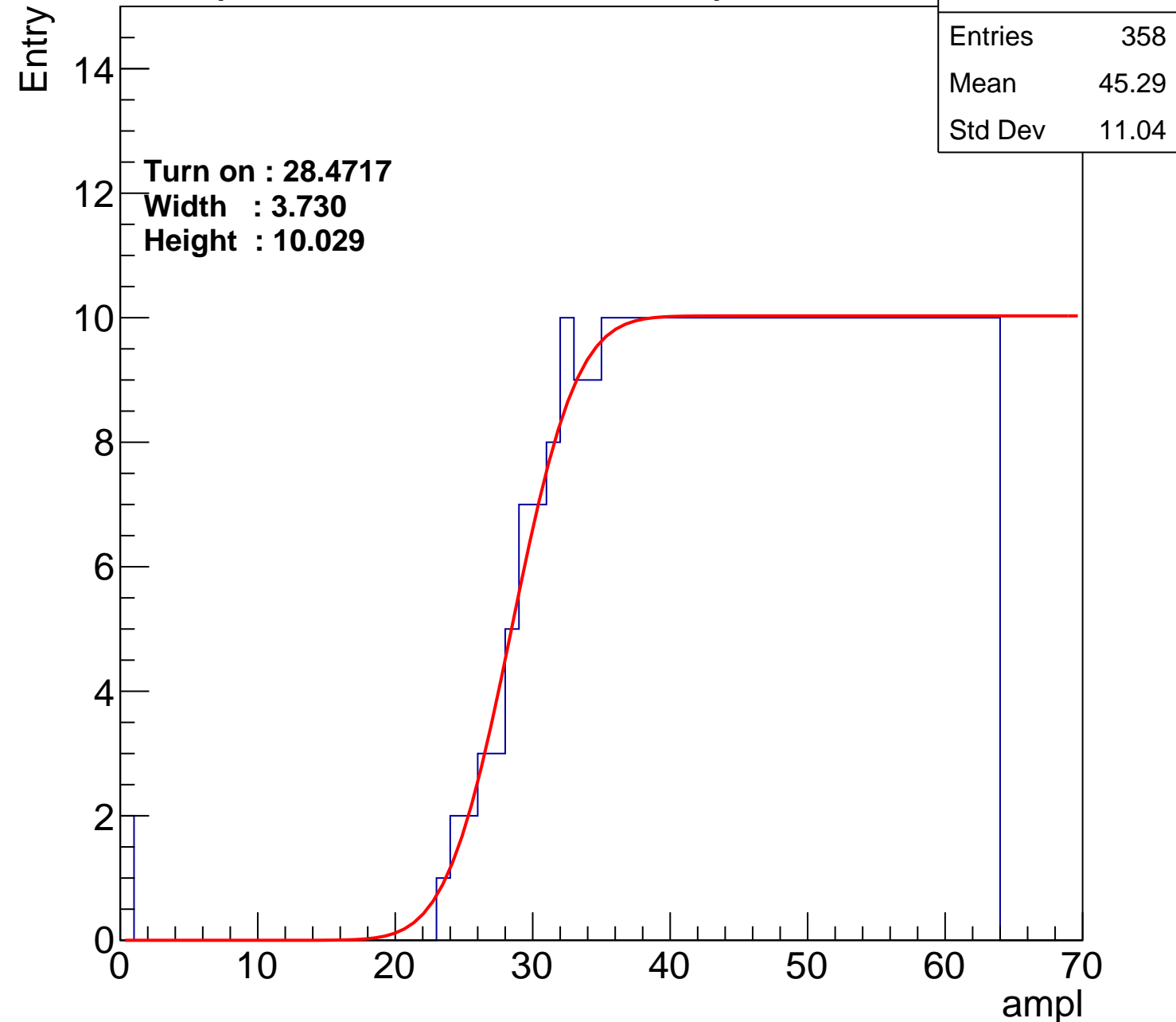
Width : 3.730

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 26.9862

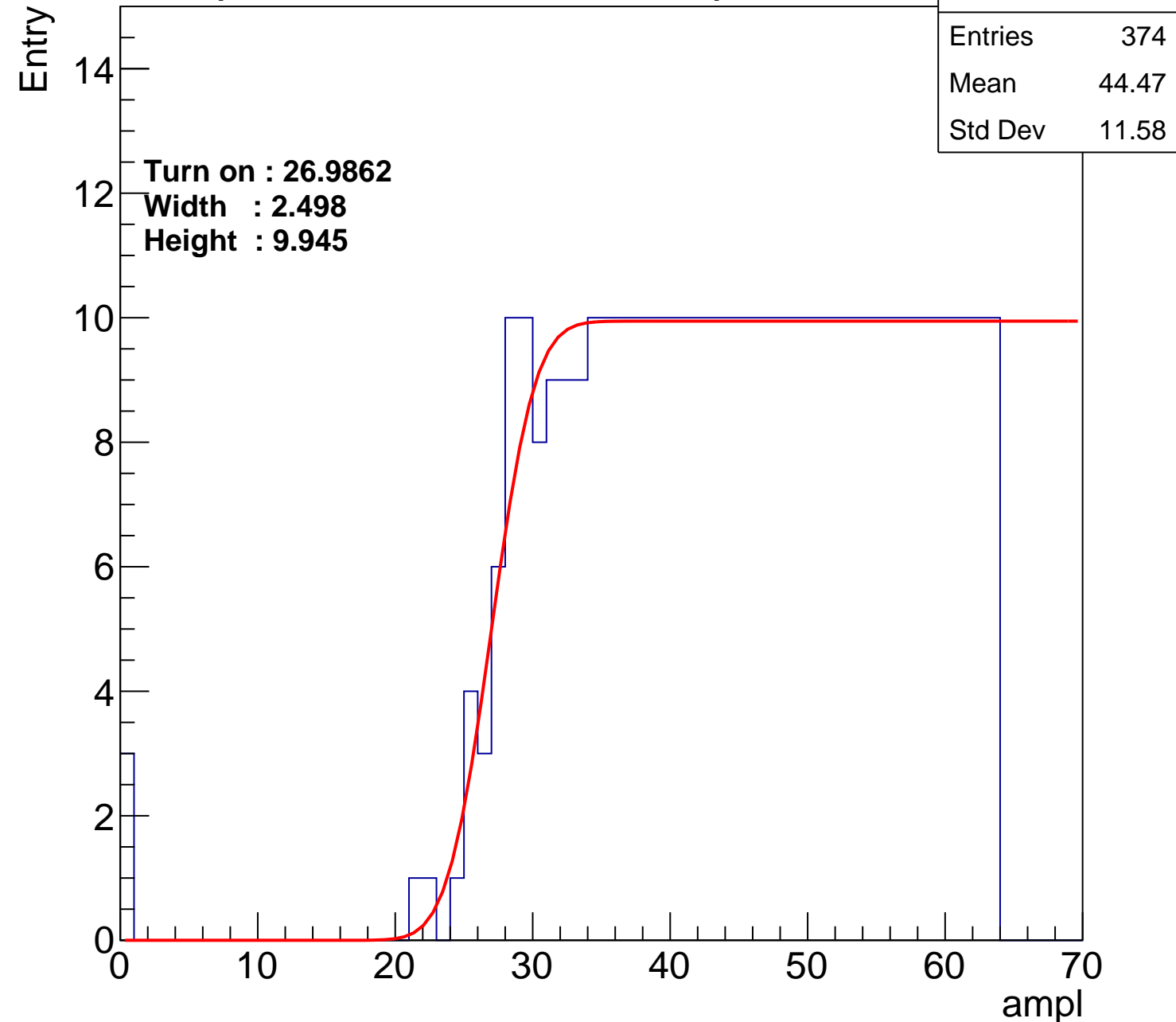
Width : 2.498

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch71

calib_packv5_042523_0143.root, FC#13, port D2

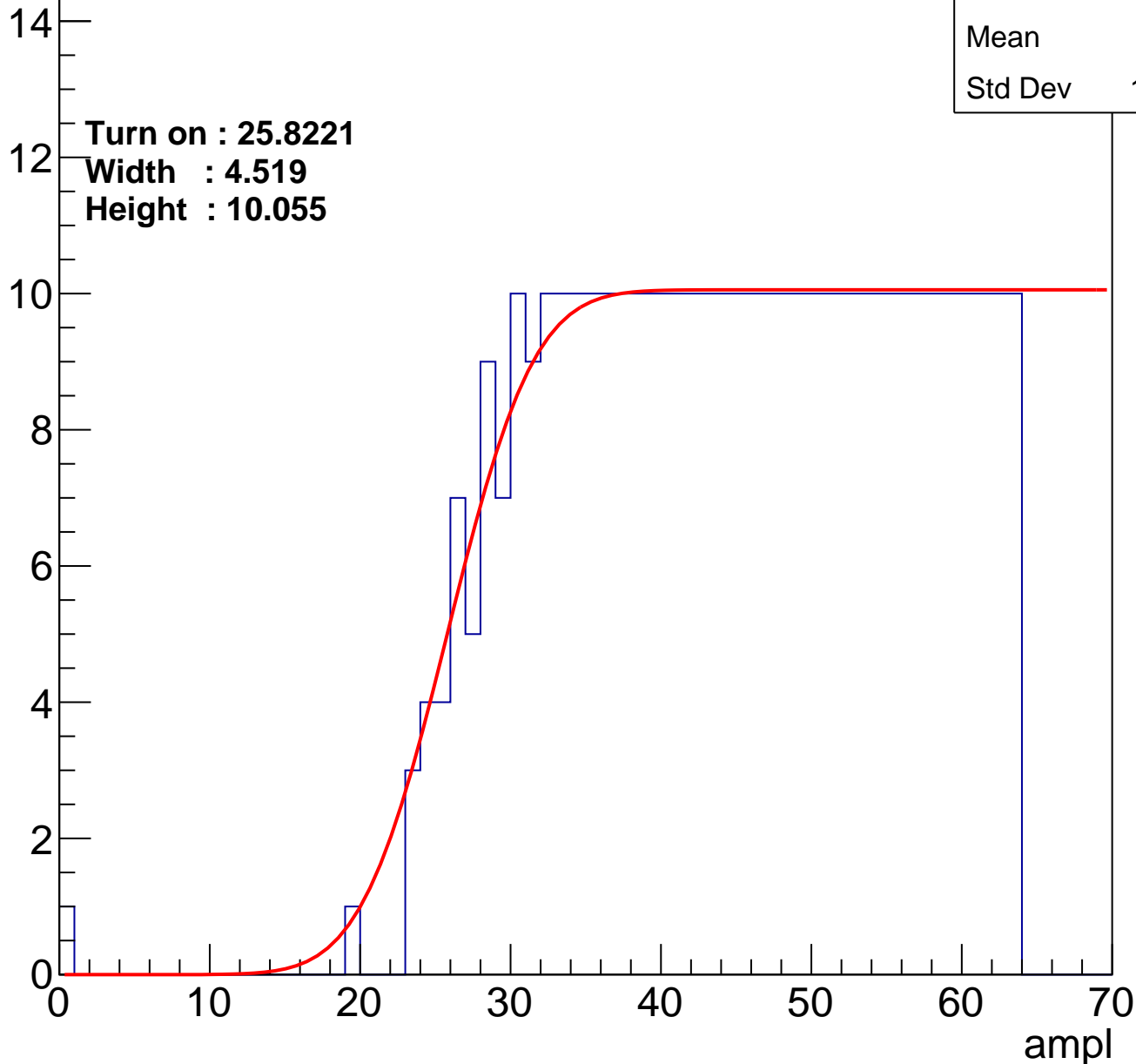
Entries	380
Mean	44.3
Std Dev	11.37

Turn on : 25.8221

Width : 4.519

Height : 10.055

Entry



B1L003S, U6-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 27.0353

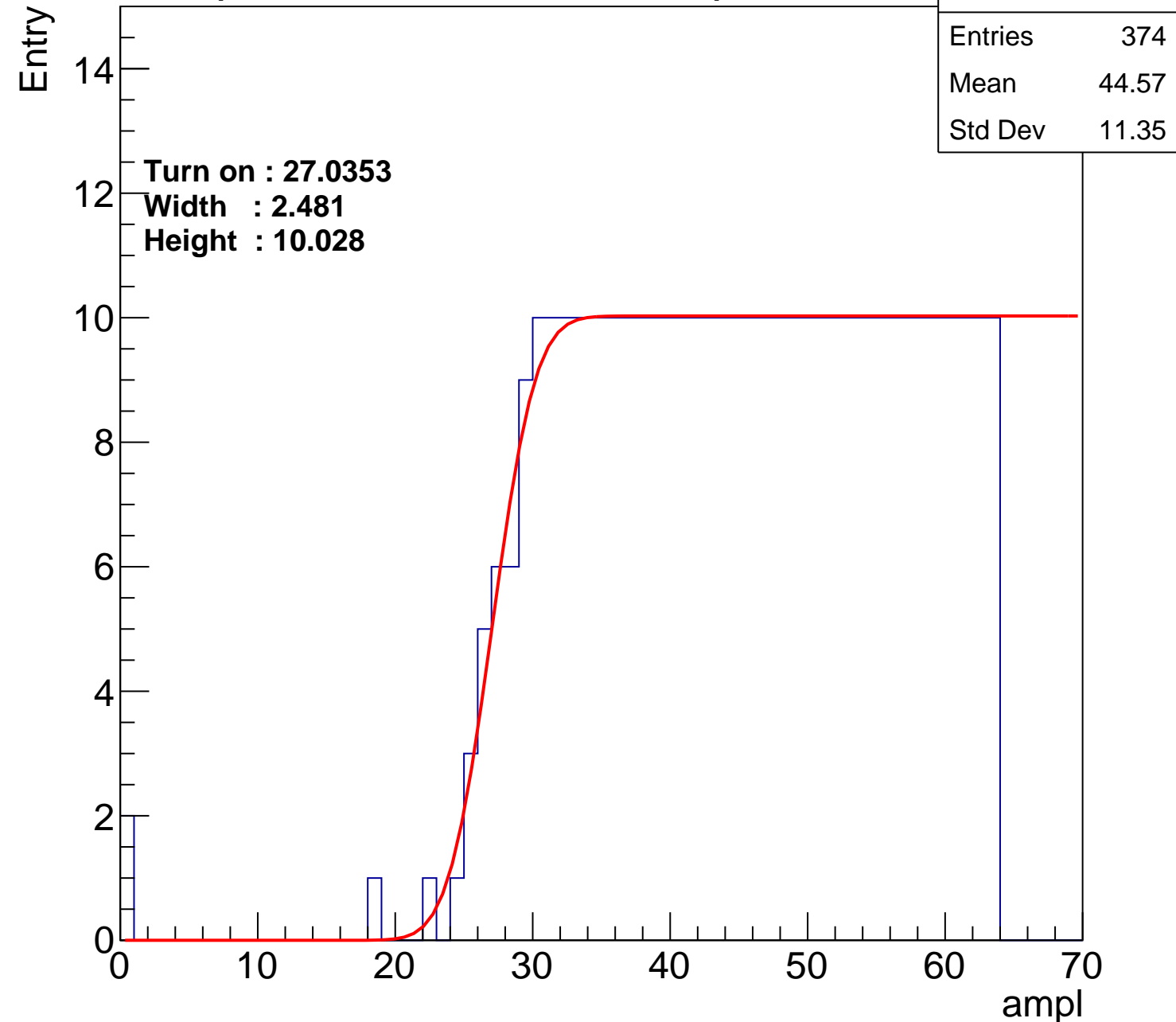
Width : 2.481

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 26.7121

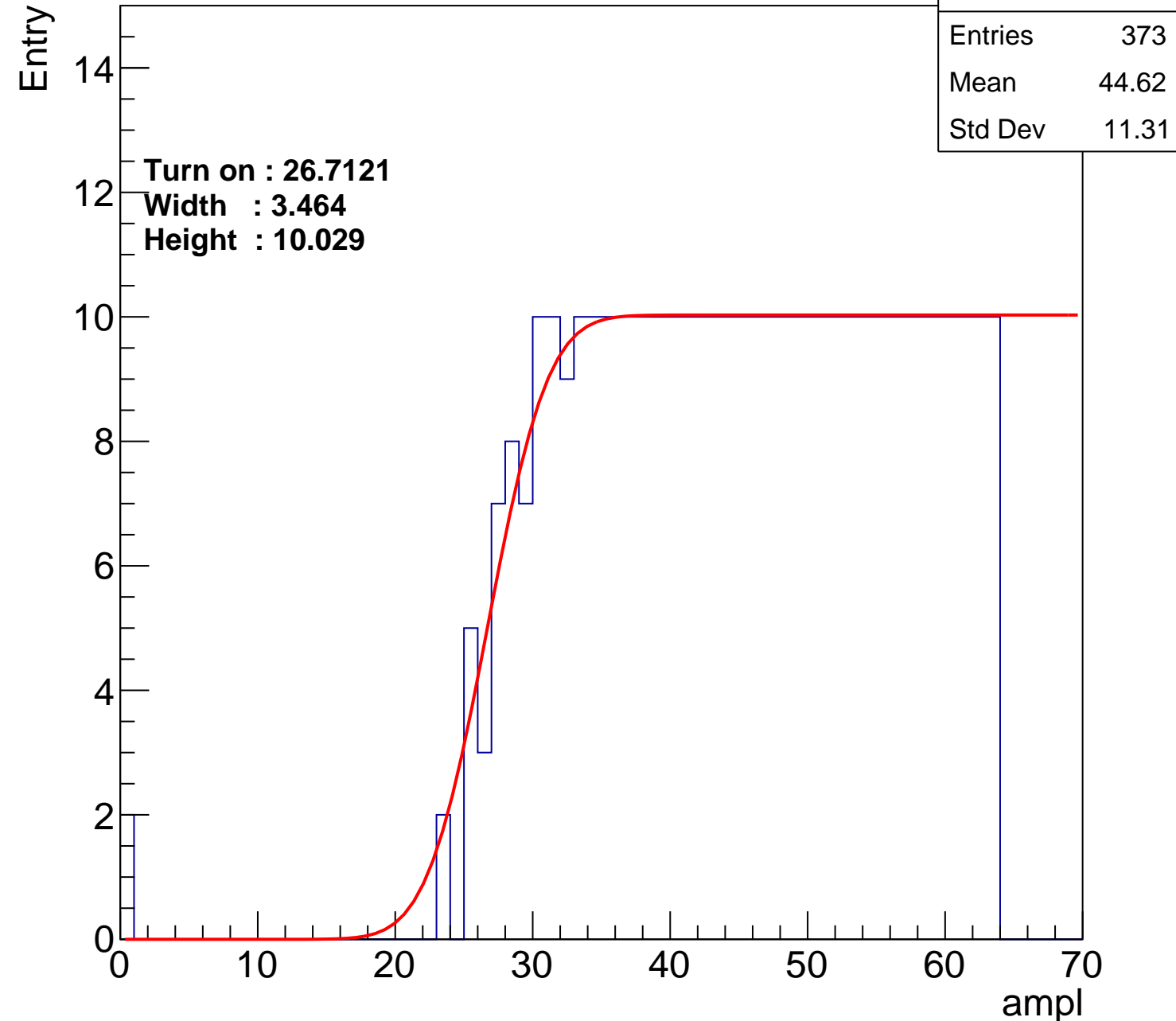
Width : 3.464

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.32
Std Dev	11.01

Turn on : 28.8201

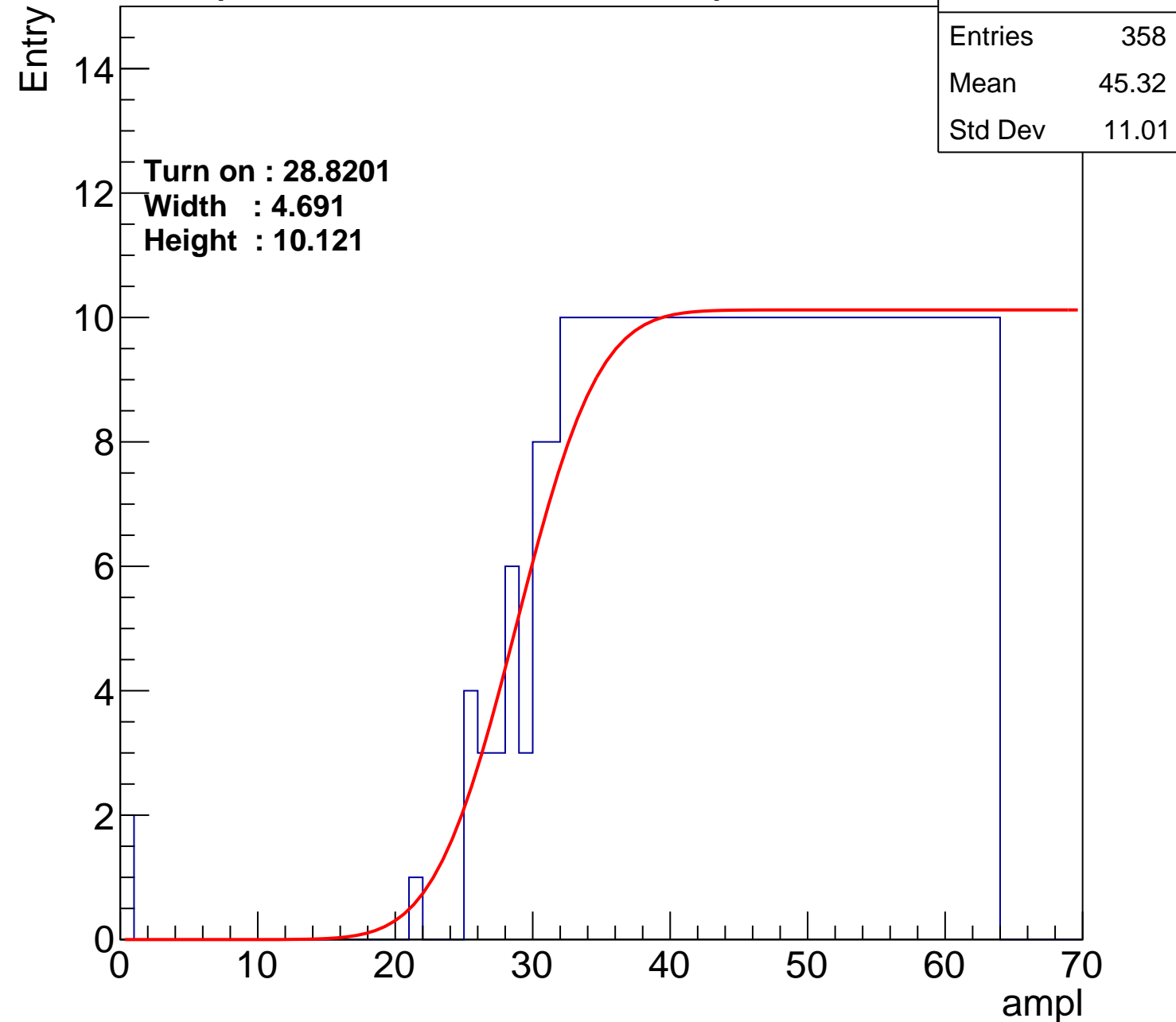
Width : 4.691

Height : 10.121

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.51
Std Dev	11.89

Turn on : 27.7407

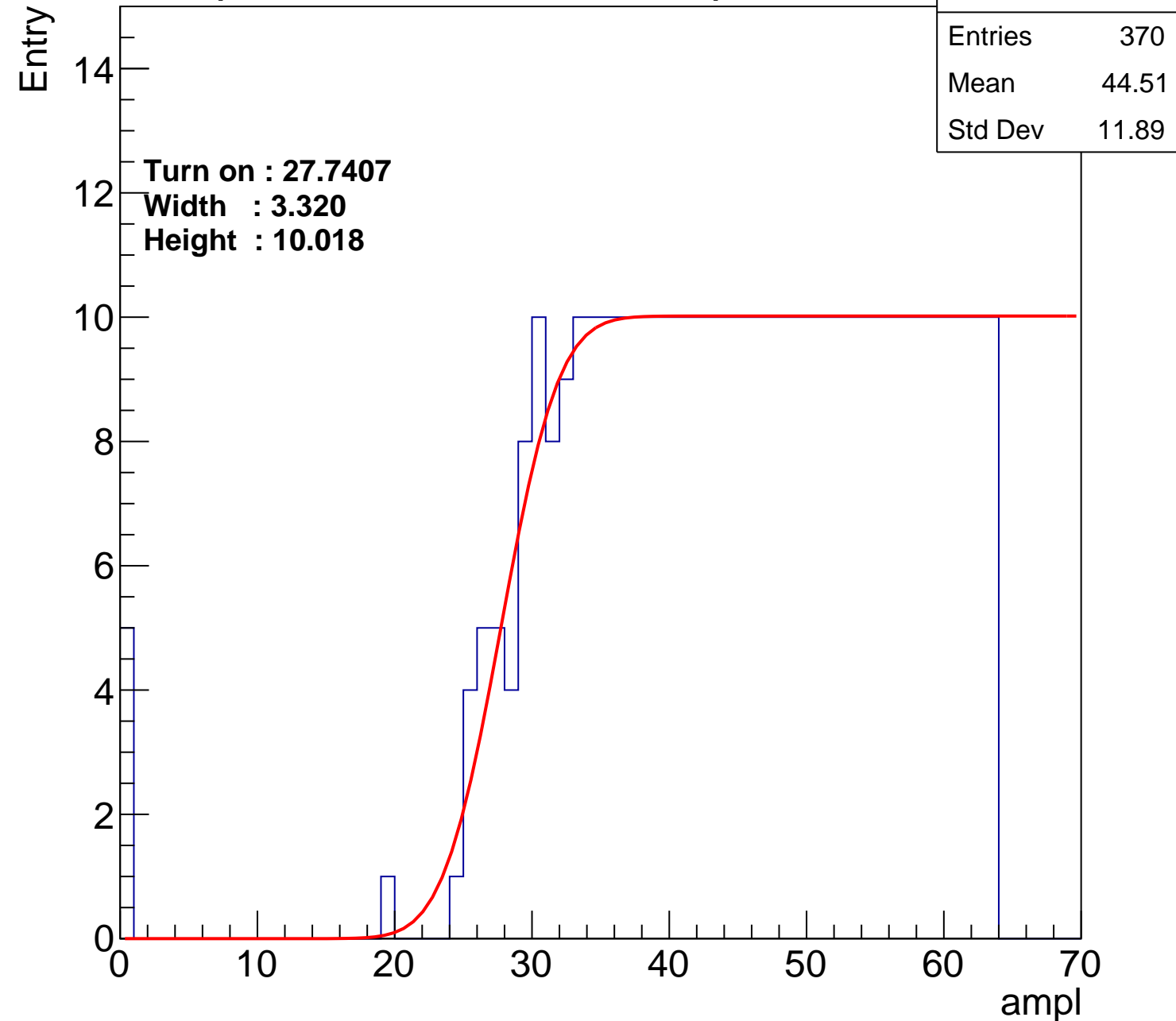
Width : 3.320

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch76

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.93
Std Dev	11.68

Turn on : 25.9574

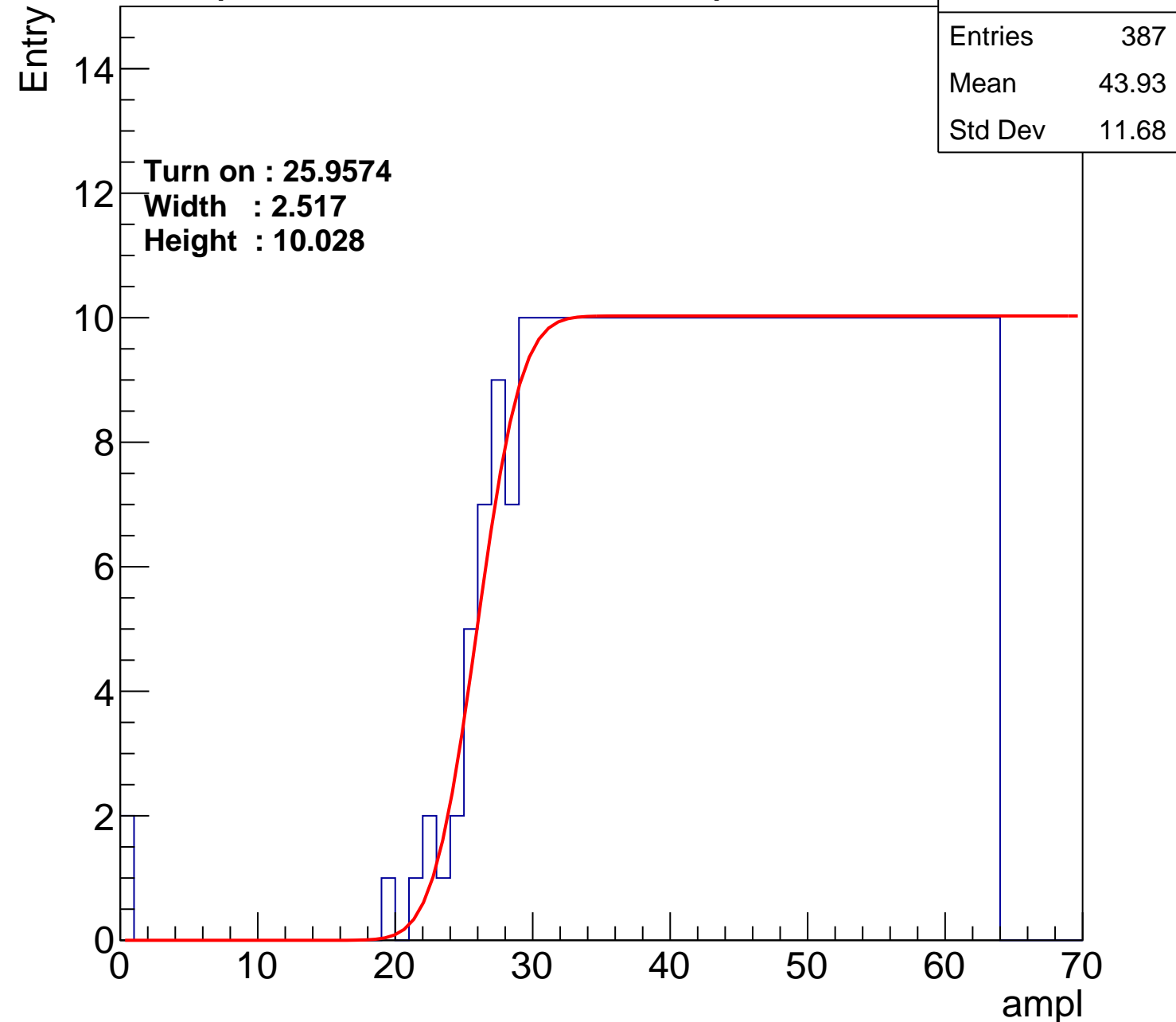
Width : 2.517

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.34
Std Dev	12.25

Turn on : 28.9049

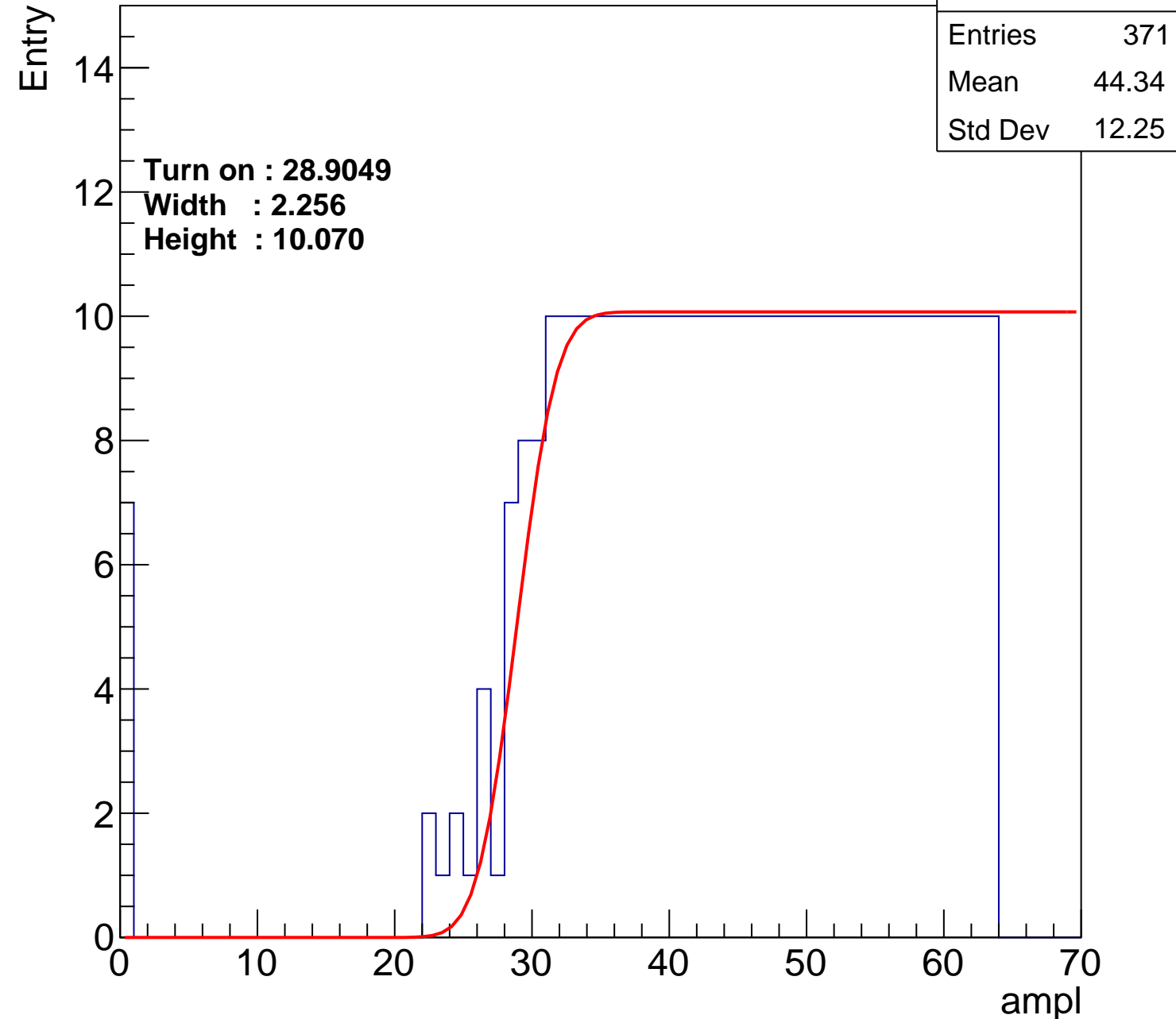
Width : 2.256

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch78

calib_packv5_042523_0143.root, FC#13, port D2

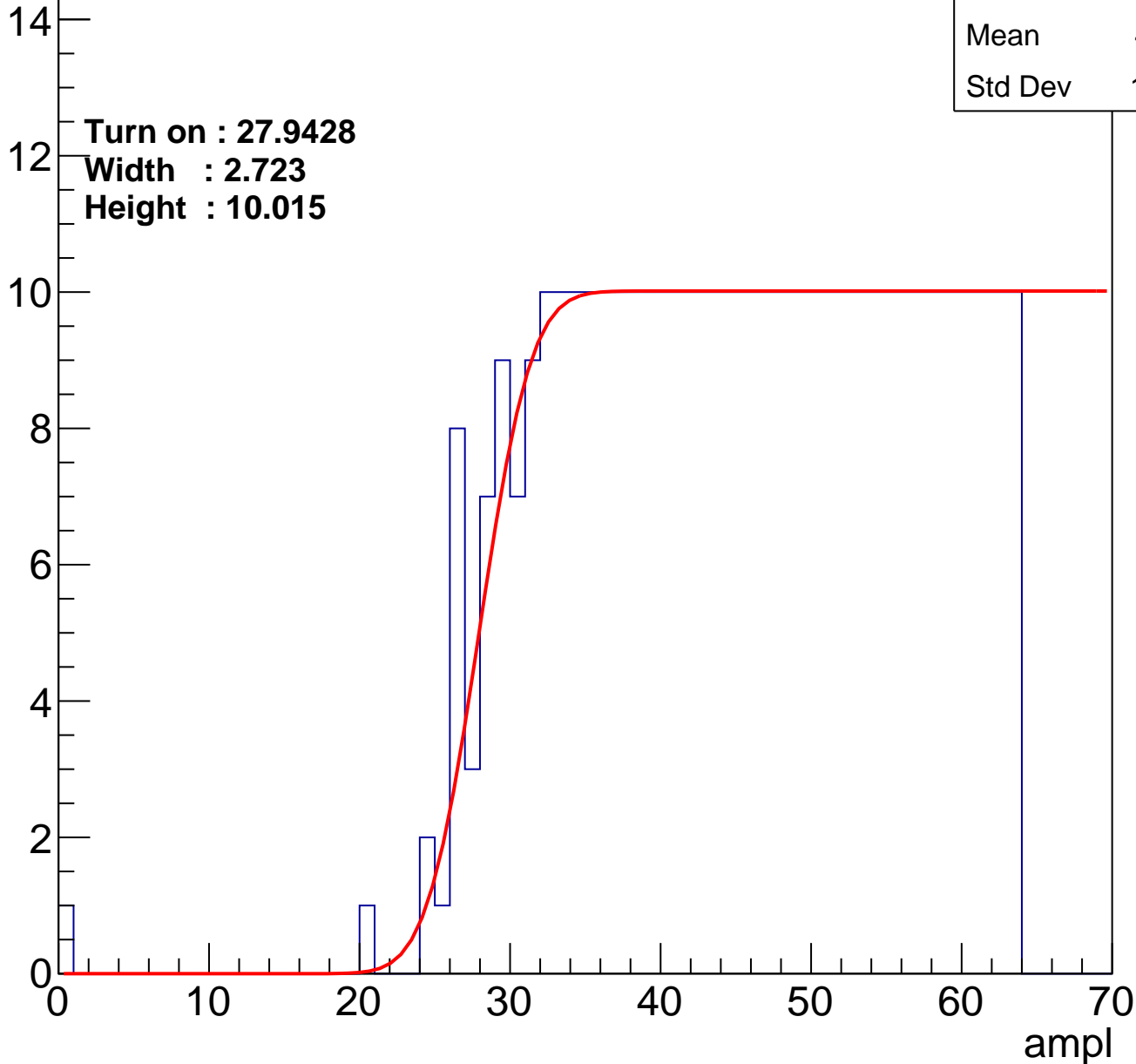
Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.9428

Width : 2.723

Height : 10.015

Entry



B1L003S, U6-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.99
Std Dev	11.64

Turn on : 25.3235

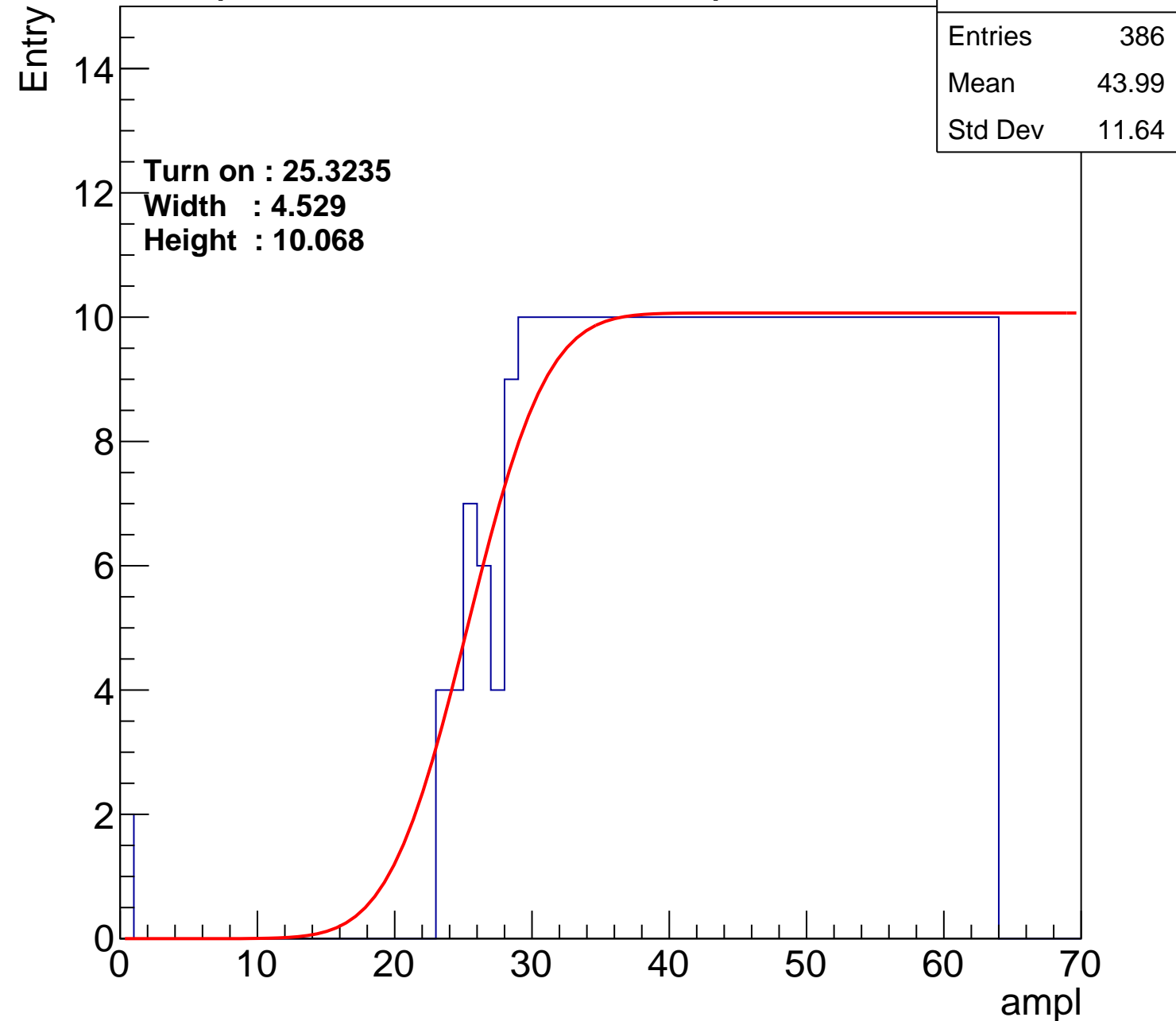
Width : 4.529

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.76
Std Dev	11.26

Turn on : 28.0291

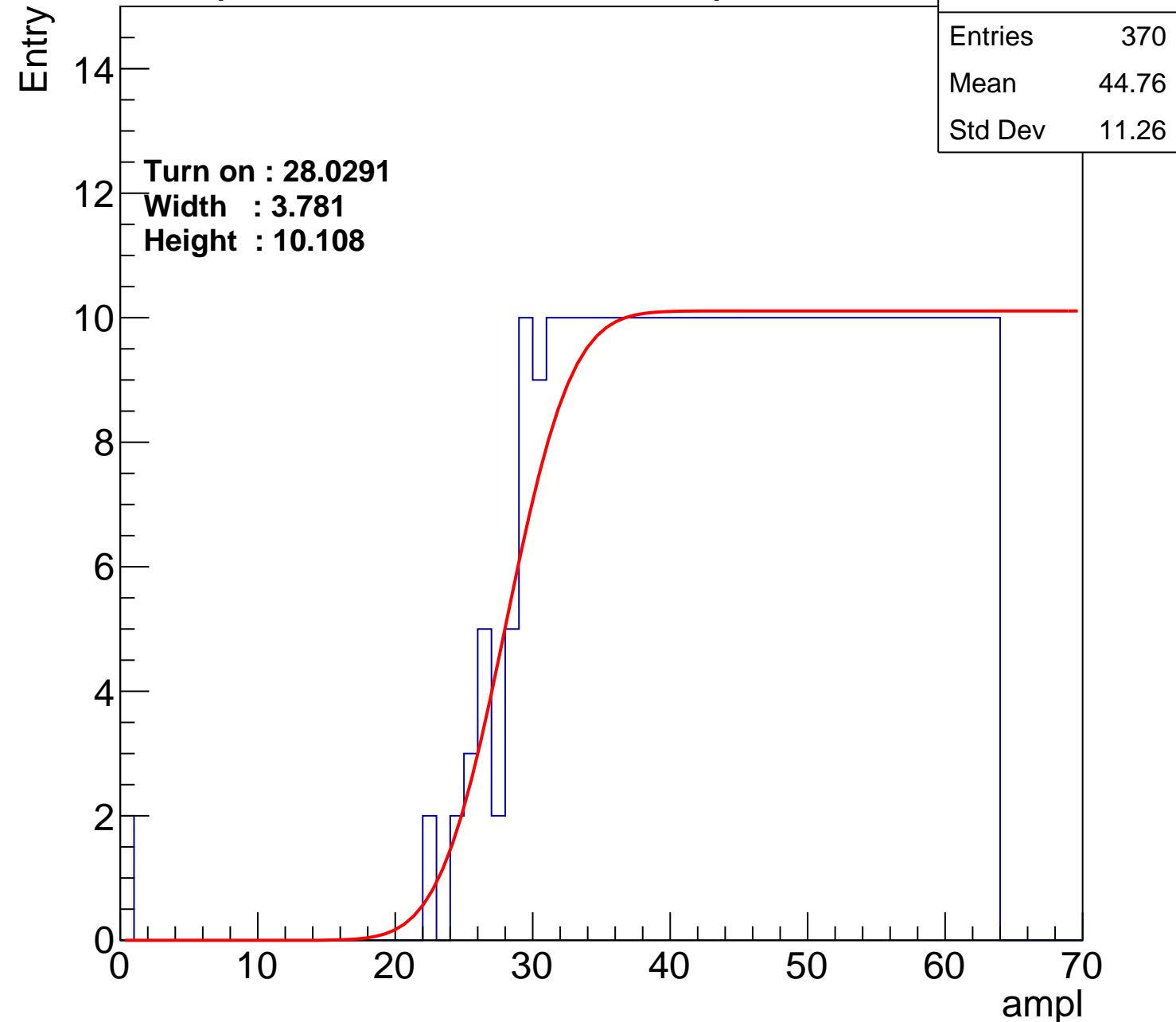
Width : 3.781

Height : 10.108

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.11
Std Dev	11.26

Turn on : 28.2972

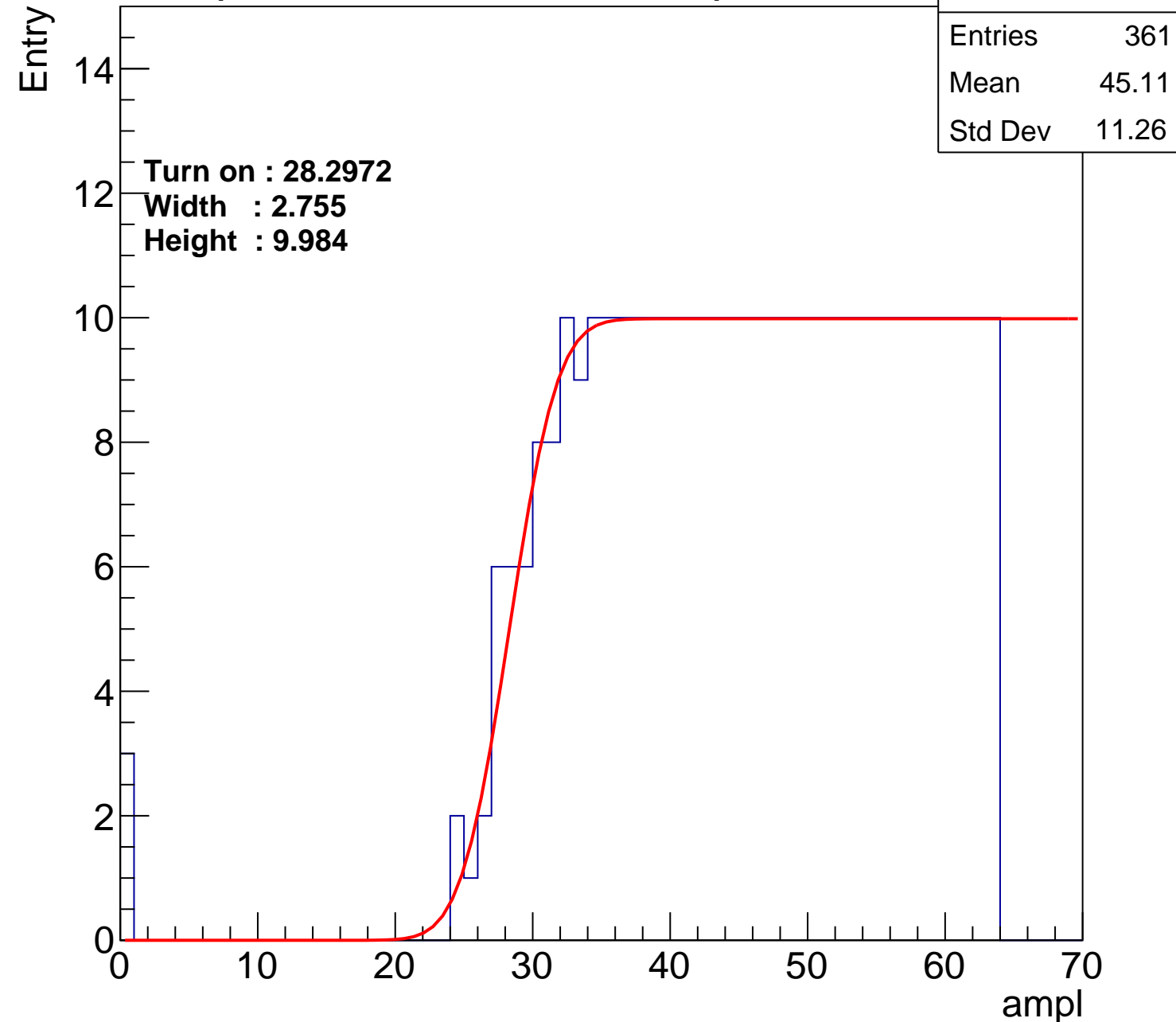
Width : 2.755

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch82

calib_packv5_042523_0143.root, FC#13, port D2

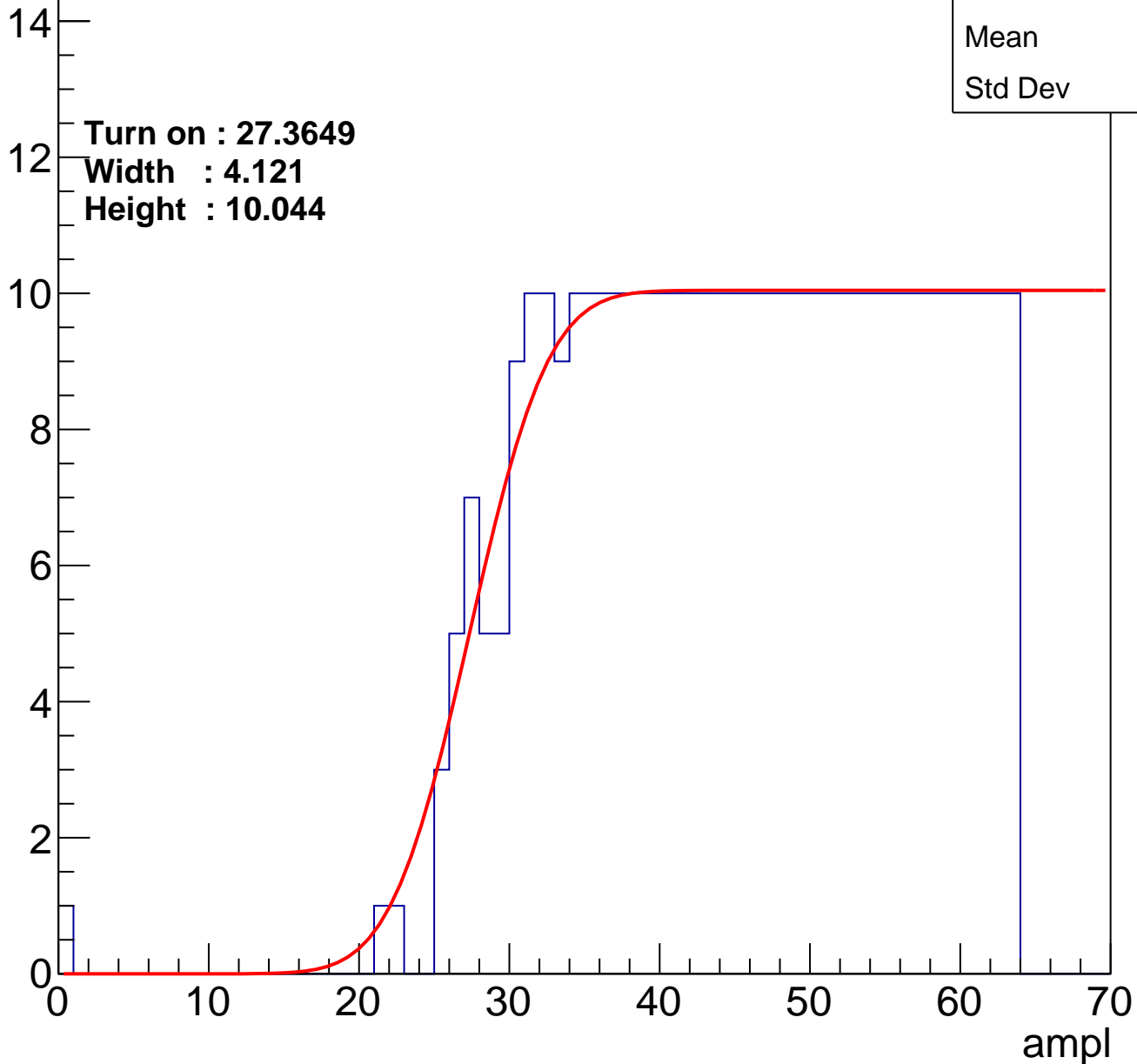
Entry

Entries	366
Mean	45
Std Dev	11

Turn on : 27.3649

Width : 4.121

Height : 10.044



B1L003S, U6-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.53
Std Dev	11.72

Turn on : 28.9298

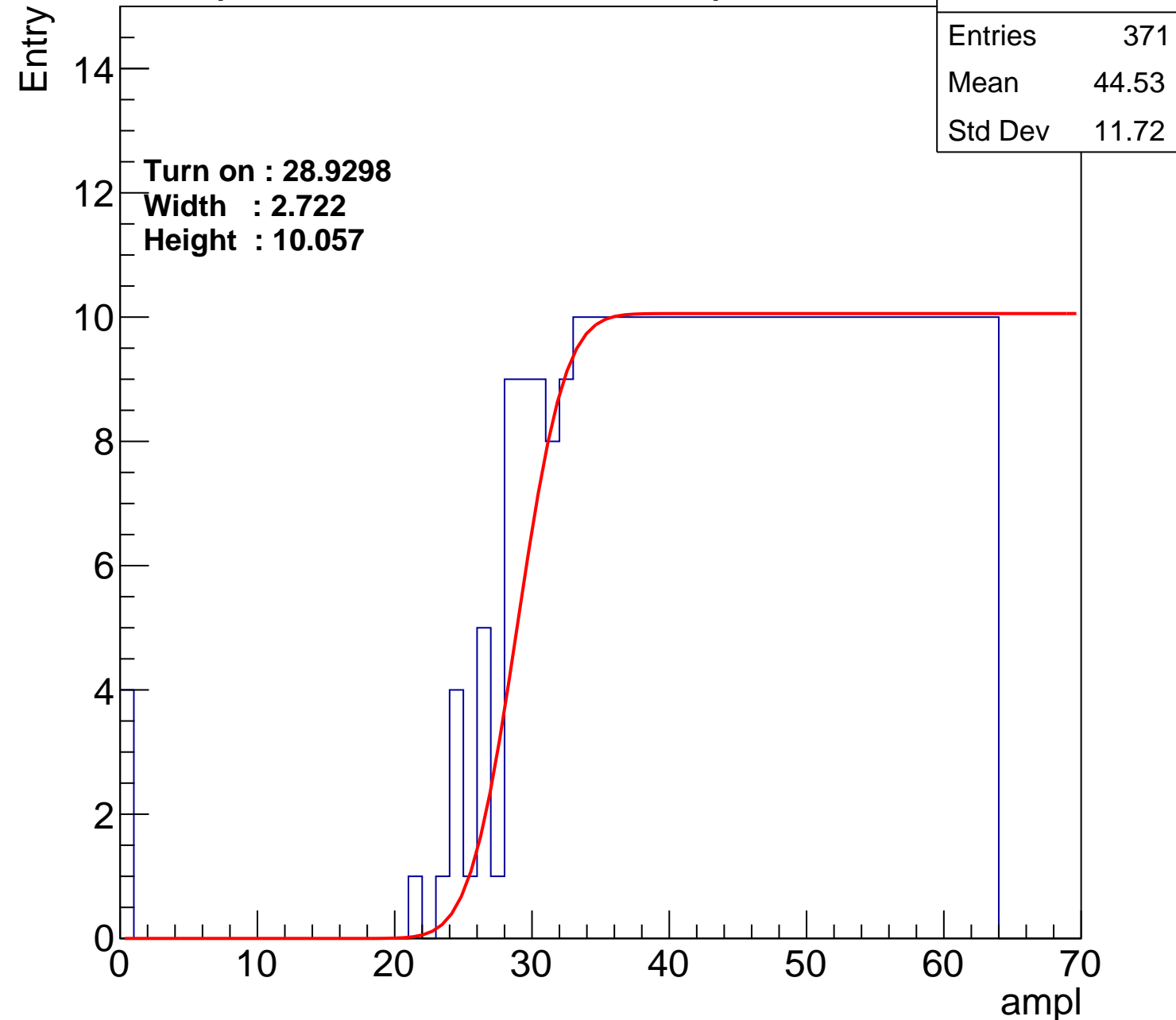
Width : 2.722

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.41
Std Dev	11.57

Turn on : 27.0840

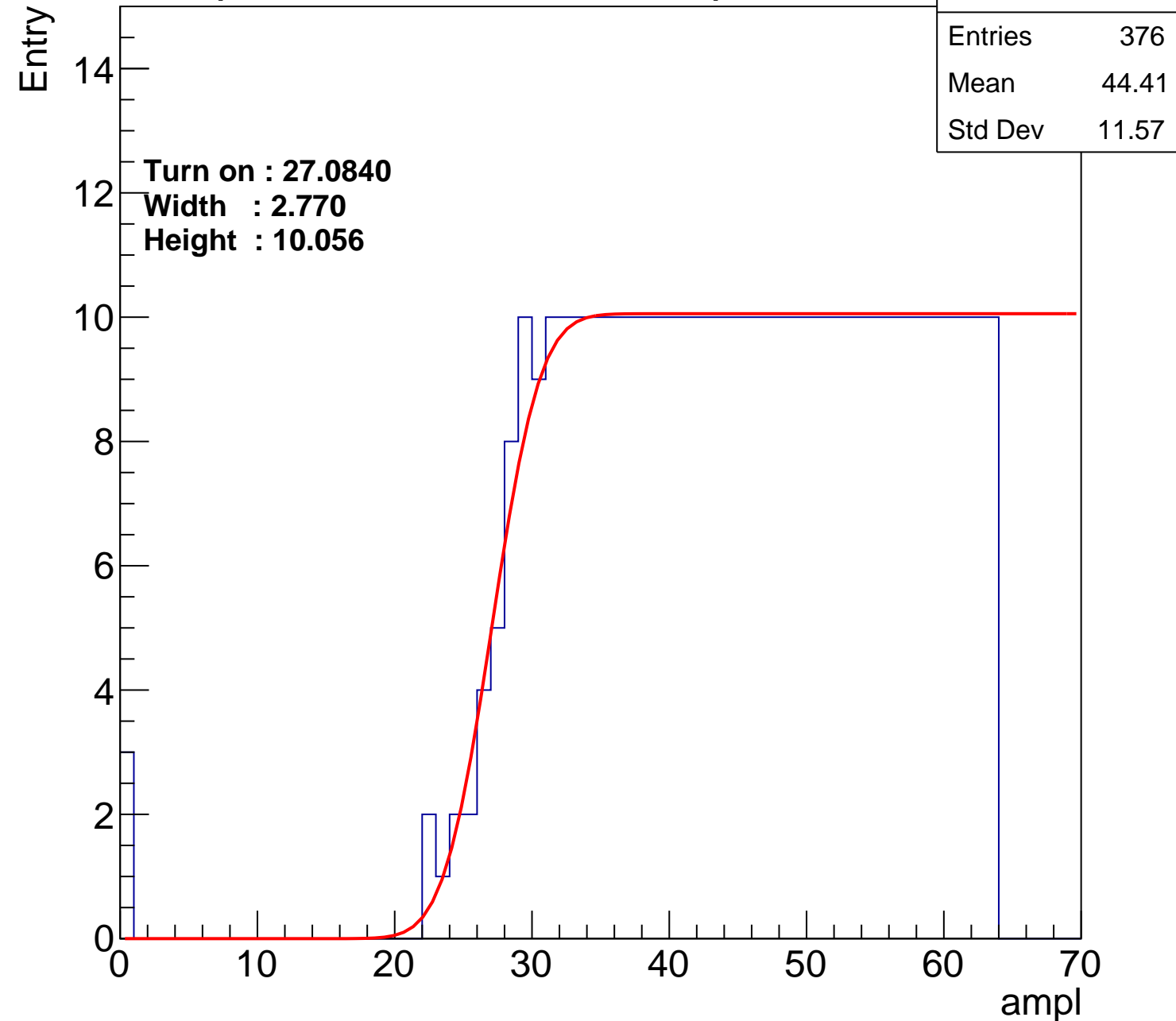
Width : 2.770

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch85

calib_packv5_042523_0143.root, FC#13, port D2

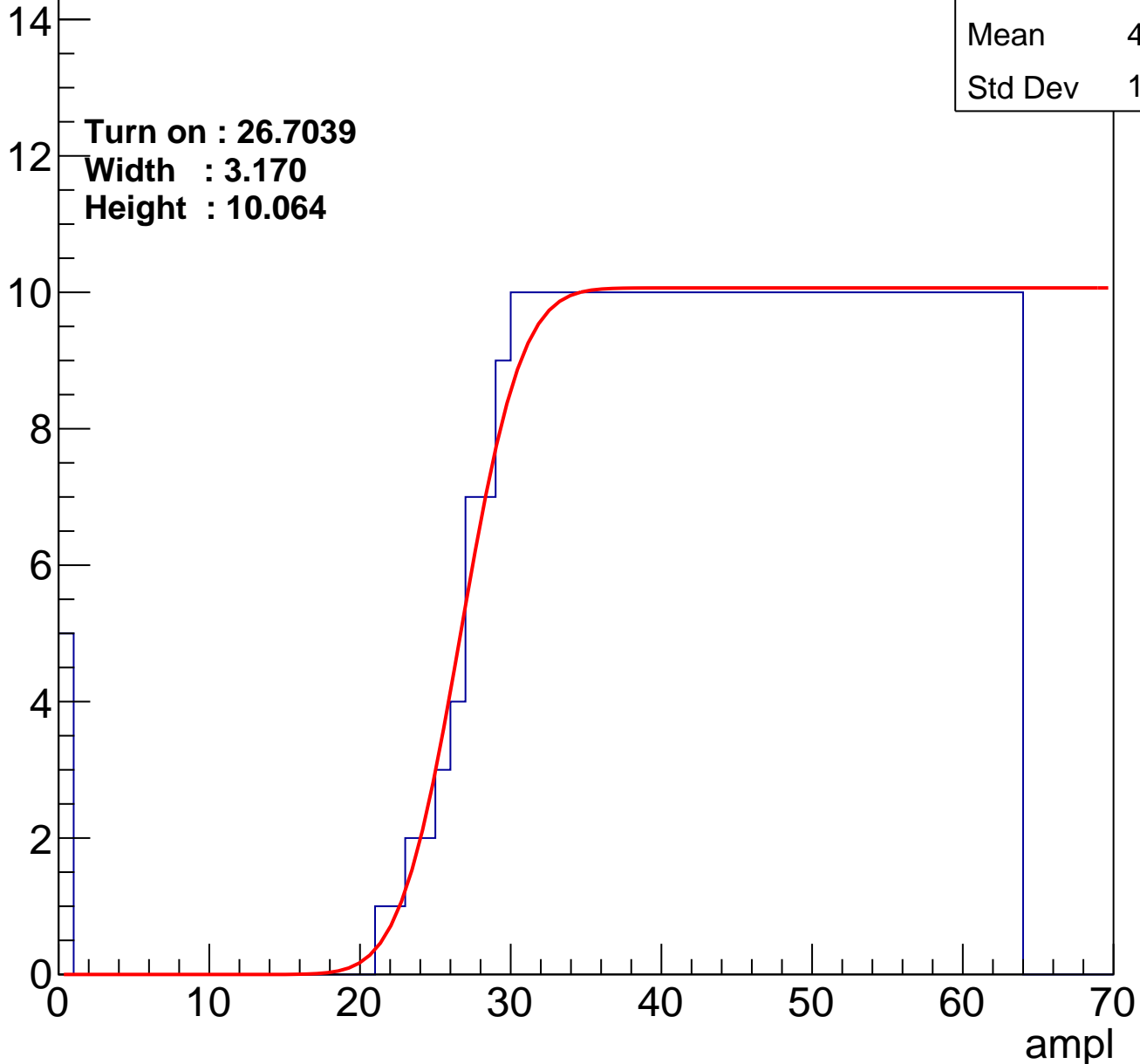
Entries	381
Mean	44.02
Std Dev	12.06

Turn on : 26.7039

Width : 3.170

Height : 10.064

Entry



B1L003S, U6-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.86
Std Dev	12.15

Turn on : 26.5879

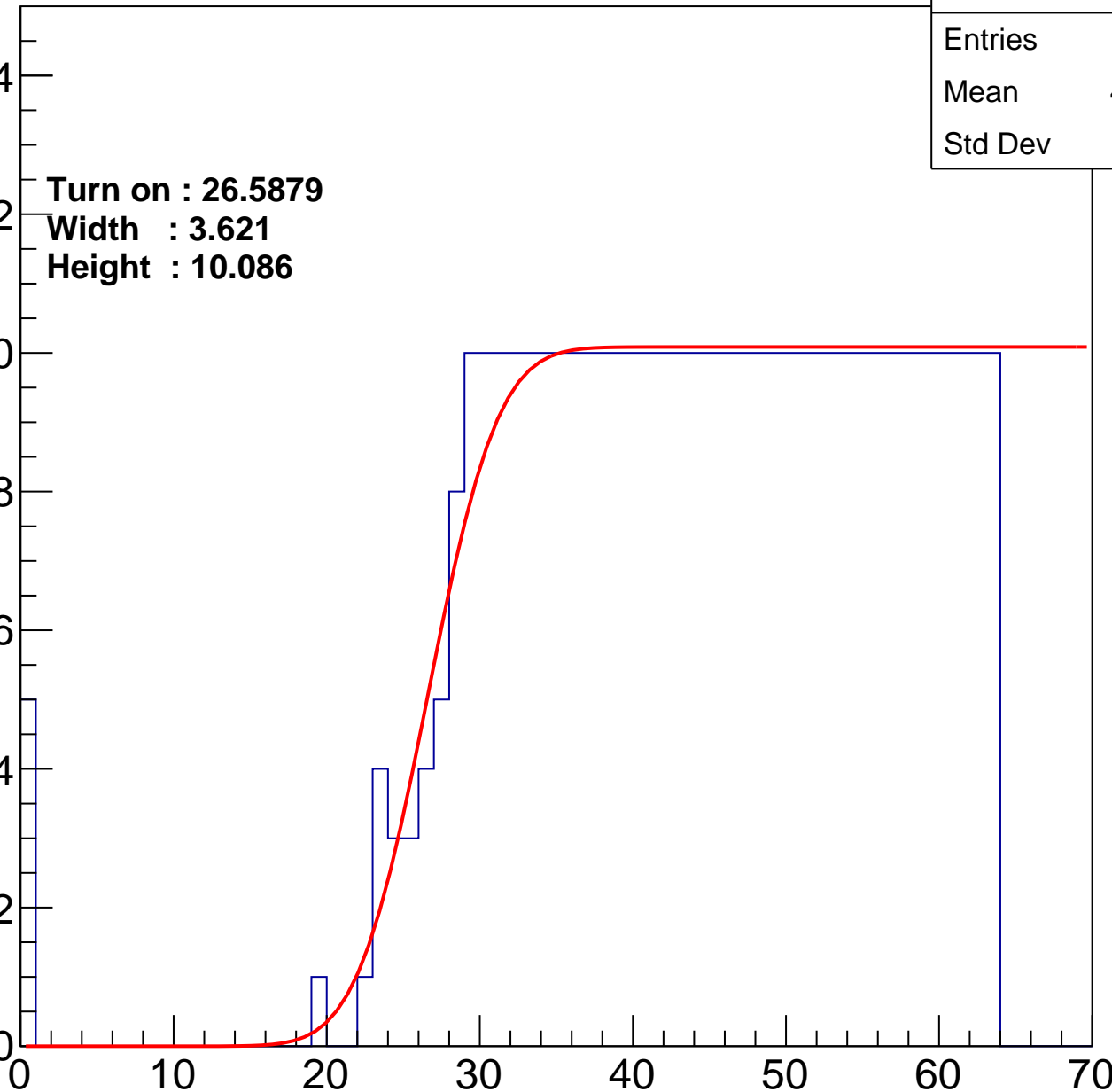
Width : 3.621

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.6
Std Dev	12.53

Turn on : 26.4702

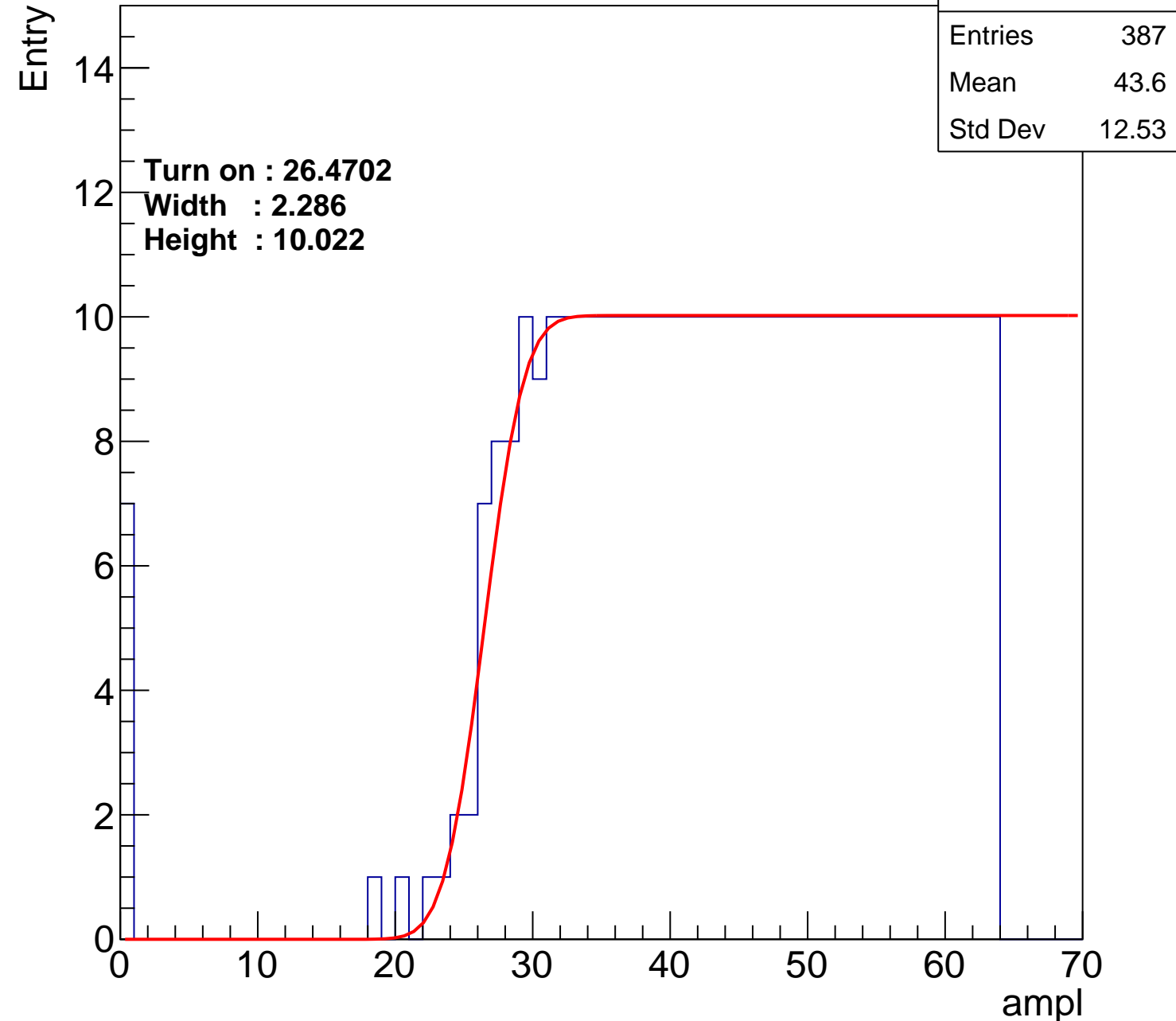
Width : 2.286

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.8666

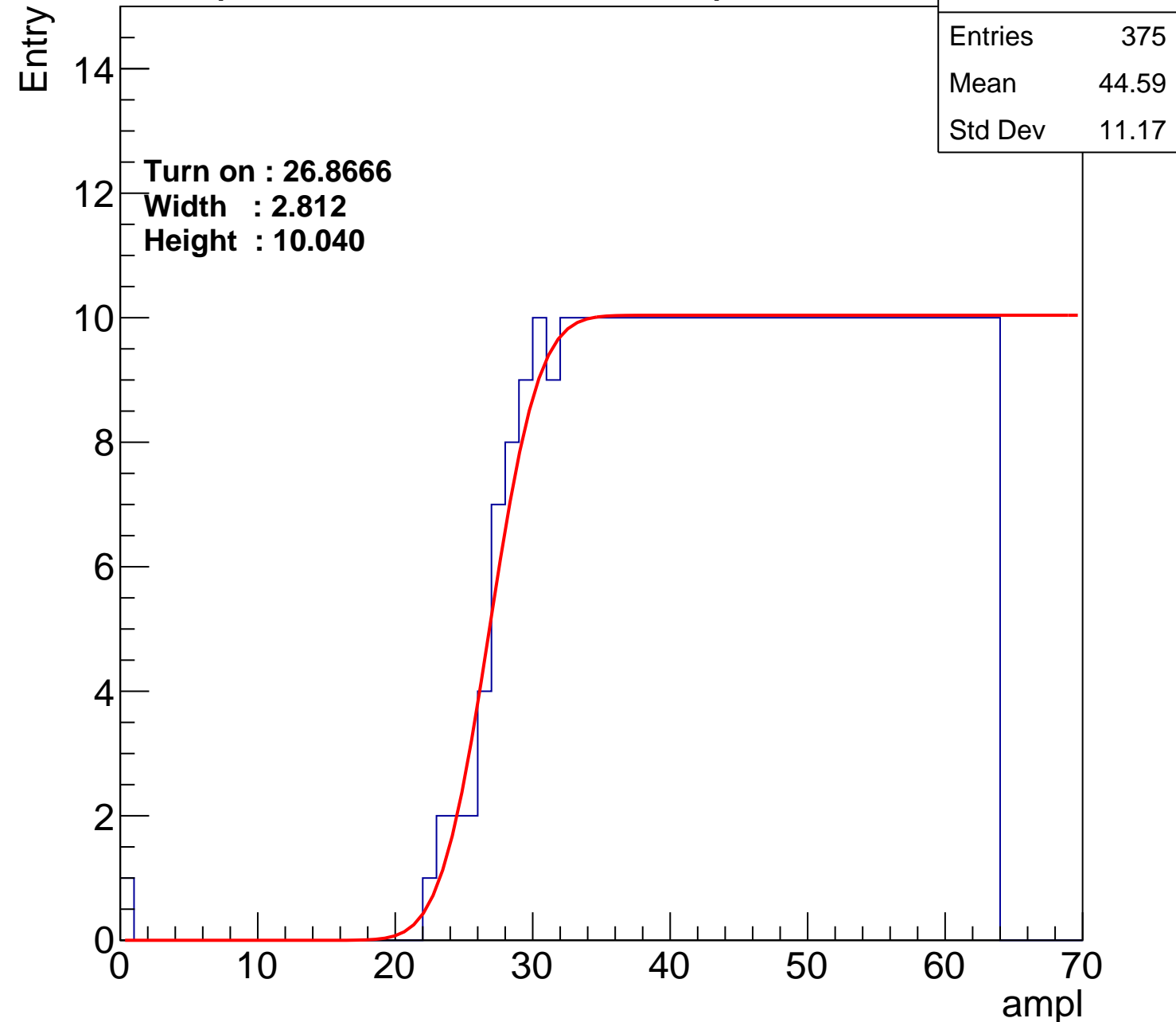
Width : 2.812

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch89

calib_packv5_042523_0143.root, FC#13, port D2

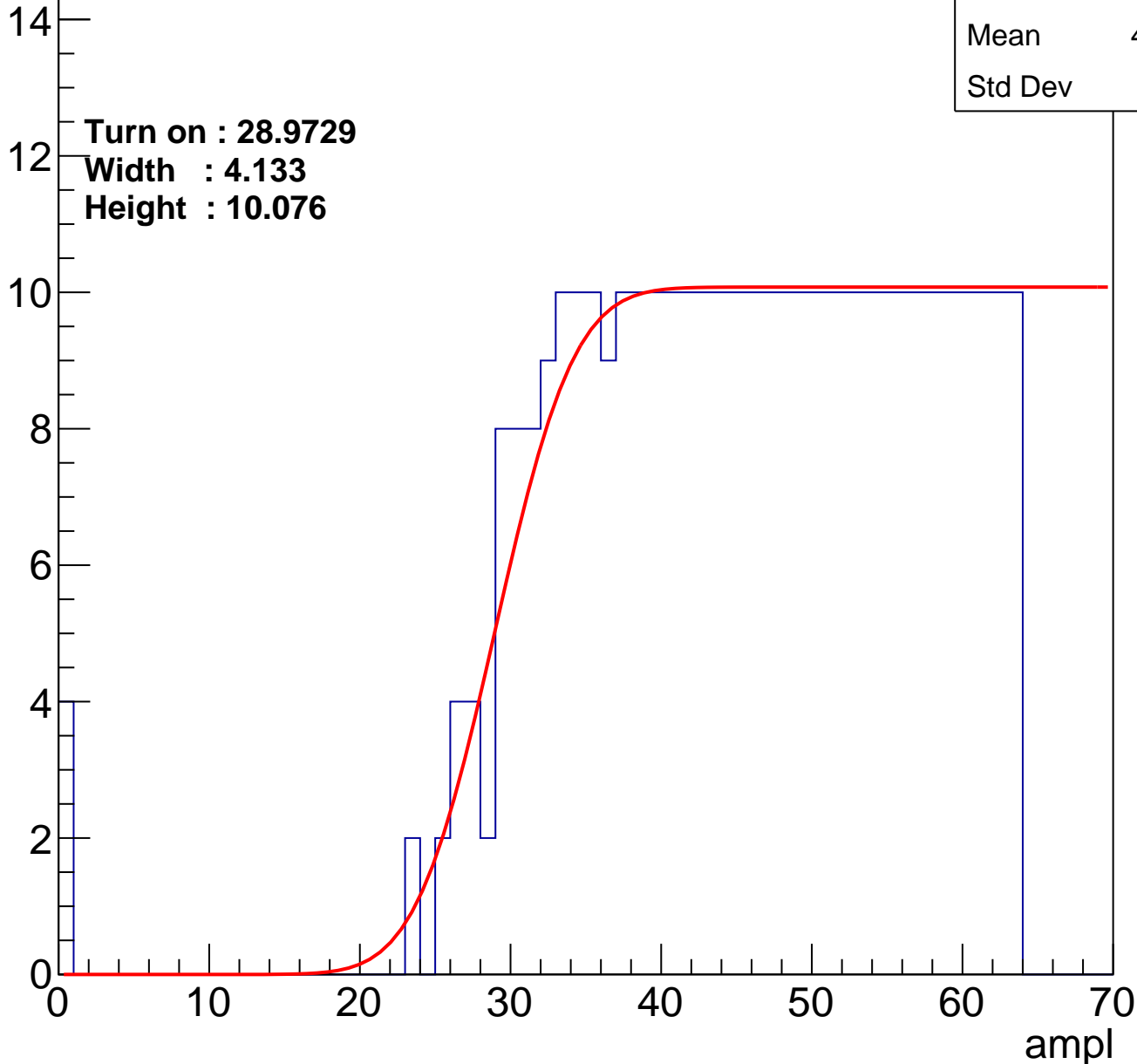
Entries	360
Mean	45.04
Std Dev	11.5

Turn on : 28.9729

Width : 4.133

Height : 10.076

Entry



B1L003S, U6-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.2232

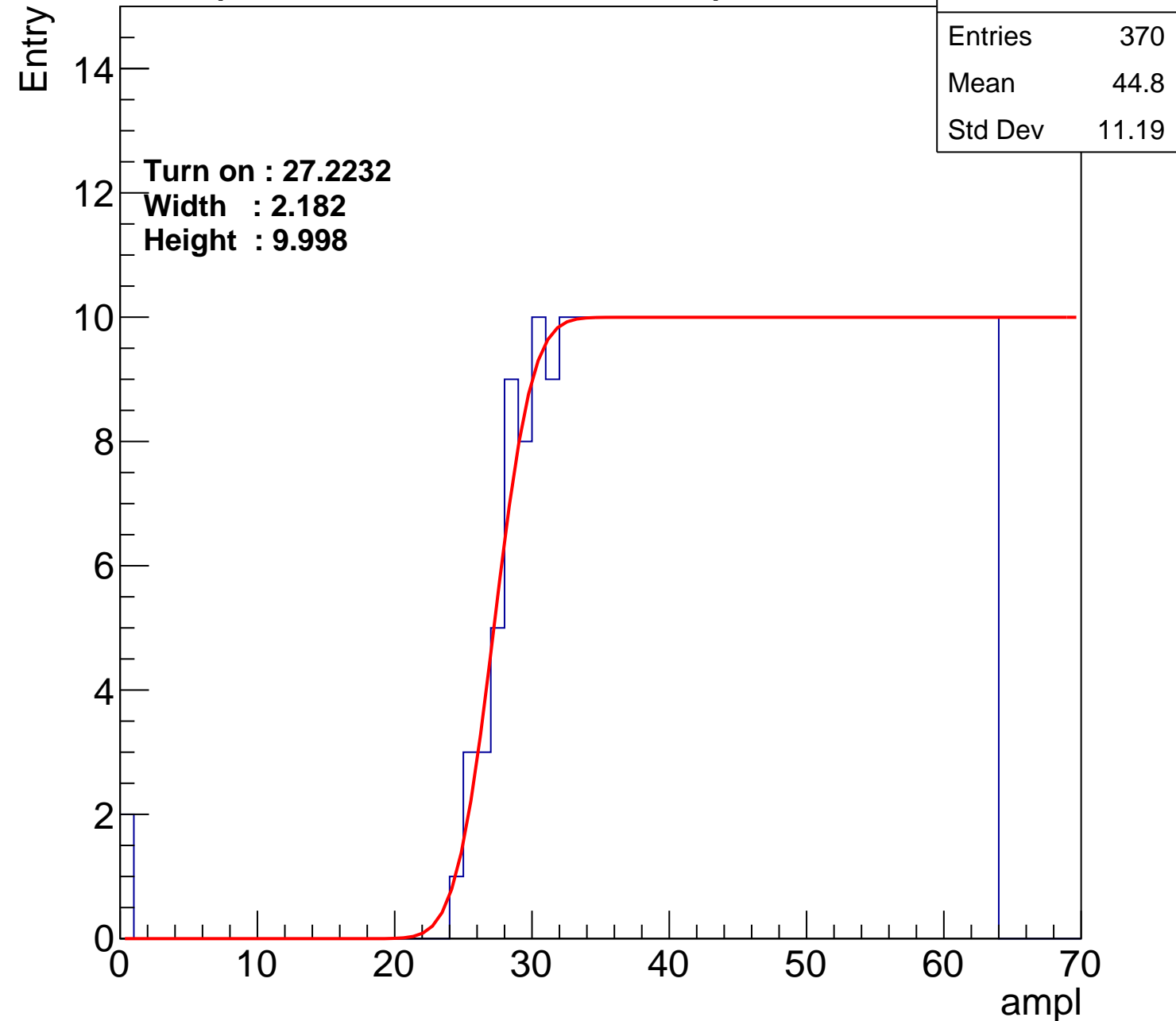
Width : 2.182

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch91

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 27.4692

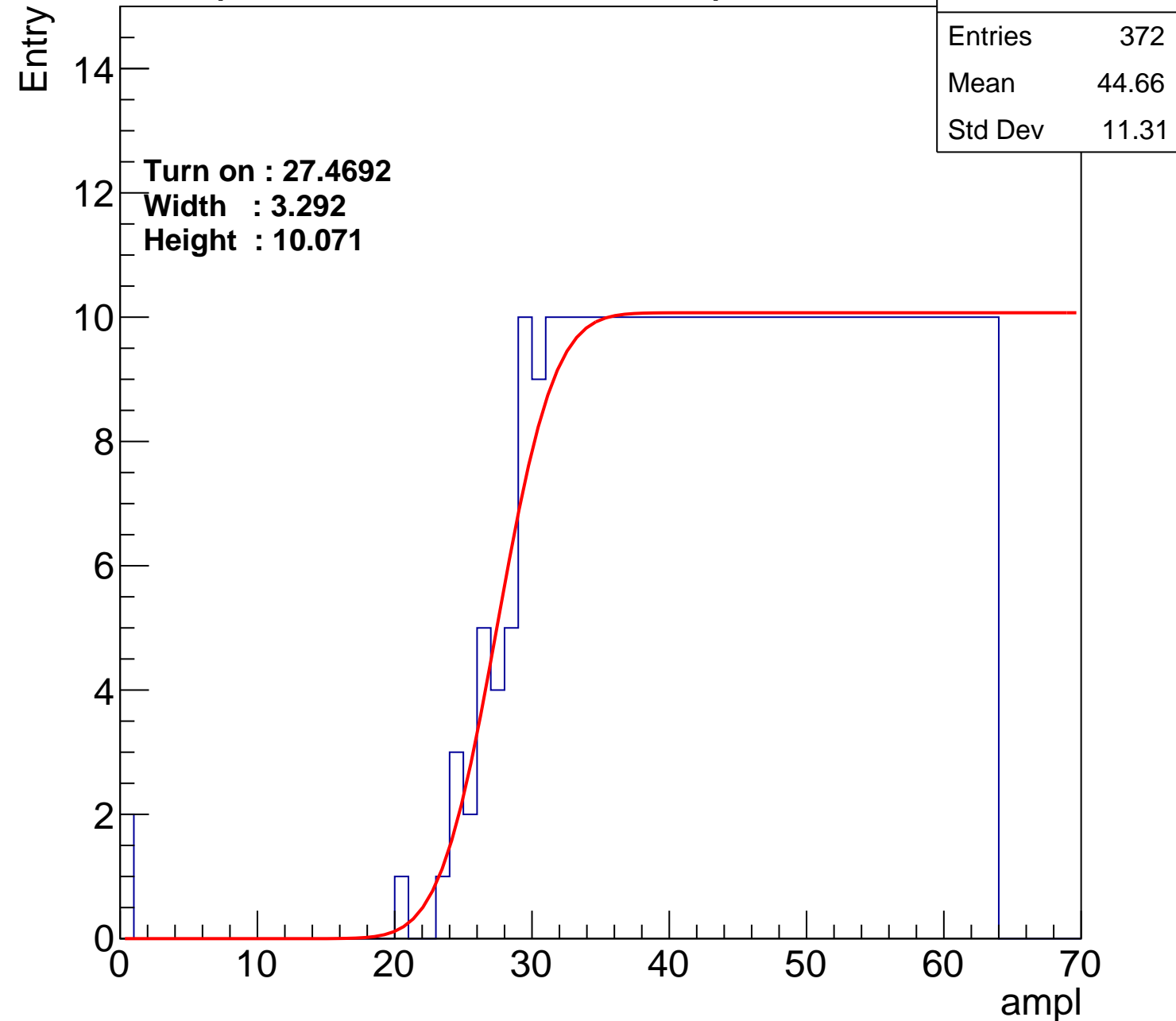
Width : 3.292

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 26.7273

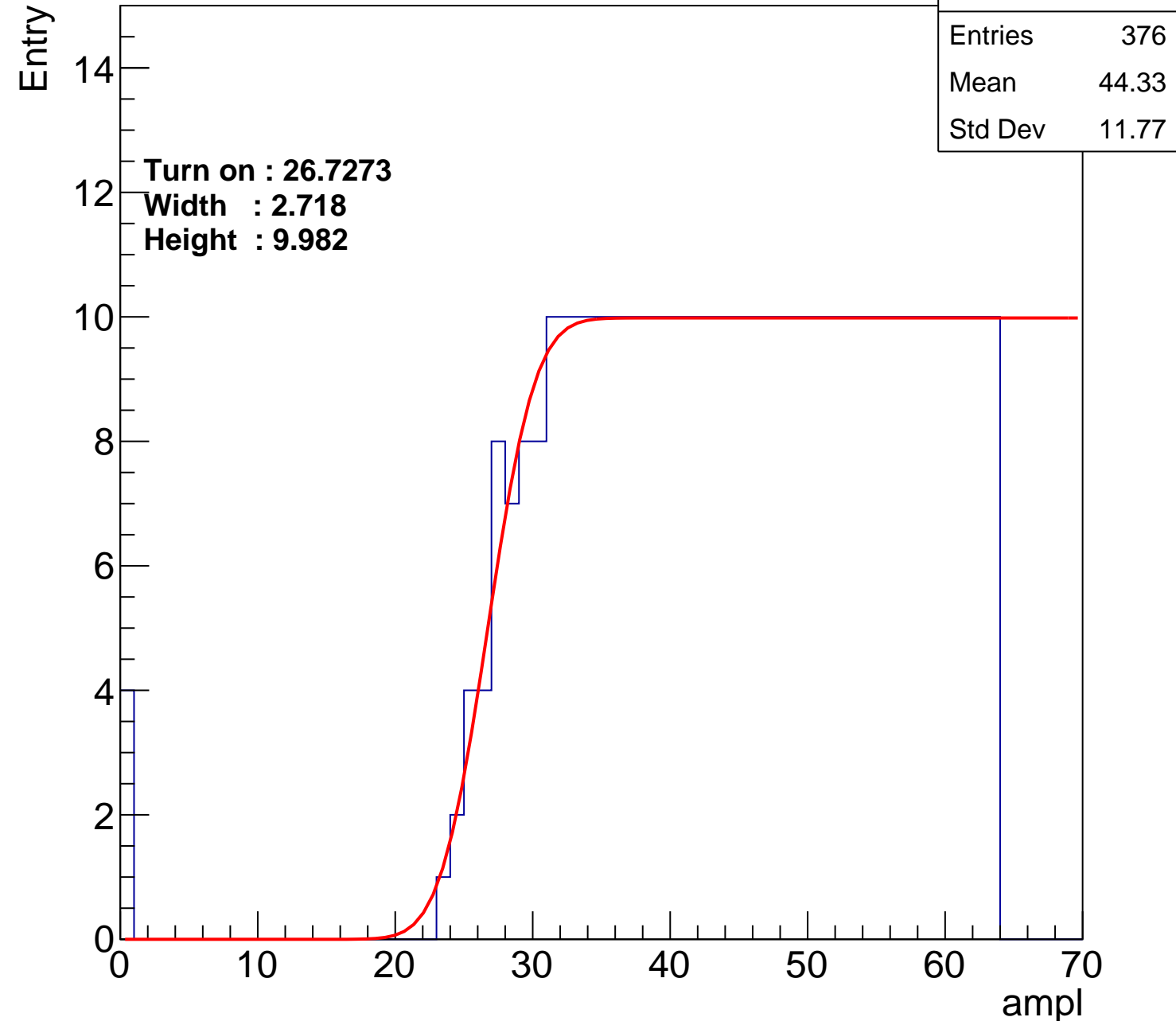
Width : 2.718

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.96
Std Dev	11

Turn on : 27.4137

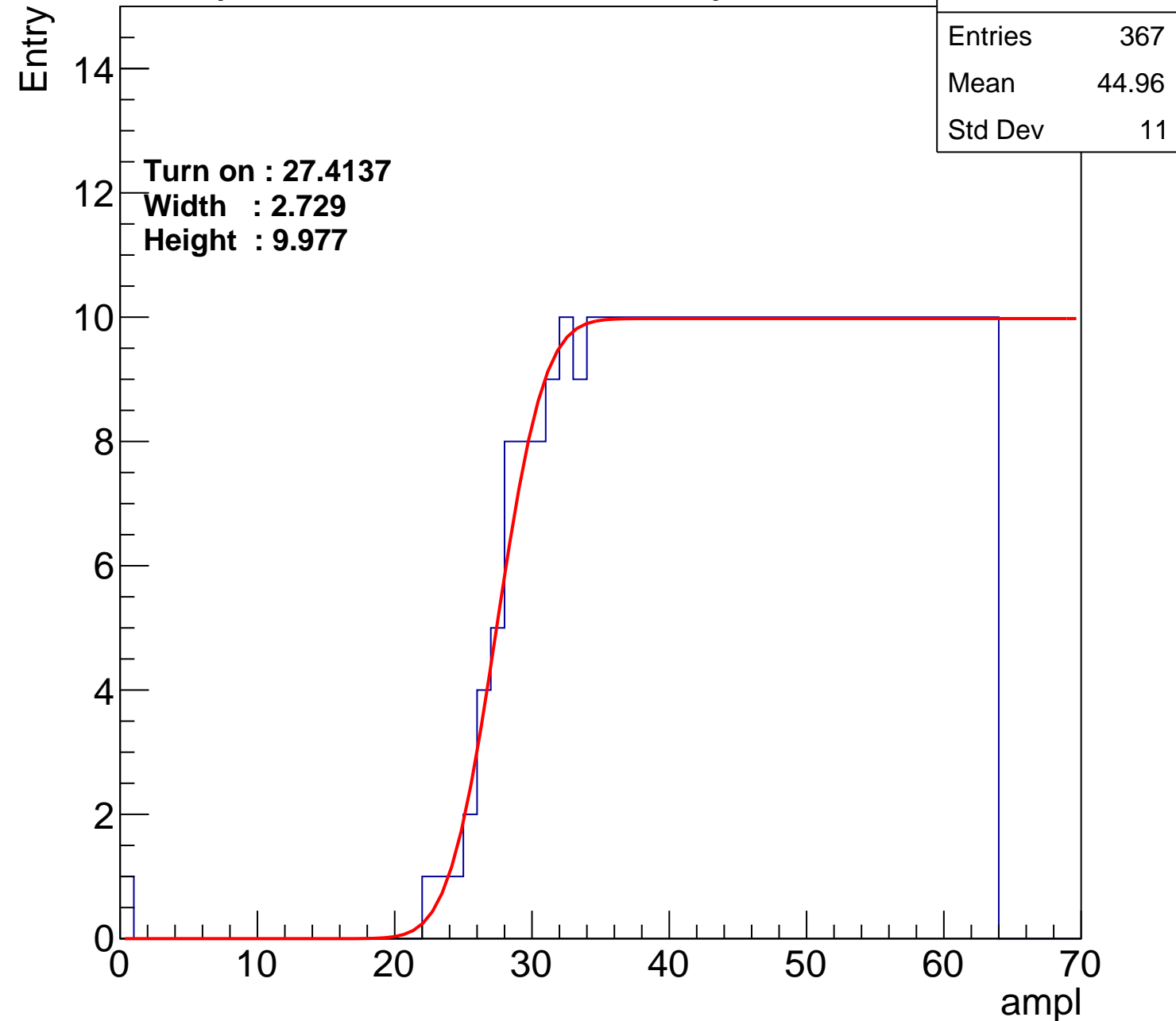
Width : 2.729

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch94

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.35
Std Dev	11.65

Turn on : 26.9645

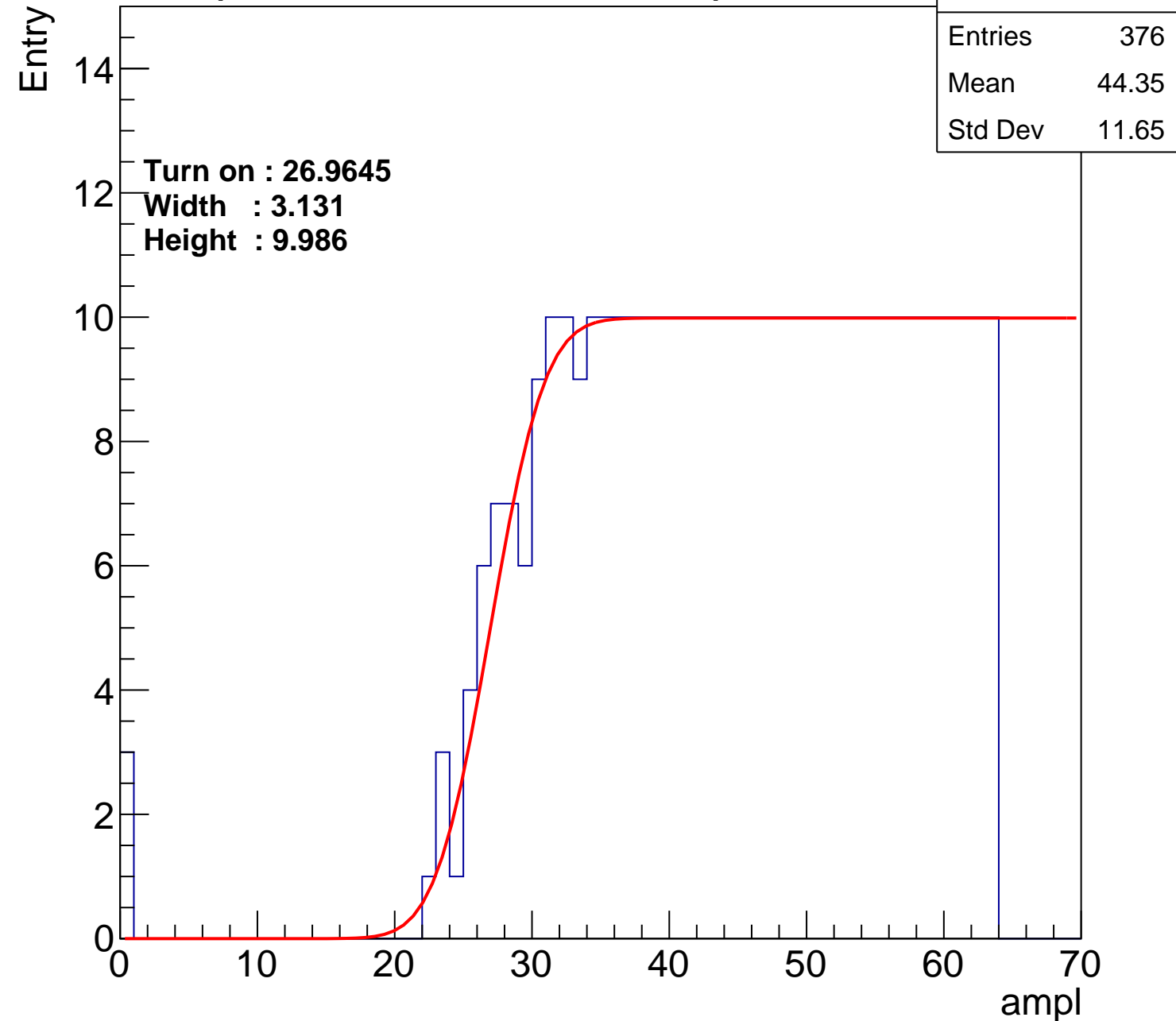
Width : 3.131

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch95

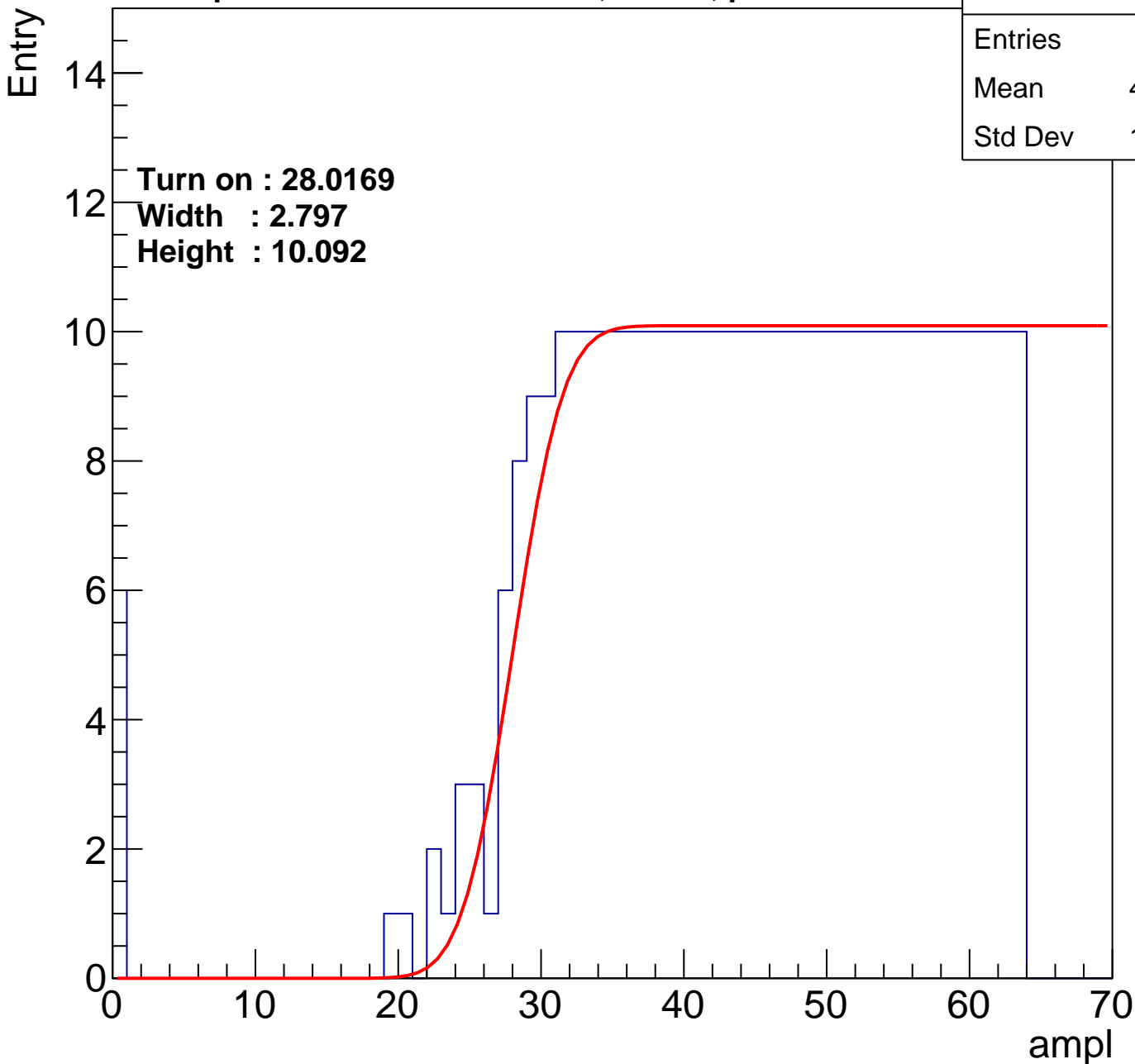
calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	43.96
Std Dev	12.28

Turn on : 28.0169

Width : 2.797

Height : 10.092



B1L003S, U6-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.85
Std Dev	11.02

Turn on : 27.0936

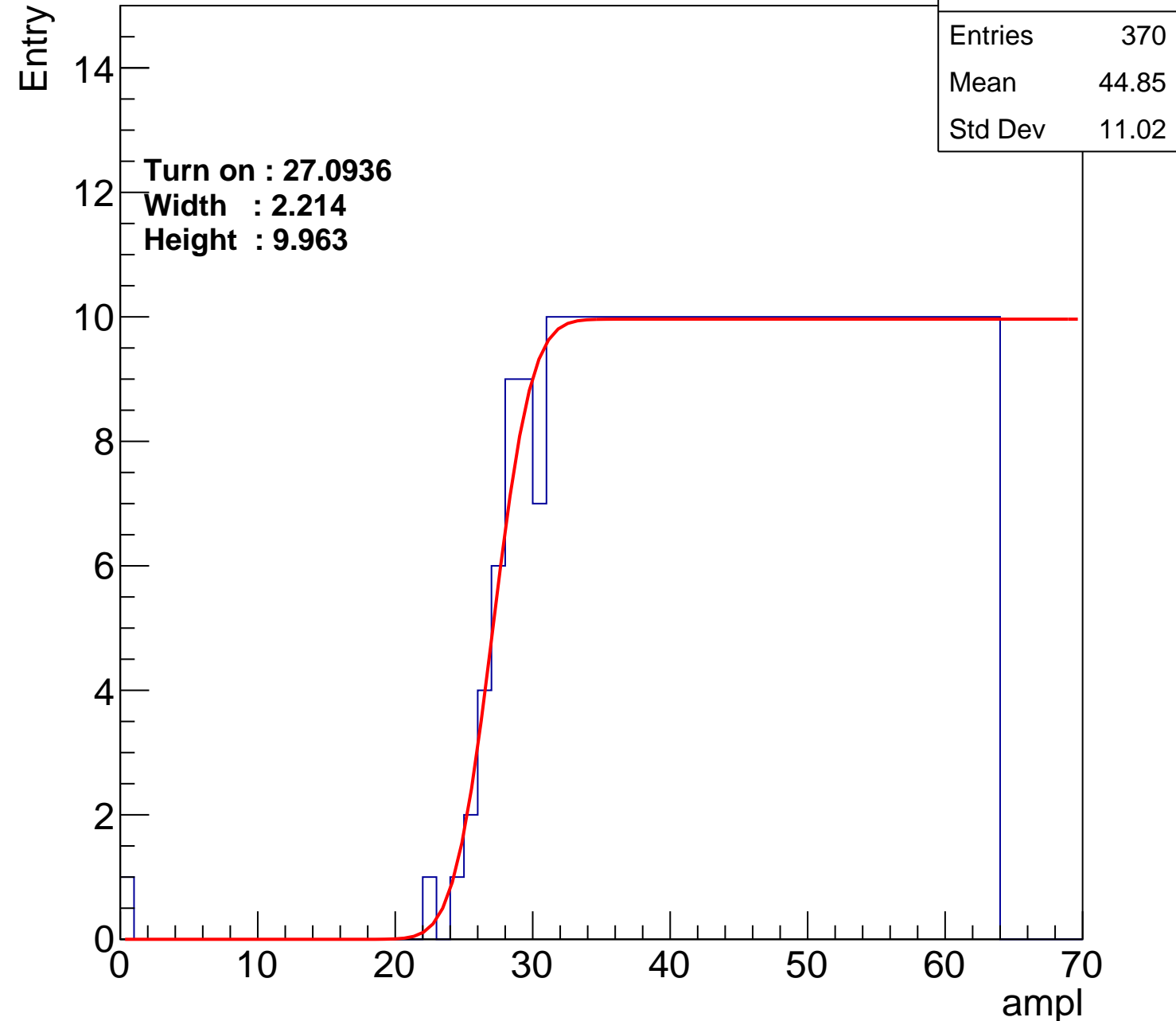
Width : 2.214

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch97

calib_packv5_042523_0143.root, FC#13, port D2

Entries	351
Mean	45.72
Std Dev	10.61

Turn on : 28.7292

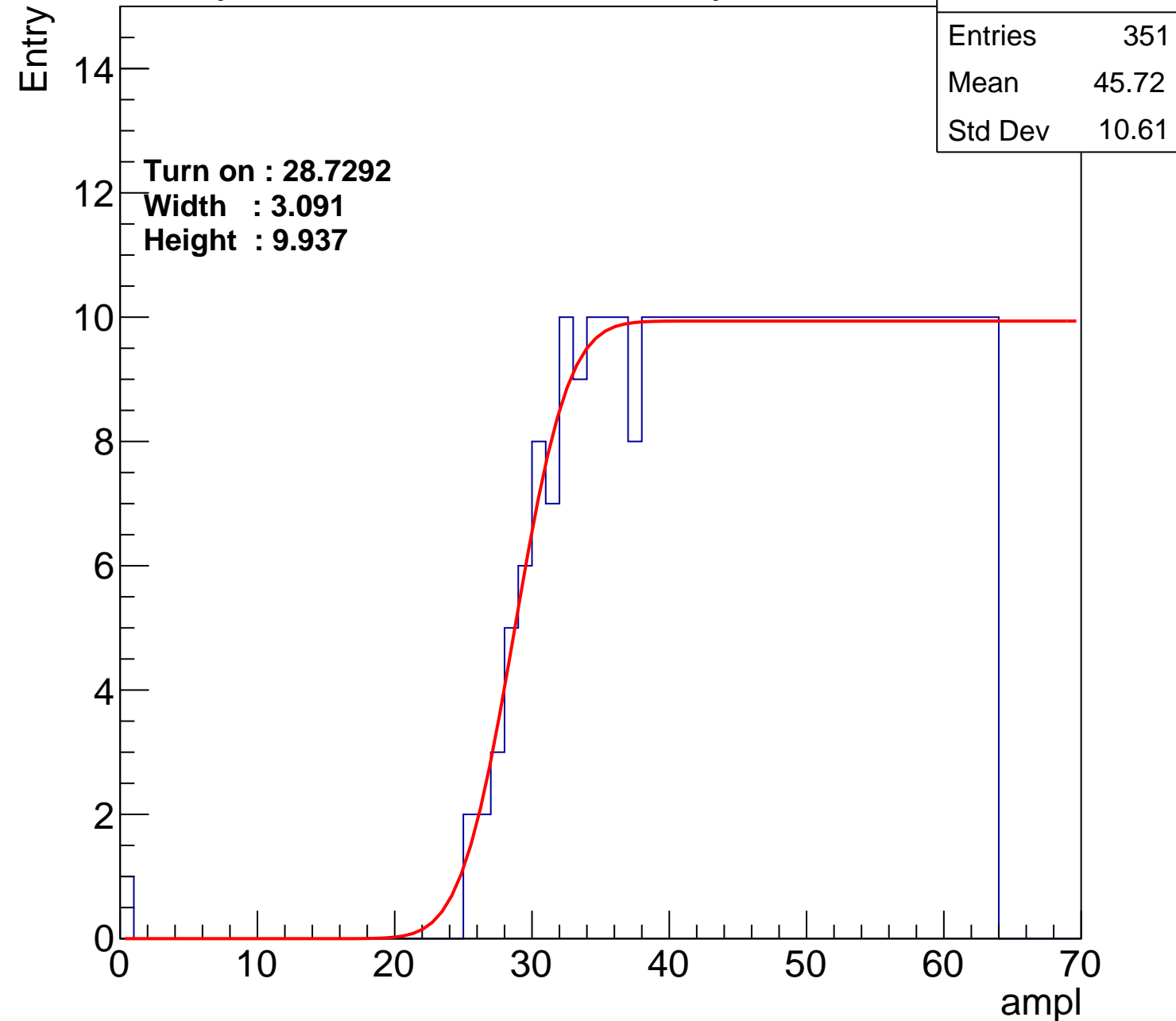
Width : 3.091

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.24
Std Dev	11.72

Turn on : 26.5501

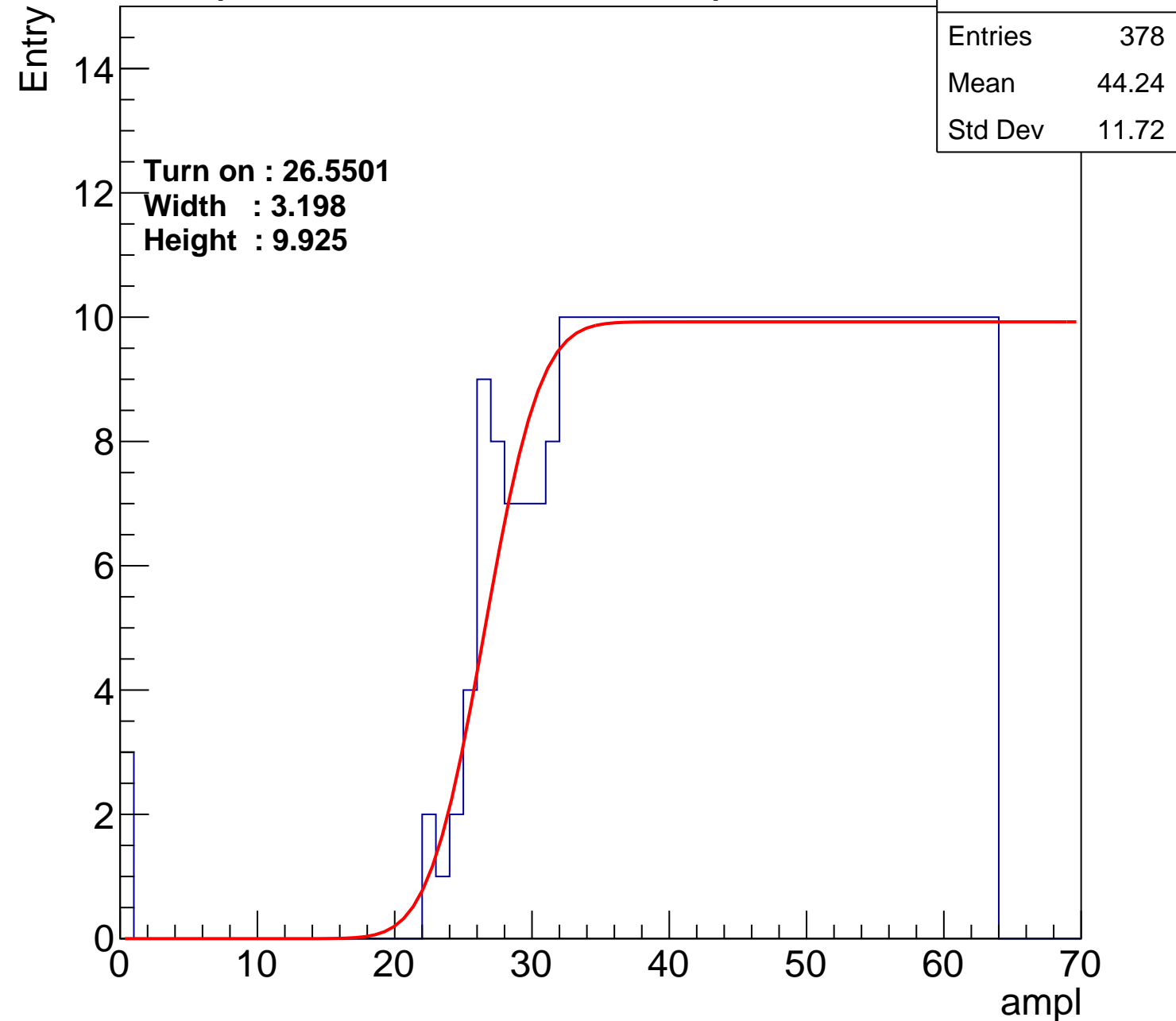
Width : 3.198

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch99

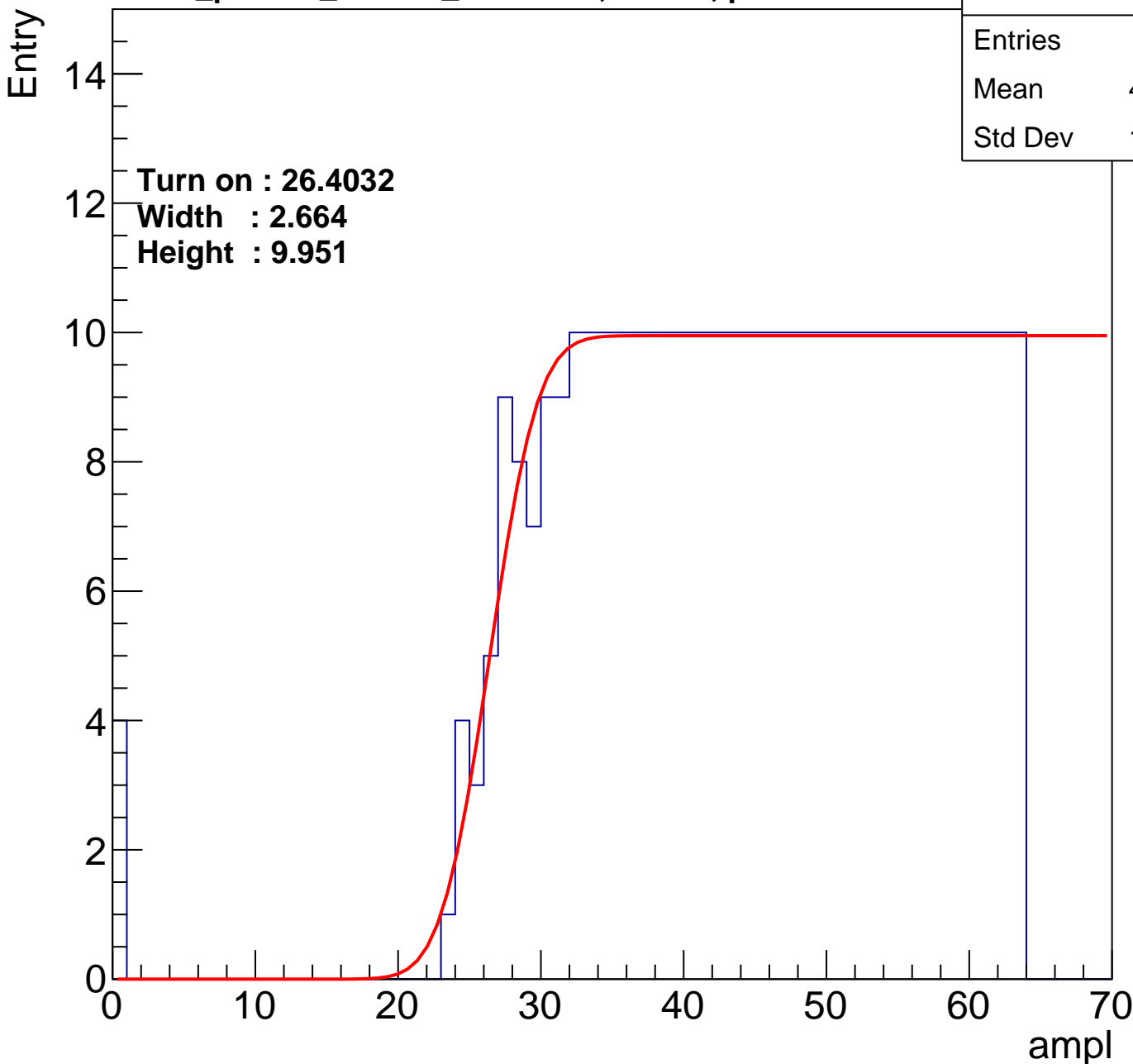
calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.18
Std Dev	11.85

Turn on : 26.4032

Width : 2.664

Height : 9.951



B1L003S, U6-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 27.7945

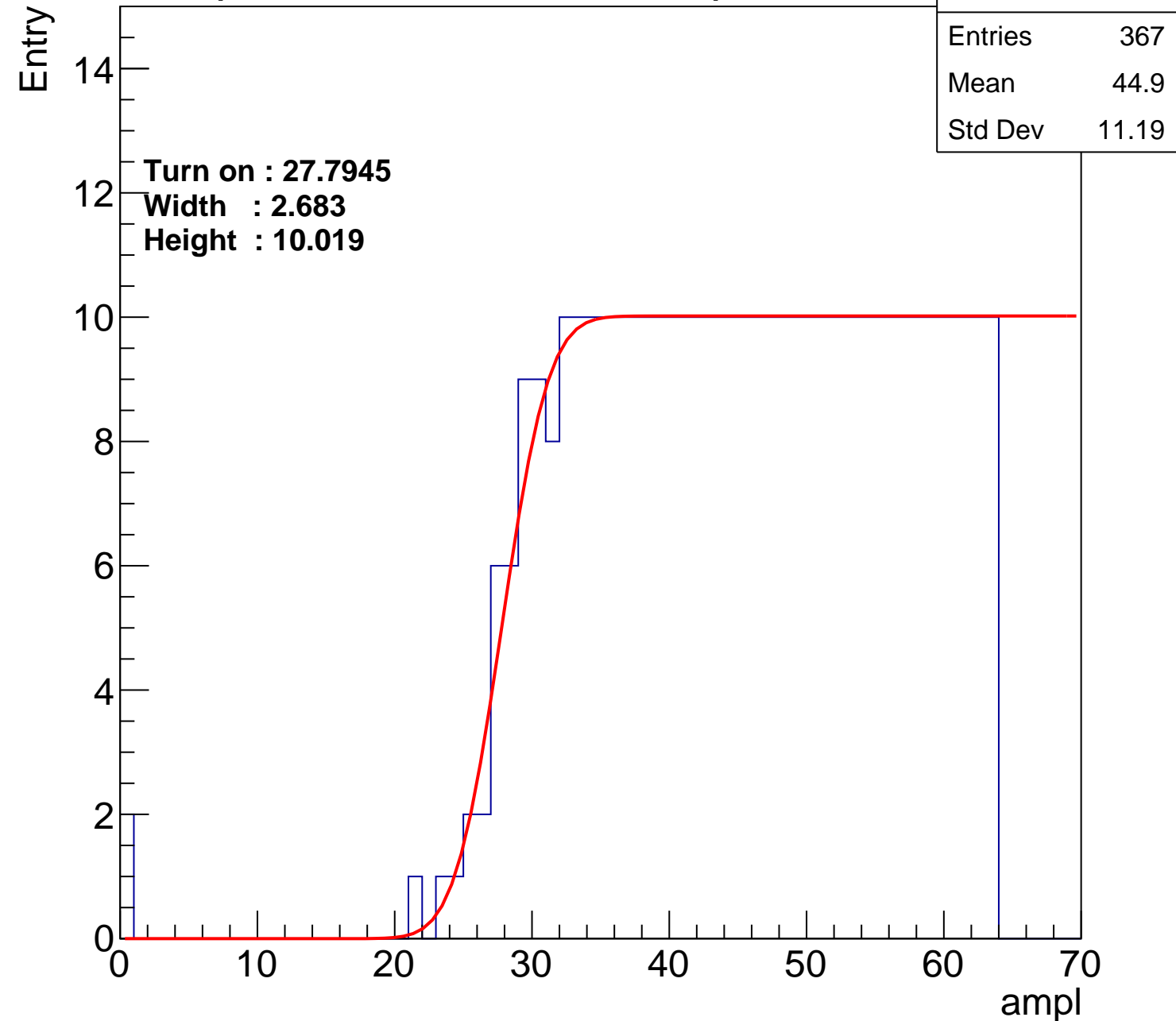
Width : 2.683

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch101

calib_packv5_042523_0143.root, FC#13, port D2

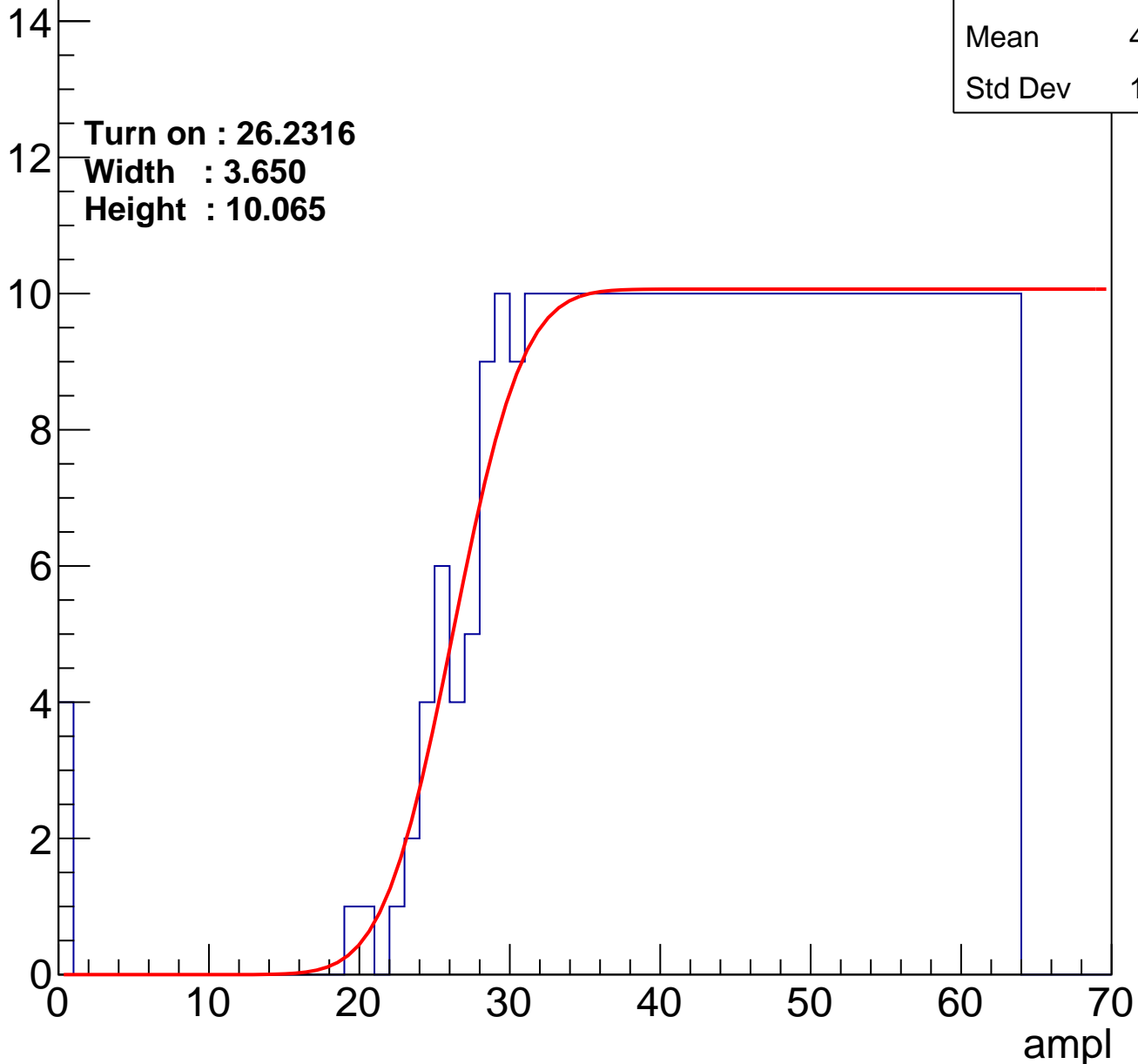
Entries	386
Mean	43.82
Std Dev	12.05

Turn on : 26.2316

Width : 3.650

Height : 10.065

Entry



B1L003S, U6-ch102

calib_packv5_042523_0143.root, FC#13, port D2

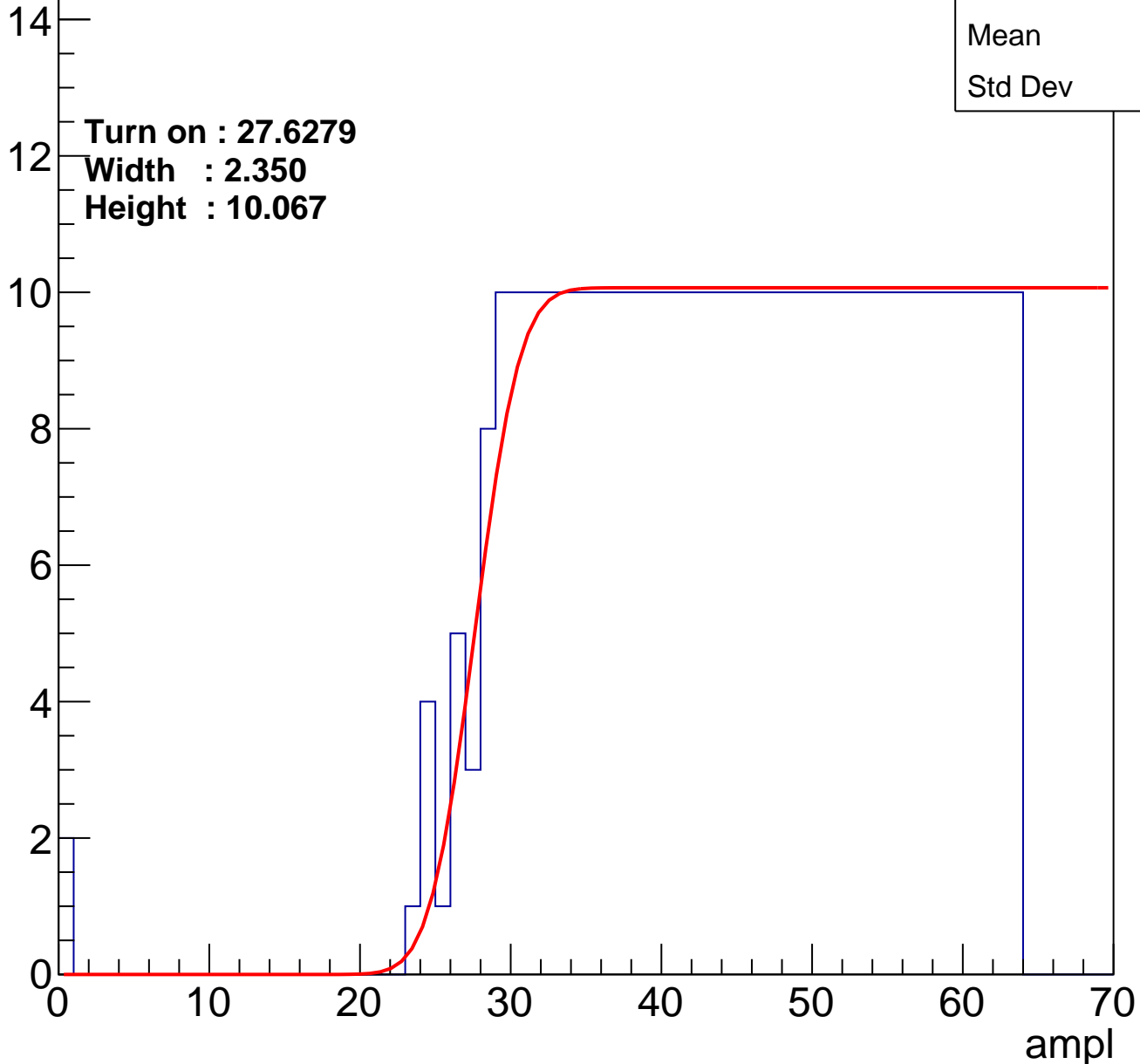
Entries	374
Mean	44.6
Std Dev	11.3

Turn on : 27.6279

Width : 2.350

Height : 10.067

Entry



B1L003S, U6-ch103

calib_packv5_042523_0143.root, FC#13, port D2

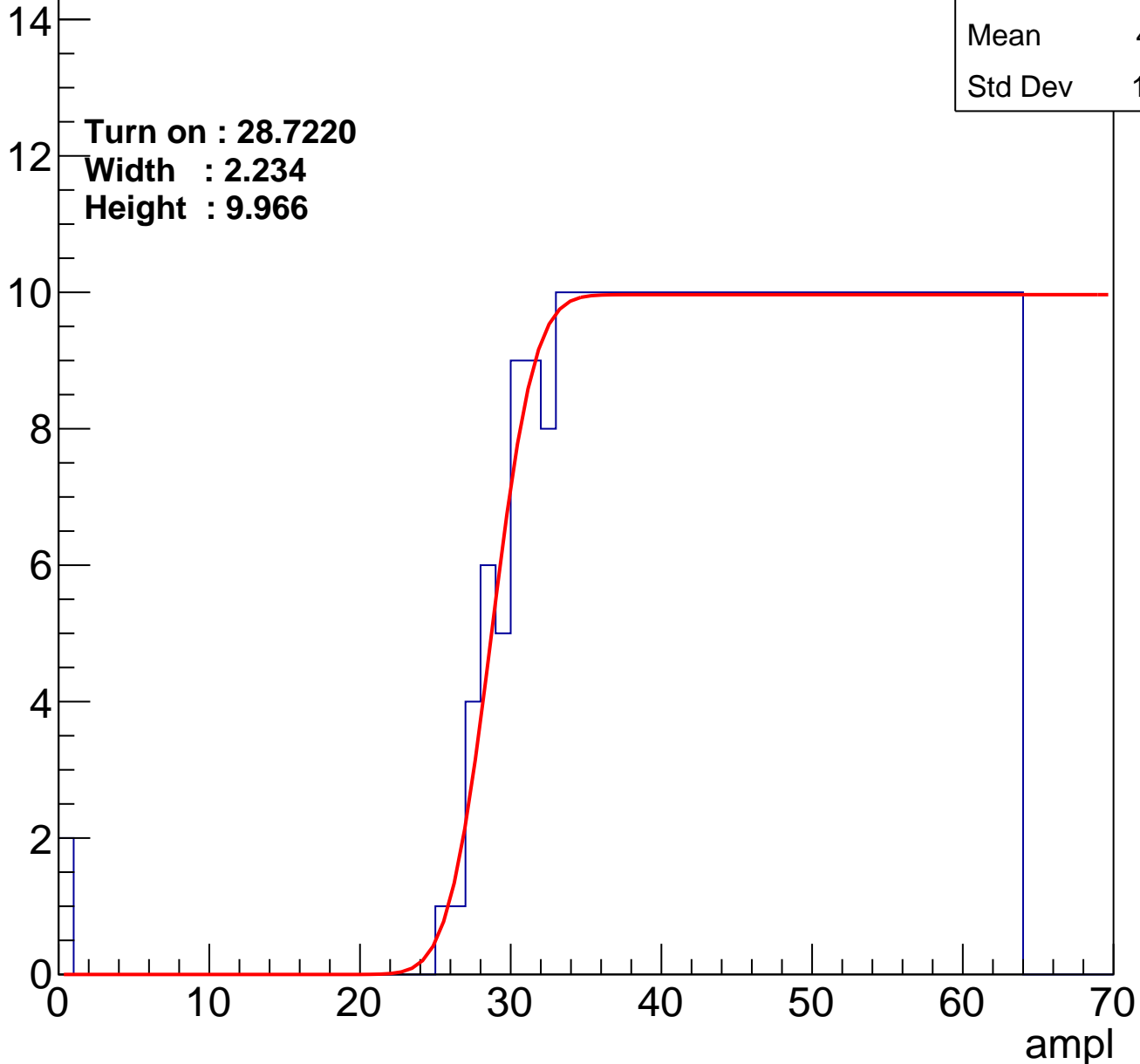
Entries	355
Mean	45.51
Std Dev	10.85

Turn on : 28.7220

Width : 2.234

Height : 9.966

Entry



B1L003S, U6-ch104

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.93
Std Dev	10.94

Turn on : 26.8135

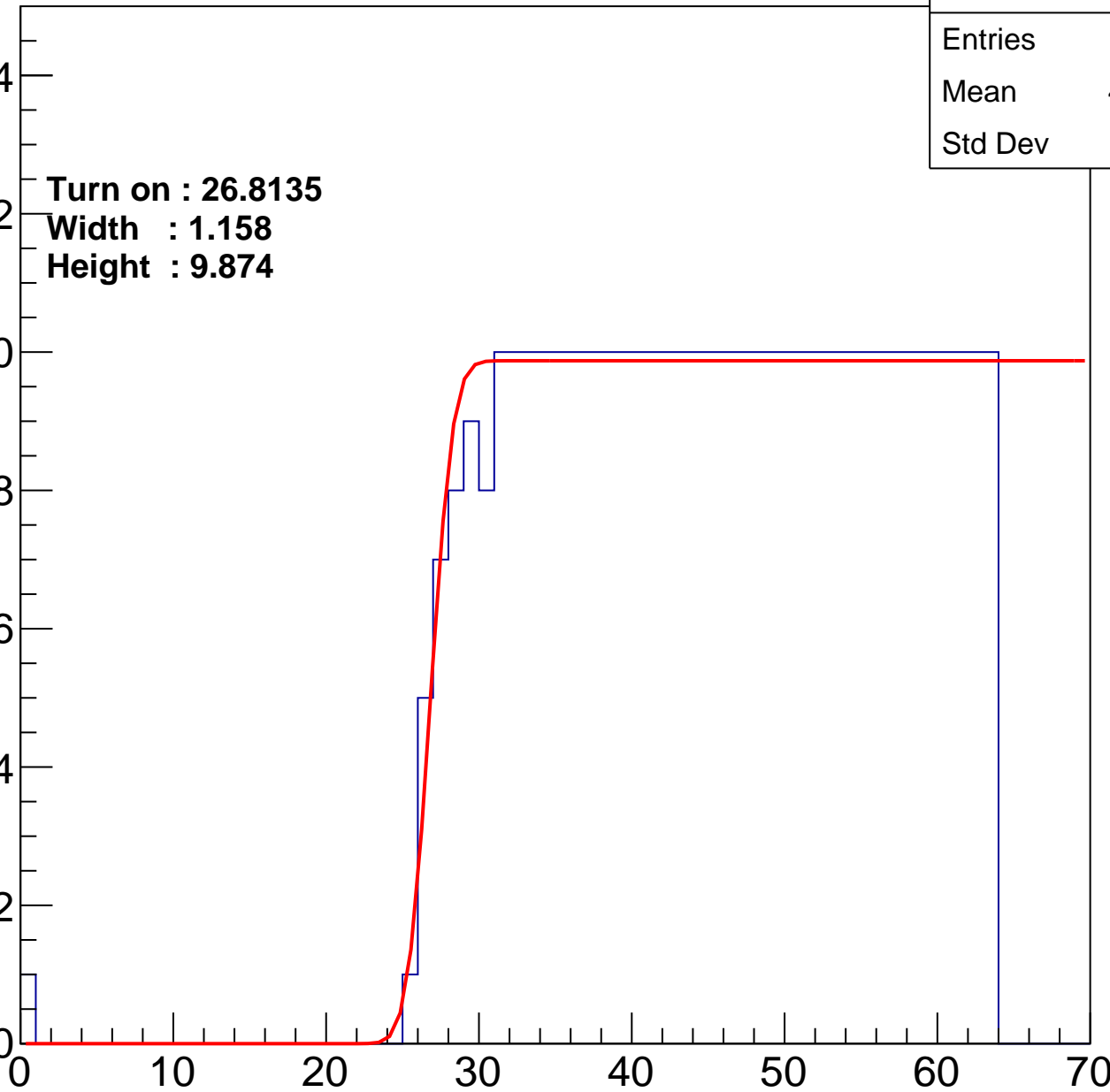
Width : 1.158

Height : 9.874

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.69
Std Dev	11.5

Turn on : 27.9906

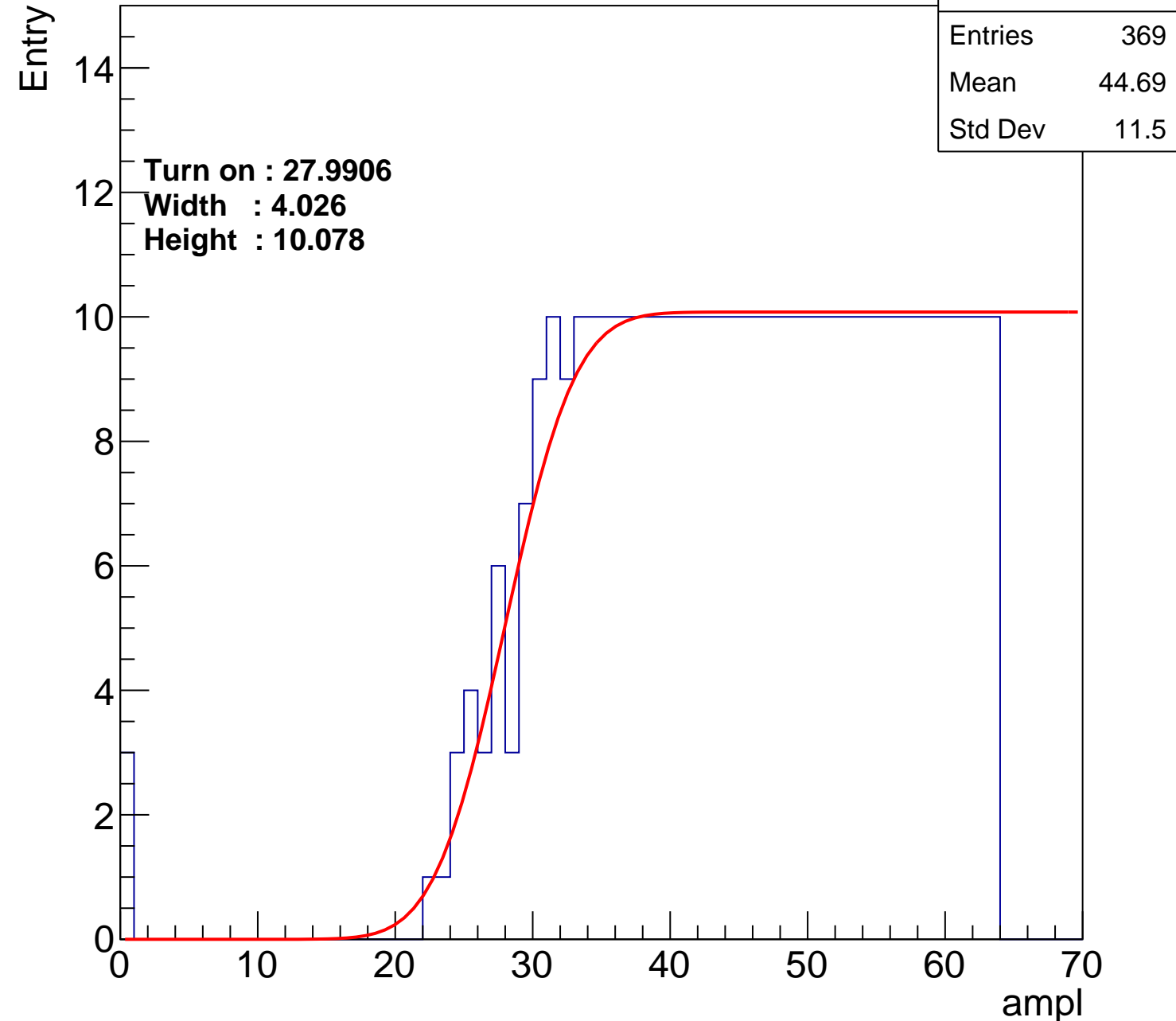
Width : 4.026

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.07
Std Dev	11.87

Turn on : 26.2467

Width : 1.794

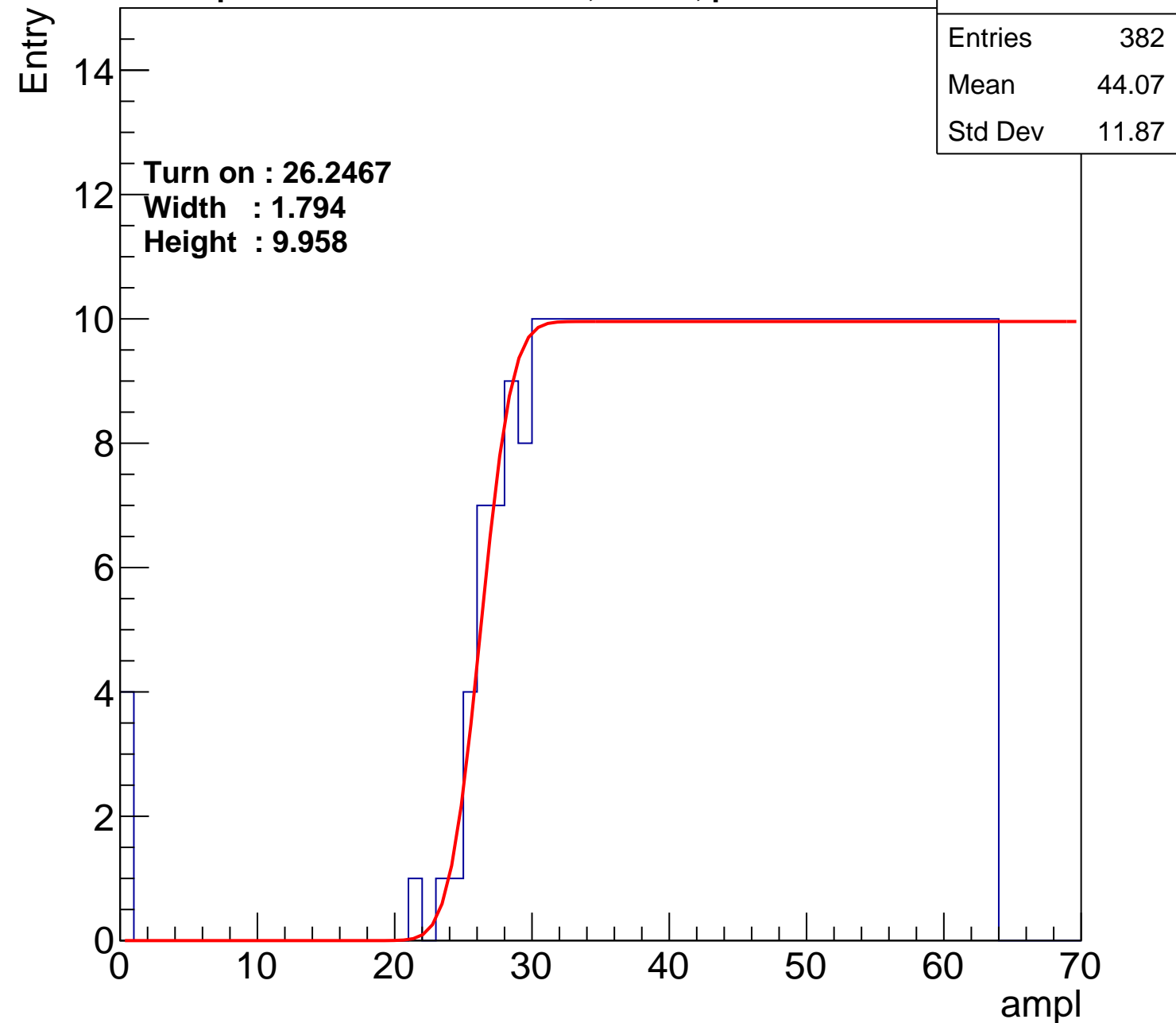
Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U6-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.57
Std Dev	11.77

Turn on : 28.0237

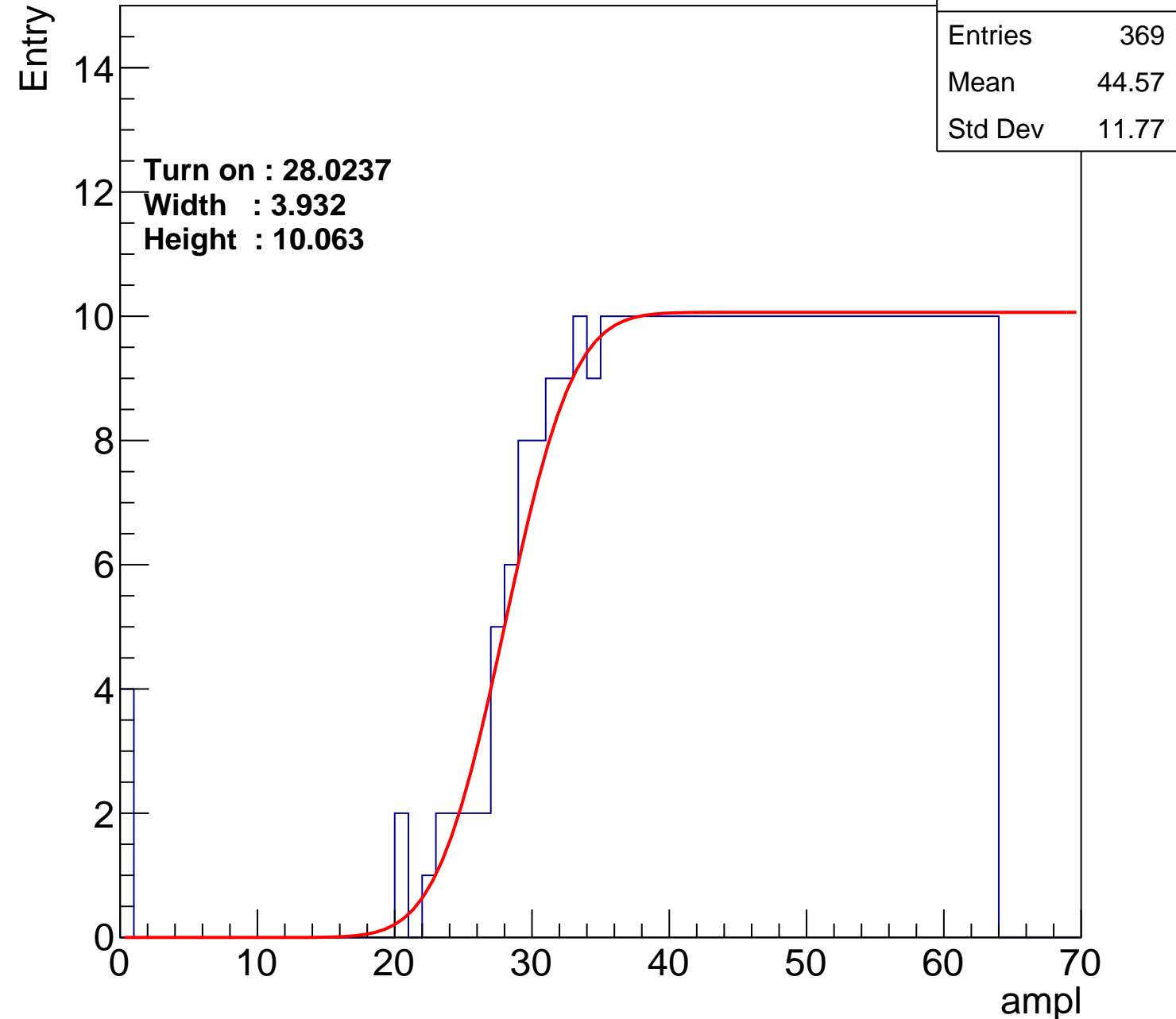
Width : 3.932

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 27.2240

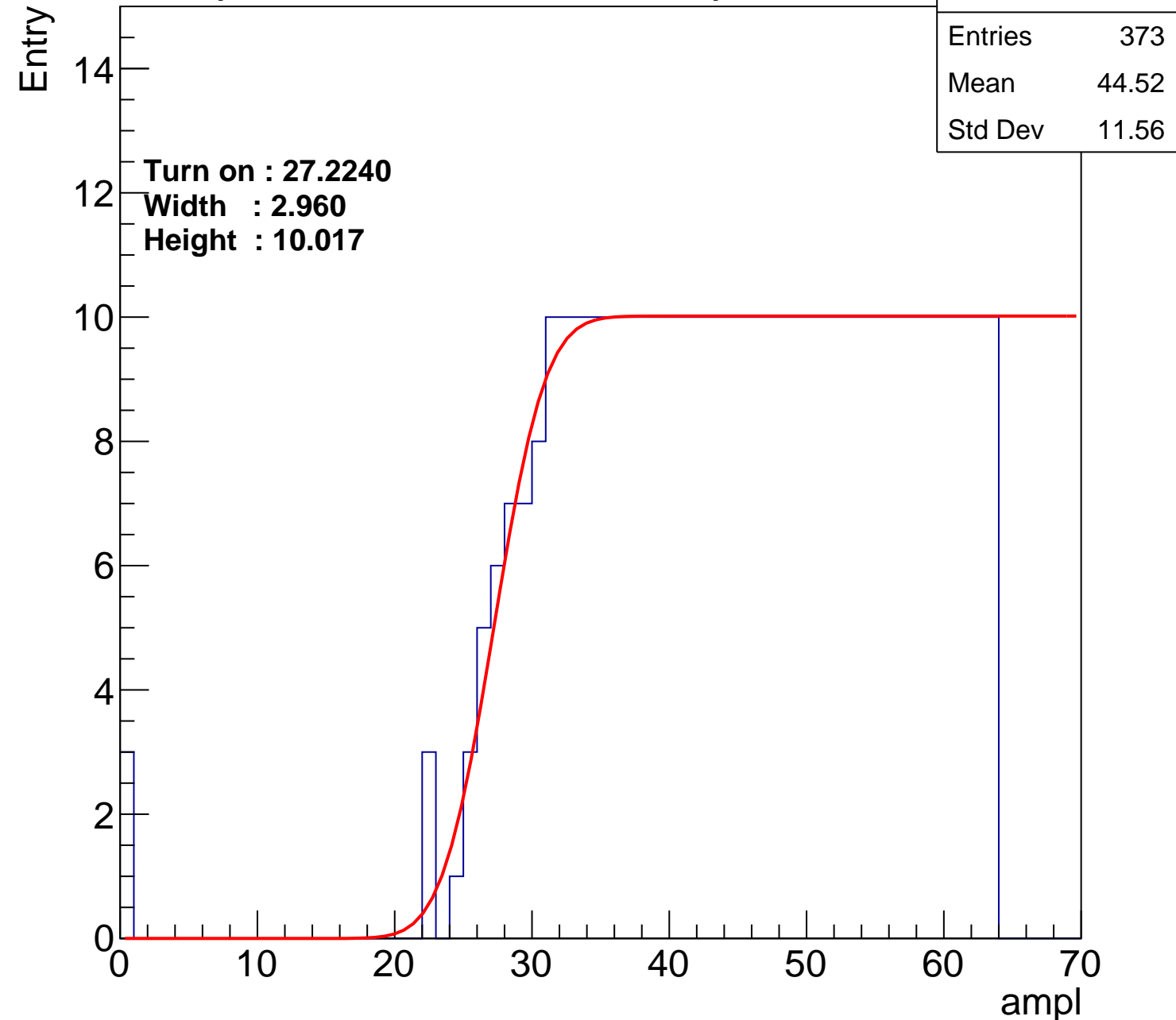
Width : 2.960

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.8
Std Dev	11.49

Turn on : 28.2213

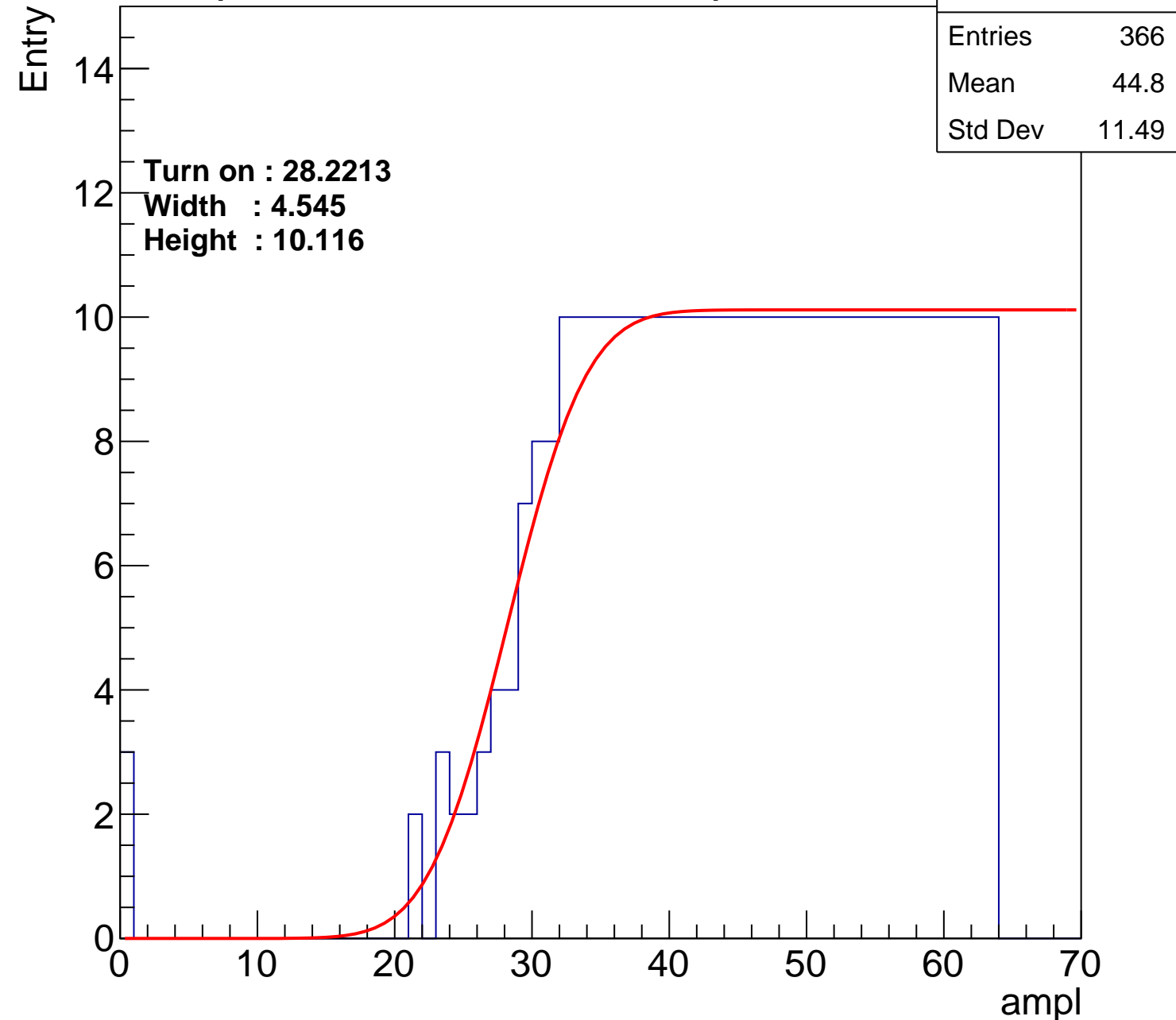
Width : 4.545

Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.18
Std Dev	10.86

Turn on : 27.8071

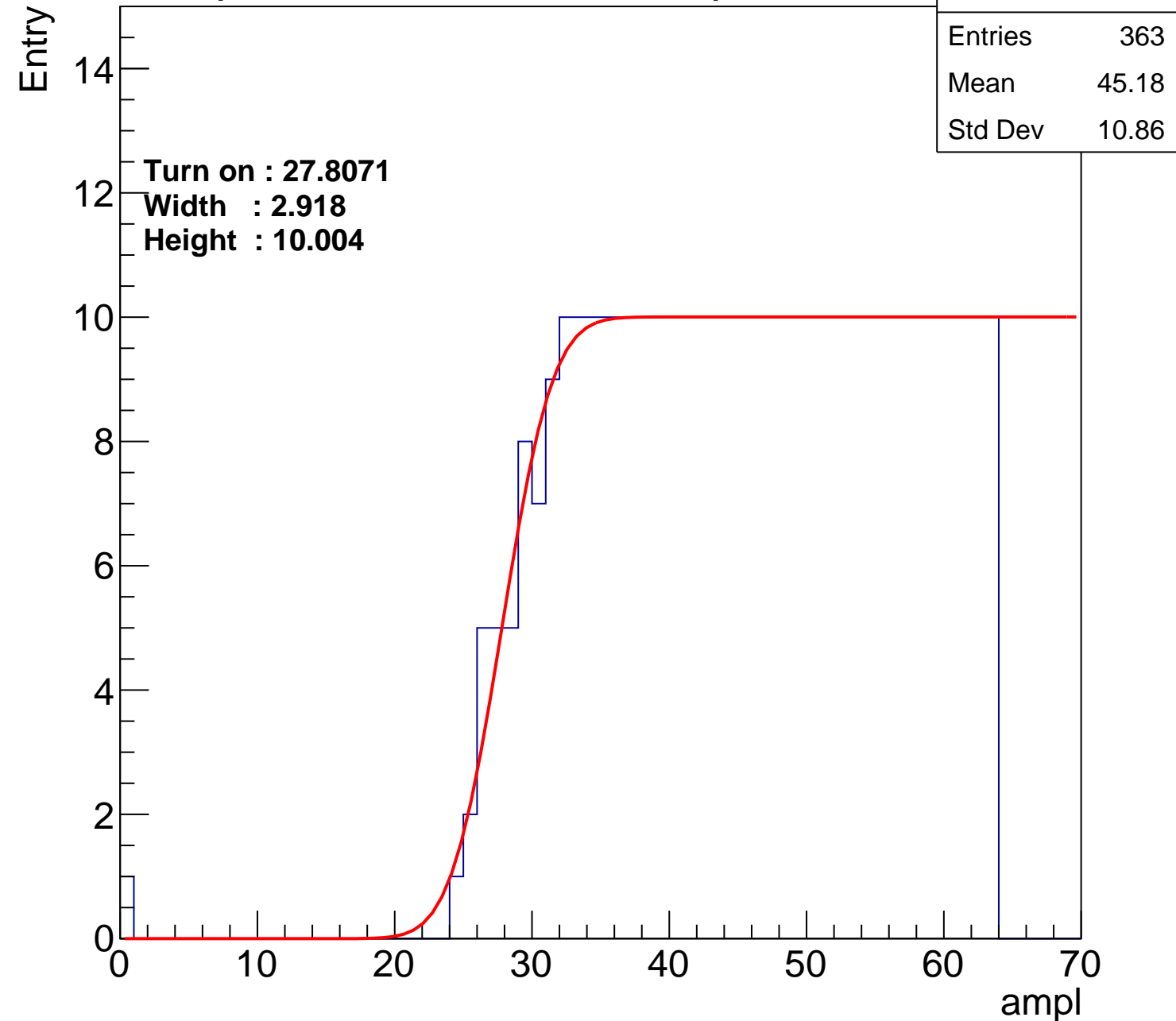
Width : 2.918

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.24
Std Dev	11.16

Turn on : 28.1062

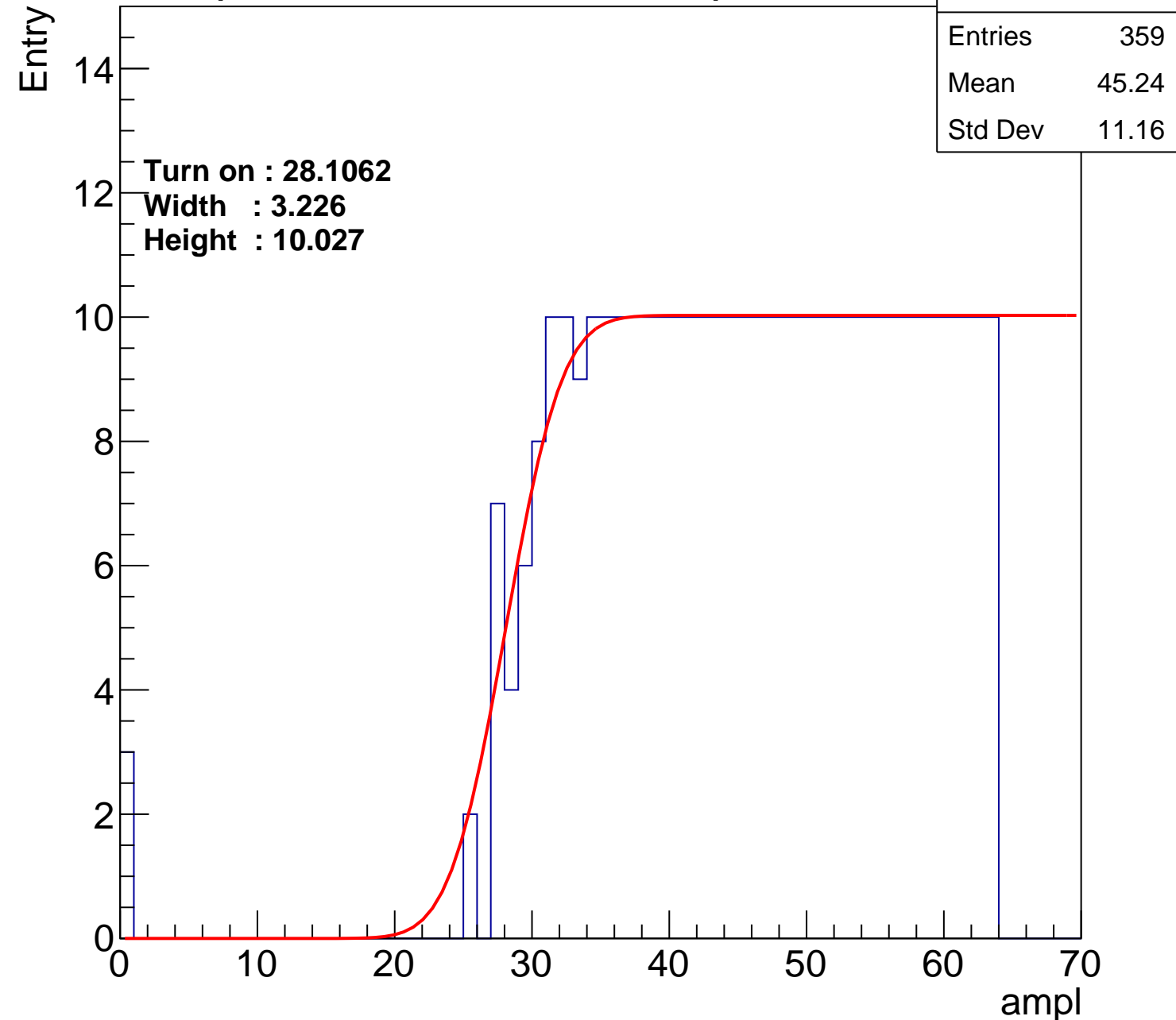
Width : 3.226

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 27.7864

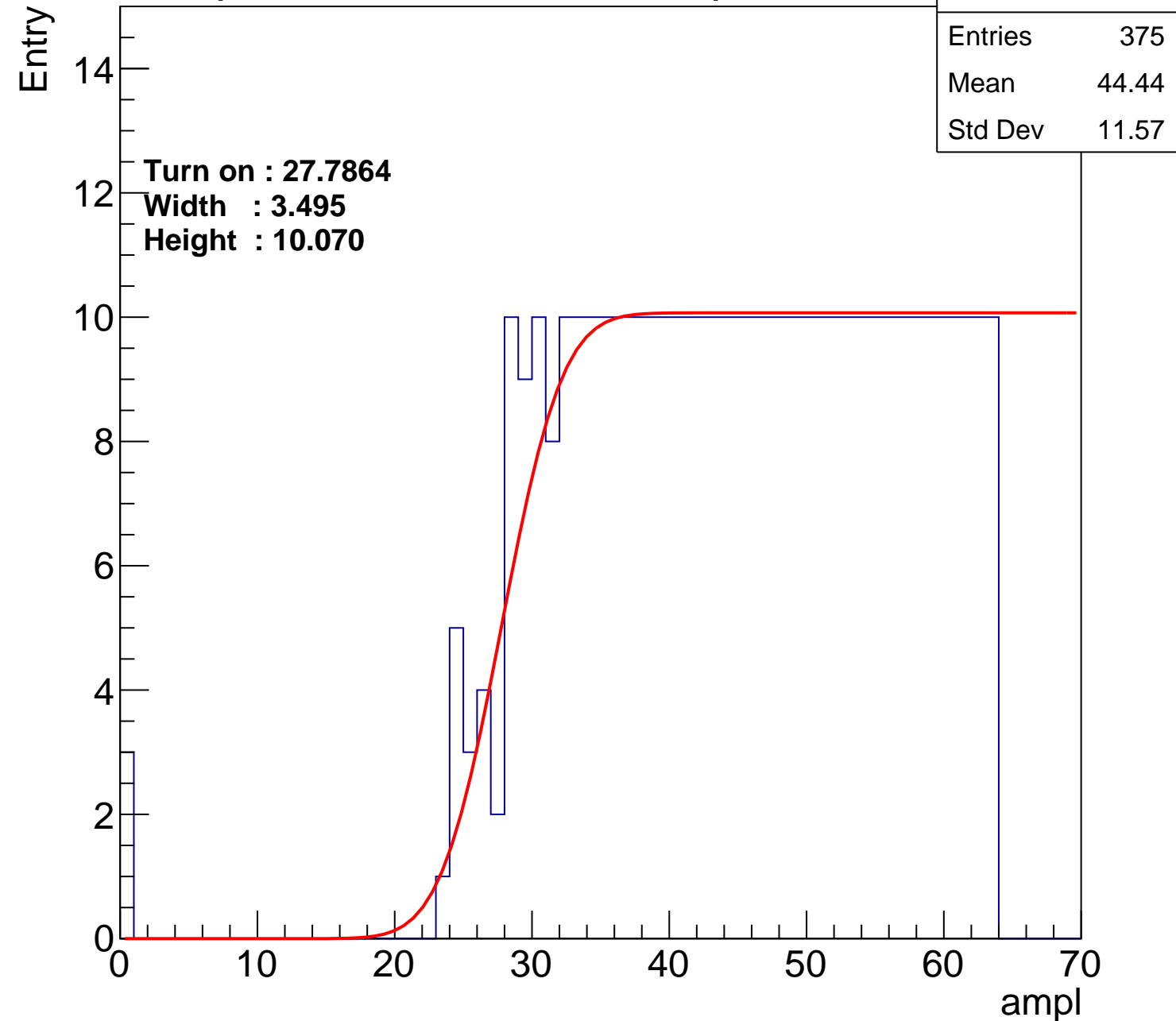
Width : 3.495

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.47
Std Dev	10.86

Turn on : 29.2206

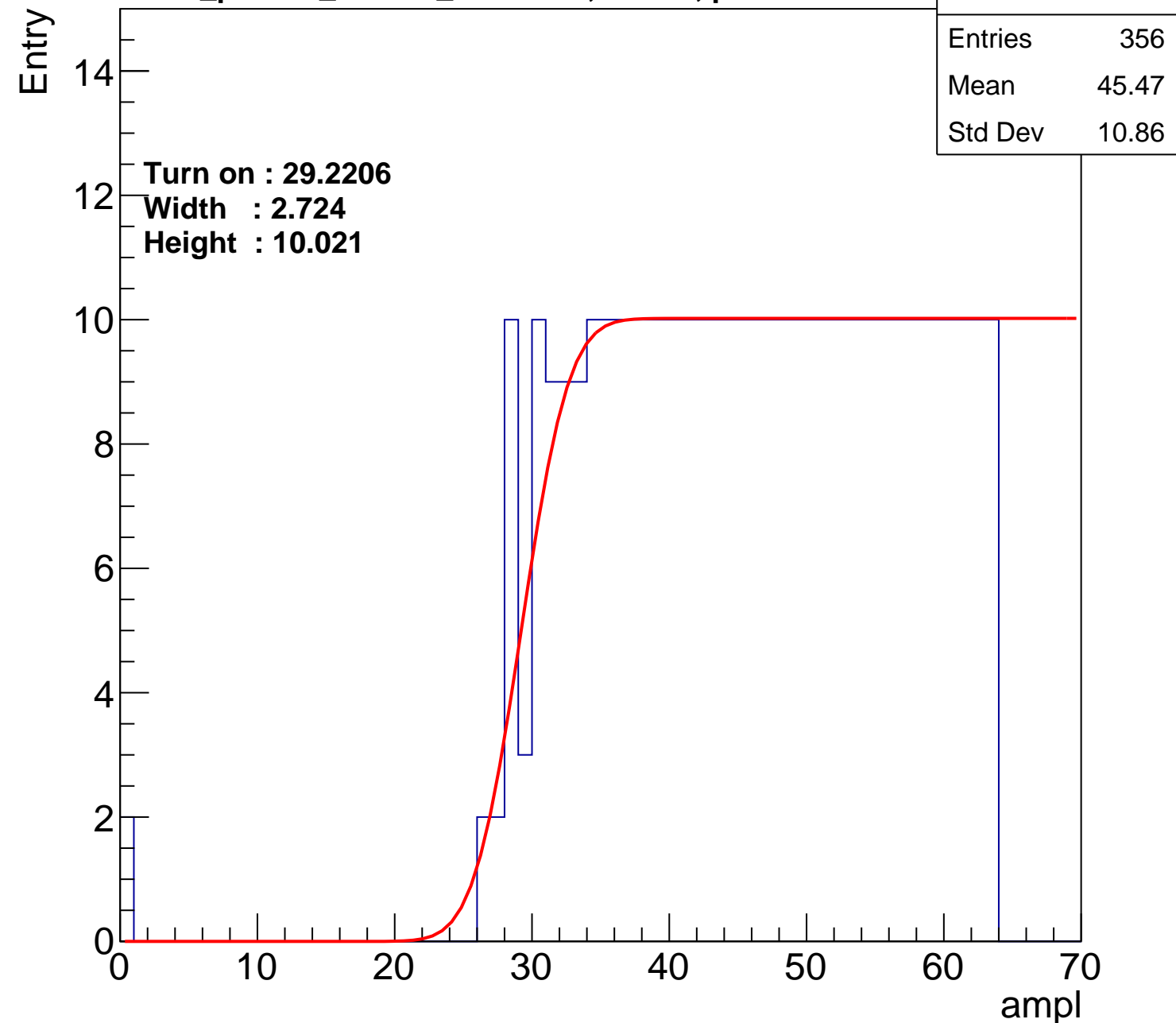
Width : 2.724

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.83
Std Dev	11.9

Turn on : 25.8393

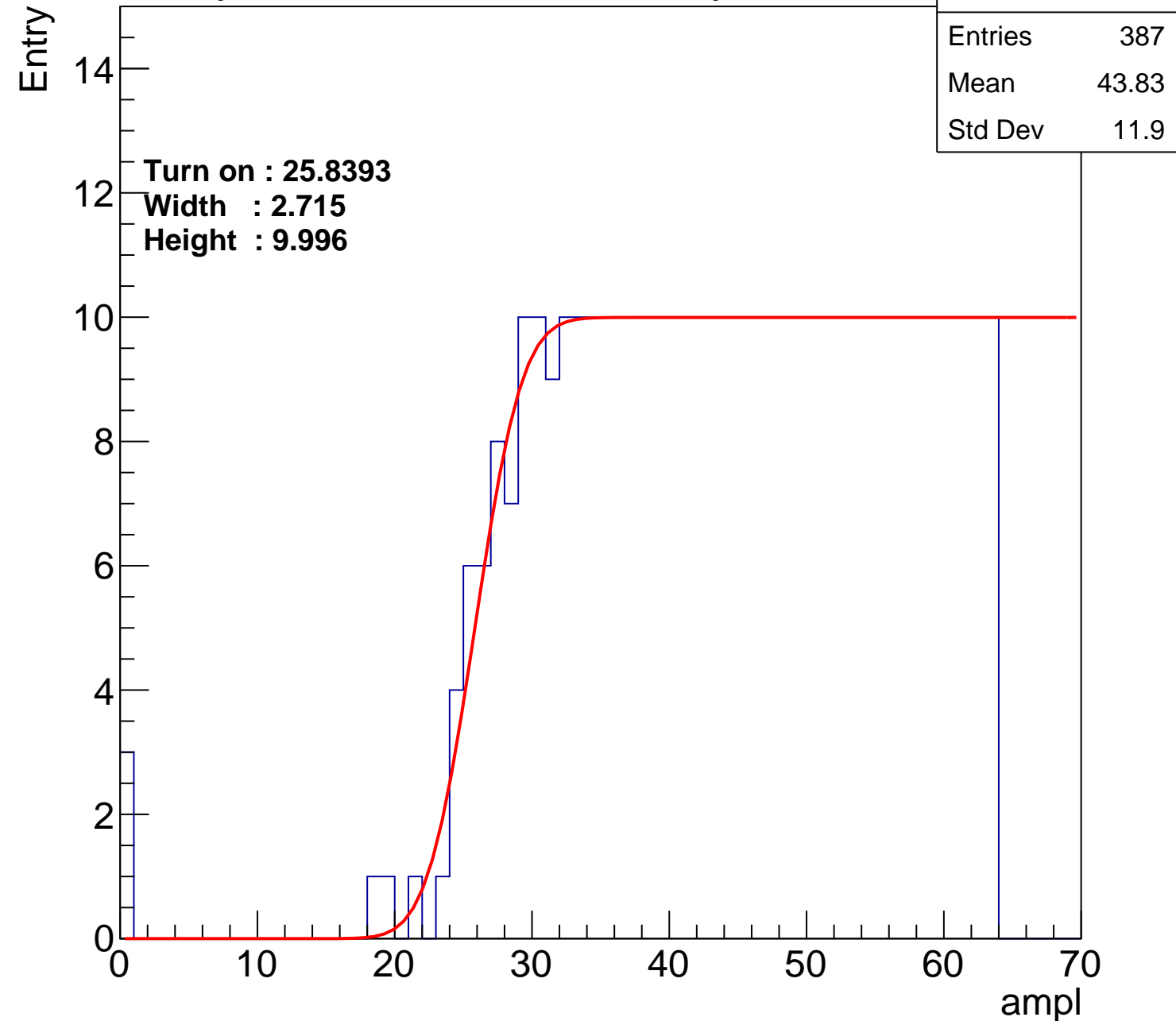
Width : 2.715

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch115

calib_packv5_042523_0143.root, FC#13, port D2

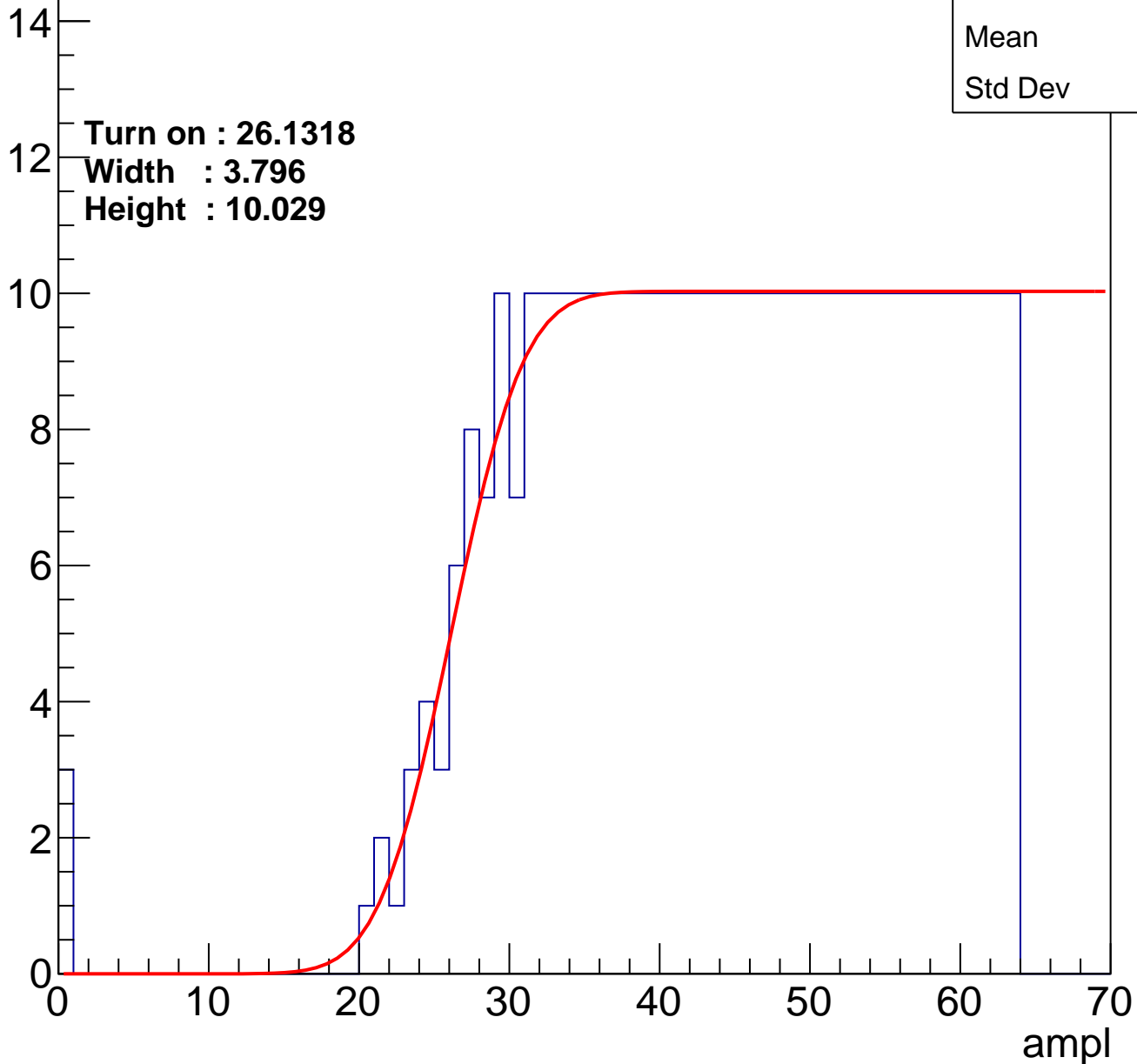
Entries	385
Mean	43.9
Std Dev	11.9

Turn on : 26.1318

Width : 3.796

Height : 10.029

Entry



B1L003S, U6-ch116

calib_packv5_042523_0143.root, FC#13, port D2

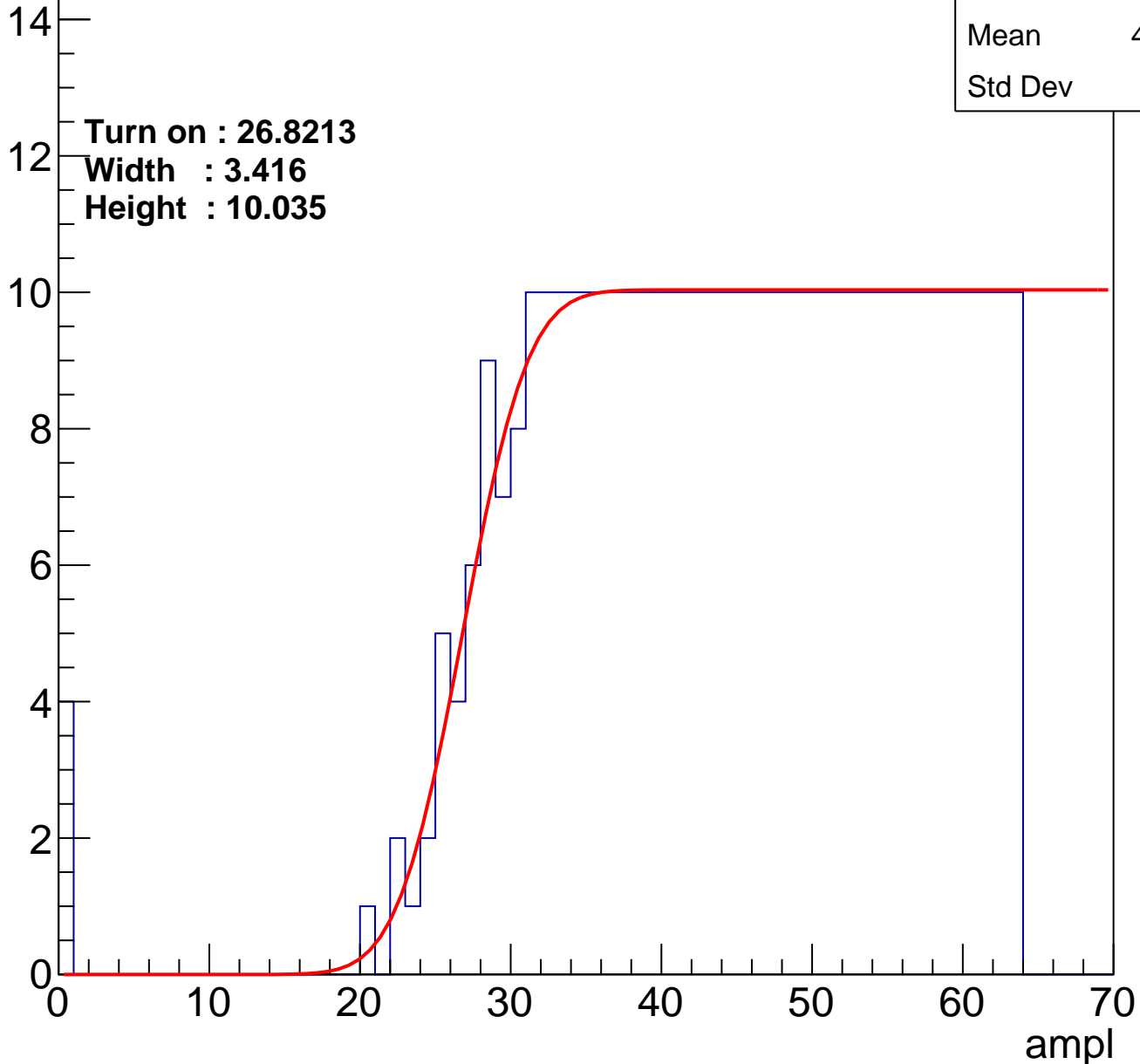
Entries	379
Mean	44.15
Std Dev	11.9

Turn on : 26.8213

Width : 3.416

Height : 10.035

Entry



B1L003S, U6-ch117

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.63
Std Dev	12.18

Turn on : 25.5729

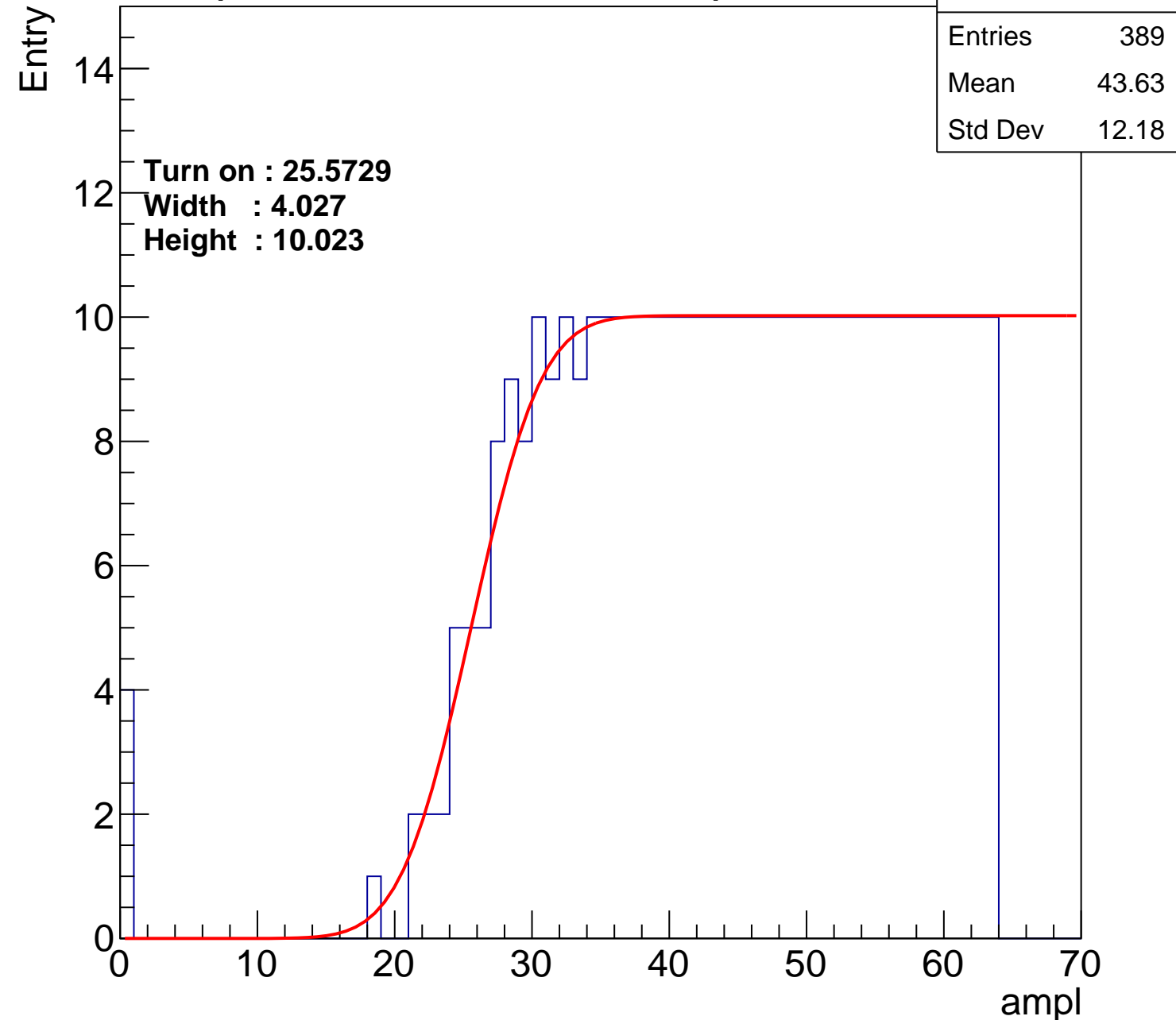
Width : 4.027

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch118

calib_packv5_042523_0143.root, FC#13, port D2

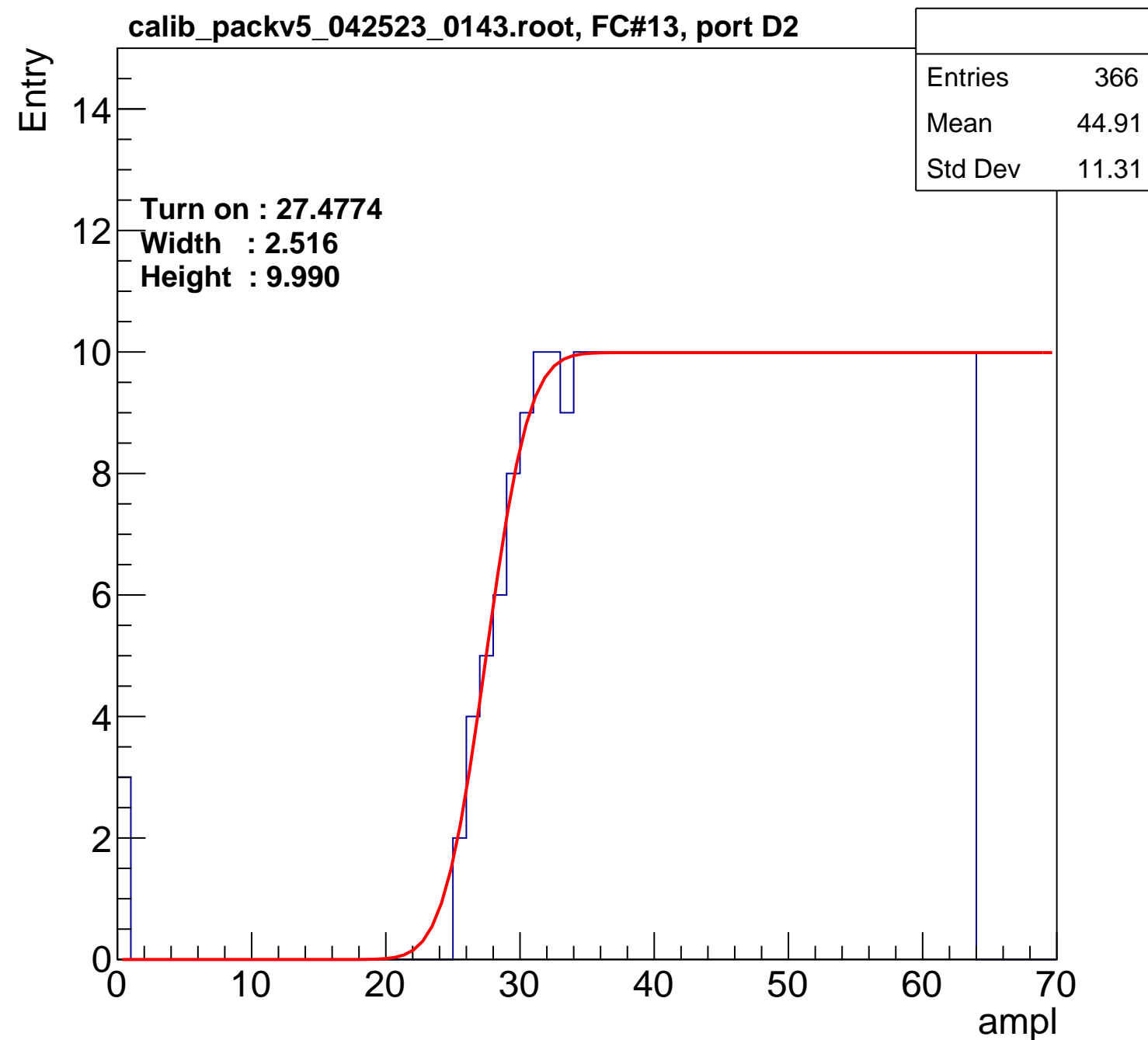
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4774
Width : 2.516
Height : 9.990

Entries	366
Mean	44.91
Std Dev	11.31

ampl



B1L003S, U6-ch119

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.69
Std Dev	11.36

Turn on : 27.6288

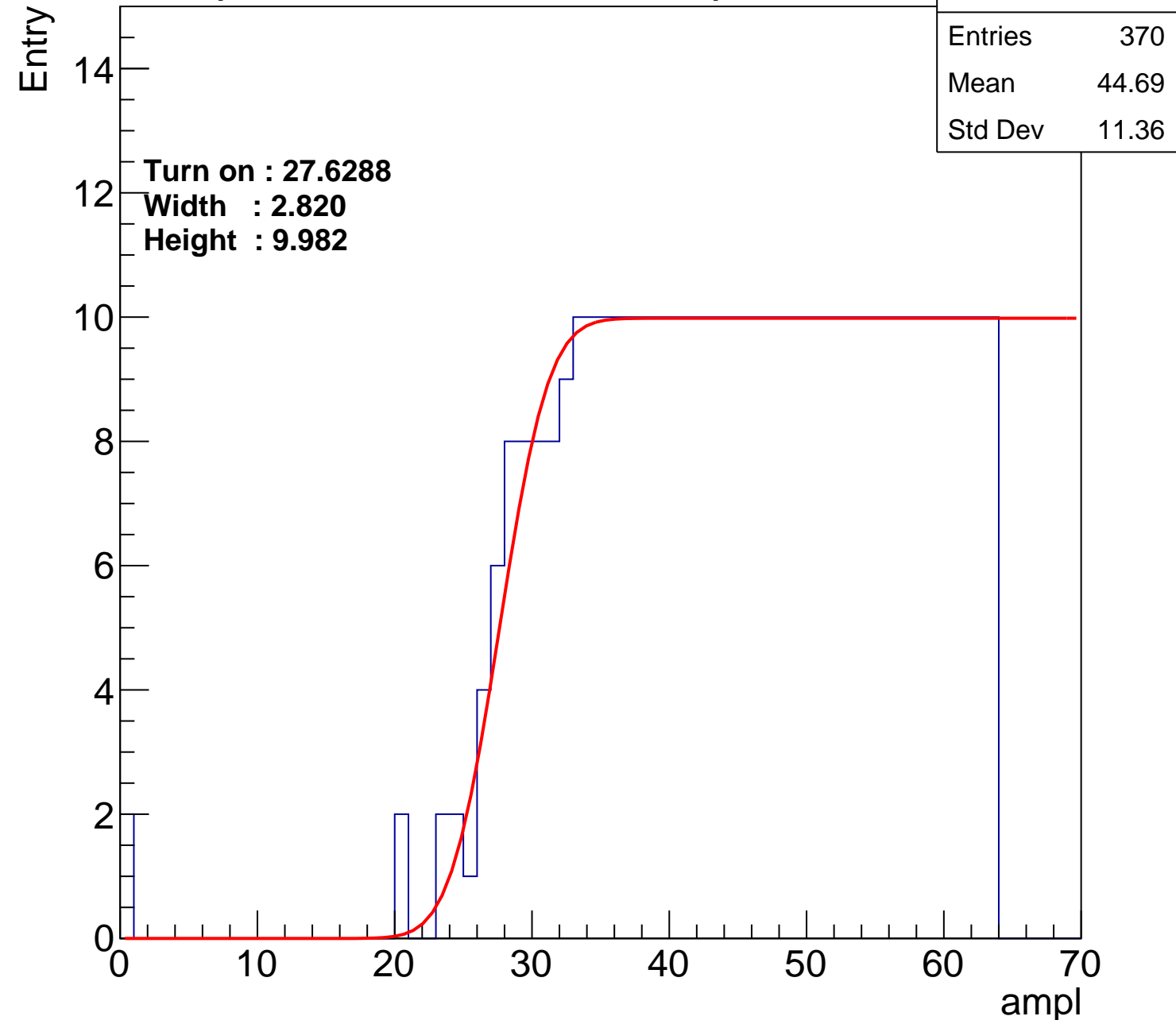
Width : 2.820

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch120

calib_packv5_042523_0143.root, FC#13, port D2

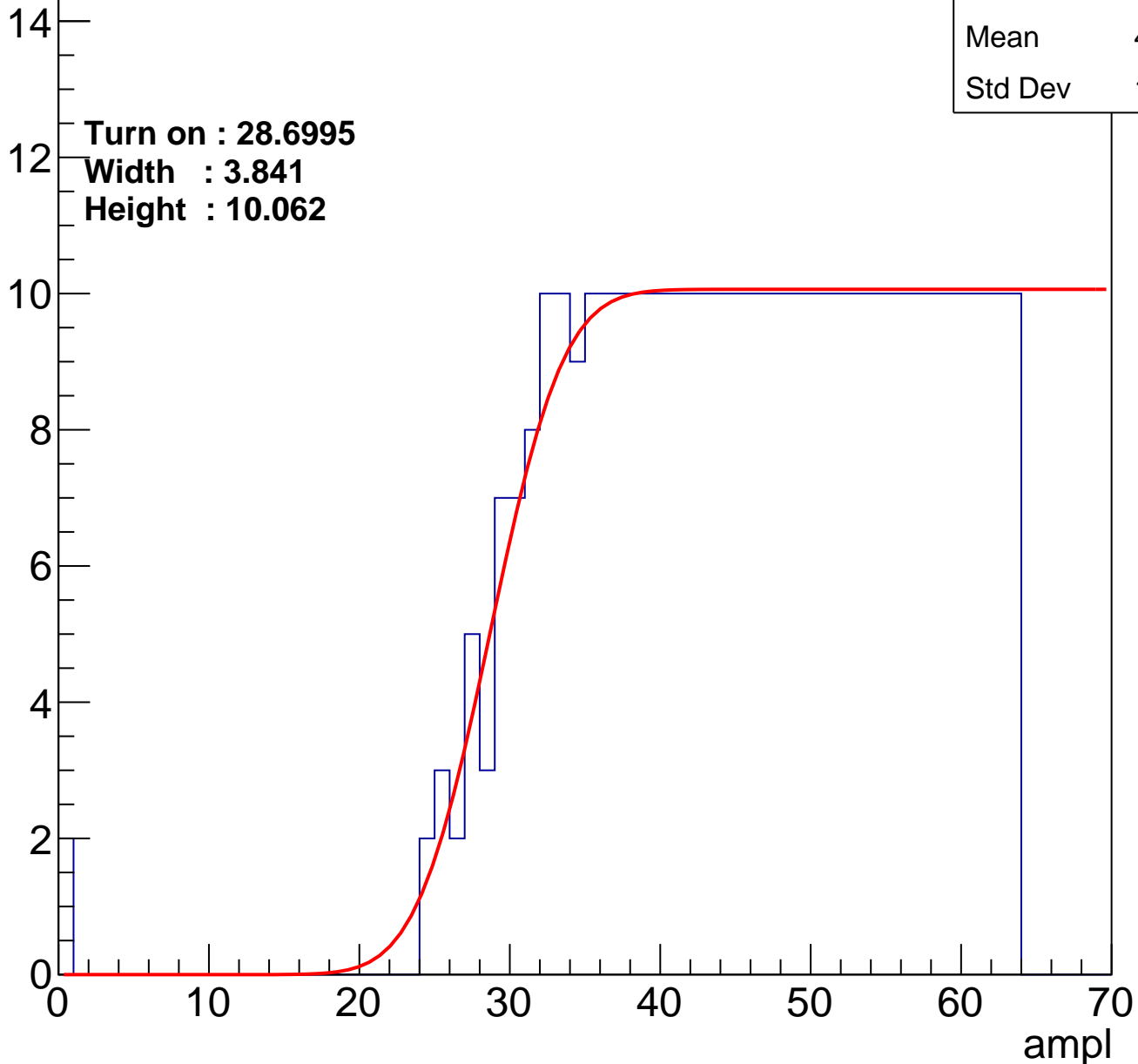
Entries	358
Mean	45.31
Std Dev	11.01

Turn on : 28.6995

Width : 3.841

Height : 10.062

Entry



B1L003S, U6-ch121

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.37
Std Dev	11.47

Turn on : 26.9439

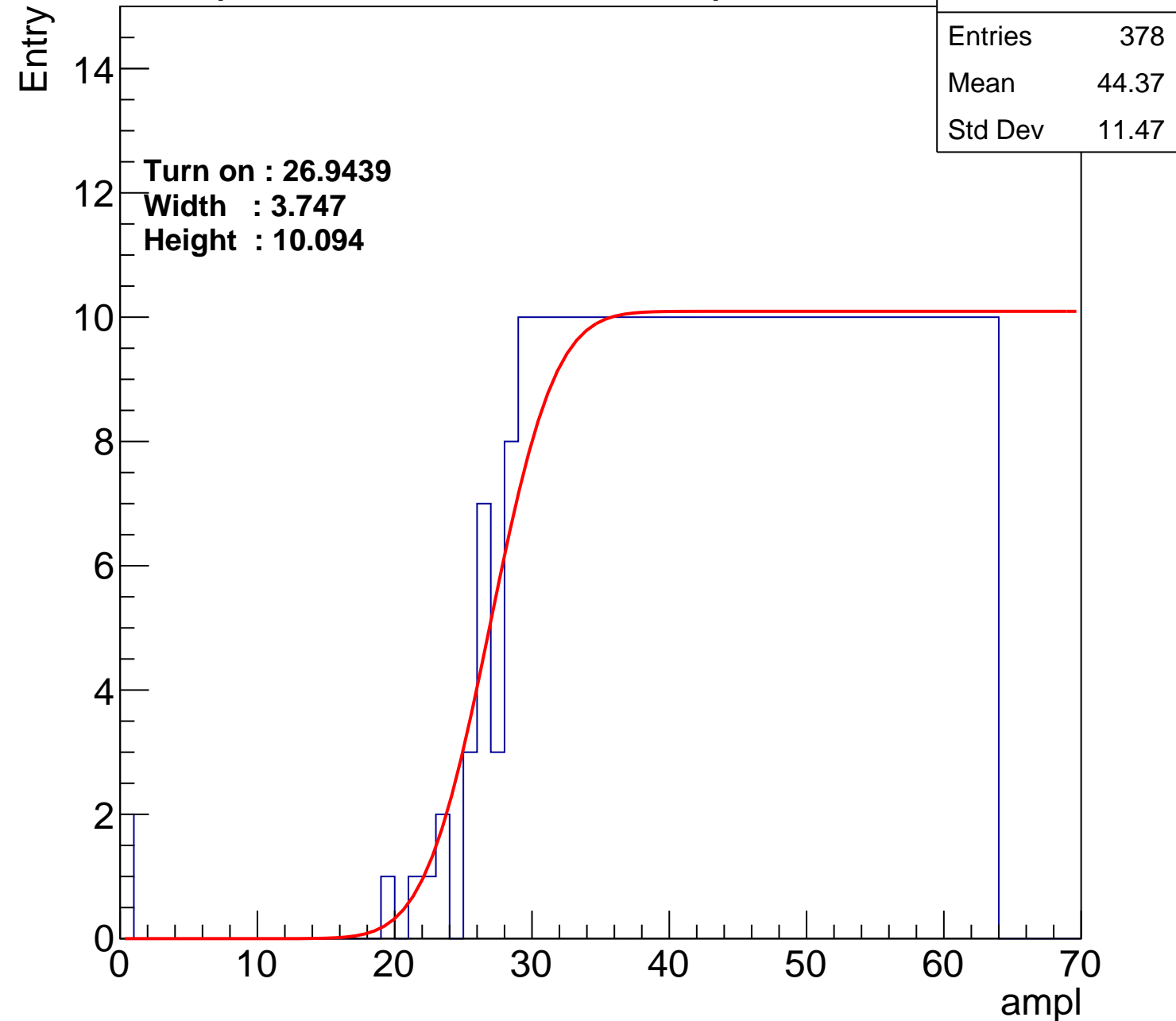
Width : 3.747

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.5
Std Dev	11.21

Turn on : 26.7776

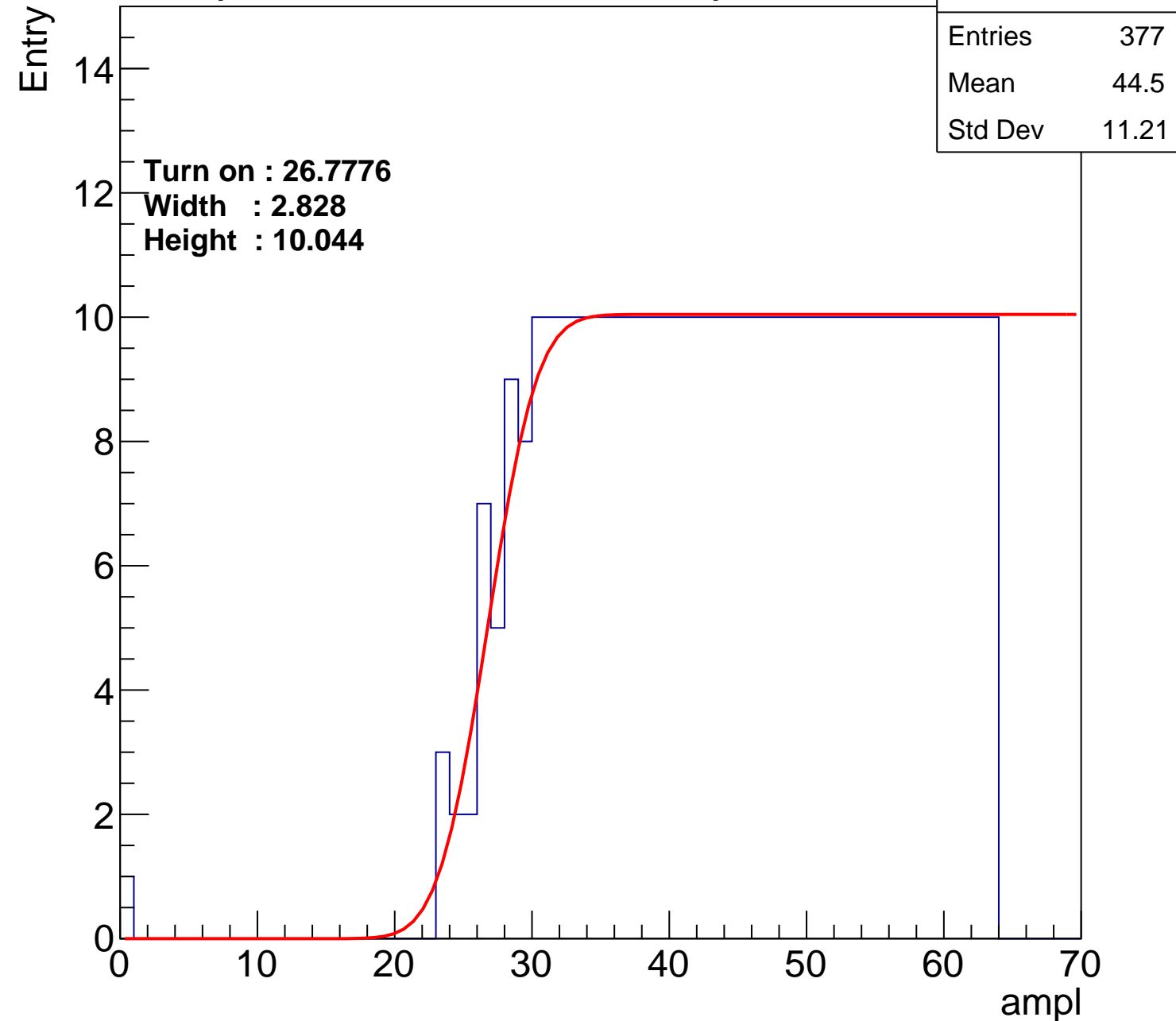
Width : 2.828

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.62
Std Dev	12.13

Turn on : 26.0587

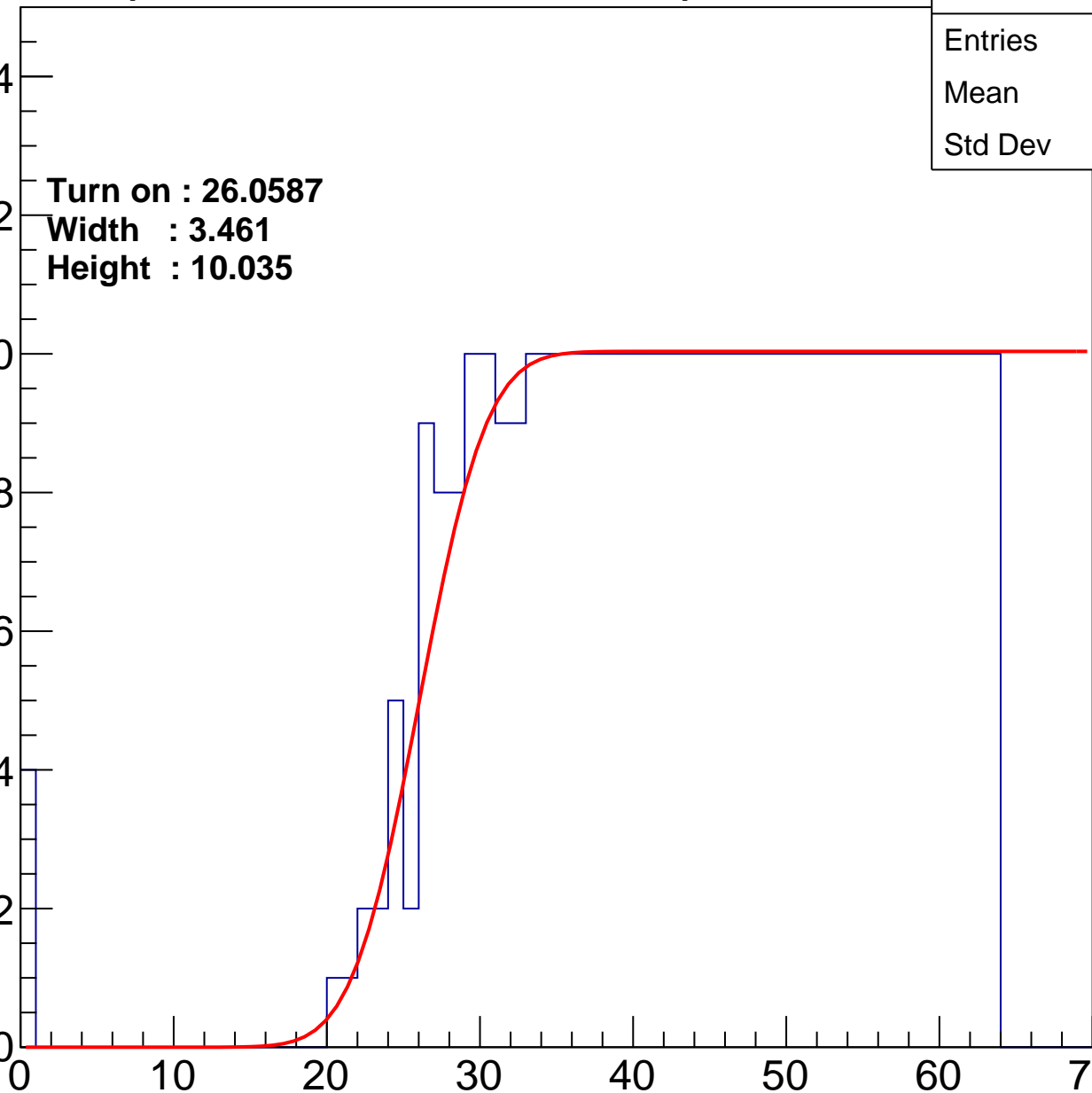
Width : 3.461

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch124

calib_packv5_042523_0143.root, FC#13, port D2

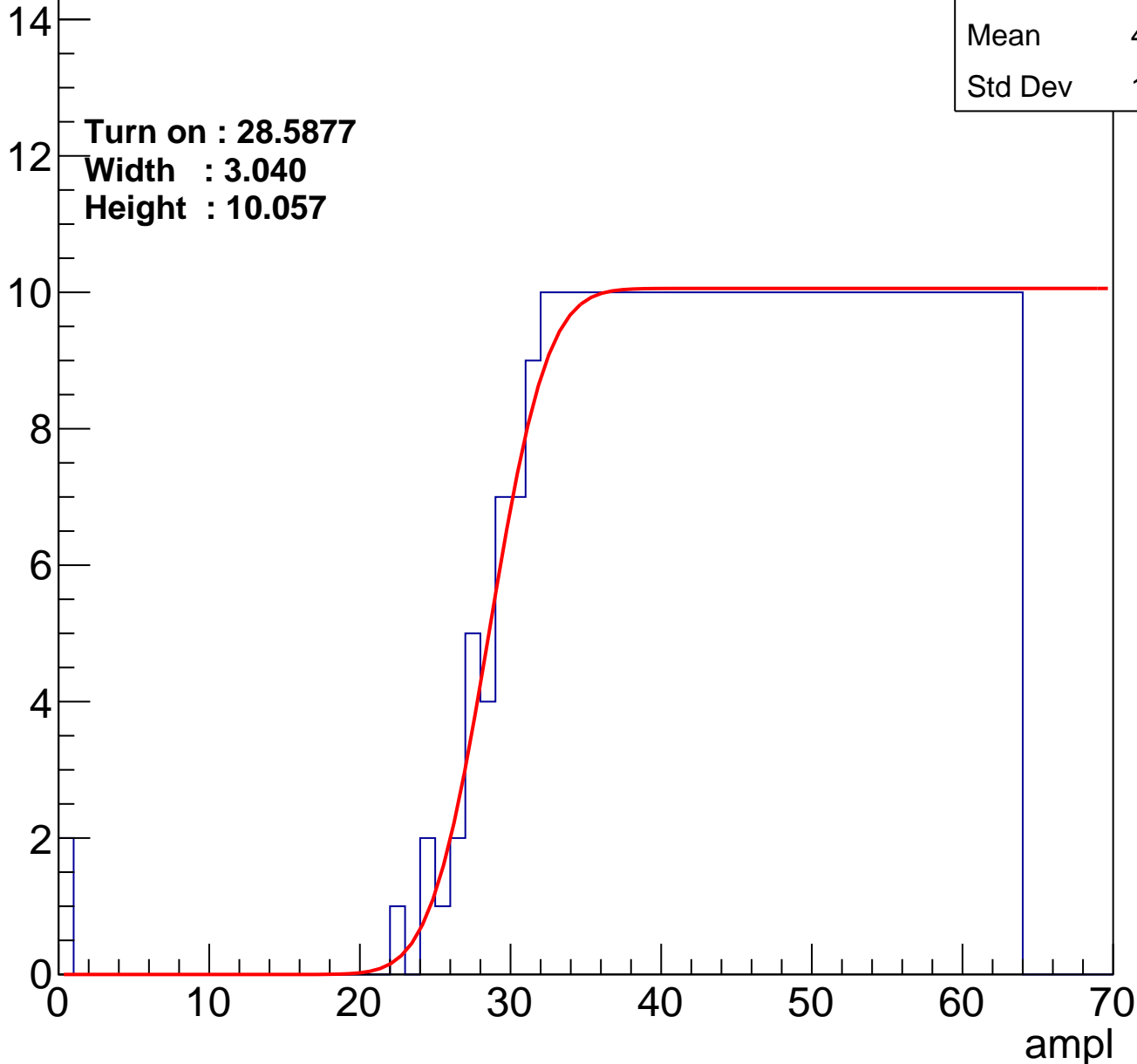
Entries	360
Mean	45.24
Std Dev	11.02

Turn on : 28.5877

Width : 3.040

Height : 10.057

Entry



B1L003S, U6-ch125

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.32
Std Dev	11.14

Turn on : 29.0517

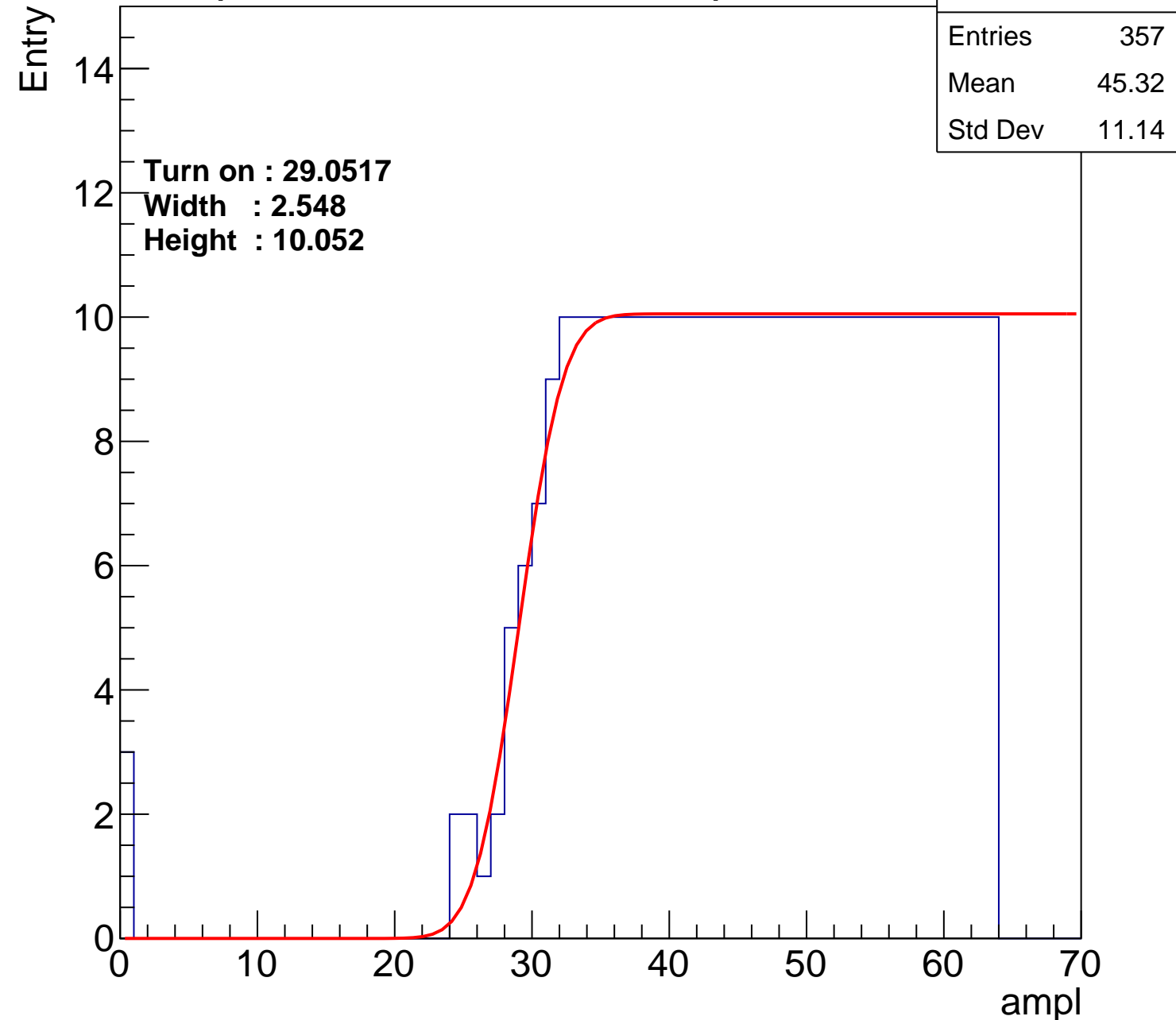
Width : 2.548

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.1698

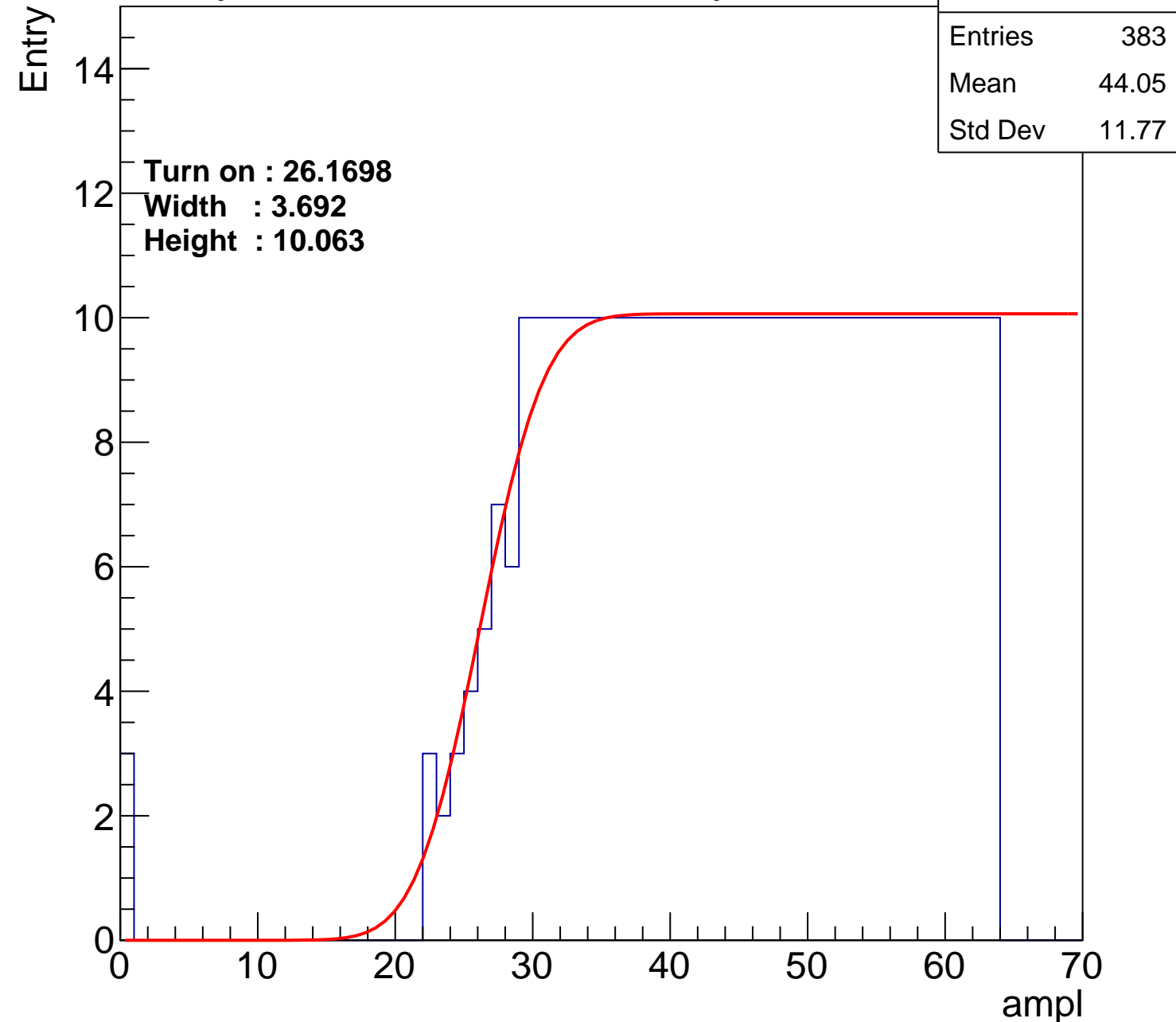
Width : 3.692

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.98
Std Dev	10.97

Turn on : 28.1390

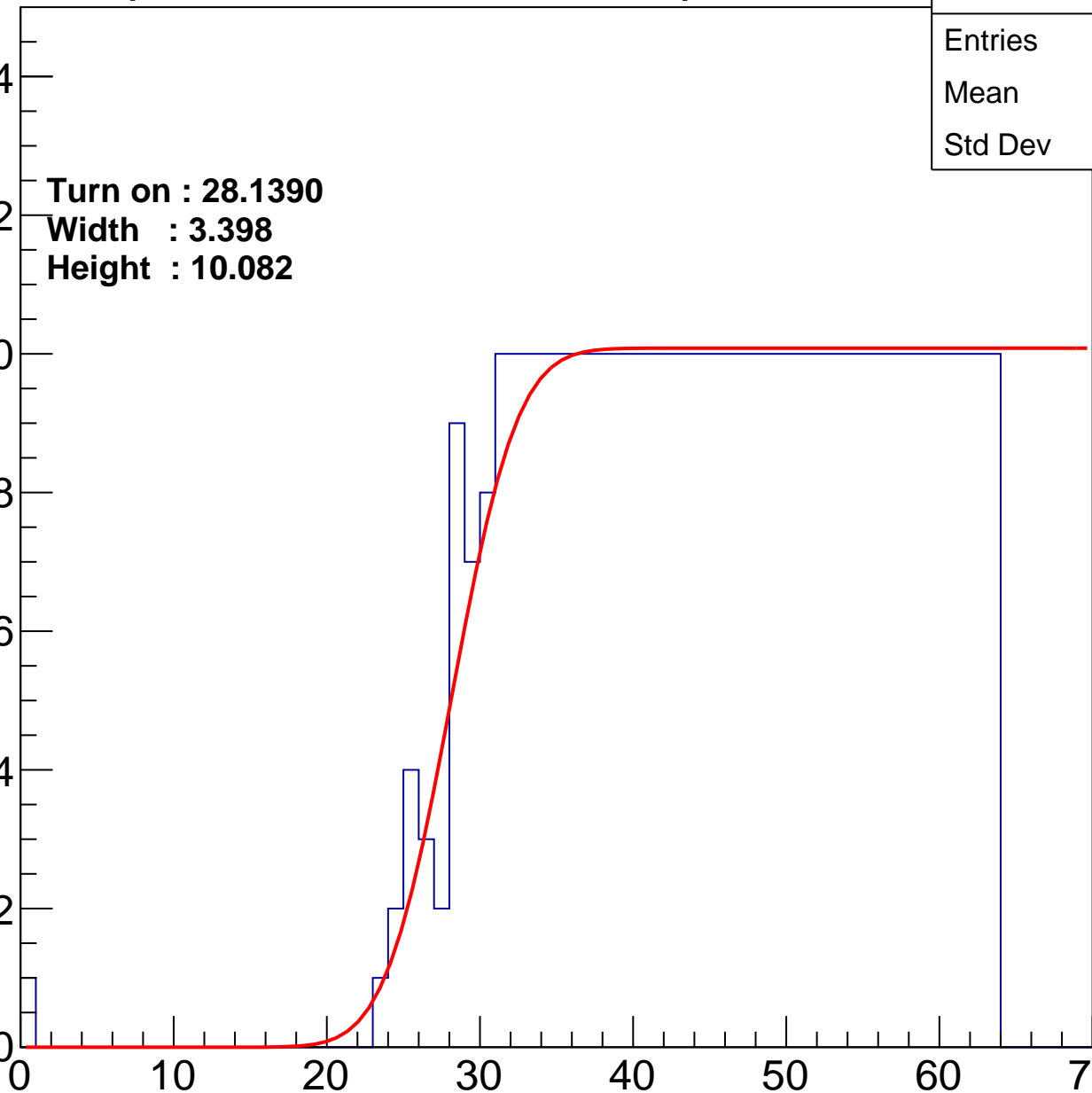
Width : 3.398

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U6-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.98
Std Dev	10.97

Turn on : 28.1390

Width : 3.398

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl

