

B1L103S, U12-ch0, adc0

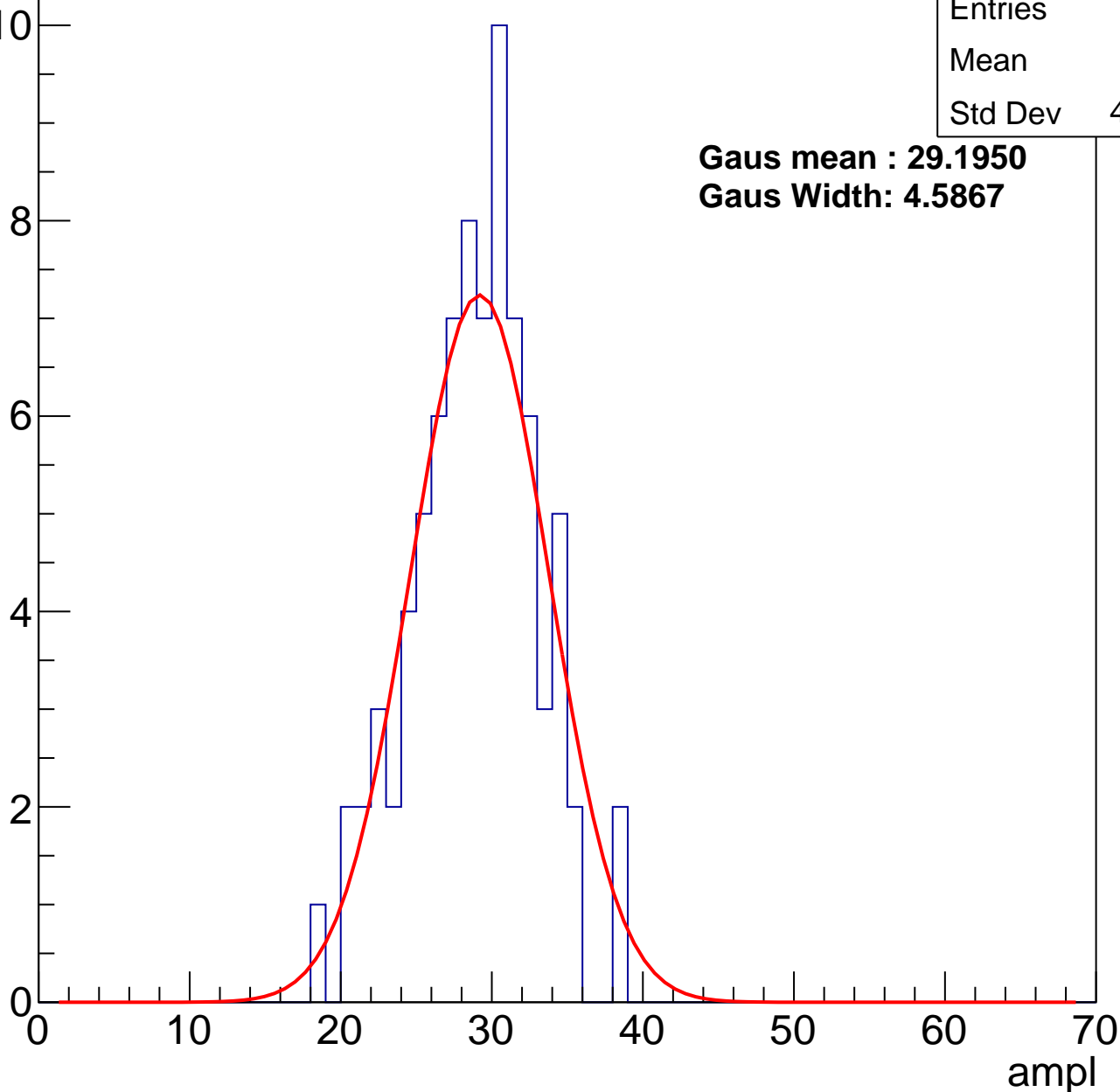
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.4
Std Dev	4.087

Gaus mean : 29.1950

Gaus Width: 4.5867



B1L103S, U12-ch0, adc1

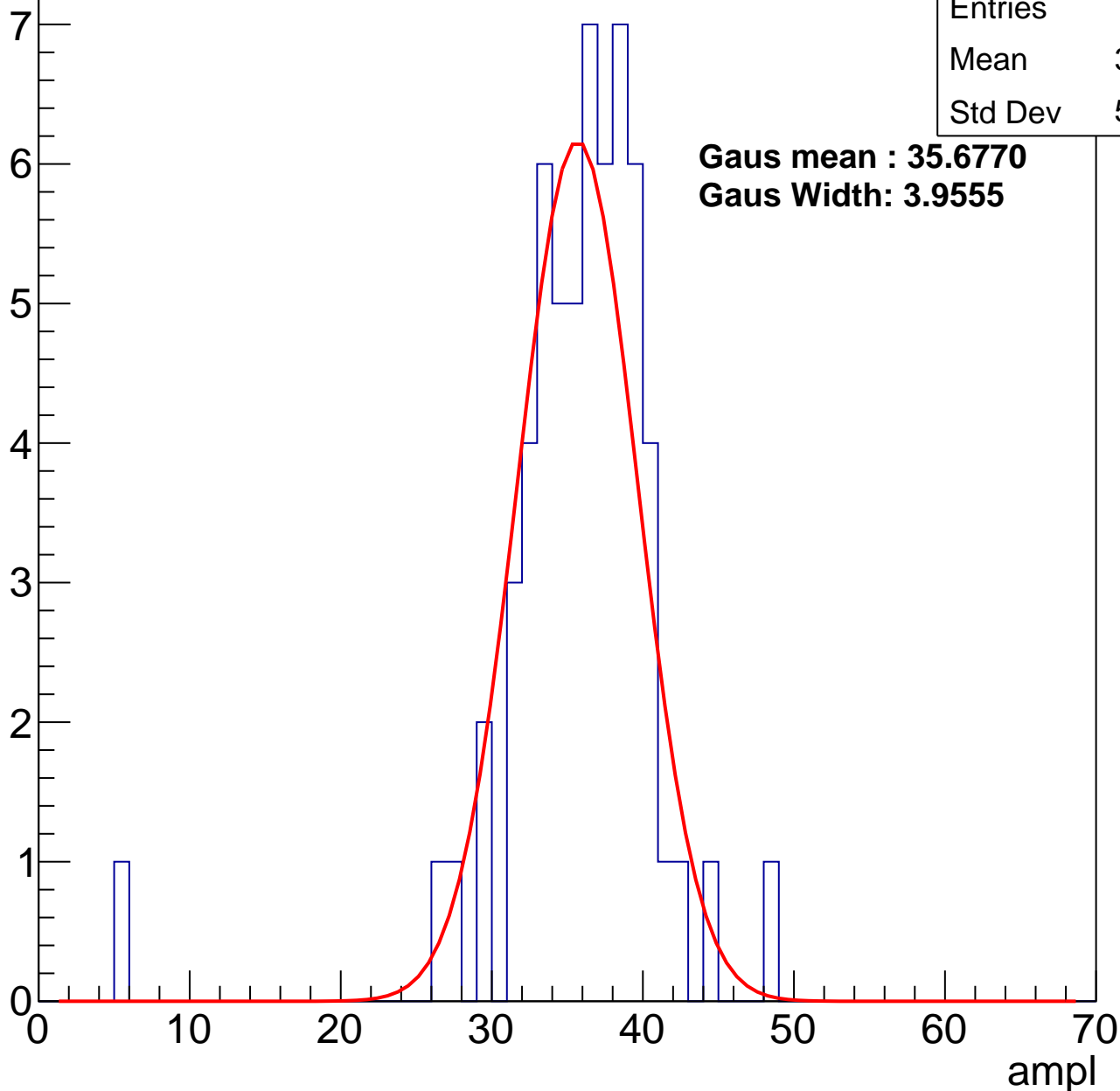
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.31
Std Dev	5.461

Gaus mean : 35.6770

Gaus Width: 3.9555



B1L103S, U12-ch0, adc2

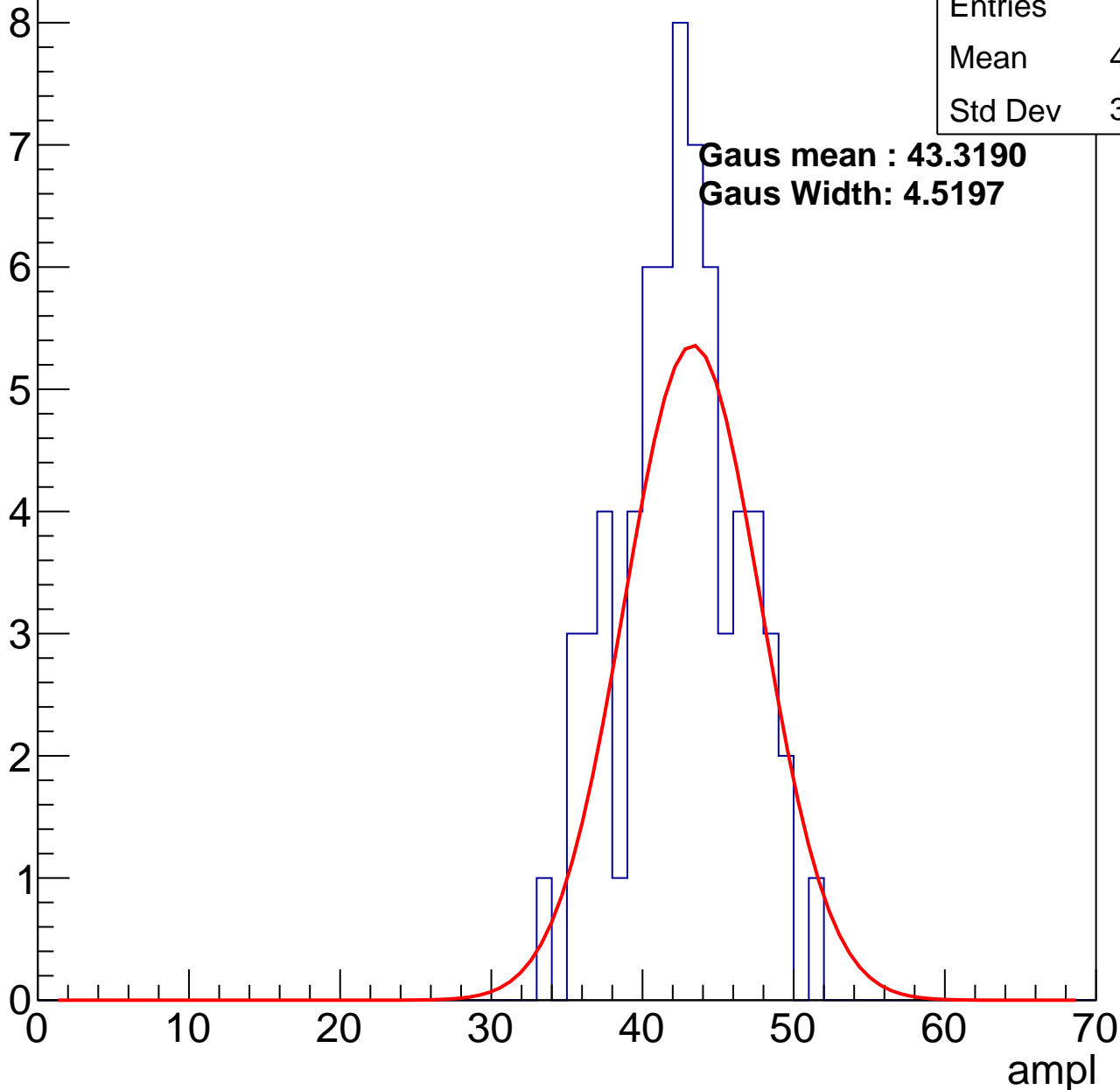
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.05
Std Dev	3.944

Gaus mean : 43.3190

Gaus Width: 4.5197

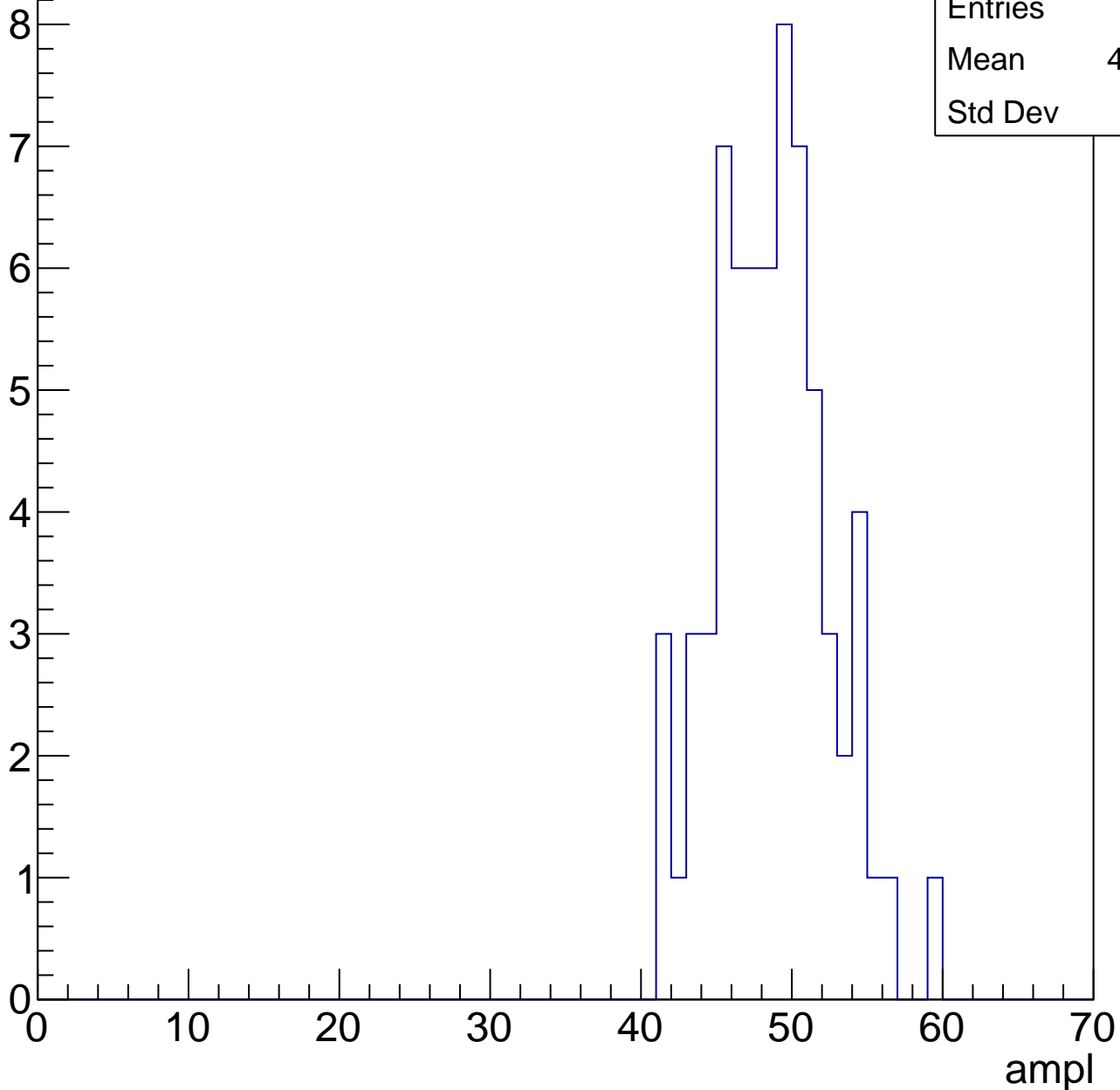


B1L103S, U12-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.24
Std Dev	3.77

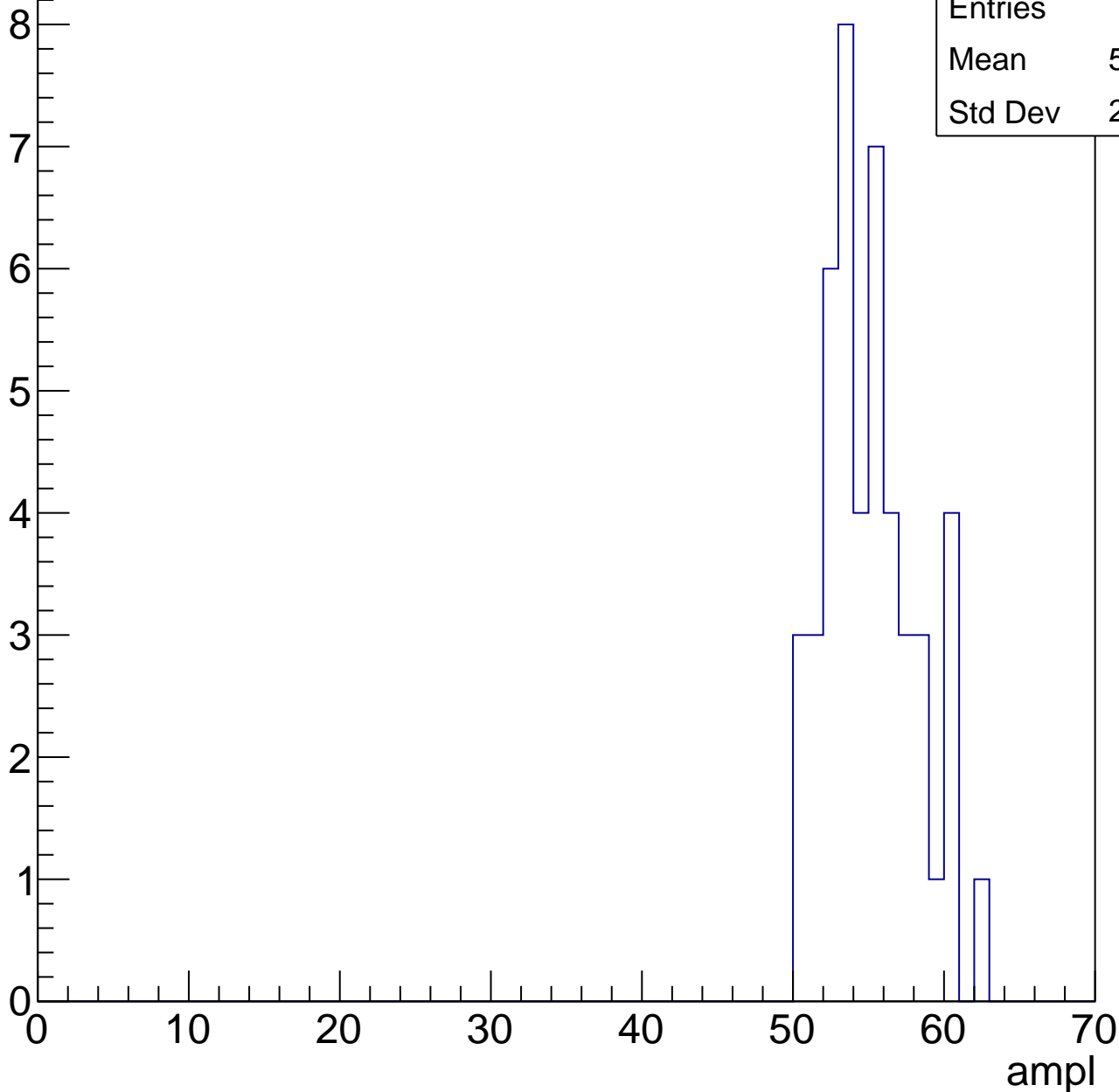


B1L103S, U12-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	54.68
Std Dev	2.976

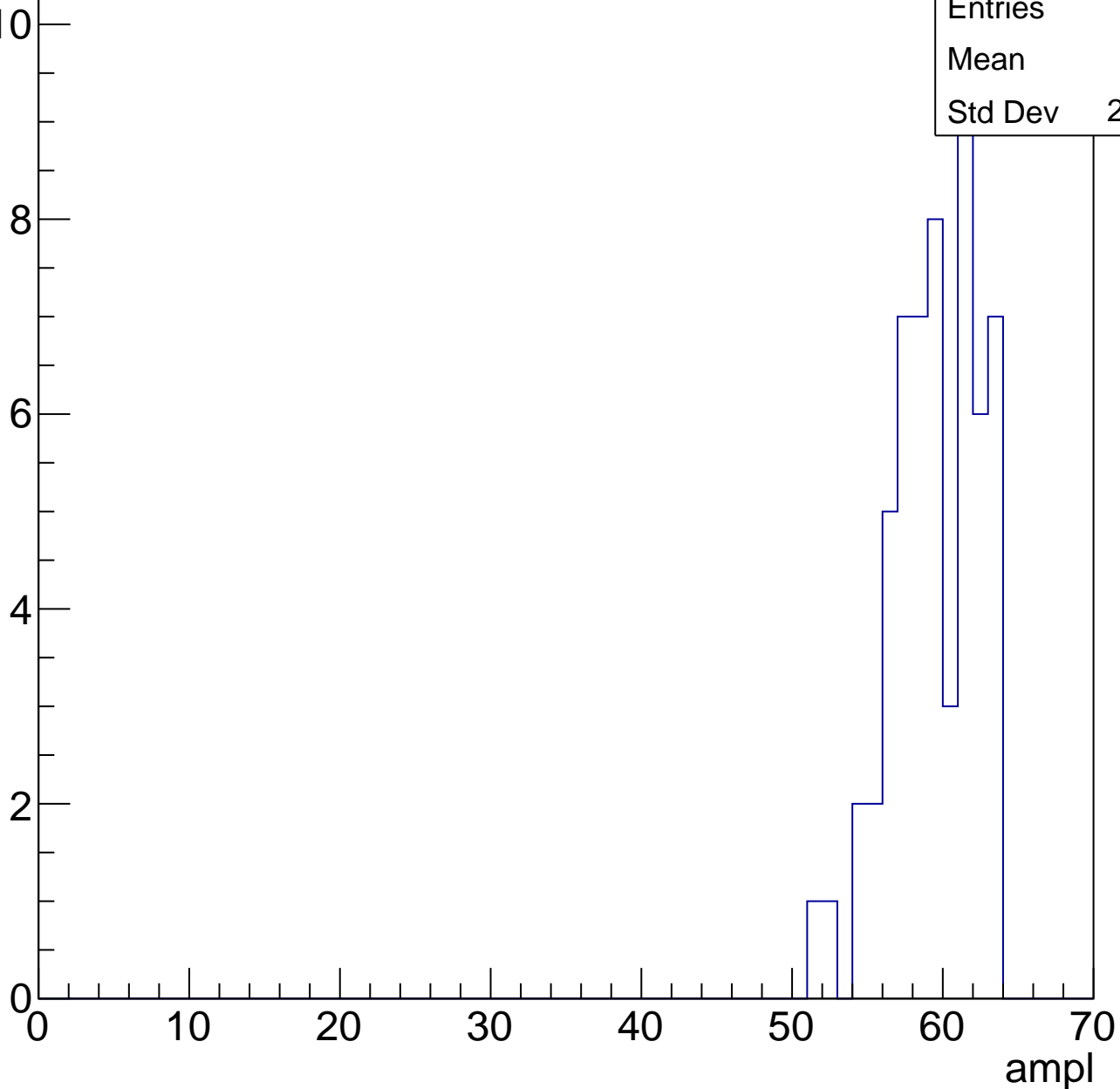


B1L103S, U12-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	59
Std Dev	2.864



B1L103S, U12-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

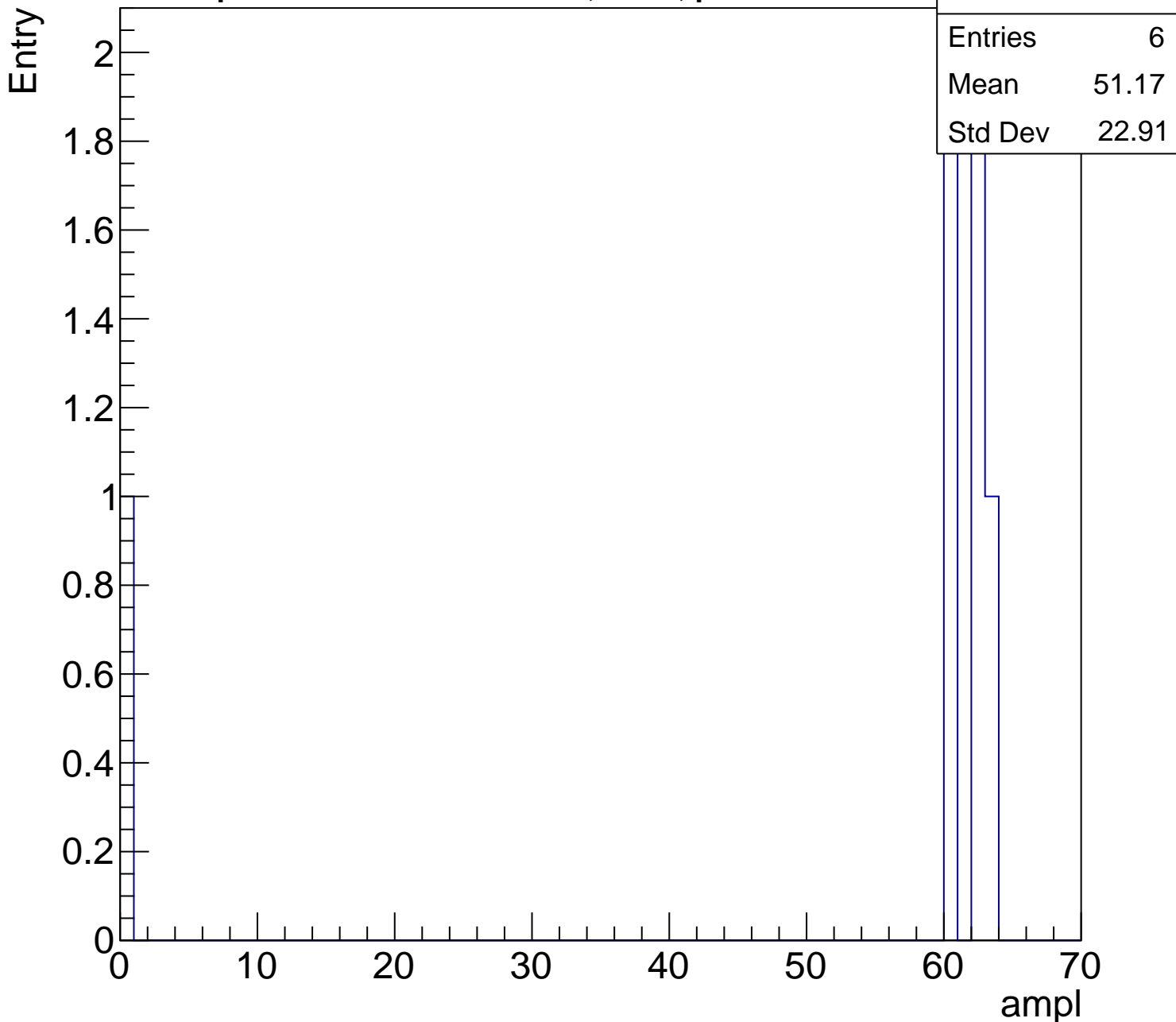
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.91

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

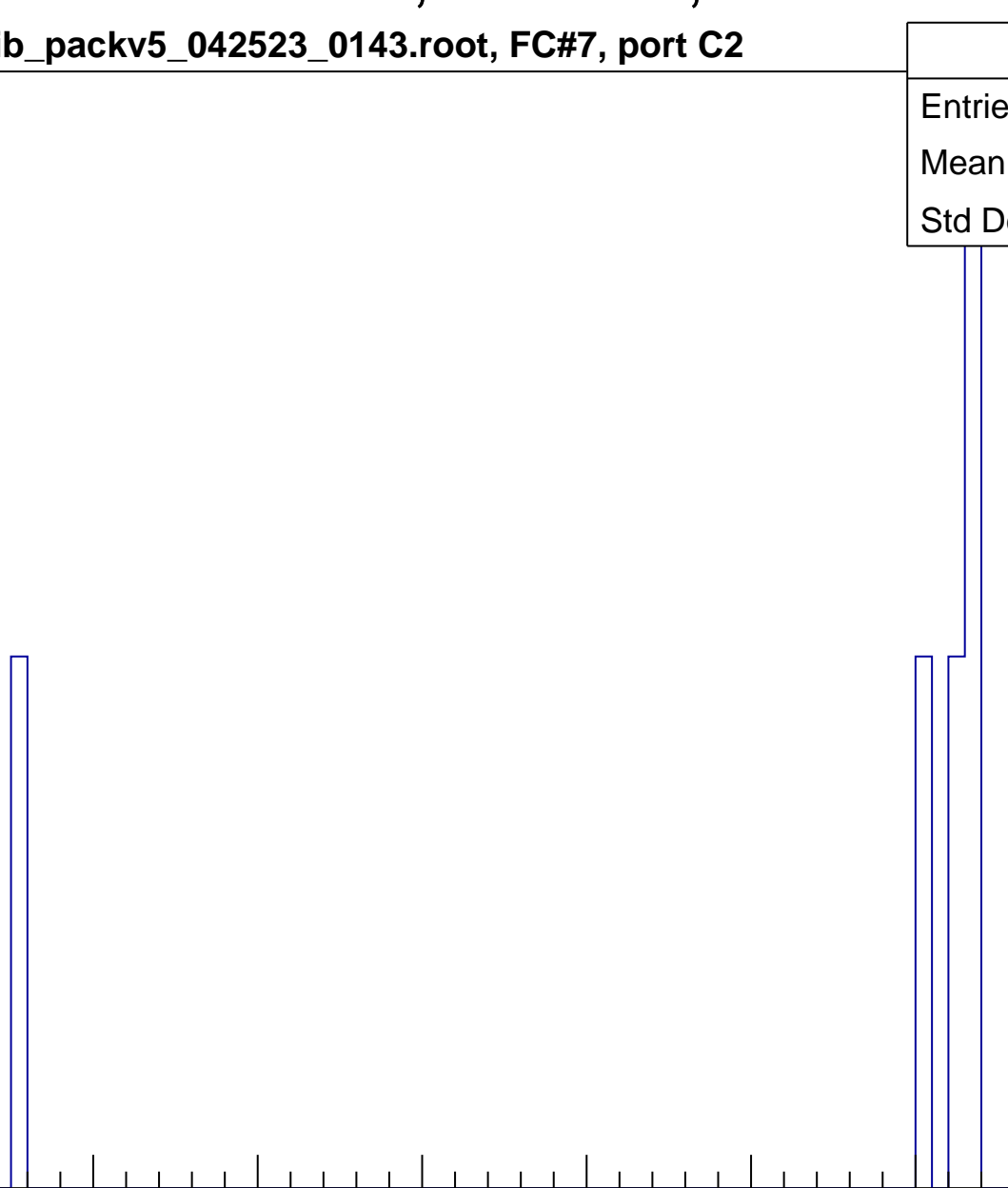
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	50.6
Std Dev	22.83

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch1, adc0

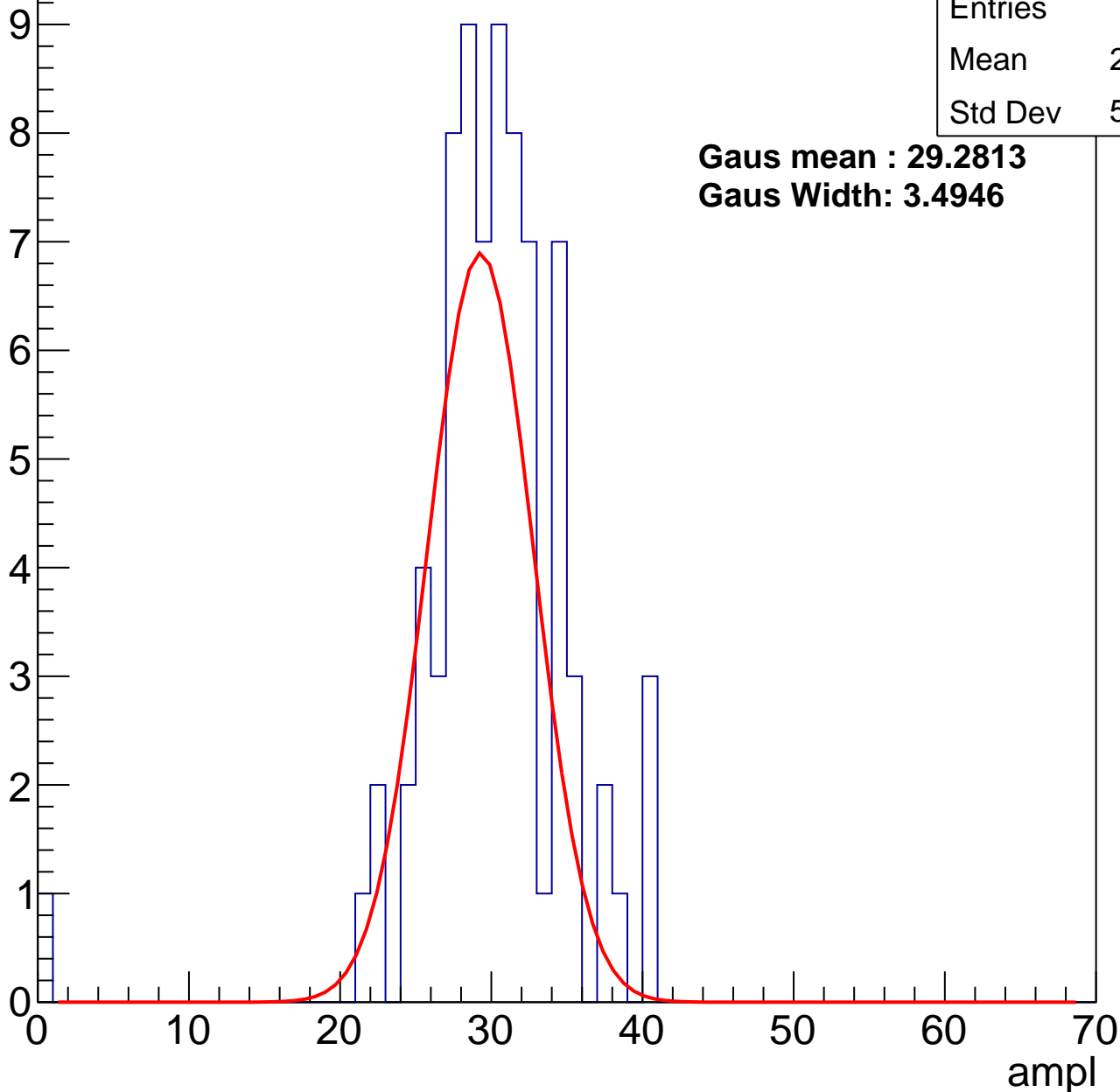
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.64
Std Dev	5.223

Gaus mean : 29.2813

Gaus Width: 3.4946



B1L103S, U12-ch1, adc1

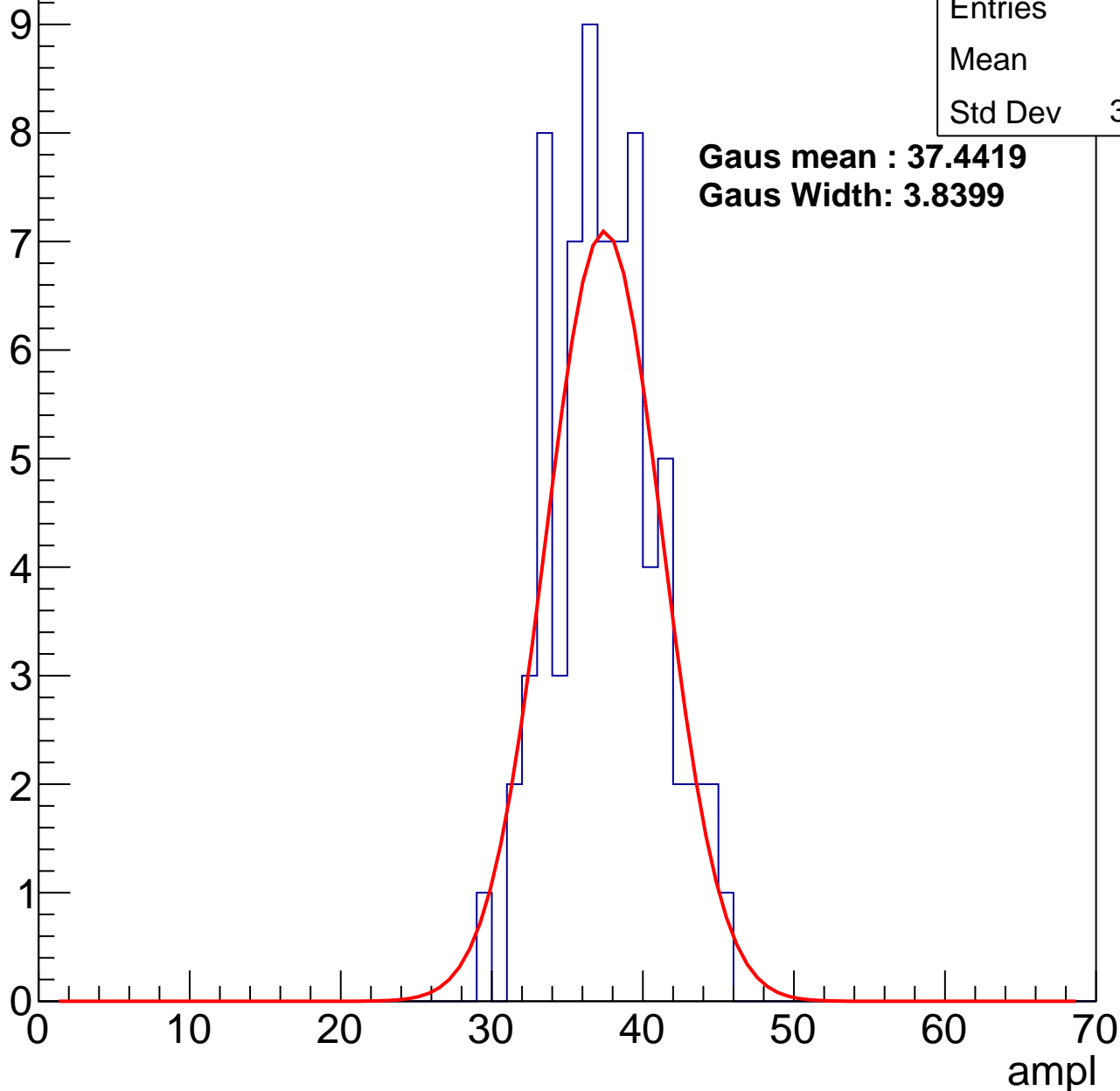
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	37
Std Dev	3.444

Gaus mean : 37.4419

Gaus Width: 3.8399



B1L103S, U12-ch1, adc2

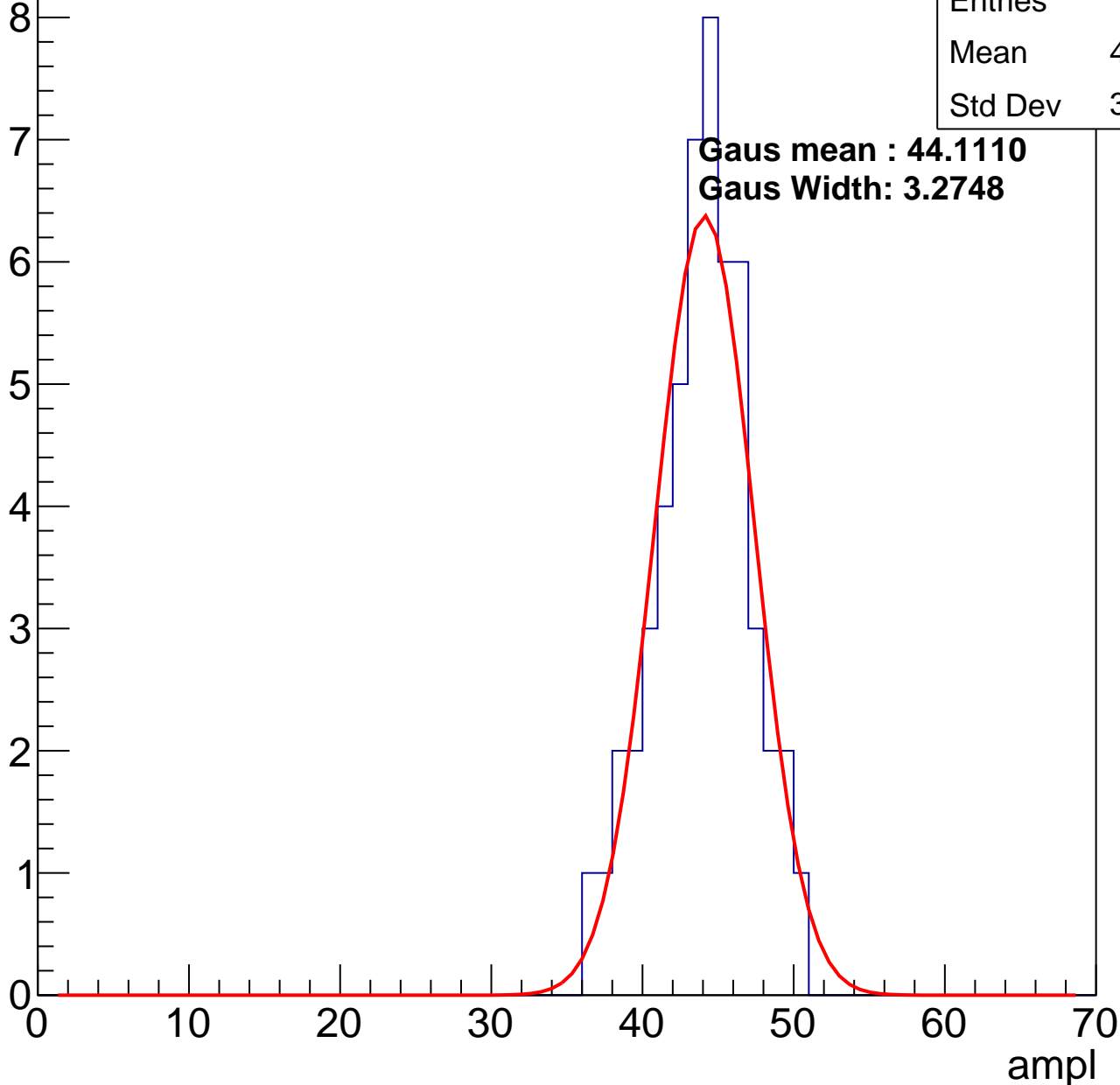
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	43.49
Std Dev	3.094

Gaus mean : 44.1110

Gaus Width: 3.2748

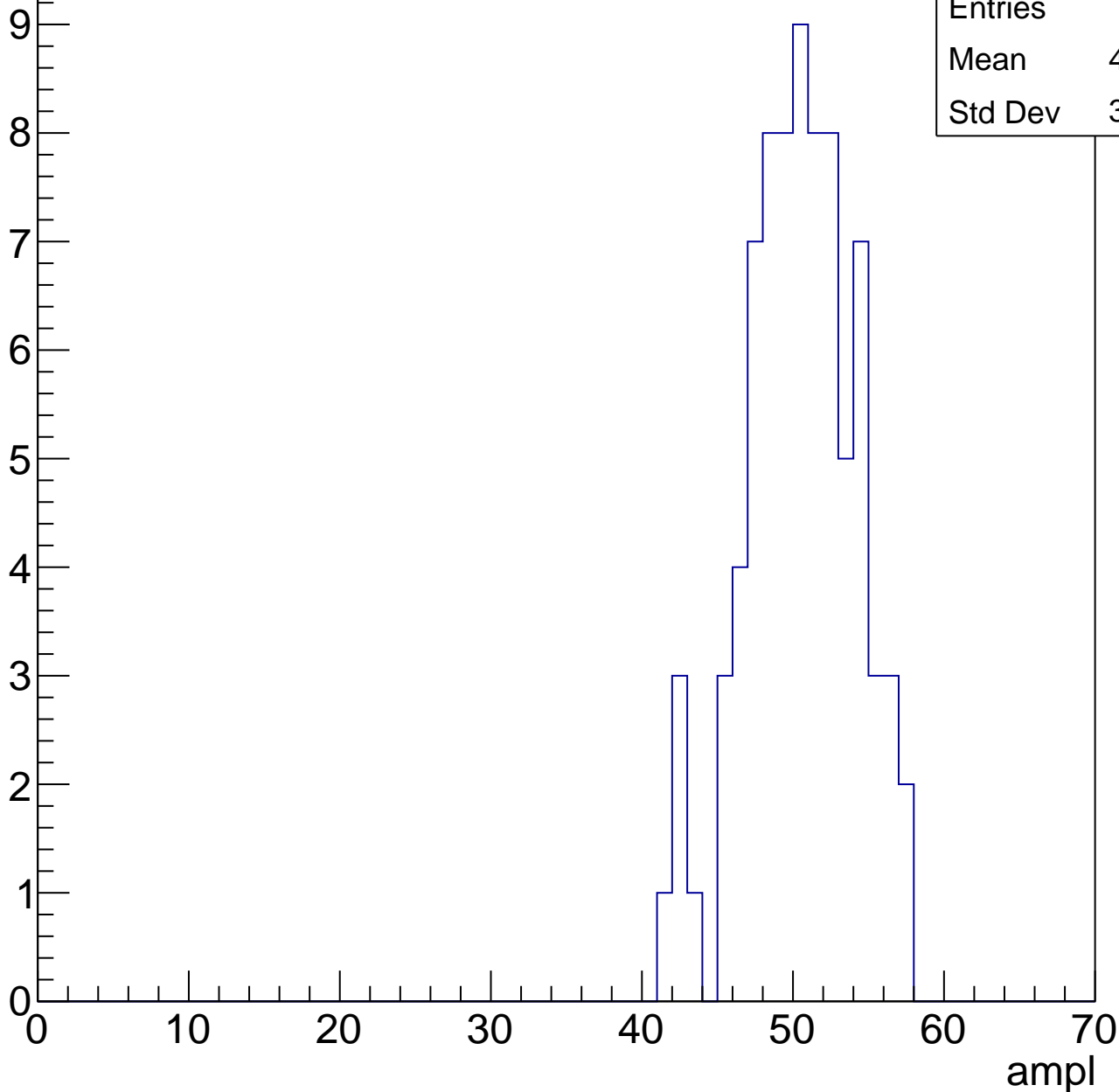


B1L103S, U12-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

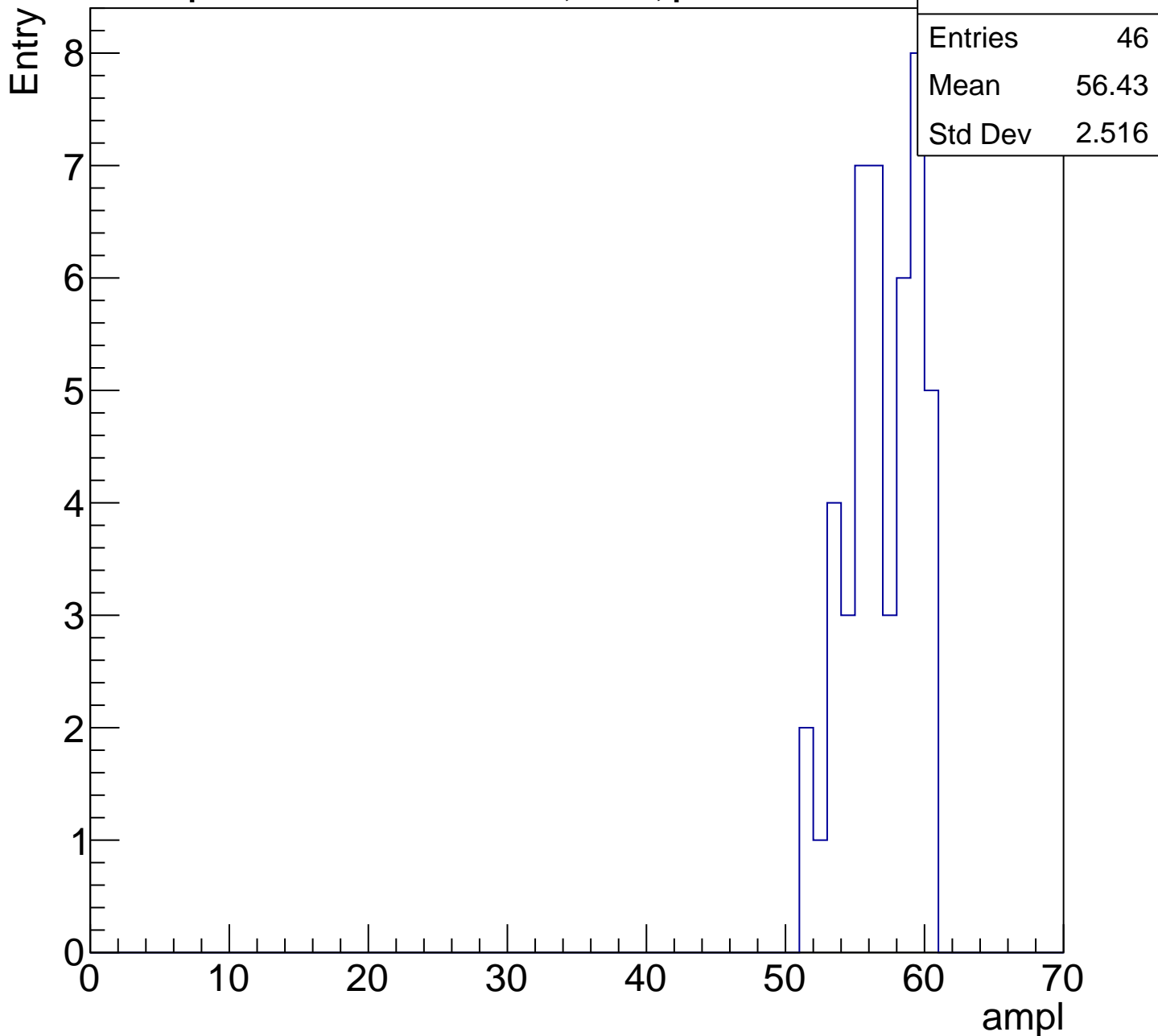
Entry

Entries	80
Mean	49.98
Std Dev	3.609



B1L103S, U12-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

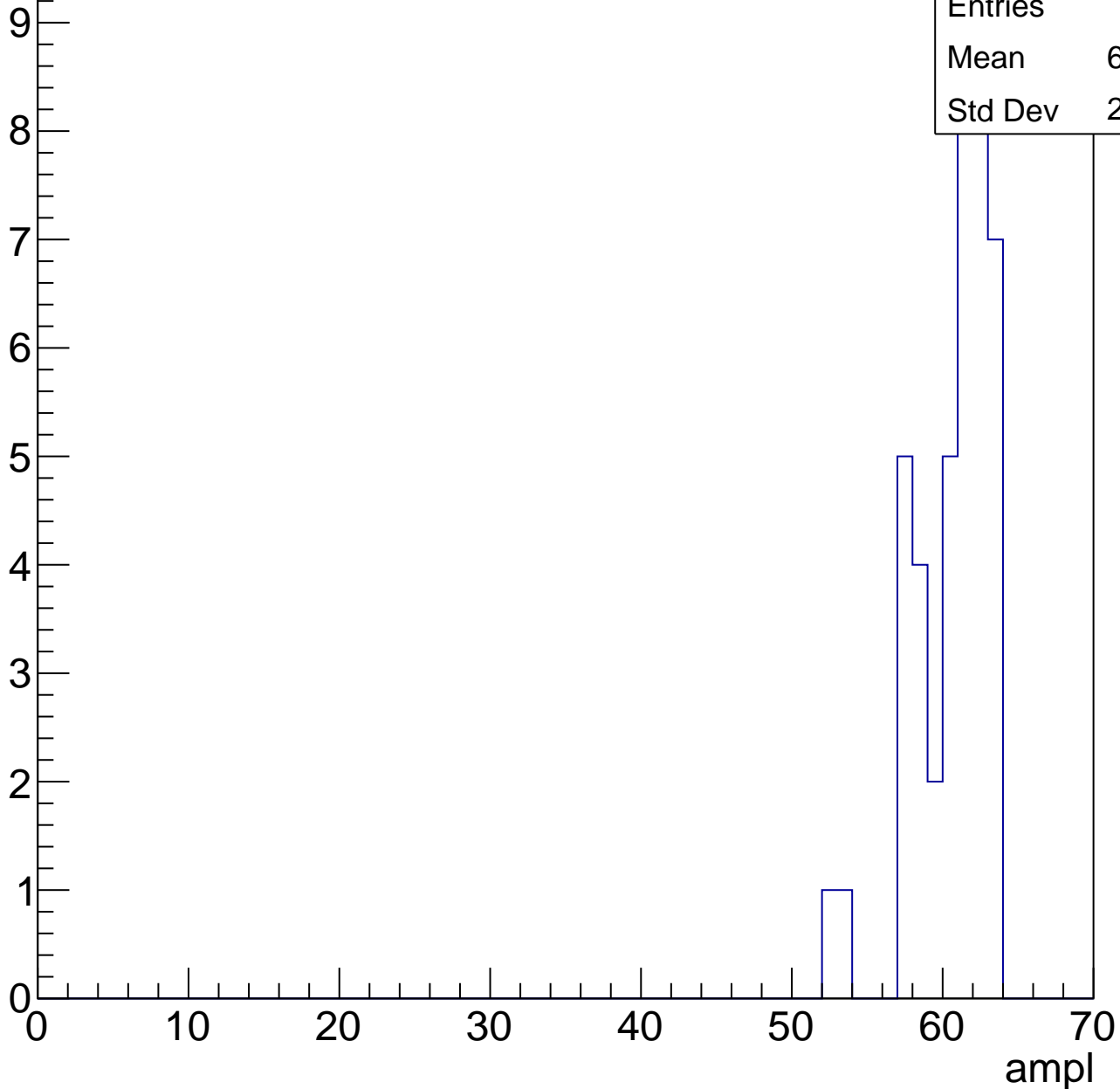


B1L103S, U12-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	60.14
Std Dev	2.578



B1L103S, U12-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch2, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	29.74
Std Dev	3.492

Gaus mean : 30.6852

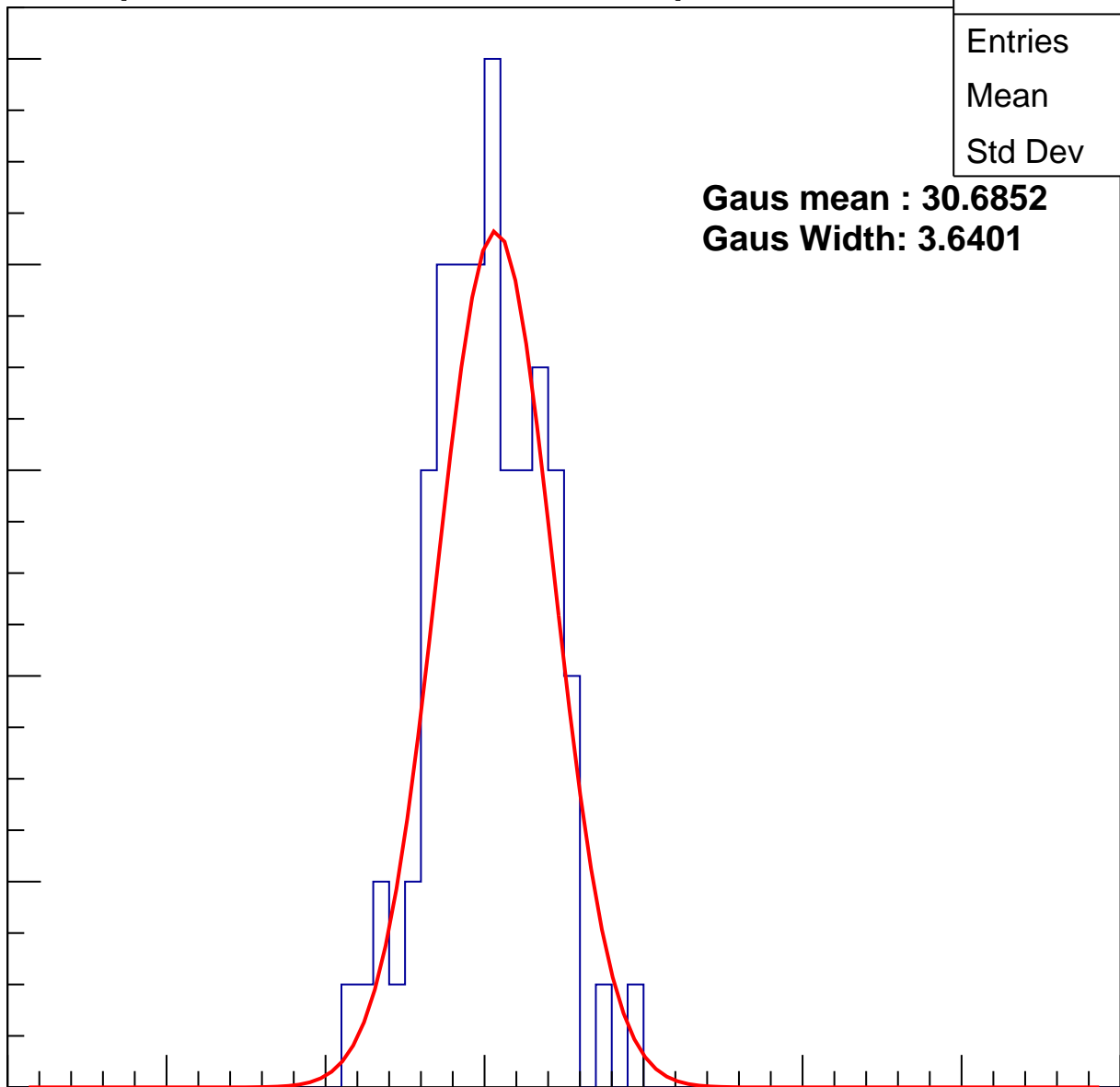
Gaus Width: 3.6401

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch2, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	37.21
Std Dev	3.312

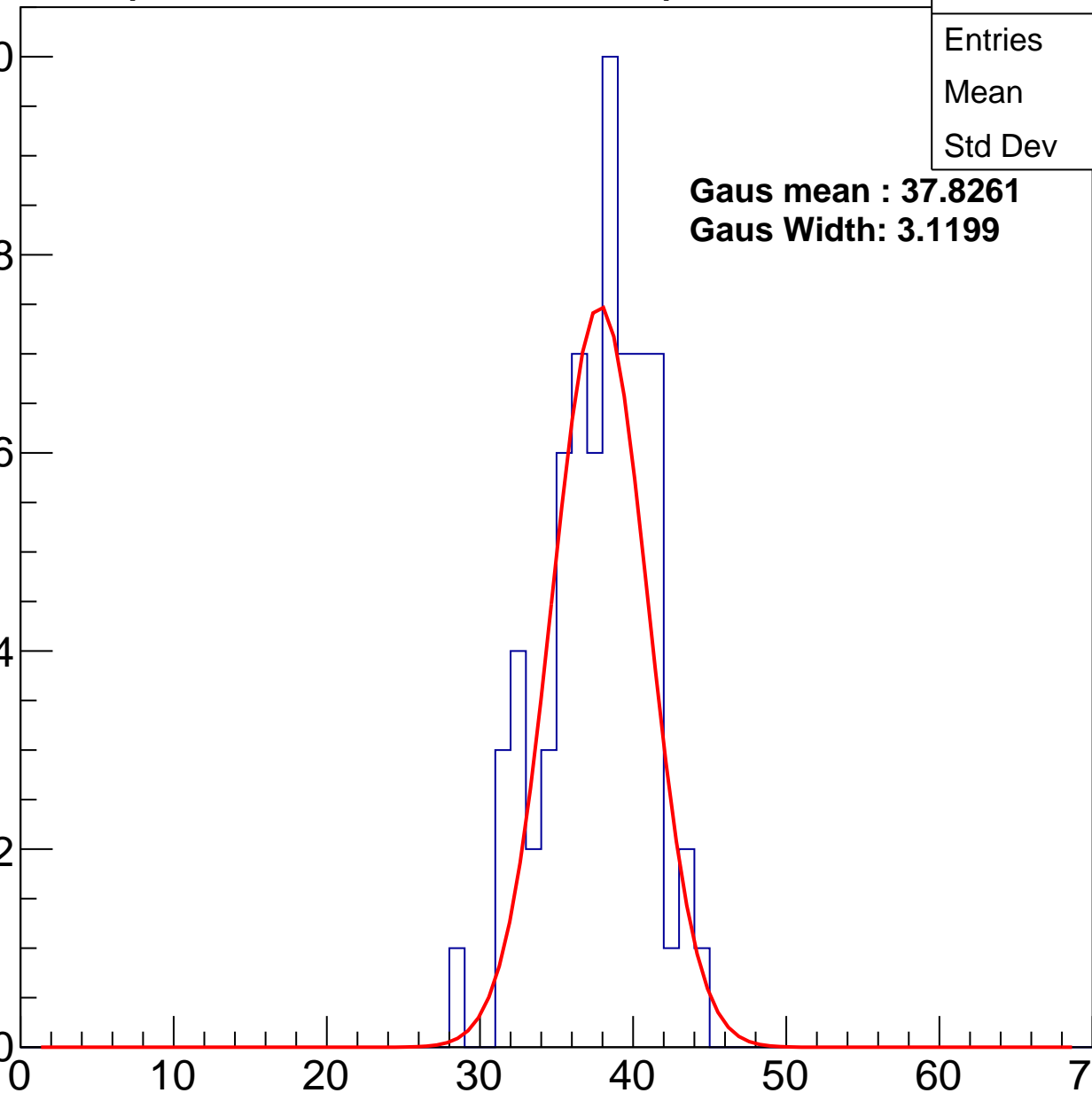
Gaus mean : 37.8261

Gaus Width: 3.1199

Entry

10
8
6
4
2
0

ampl



B1L103S, U12-ch2, adc2

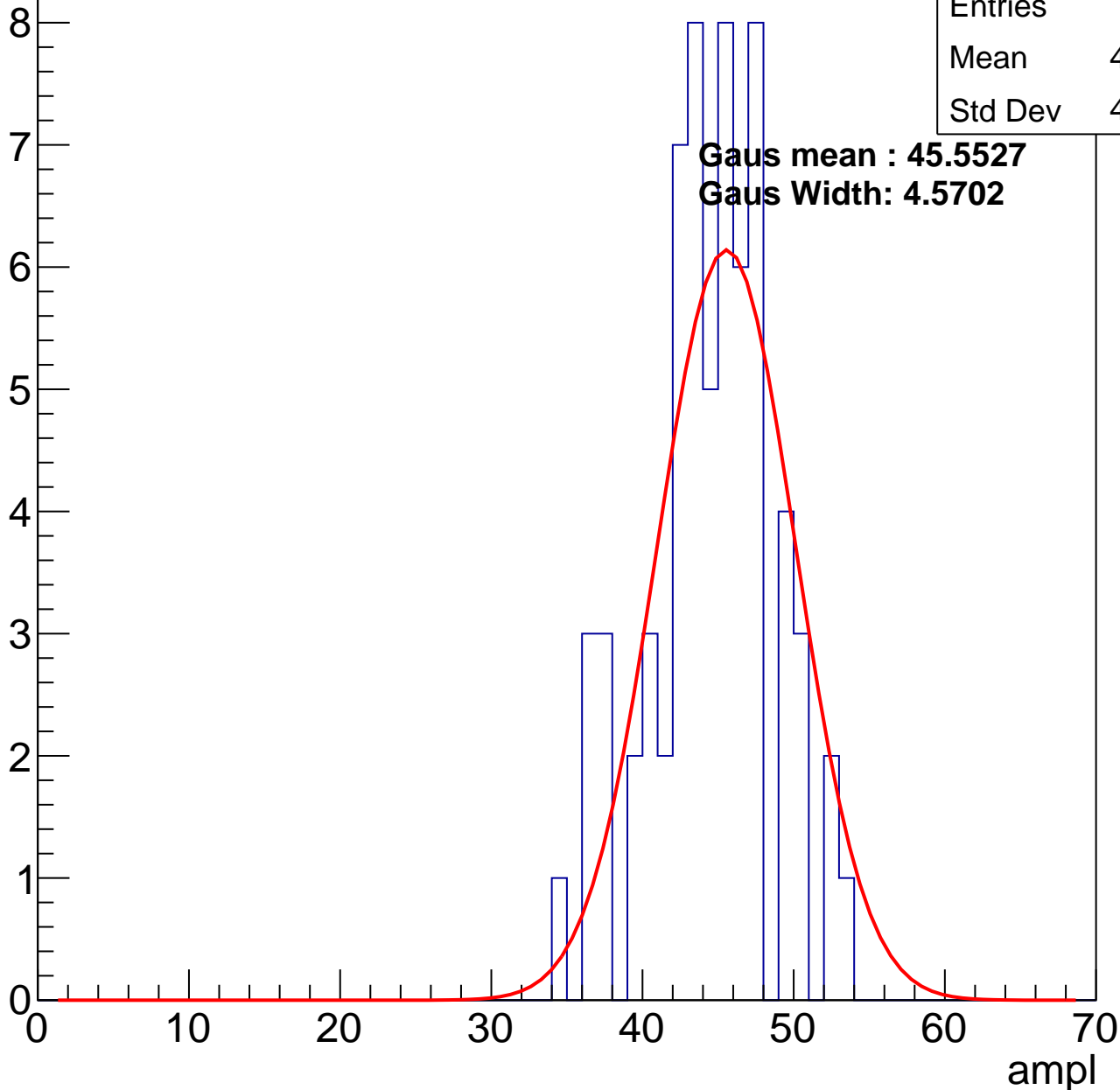
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	44.03
Std Dev	4.105

Gaus mean : 45.5527

Gaus Width: 4.5702

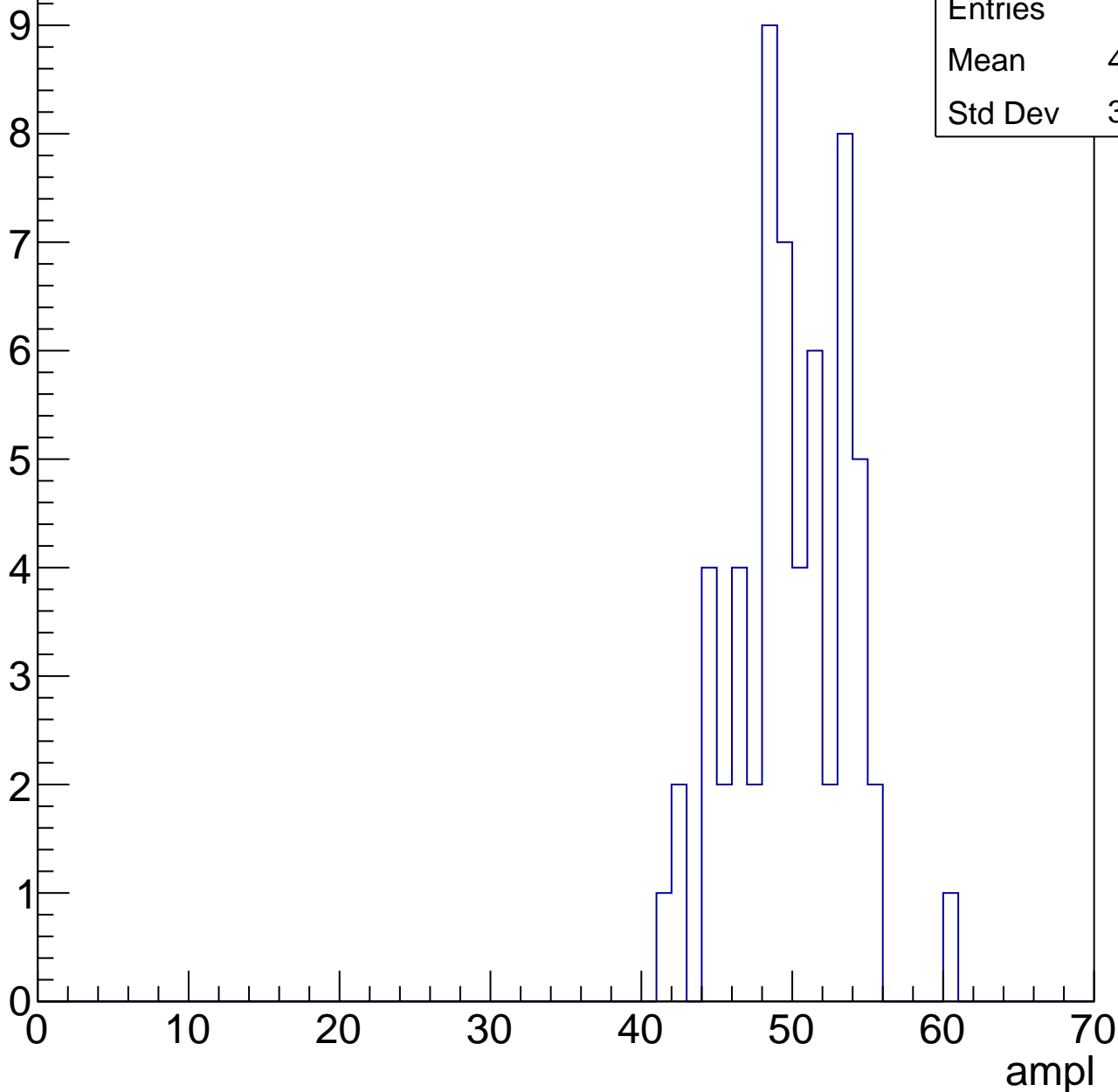


B1L103S, U12-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.46
Std Dev	3.743

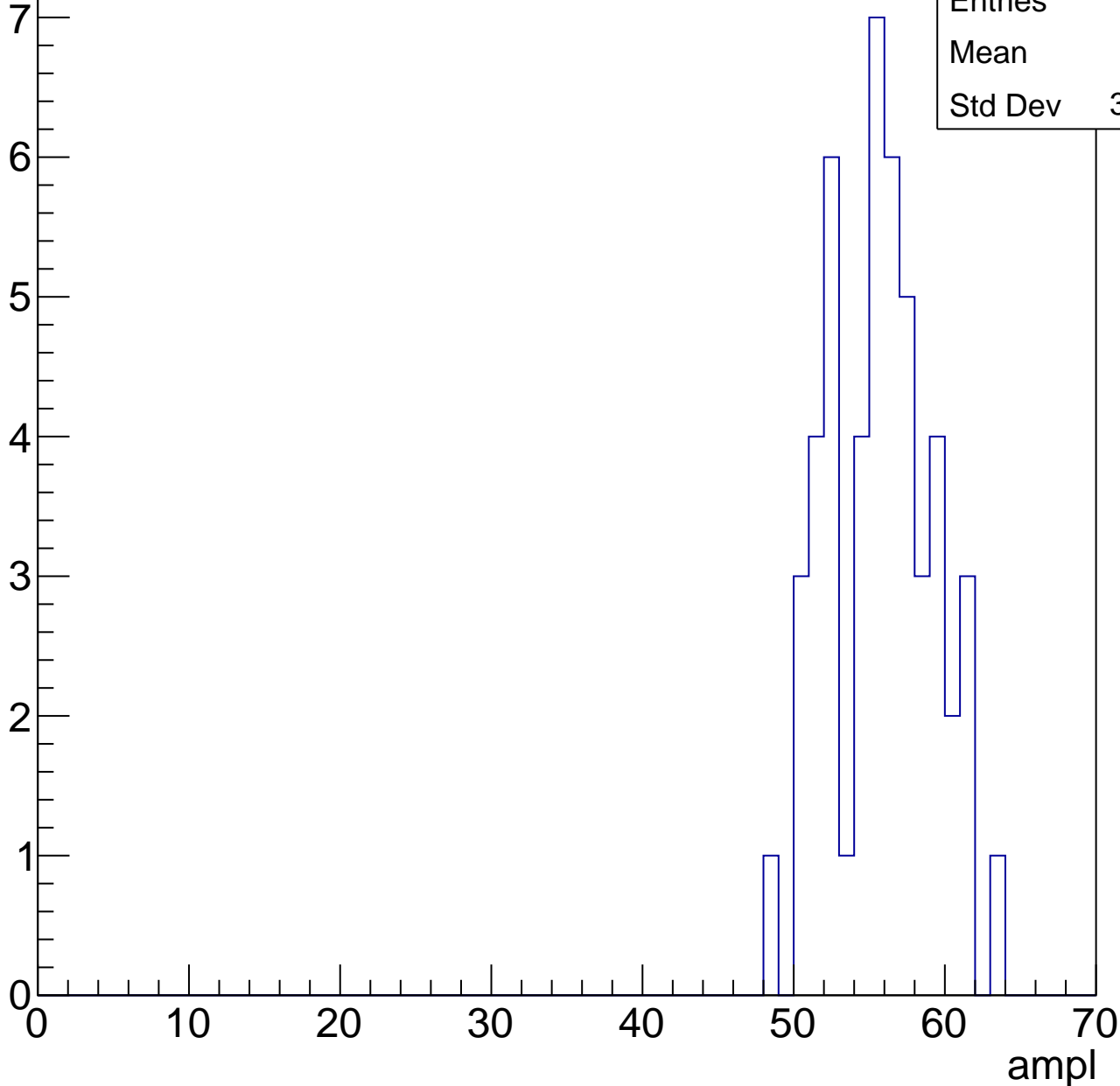


B1L103S, U12-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.3
Std Dev	3.419

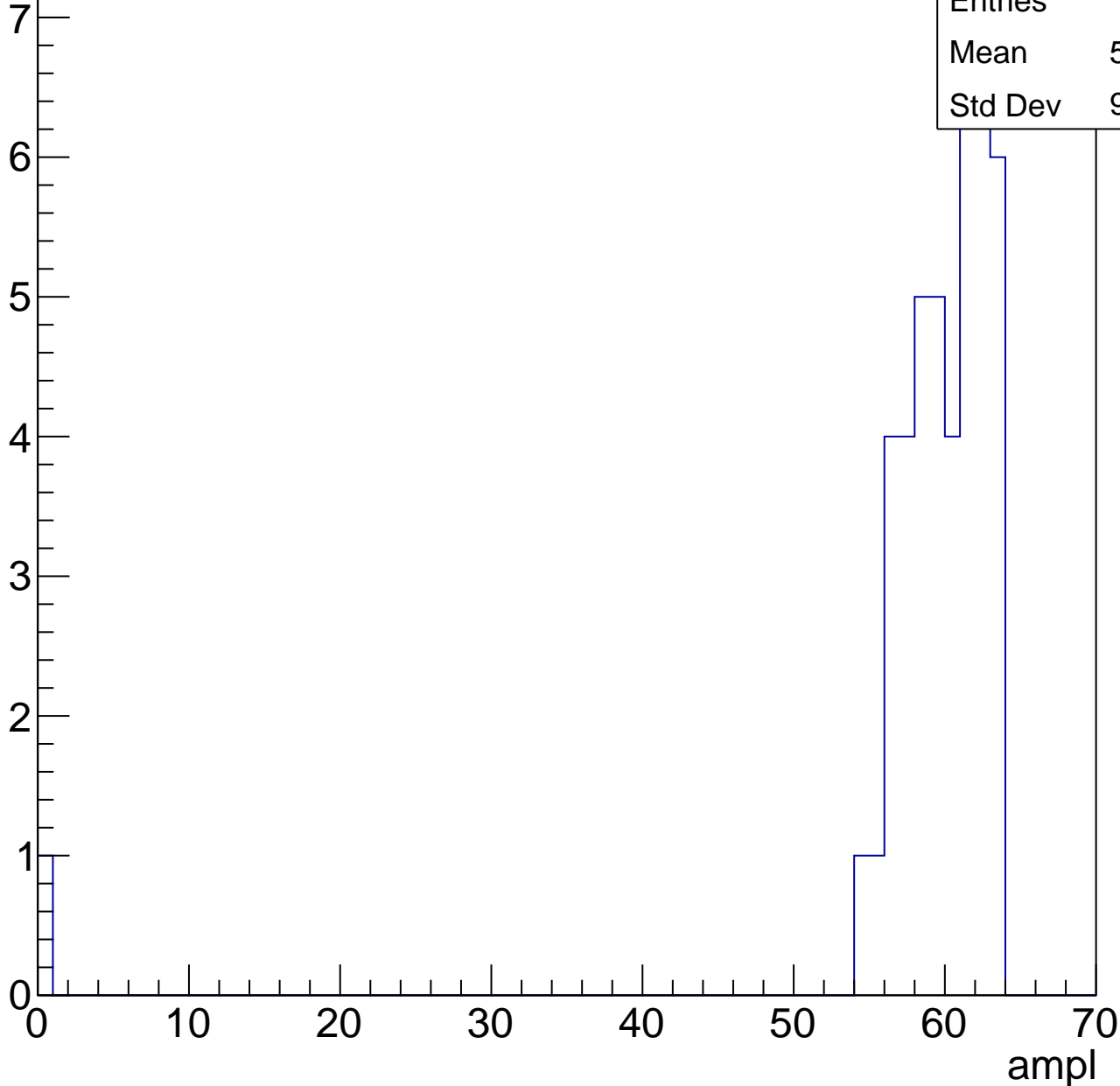


B1L103S, U12-ch2, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

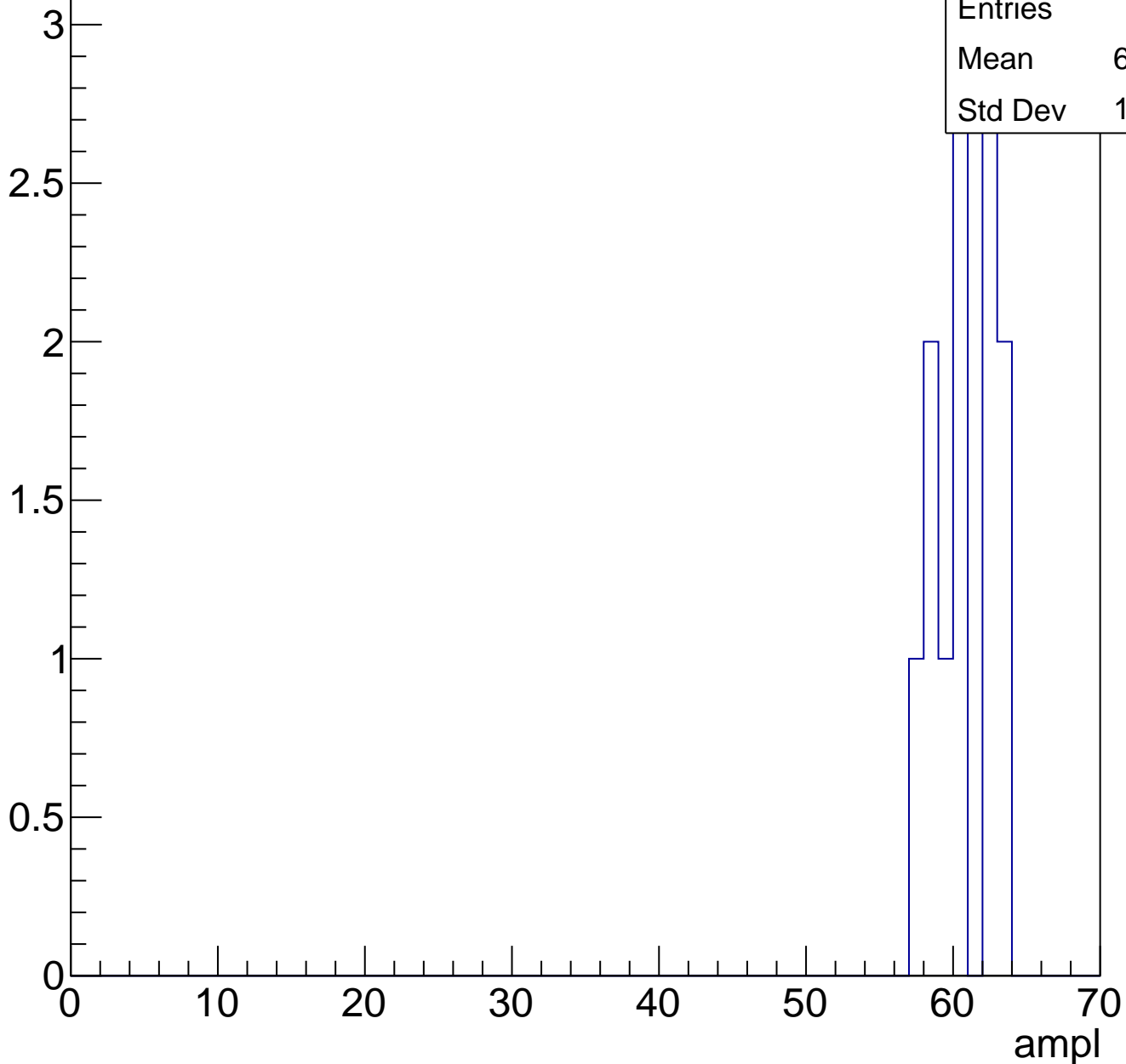
Entries	45
Mean	58.33
Std Dev	9.129



B1L103S, U12-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L103S, U12-ch3, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	93
Mean	32.18
Std Dev	5.469

Gaus mean : 32.8265

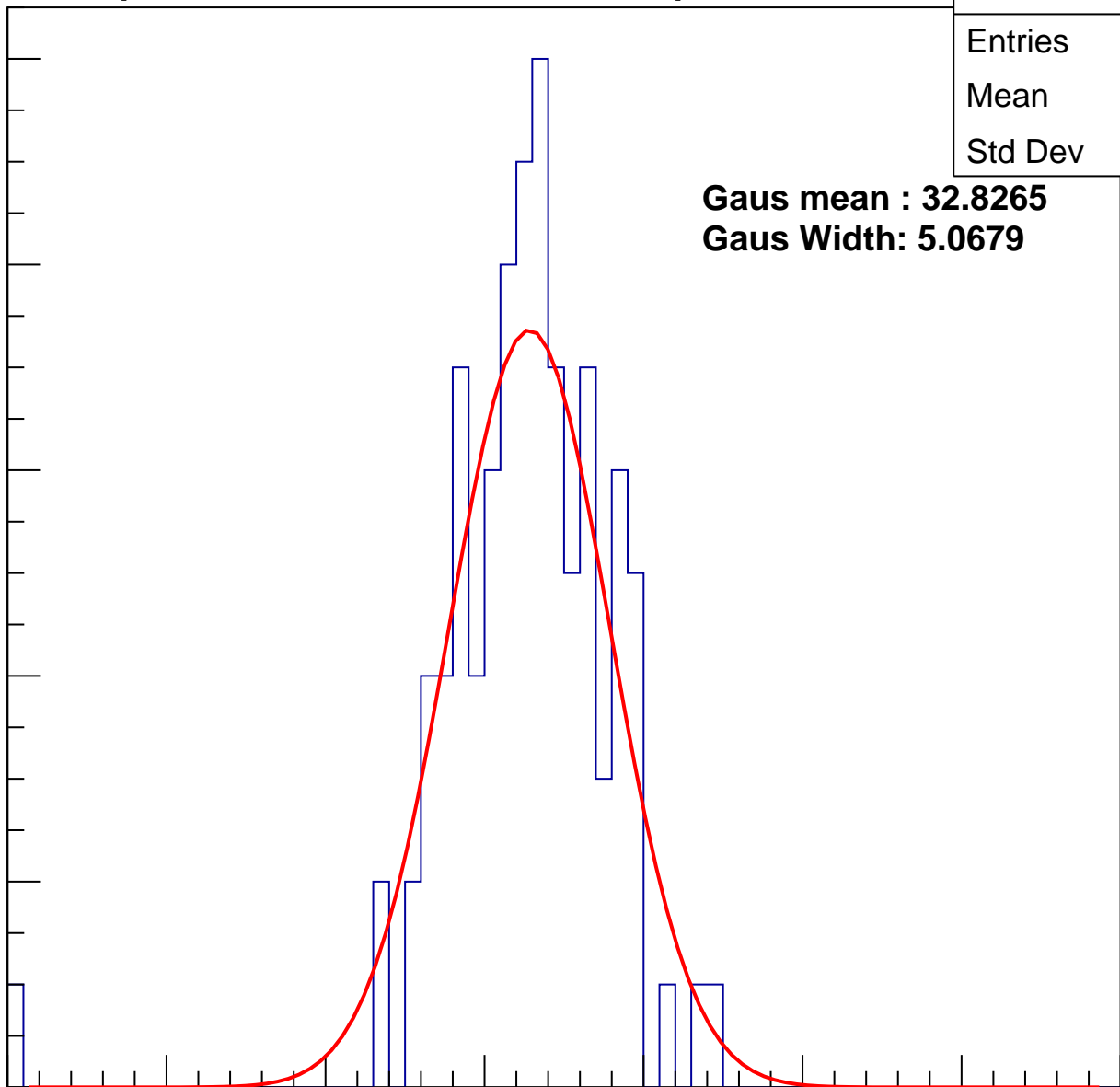
Gaus Width: 5.0679

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch3, adc1

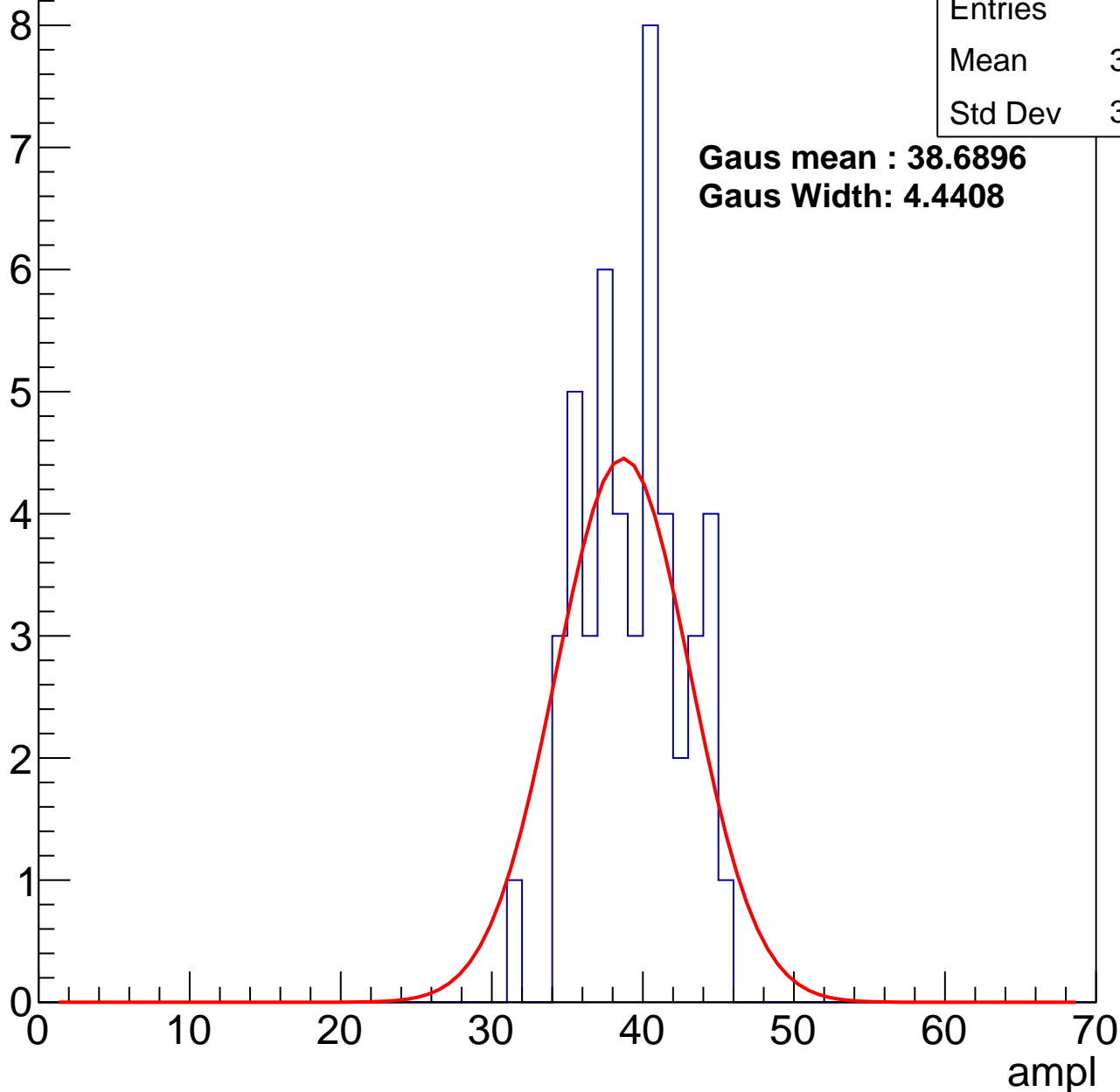
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	38.83
Std Dev	3.257

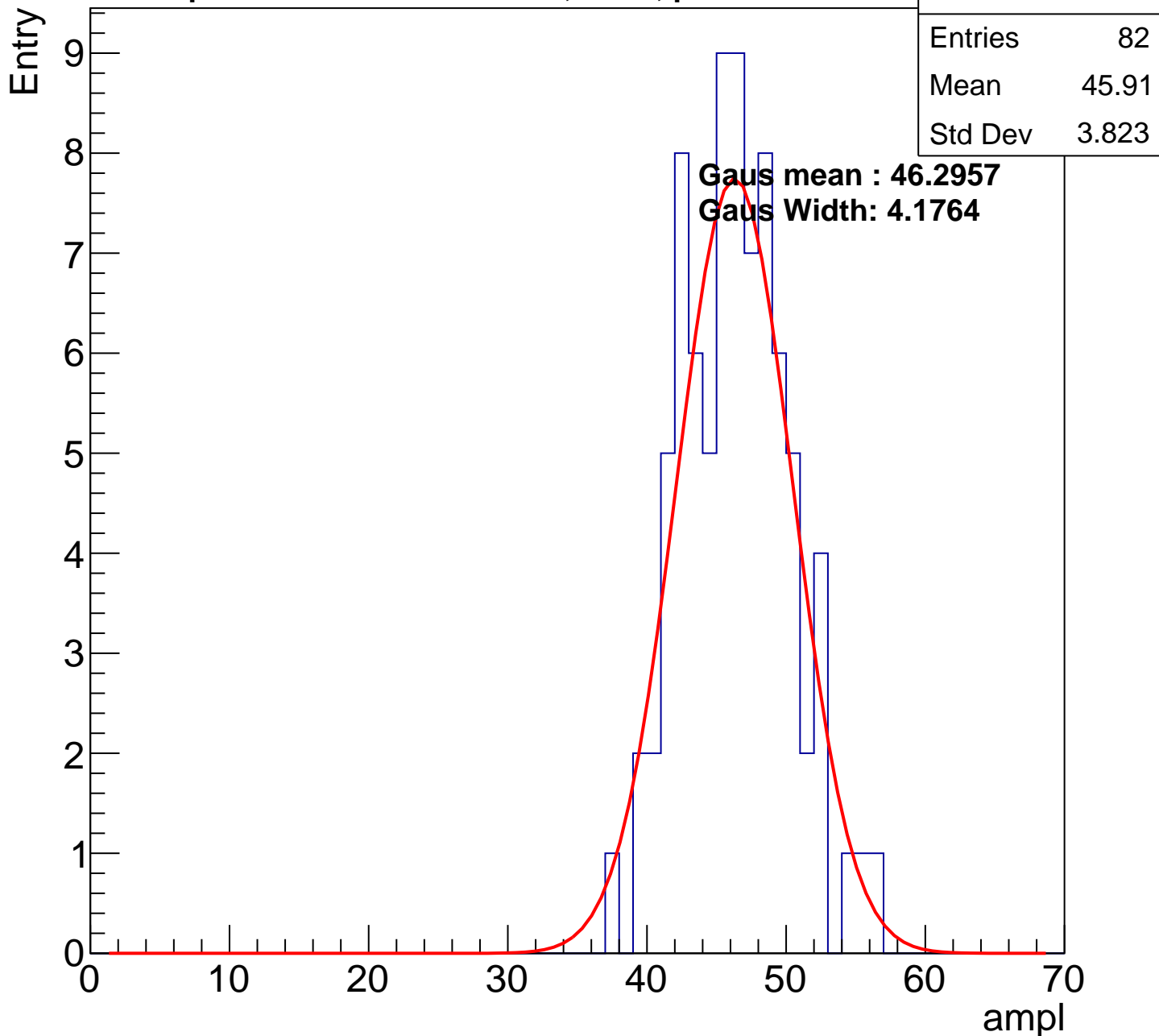
Gaus mean : 38.6896

Gaus Width: 4.4408



B1L103S, U12-ch3, adc2

calib_packv5_042523_0143.root, FC#7, port C2

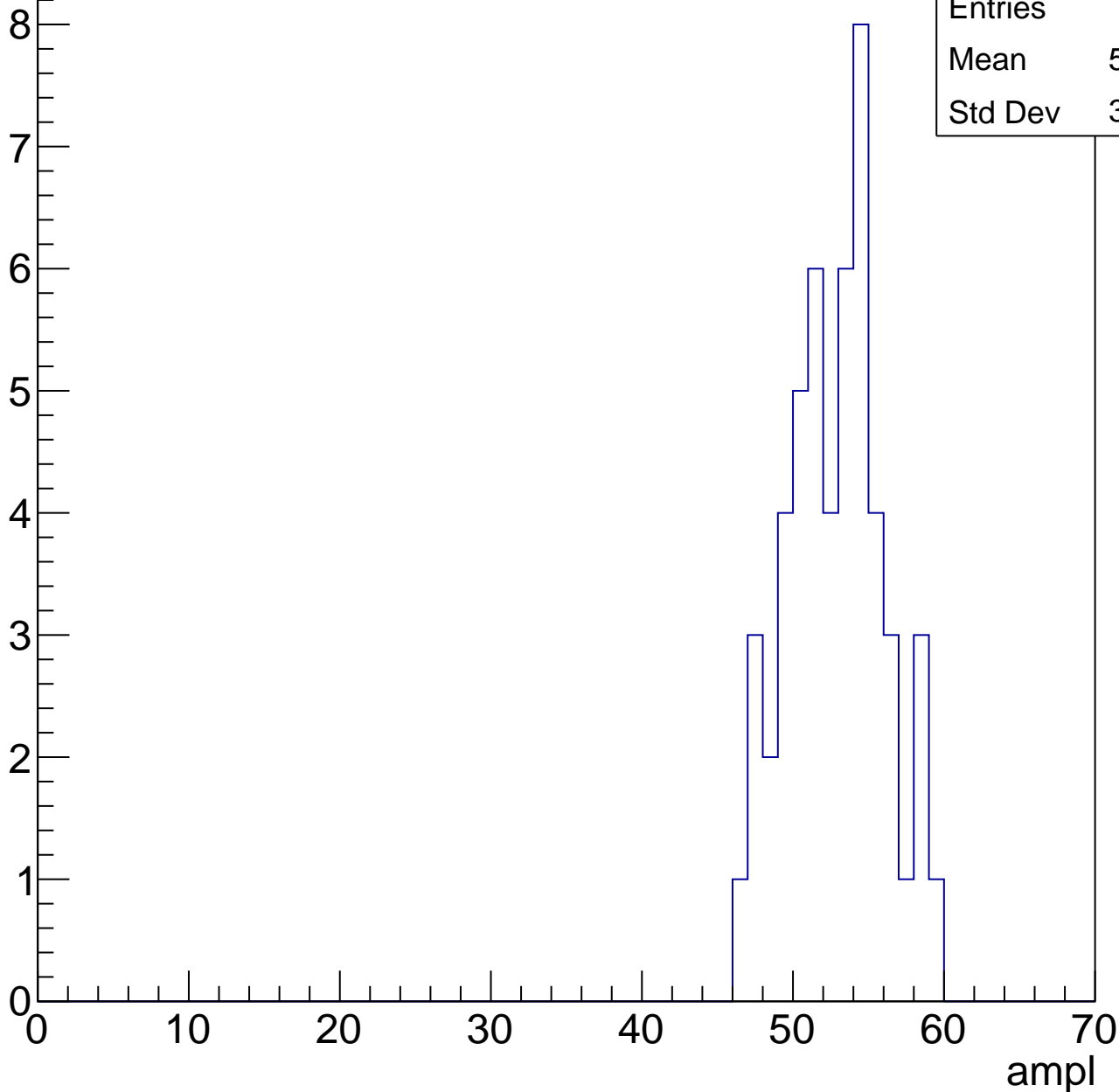


B1L103S, U12-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	52.37
Std Dev	3.137



B1L103S, U12-ch3, adc4

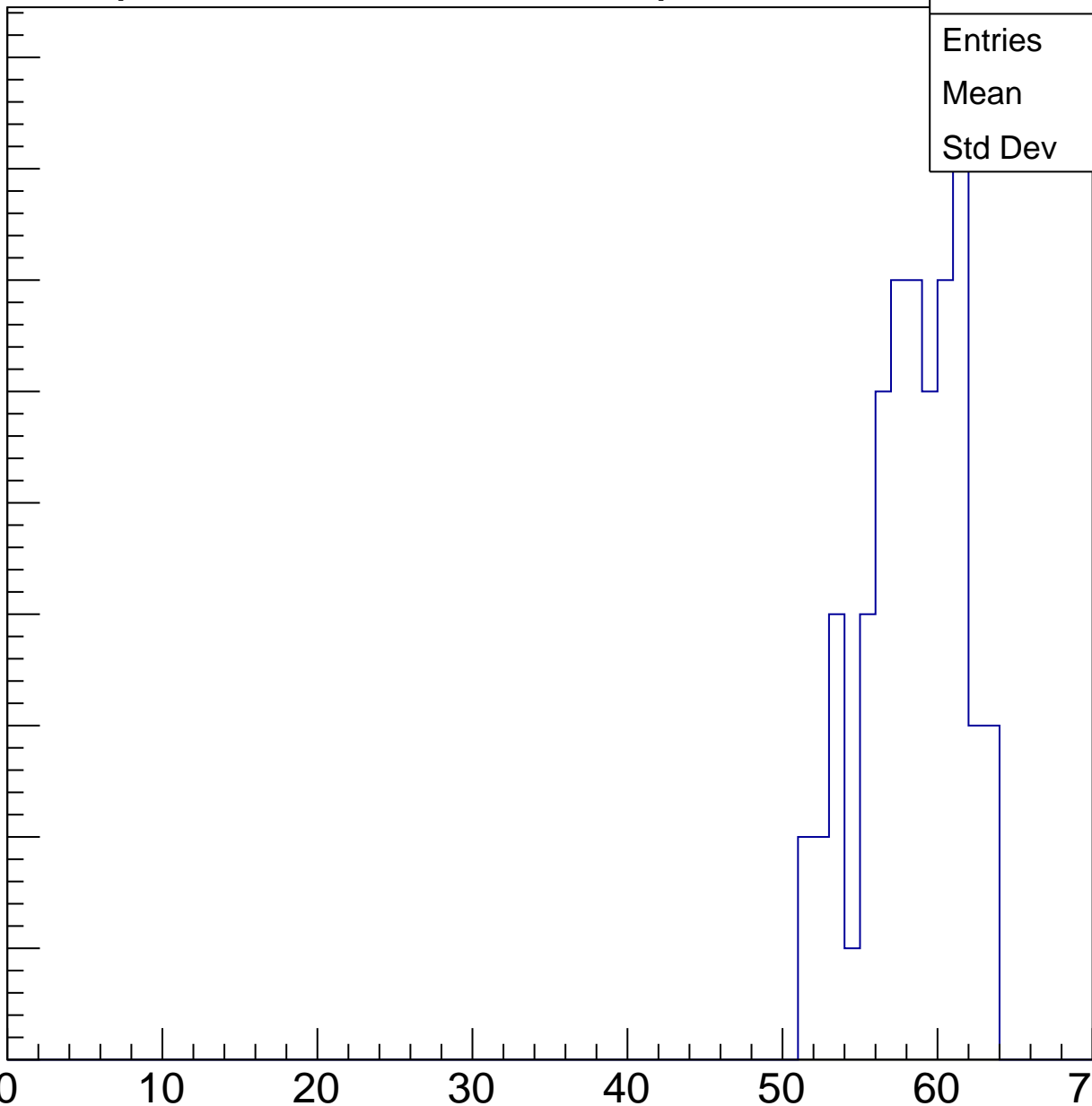
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	61
Mean	57.89
Std Dev	3.111

ampl

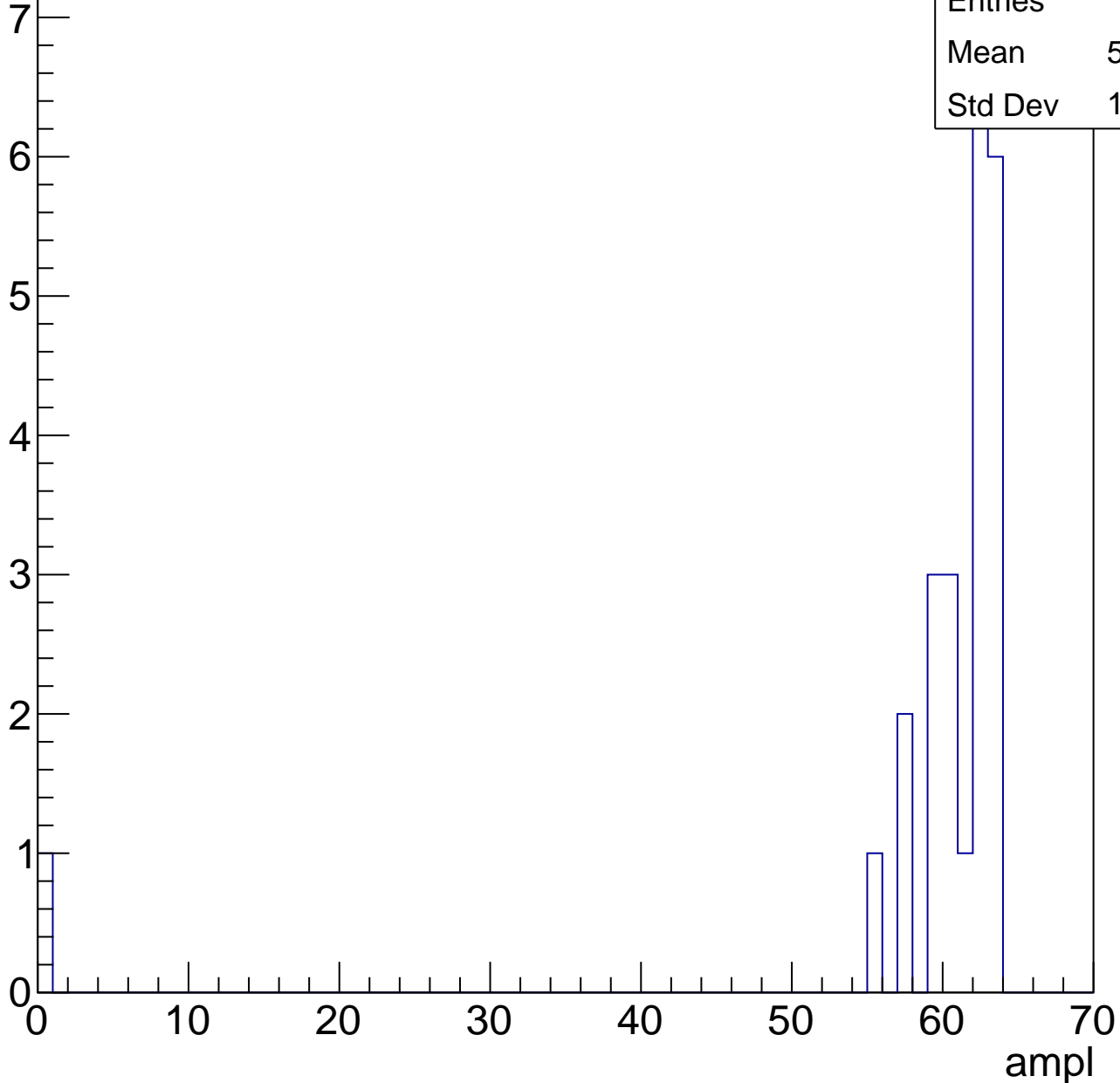


B1L103S, U12-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	58.29
Std Dev	12.35



B1L103S, U12-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch4, adc0

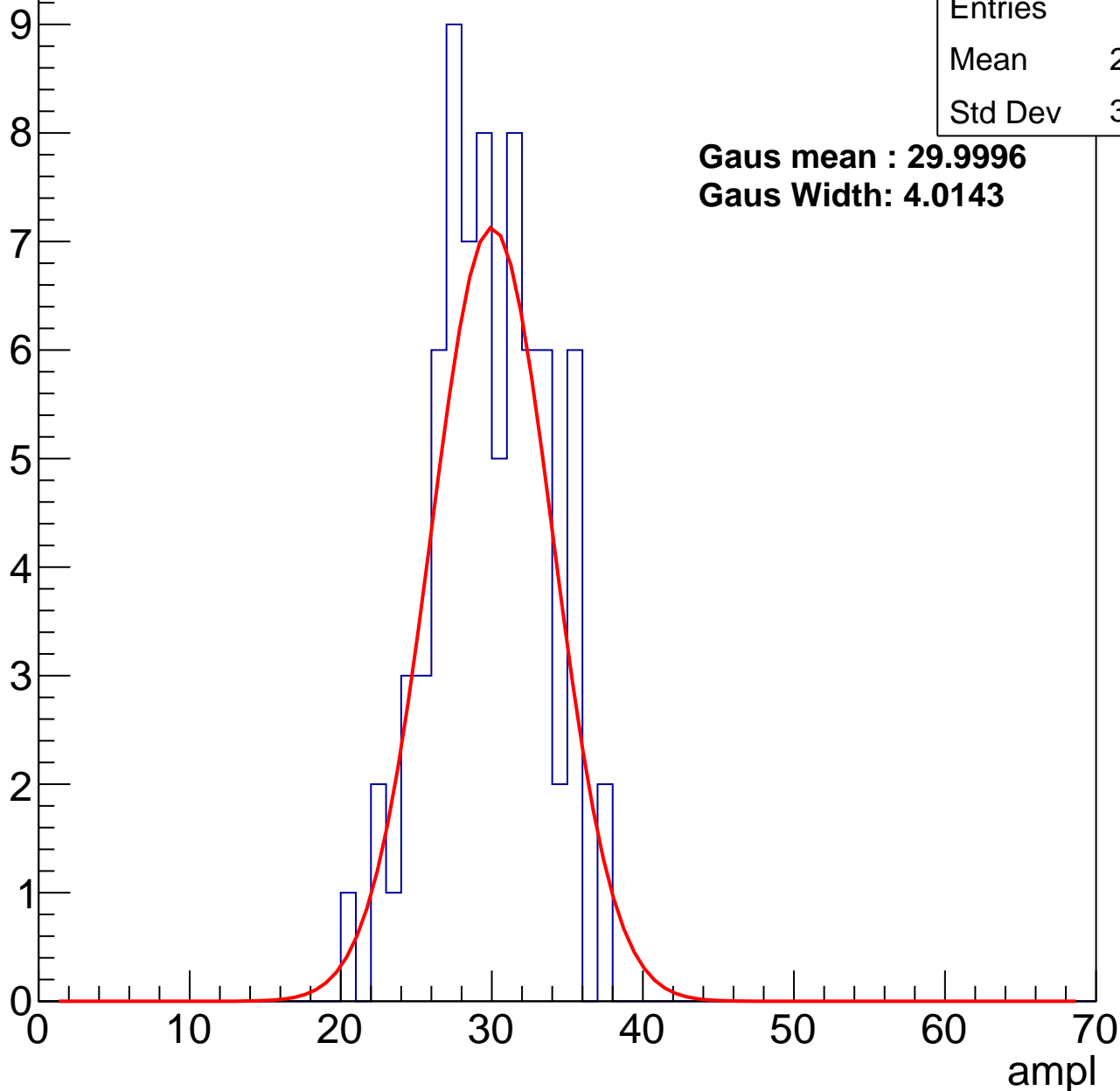
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	29.35
Std Dev	3.664

Gaus mean : 29.9996

Gaus Width: 4.0143



B1L103S, U12-ch4, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	37.03
Std Dev	3.683

Gaus mean : 37.8651

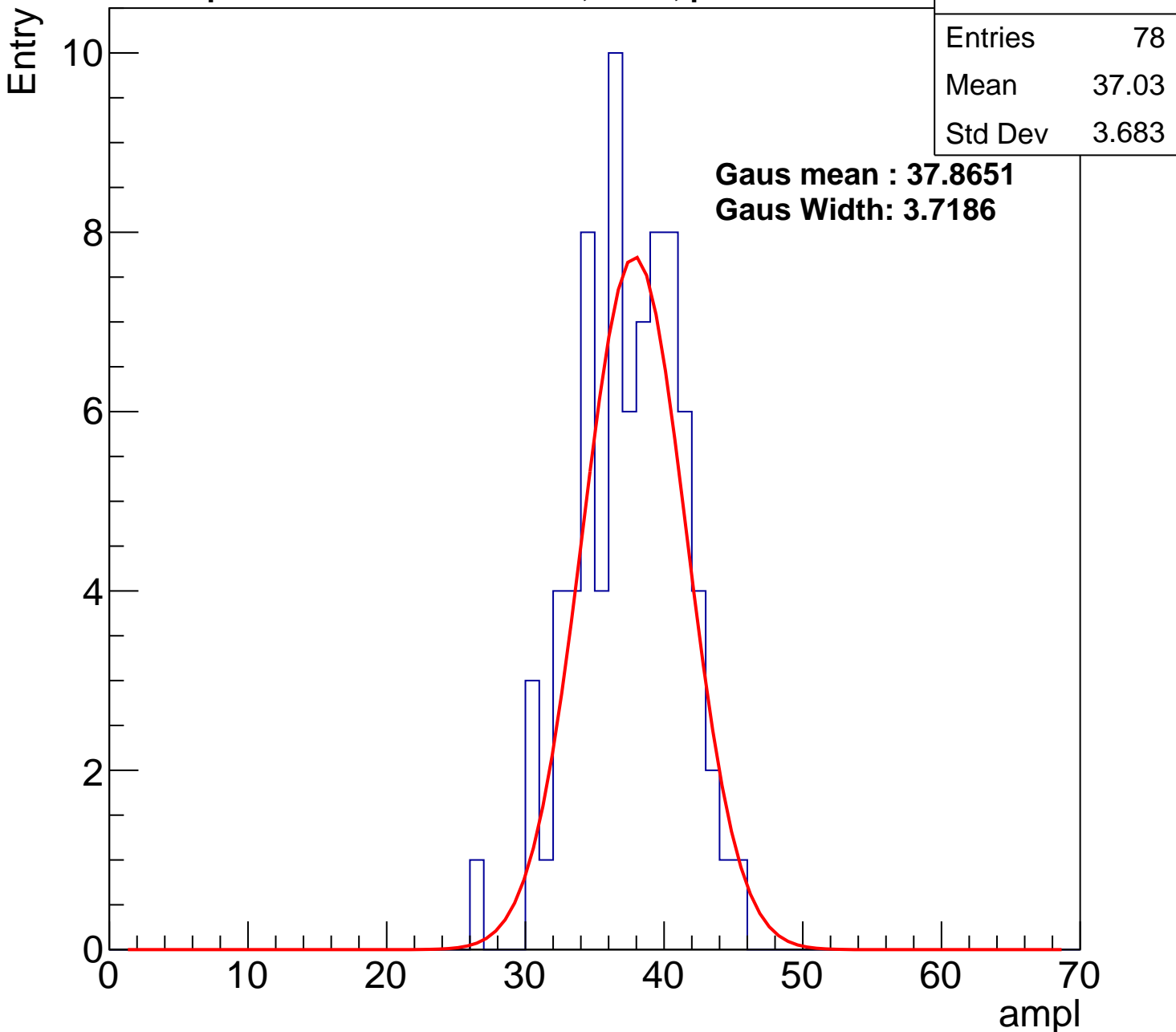
Gaus Width: 3.7186

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch4, adc2

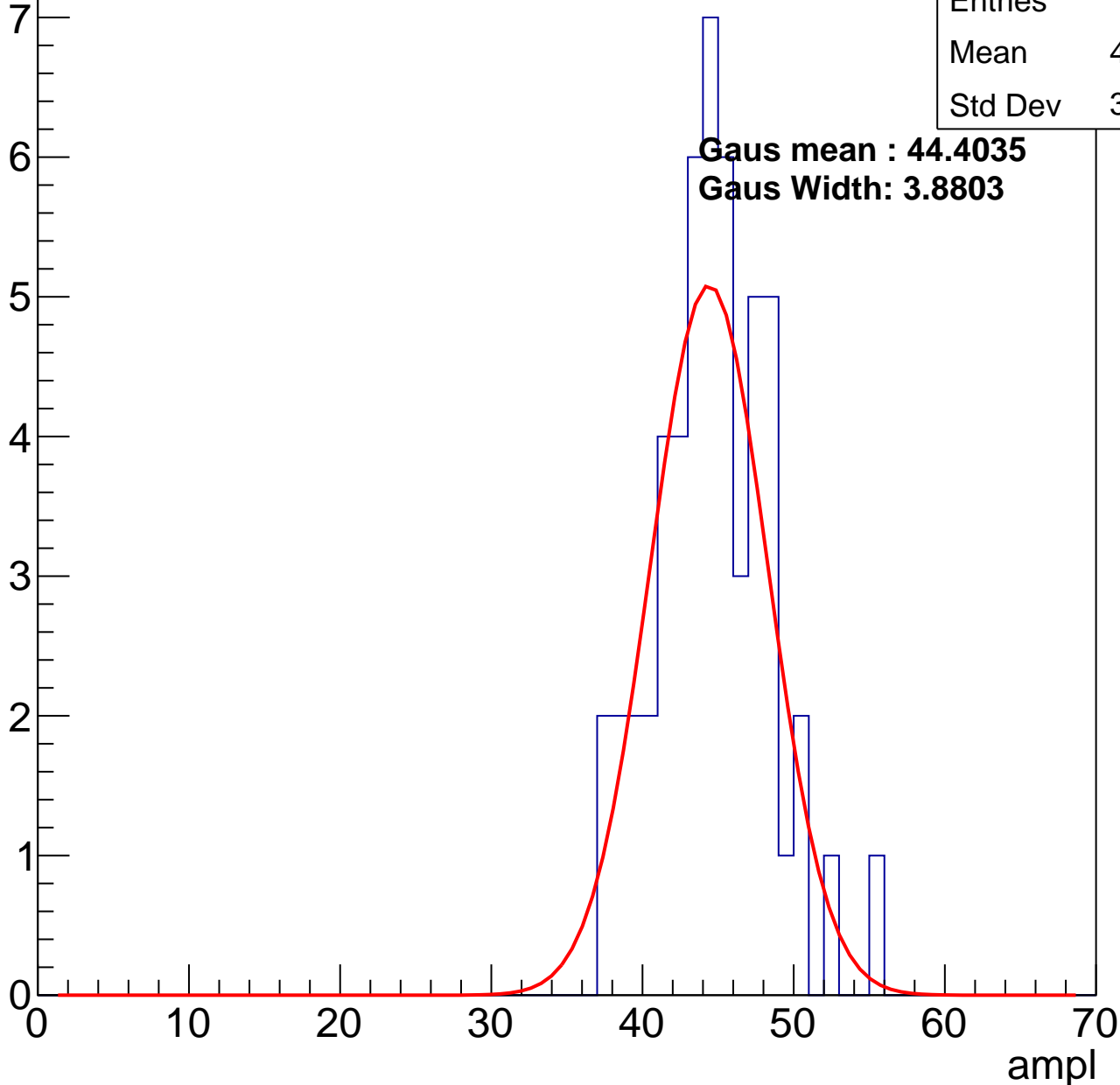
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.25
Std Dev	3.716

Gaus mean : 44.4035

Gaus Width: 3.8803

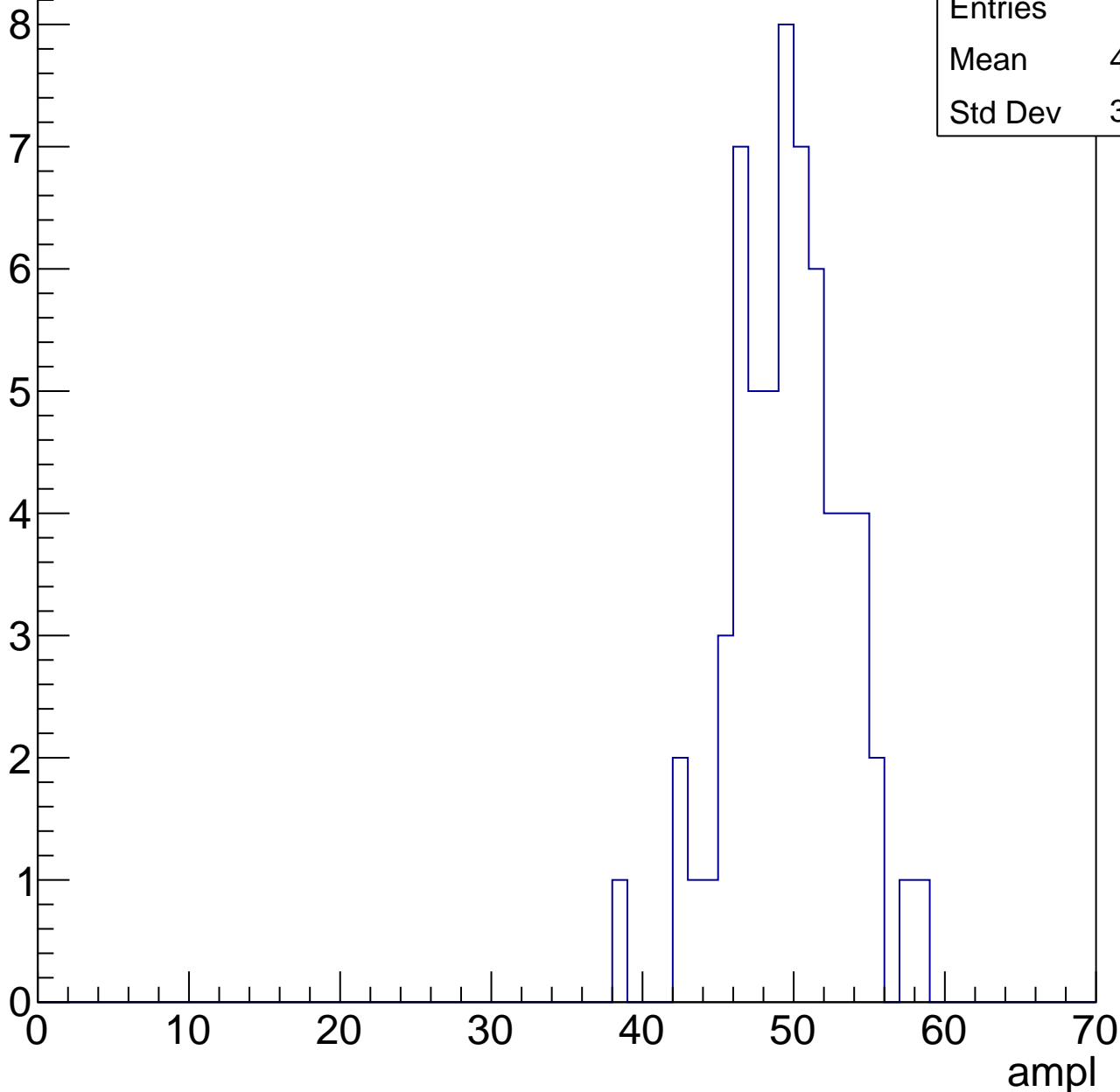


B1L103S, U12-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

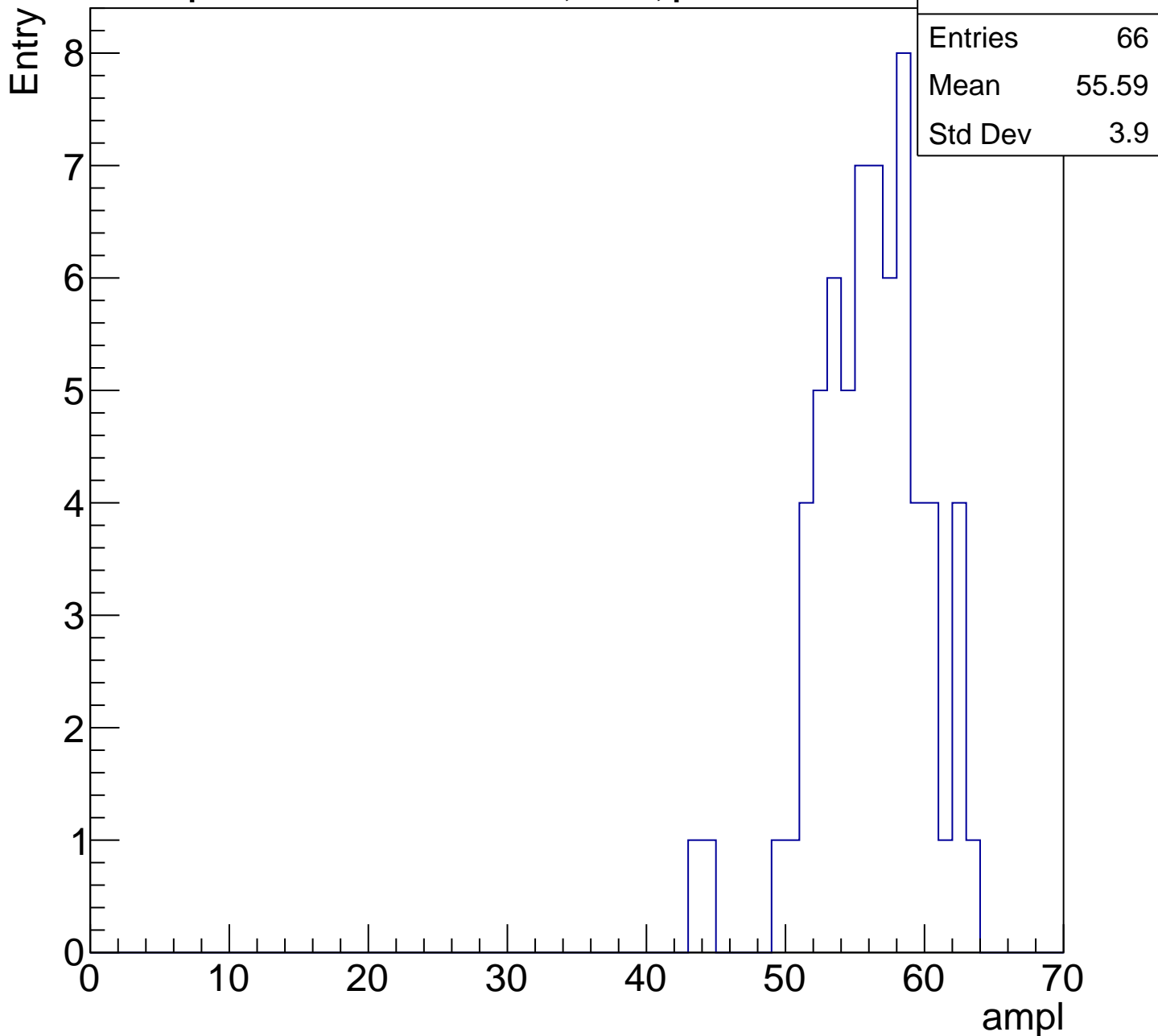
Entry

Entries	62
Mean	49.19
Std Dev	3.728



B1L103S, U12-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

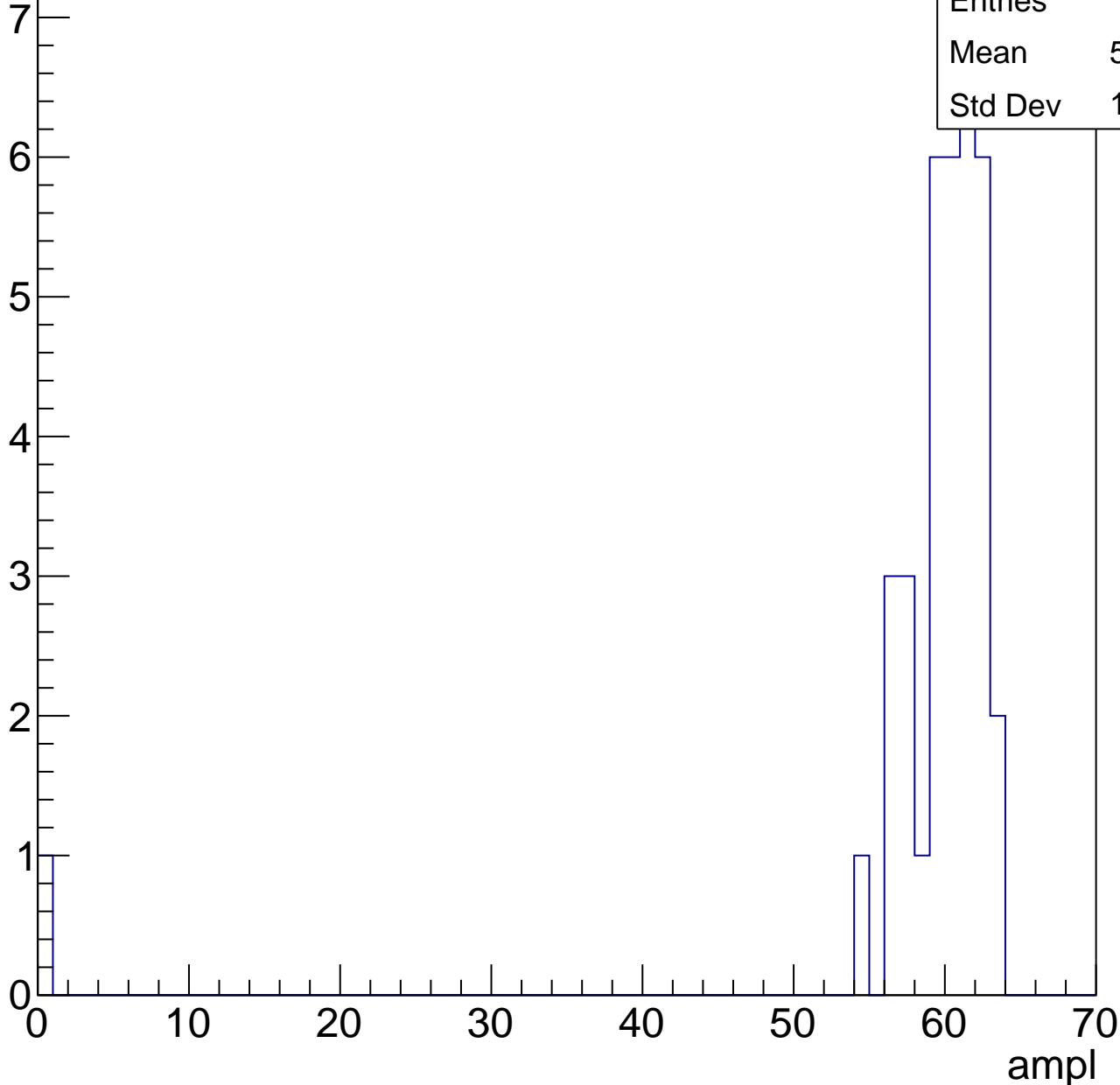


B1L103S, U12-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.06
Std Dev	10.05

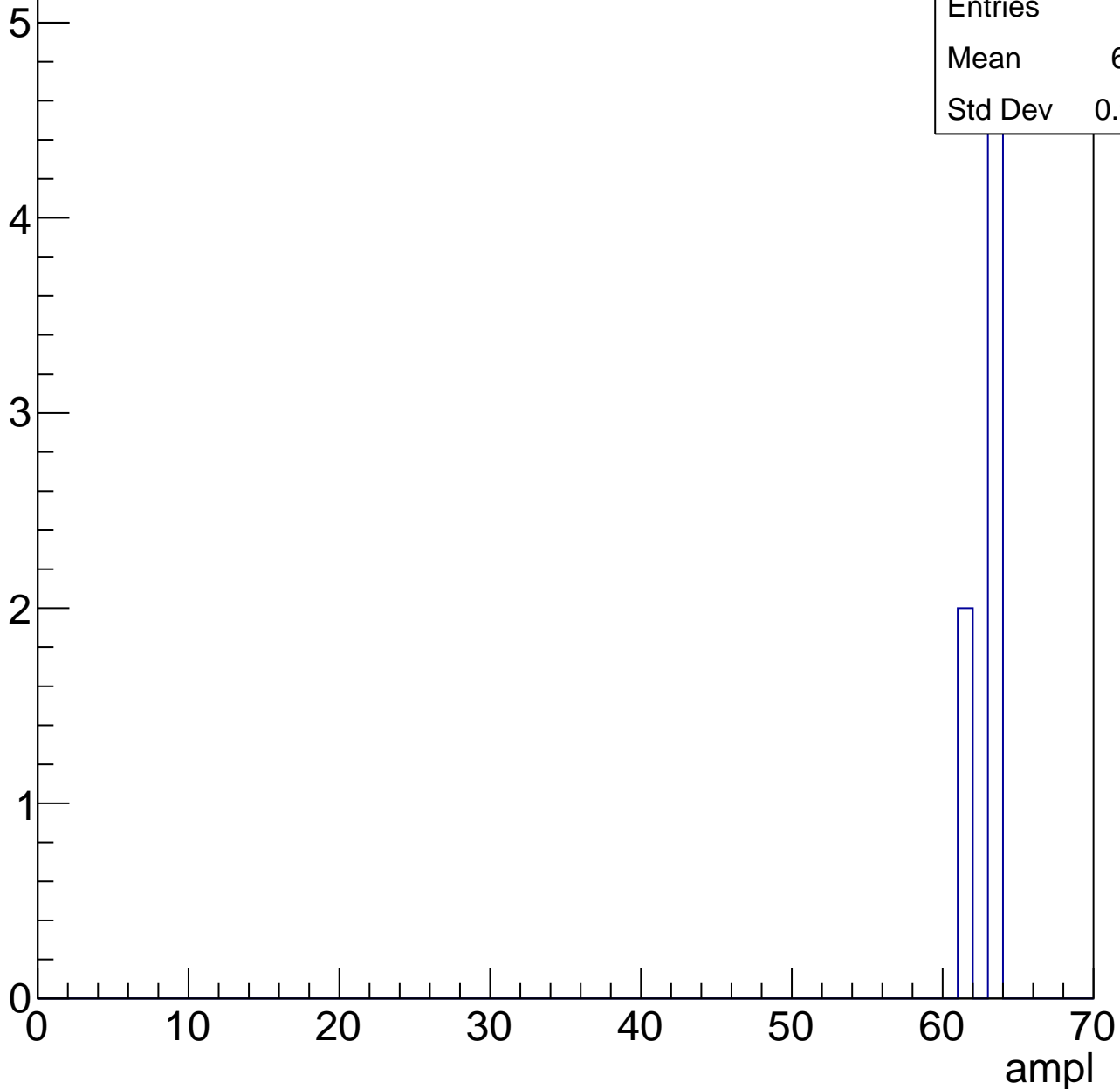


B1L103S, U12-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

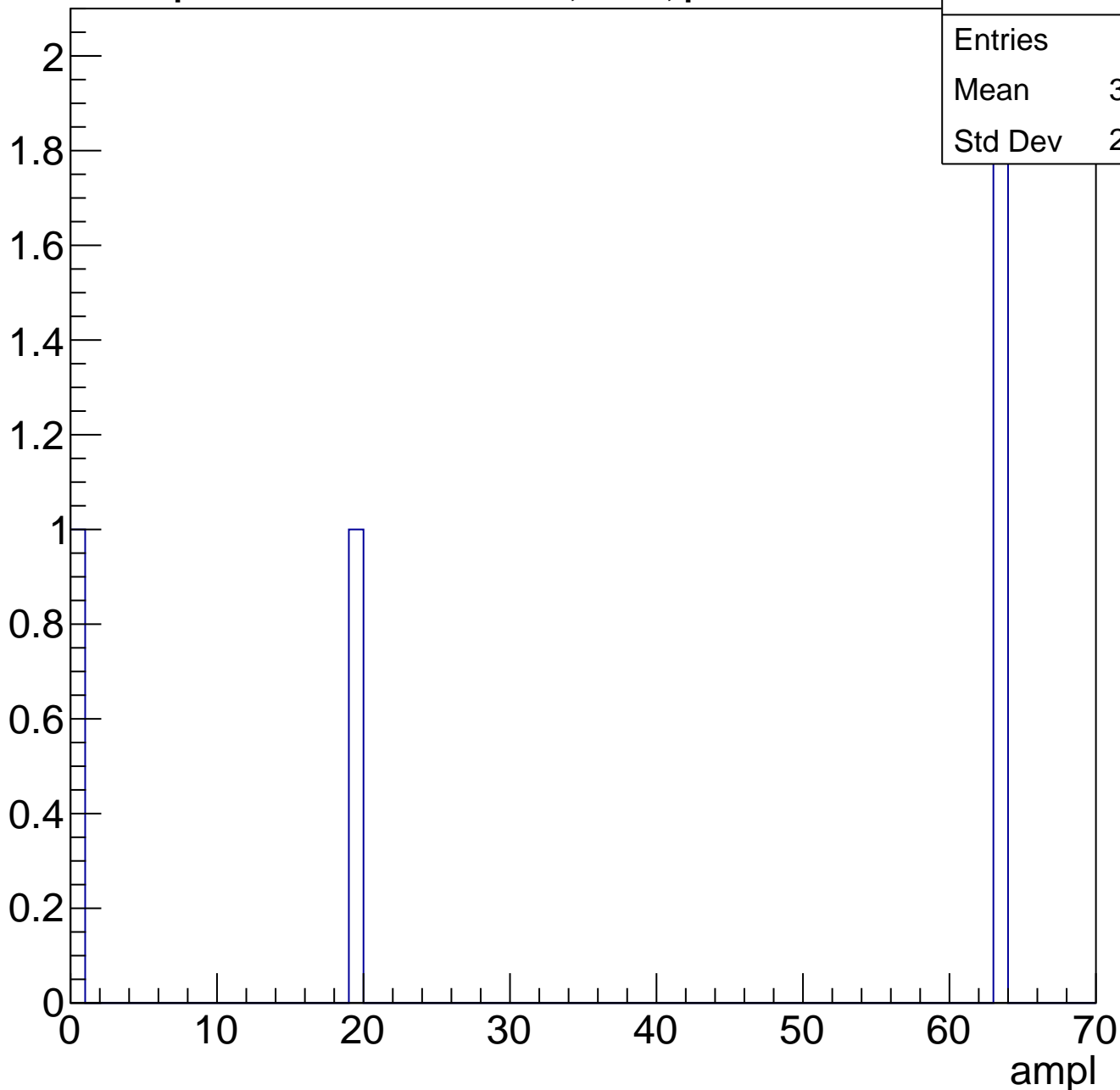
Entries	7
Mean	62.43
Std Dev	0.9035



B1L103S, U12-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch5, adc0

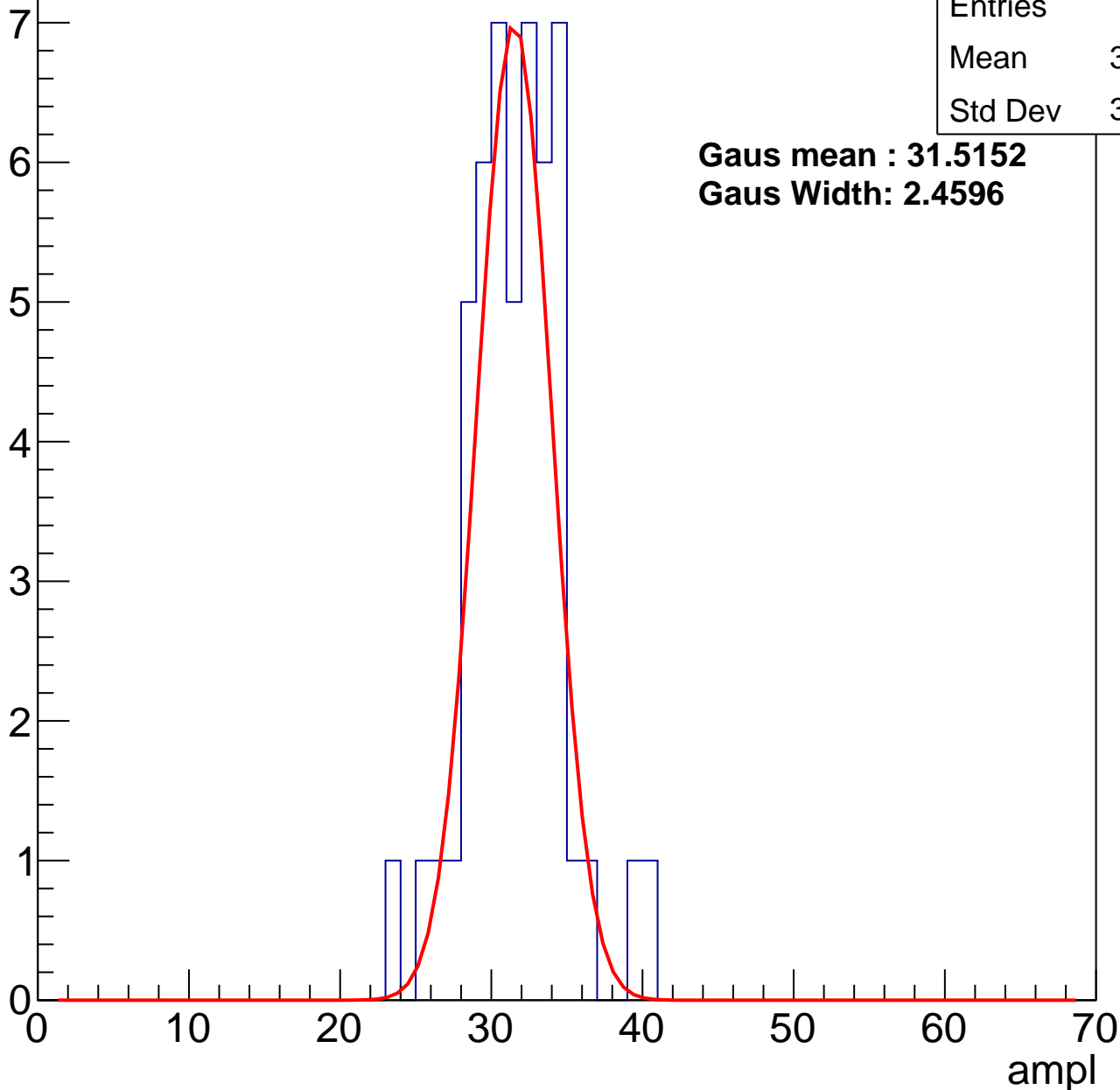
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	31.18
Std Dev	3.117

Gaus mean : 31.5152

Gaus Width: 2.4596



B1L103S, U12-ch5, adc1

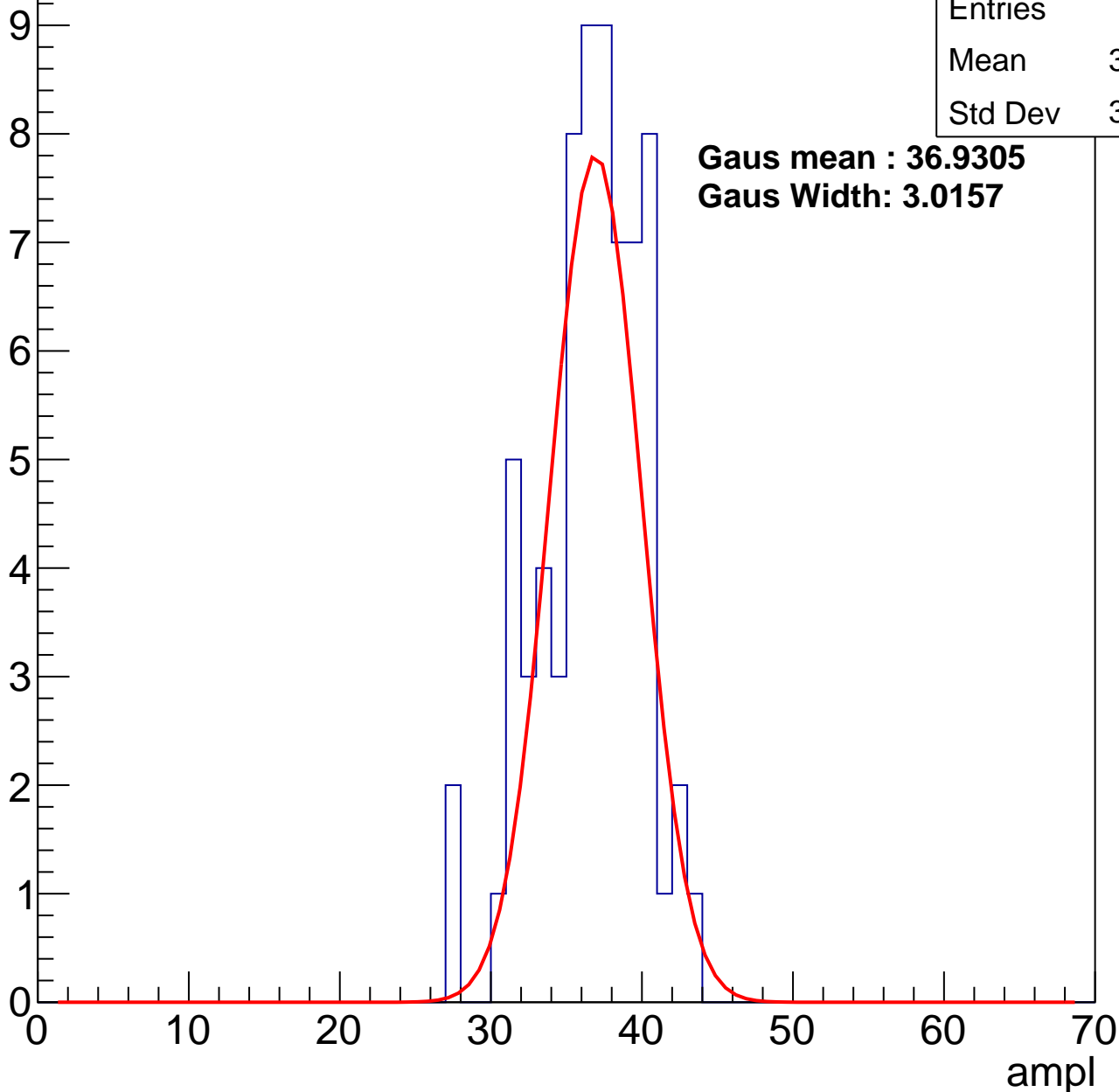
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.19
Std Dev	3.378

Gaus mean : 36.9305

Gaus Width: 3.0157



B1L103S, U12-ch5, adc2

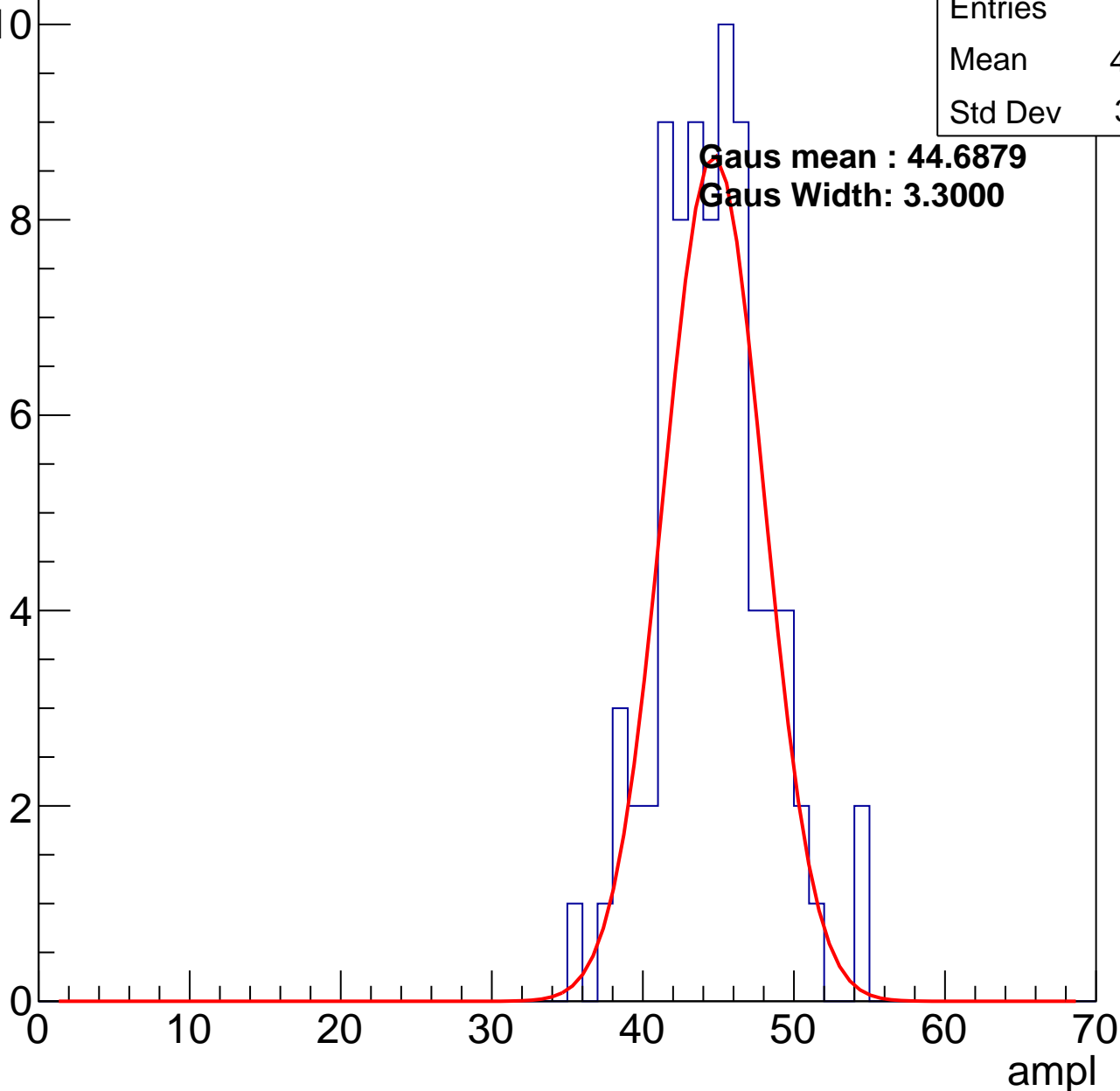
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	44.14
Std Dev	3.571

Gaus mean : 44.6879

Gaus Width: 3.3000

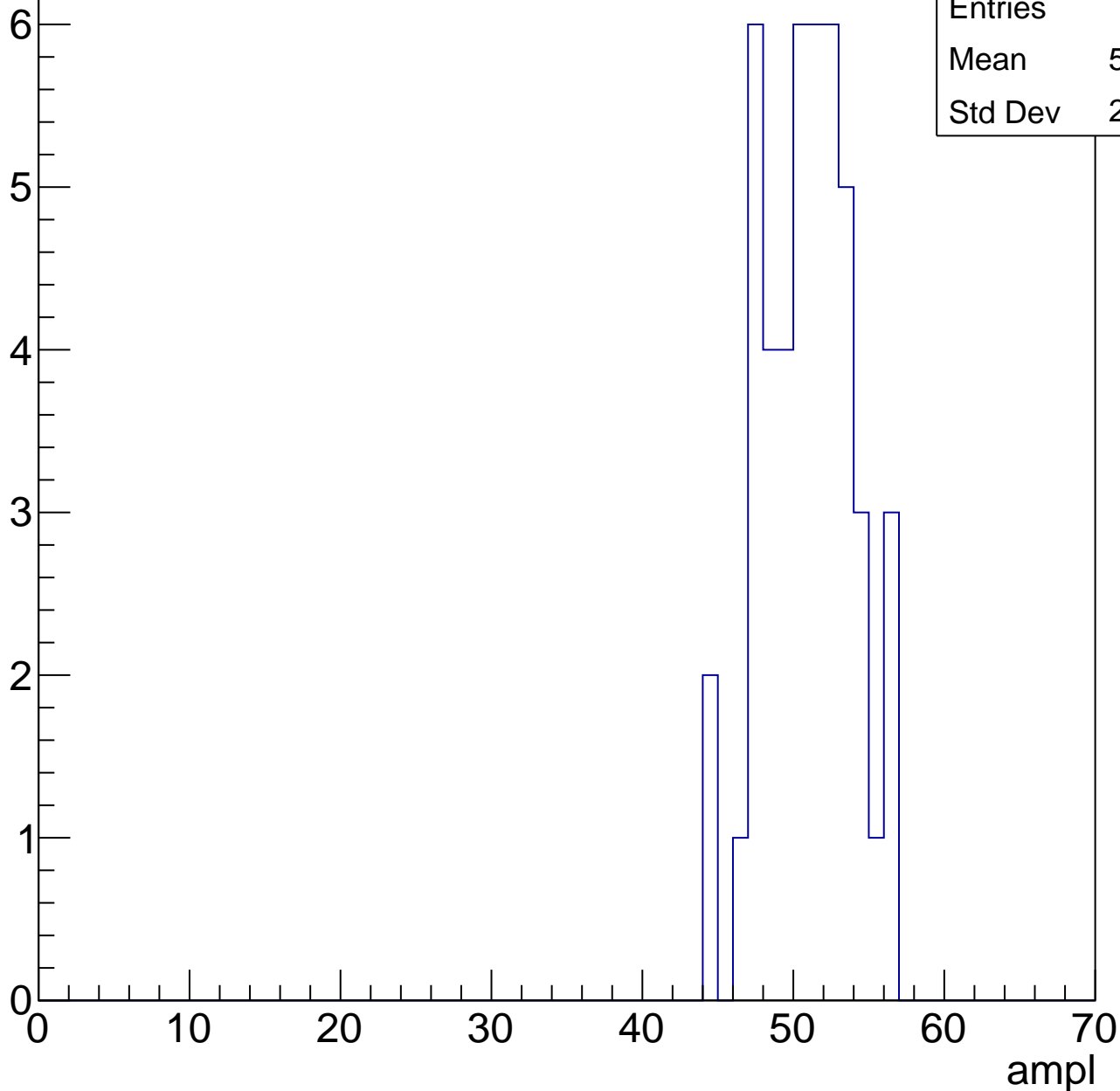


B1L103S, U12-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

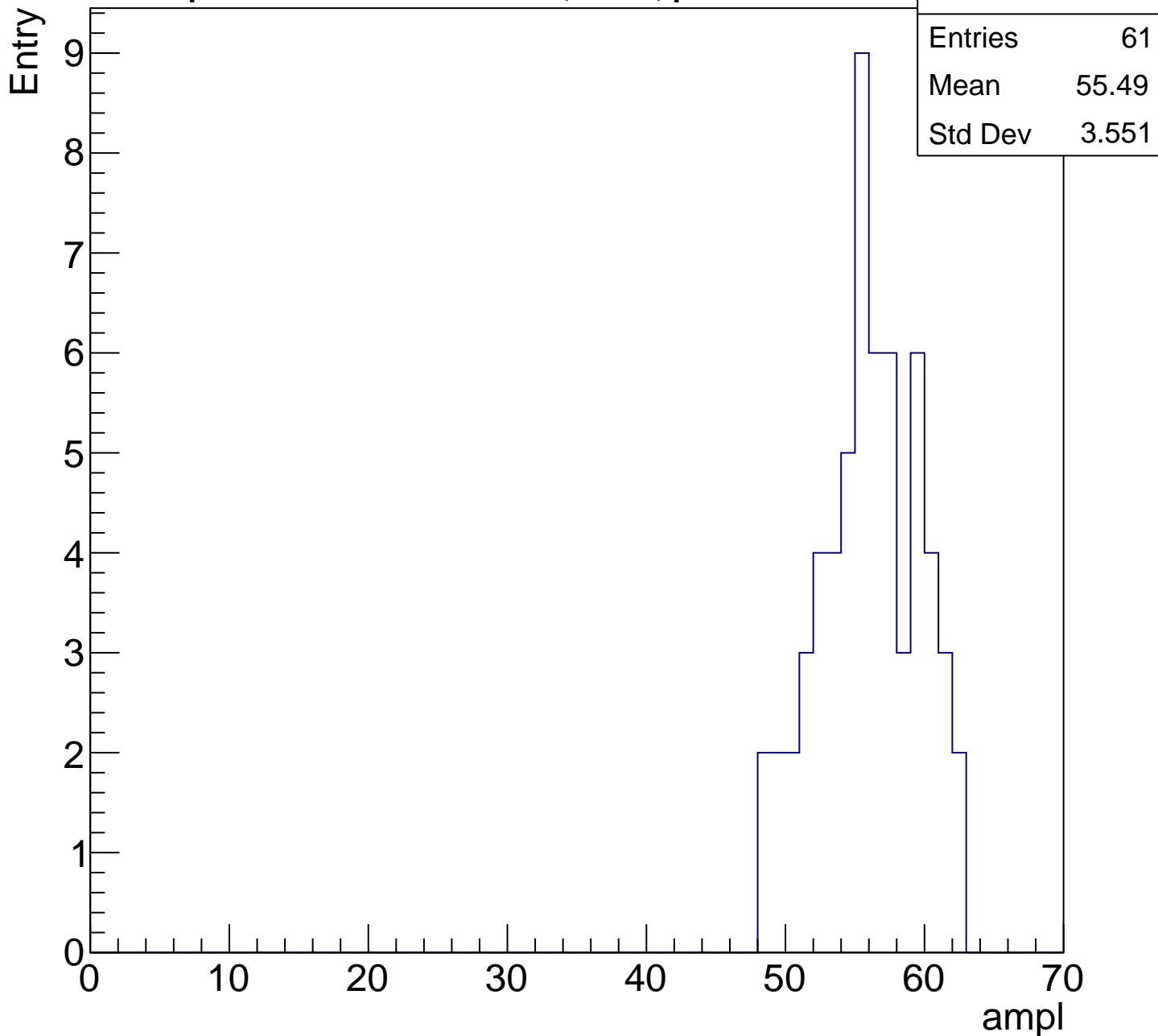
Entry

Entries	47
Mean	50.47
Std Dev	2.945



B1L103S, U12-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

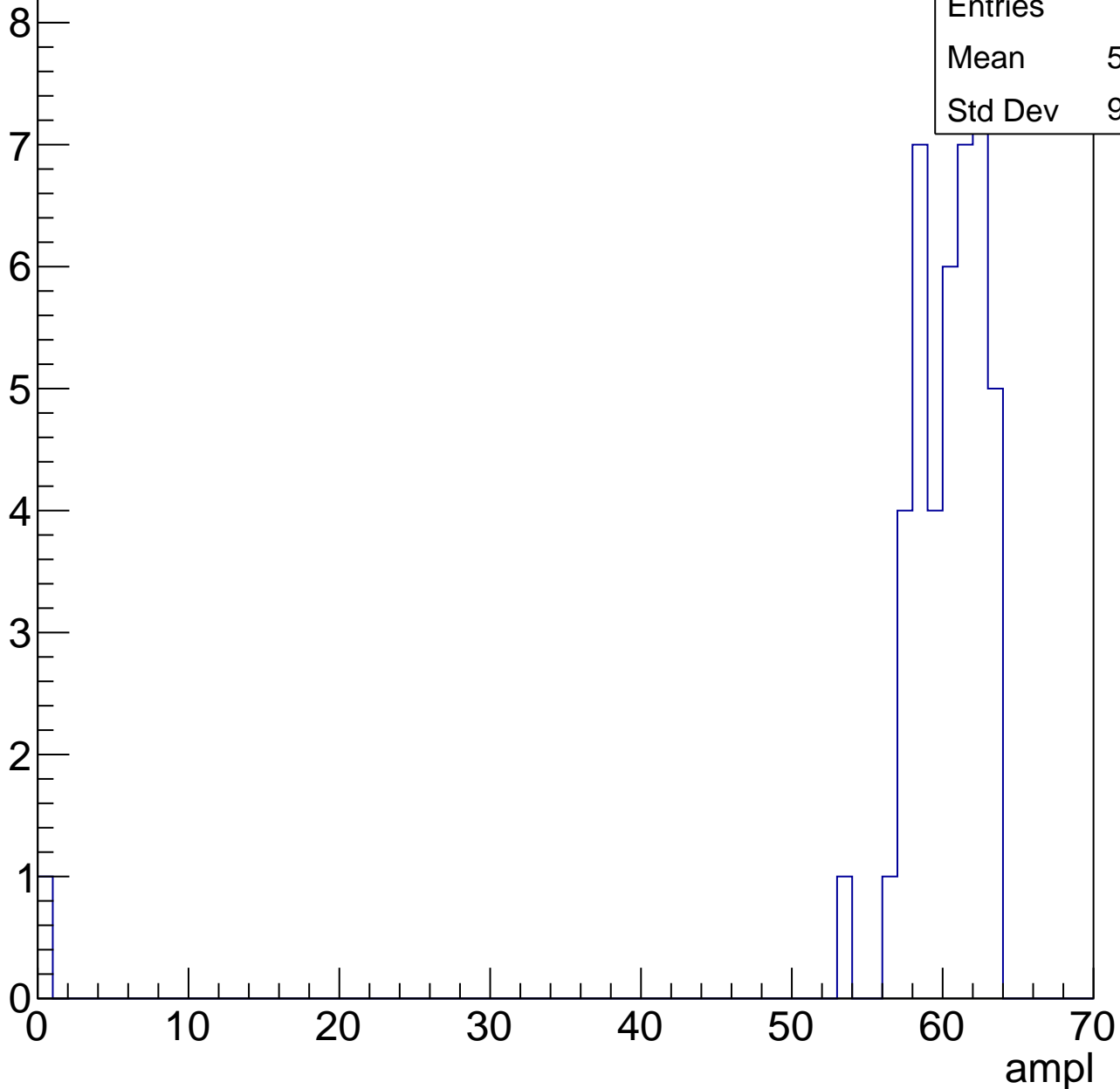


B1L103S, U12-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

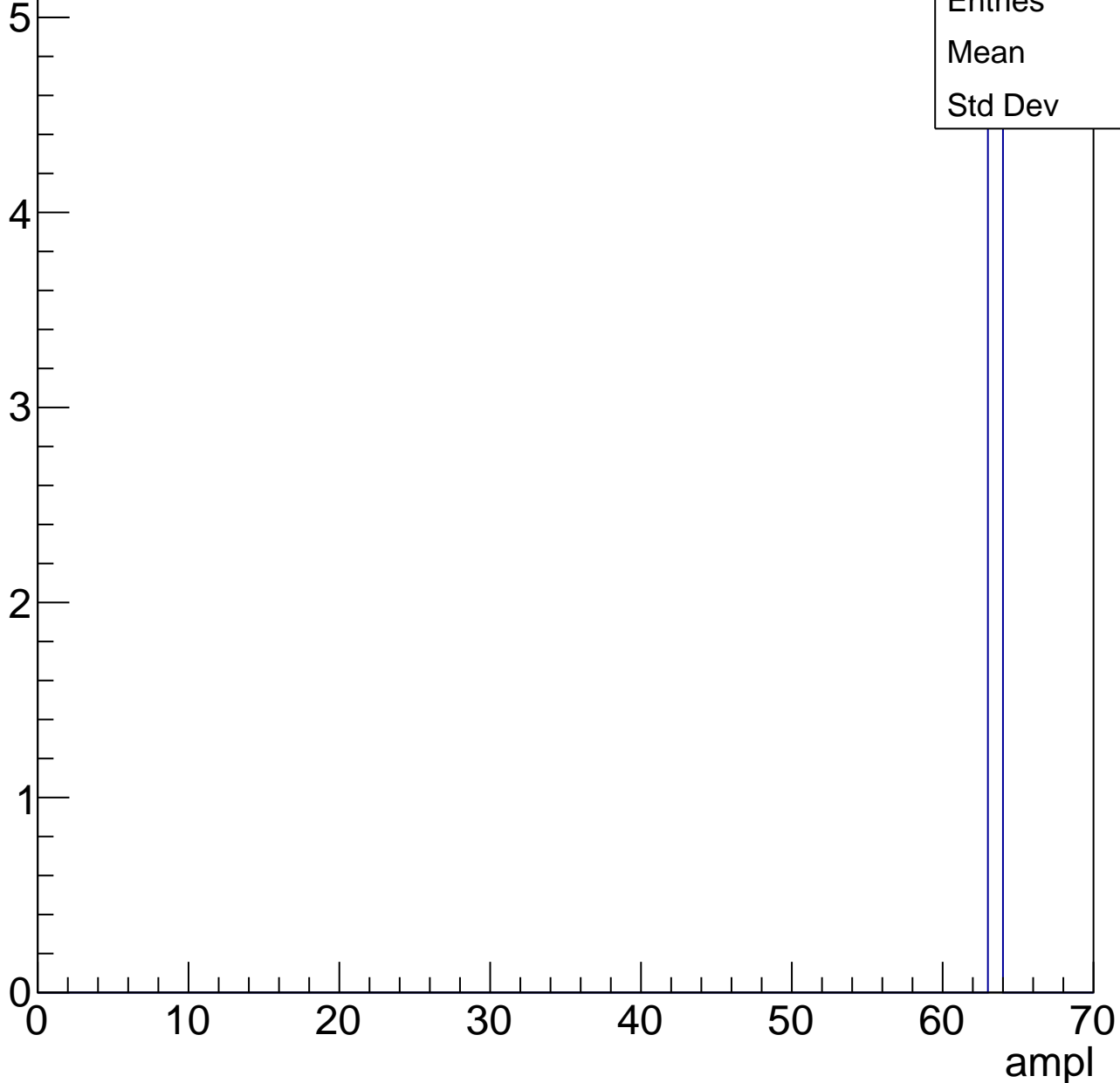
Entries	44
Mean	58.57
Std Dev	9.203



B1L103S, U12-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch6, adc0

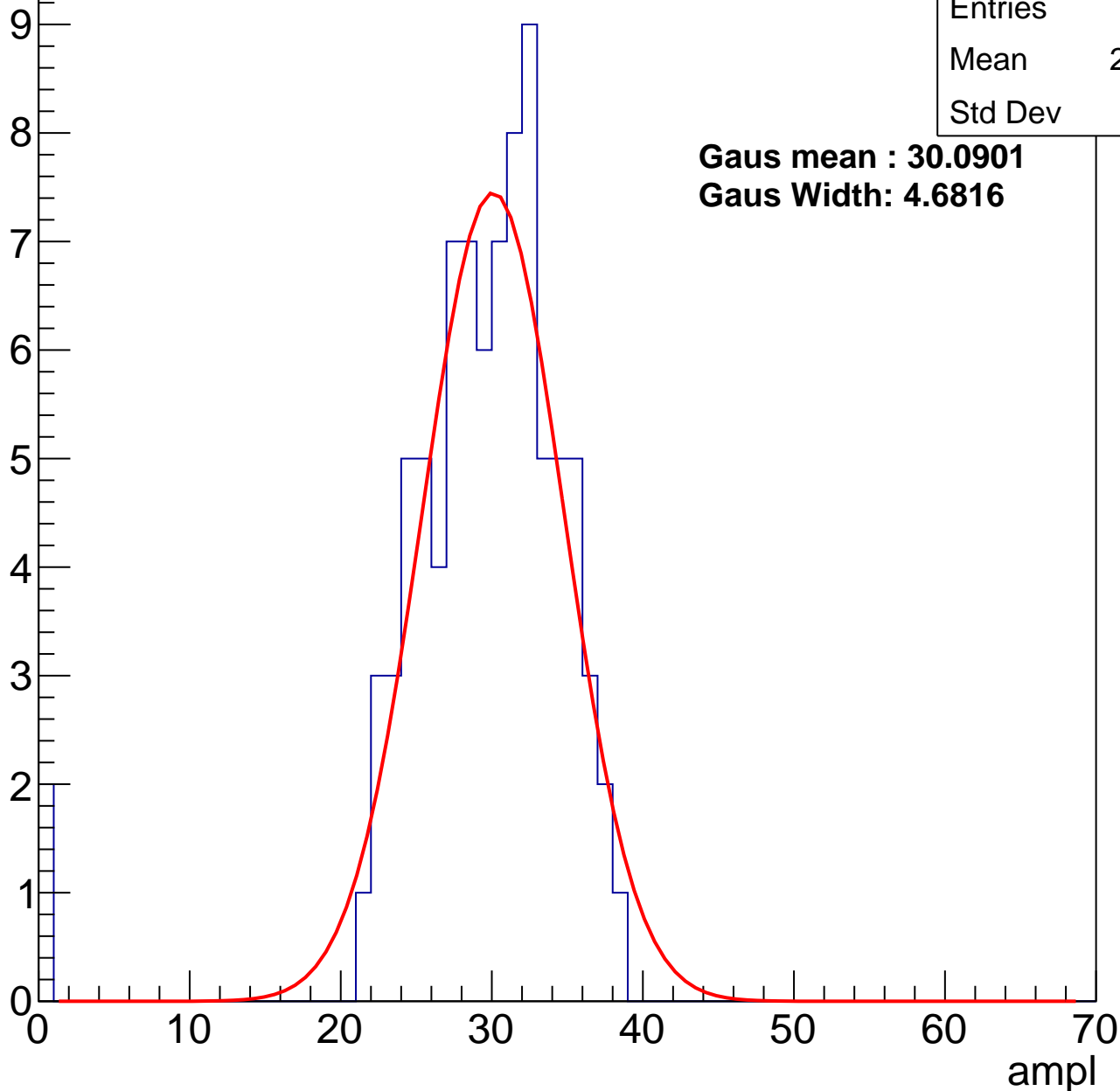
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	88
Mean	28.86
Std Dev	5.97

Gaus mean : 30.0901

Gaus Width: 4.6816



B1L103S, U12-ch6, adc1

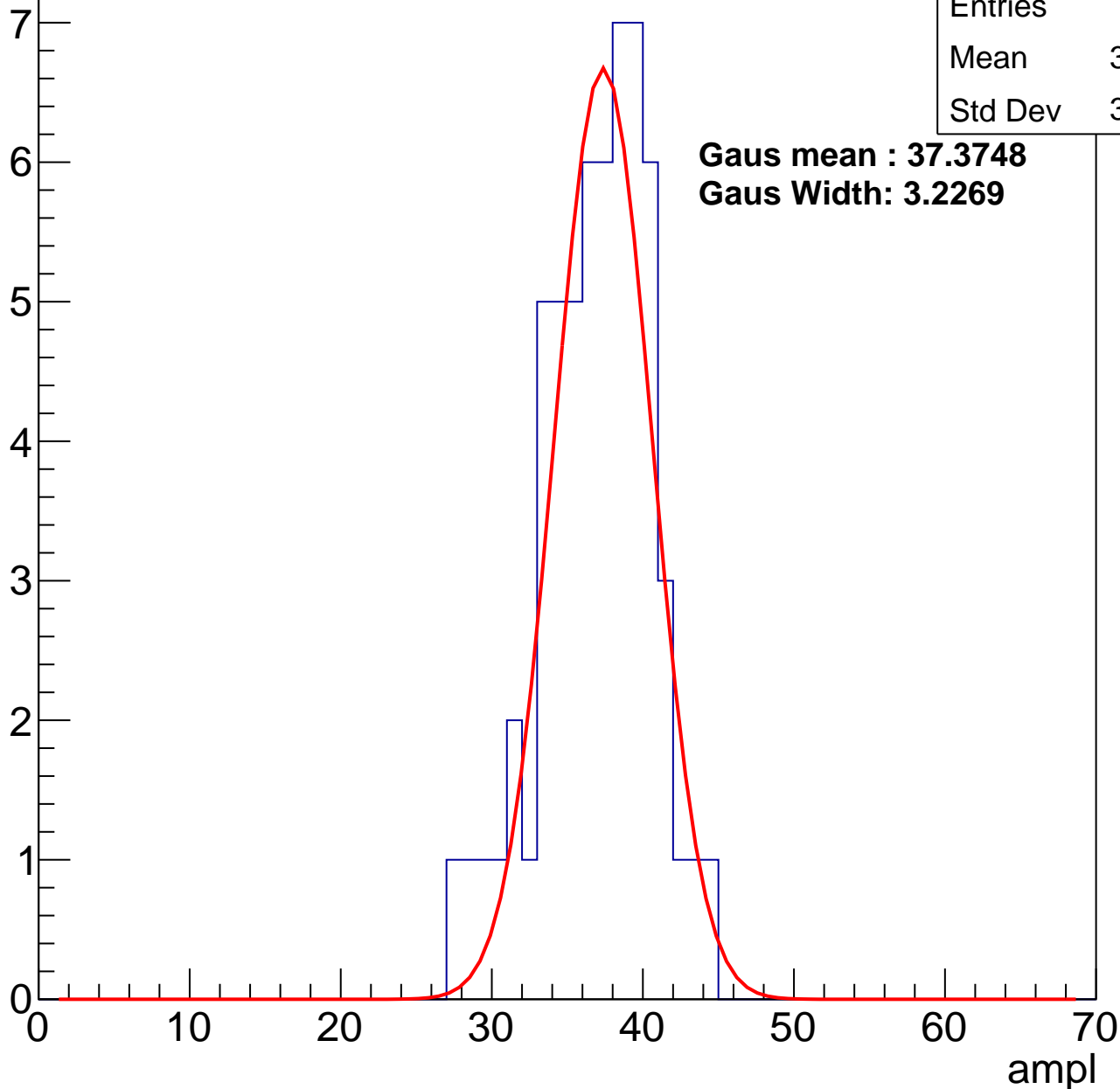
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.45
Std Dev	3.584

Gaus mean : 37.3748

Gaus Width: 3.2269



B1L103S, U12-ch6, adc2

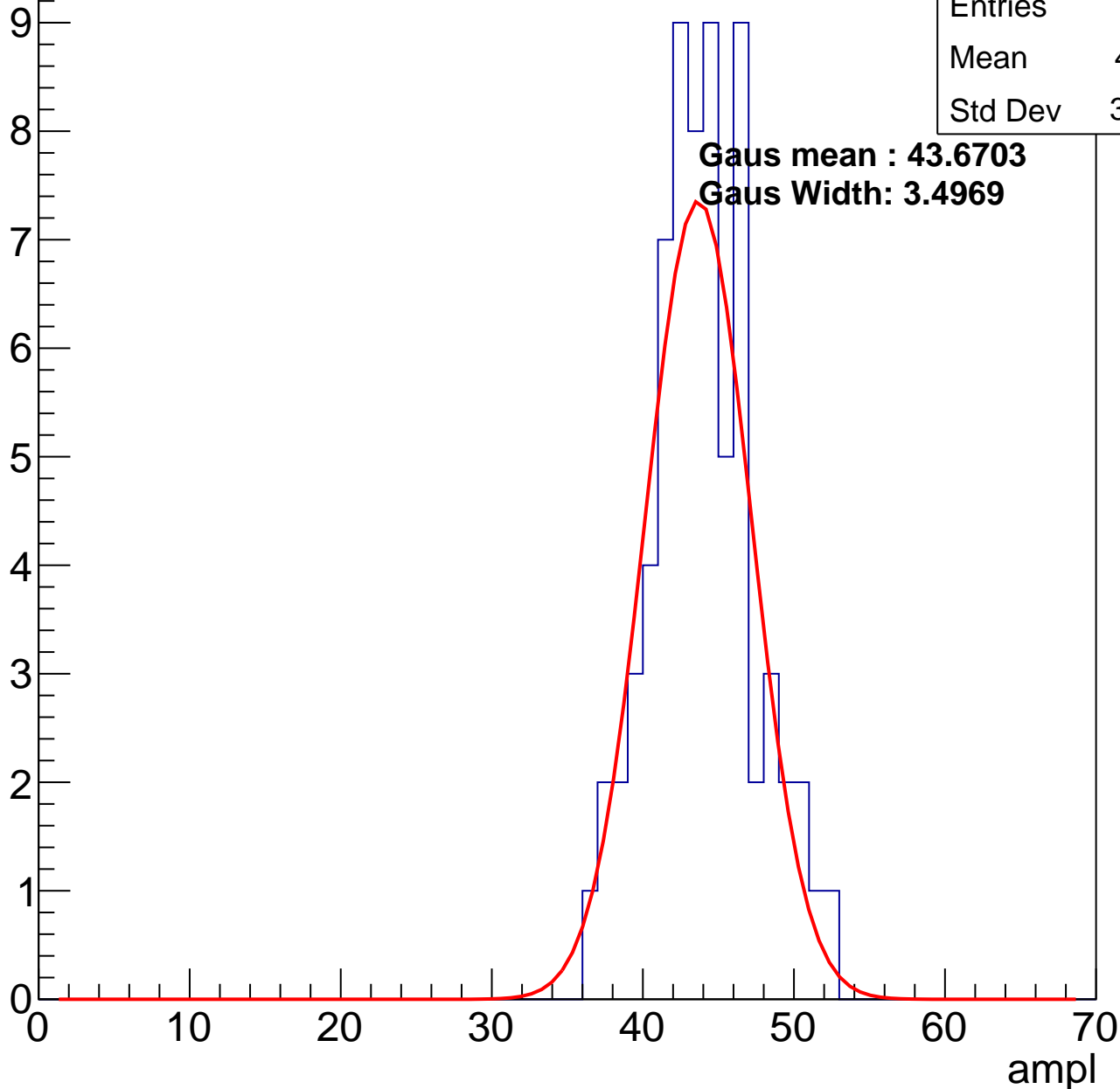
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.51
Std Dev	3.392

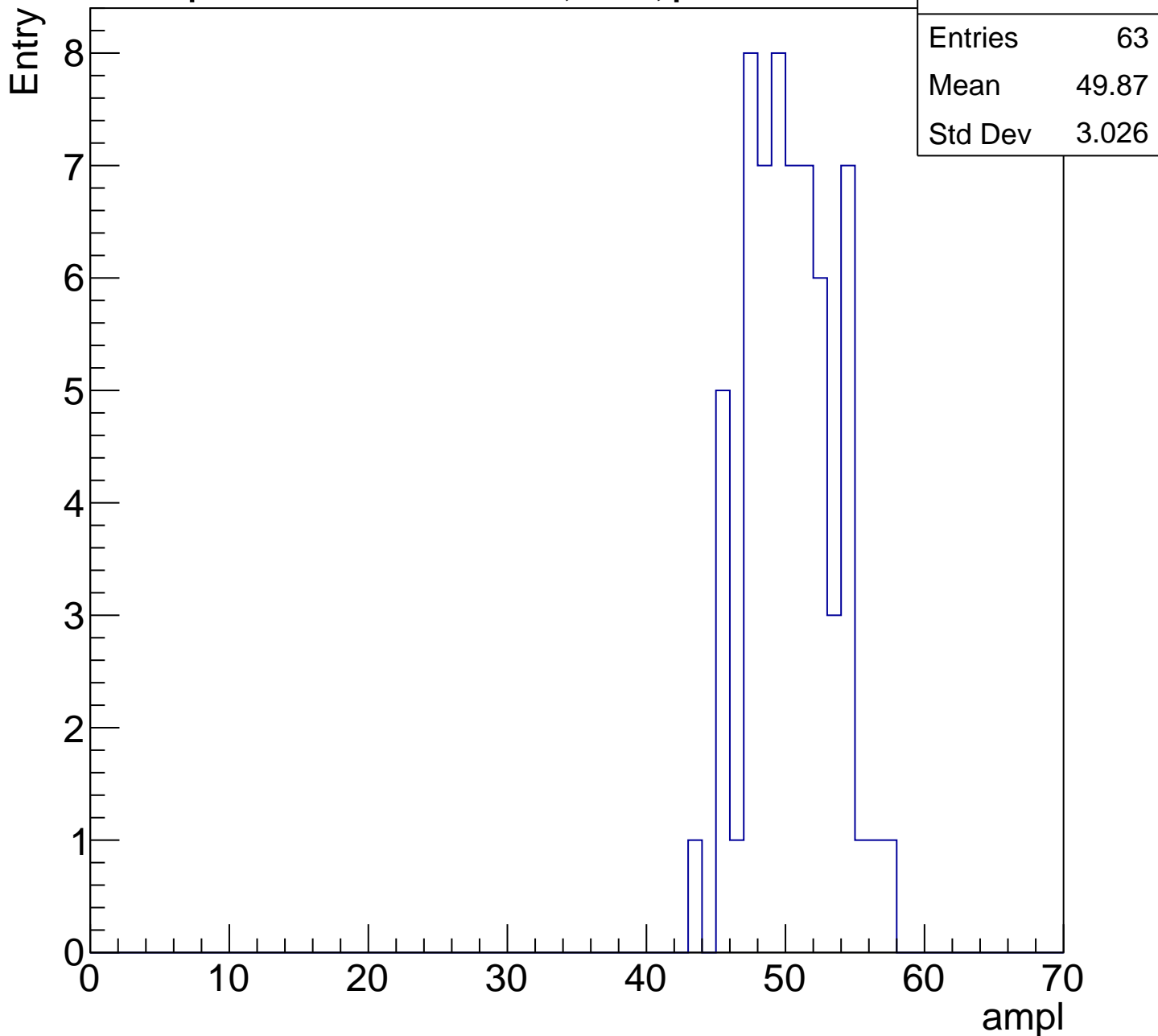
Gaus mean : 43.6703

Gaus Width: 3.4969



B1L103S, U12-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

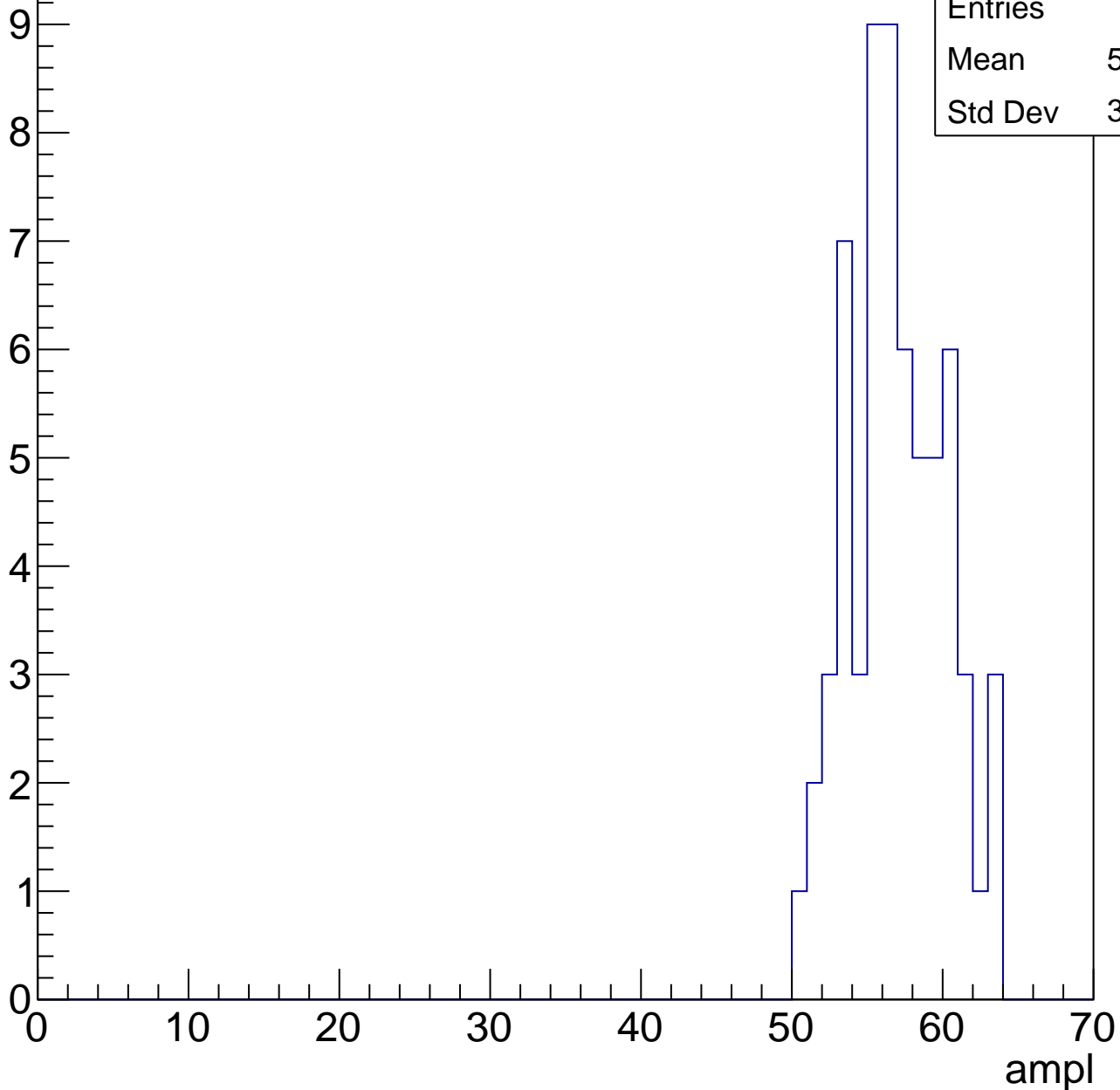


B1L103S, U12-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.52
Std Dev	3.152

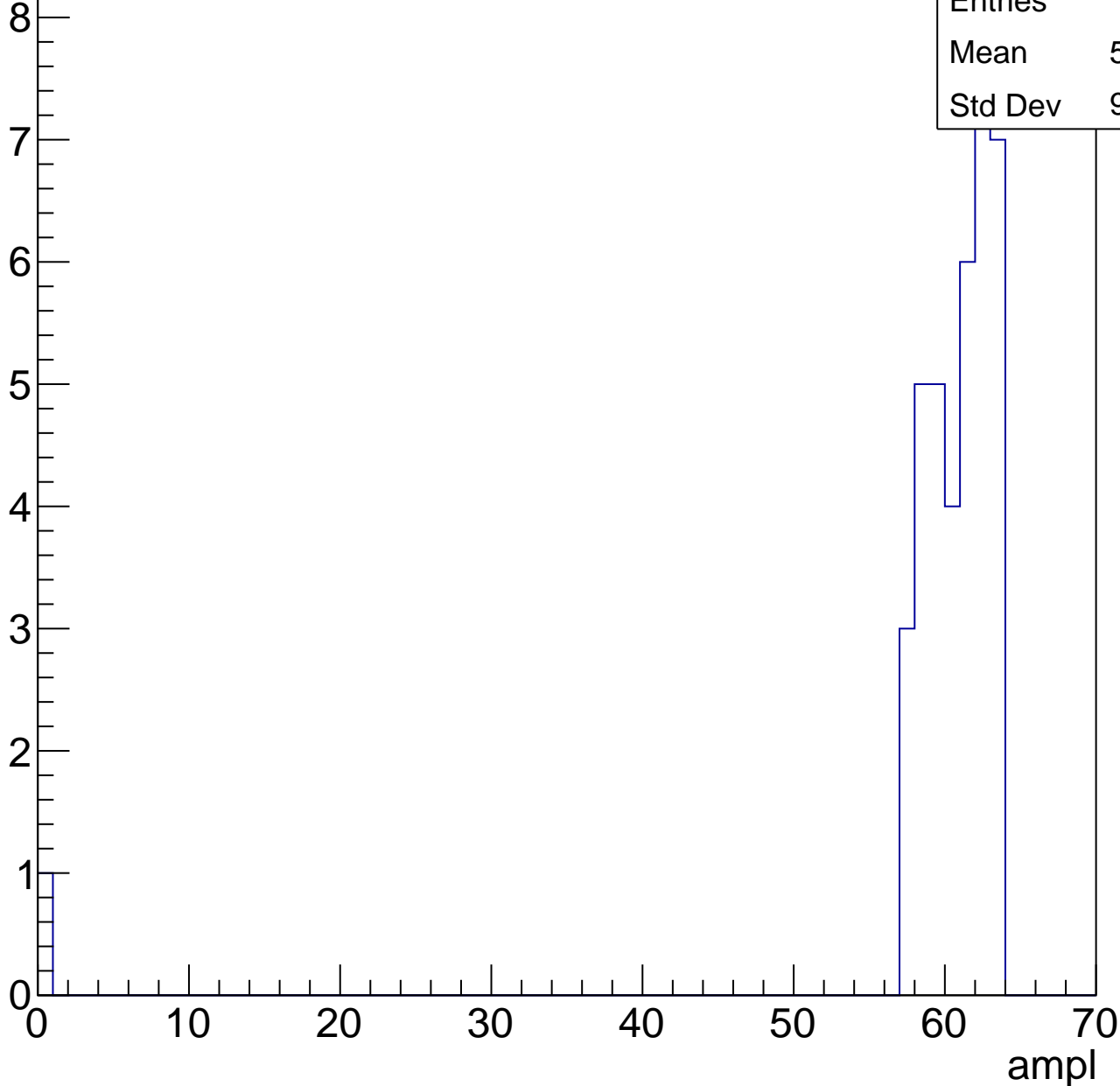


B1L103S, U12-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.95
Std Dev	9.753



B1L103S, U12-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch7, adc0

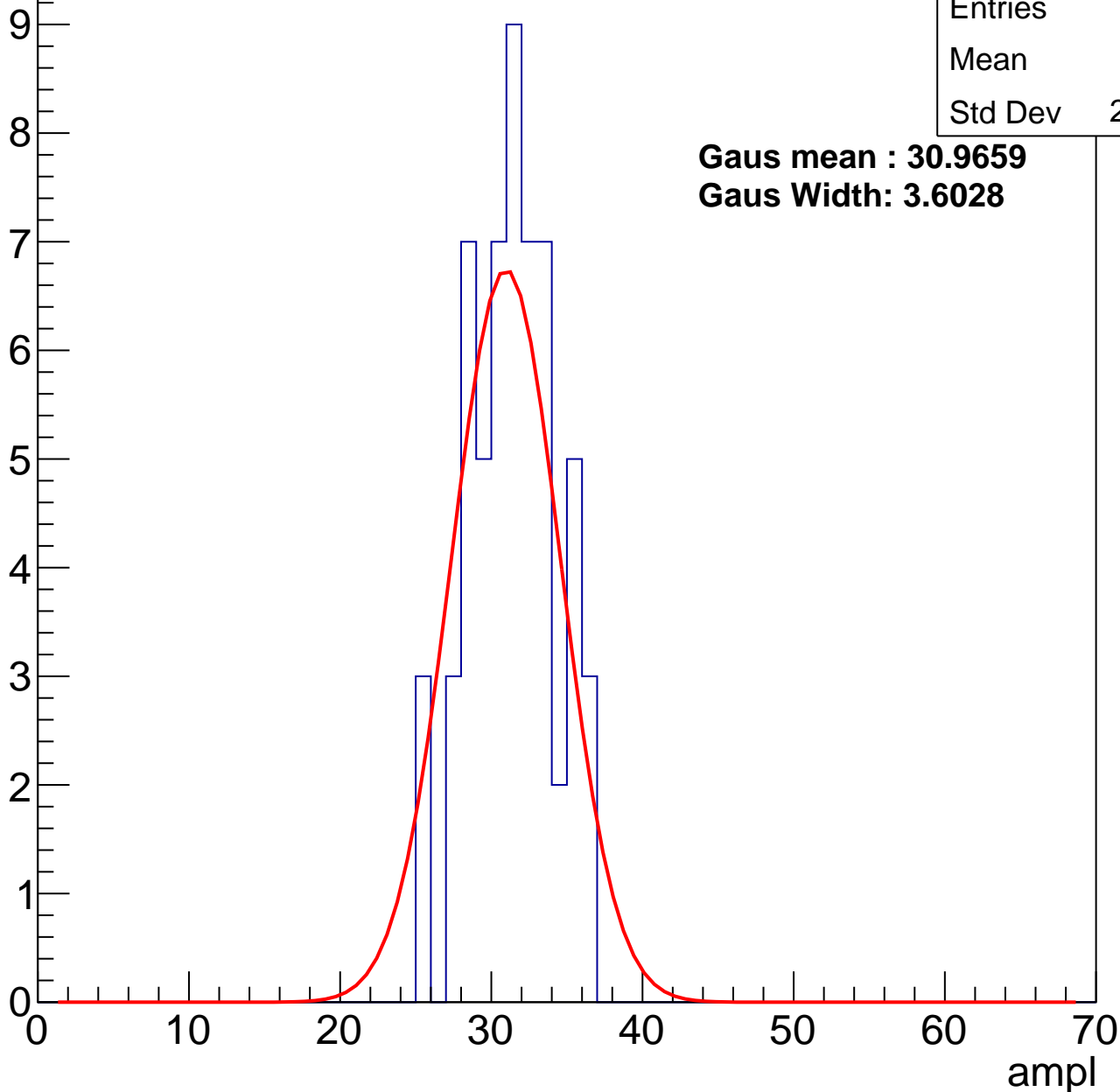
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	30.9
Std Dev	2.796

Gaus mean : 30.9659

Gaus Width: 3.6028



B1L103S, U12-ch7, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	82
Mean	37.8
Std Dev	3.373

Gaus mean : 38.3779

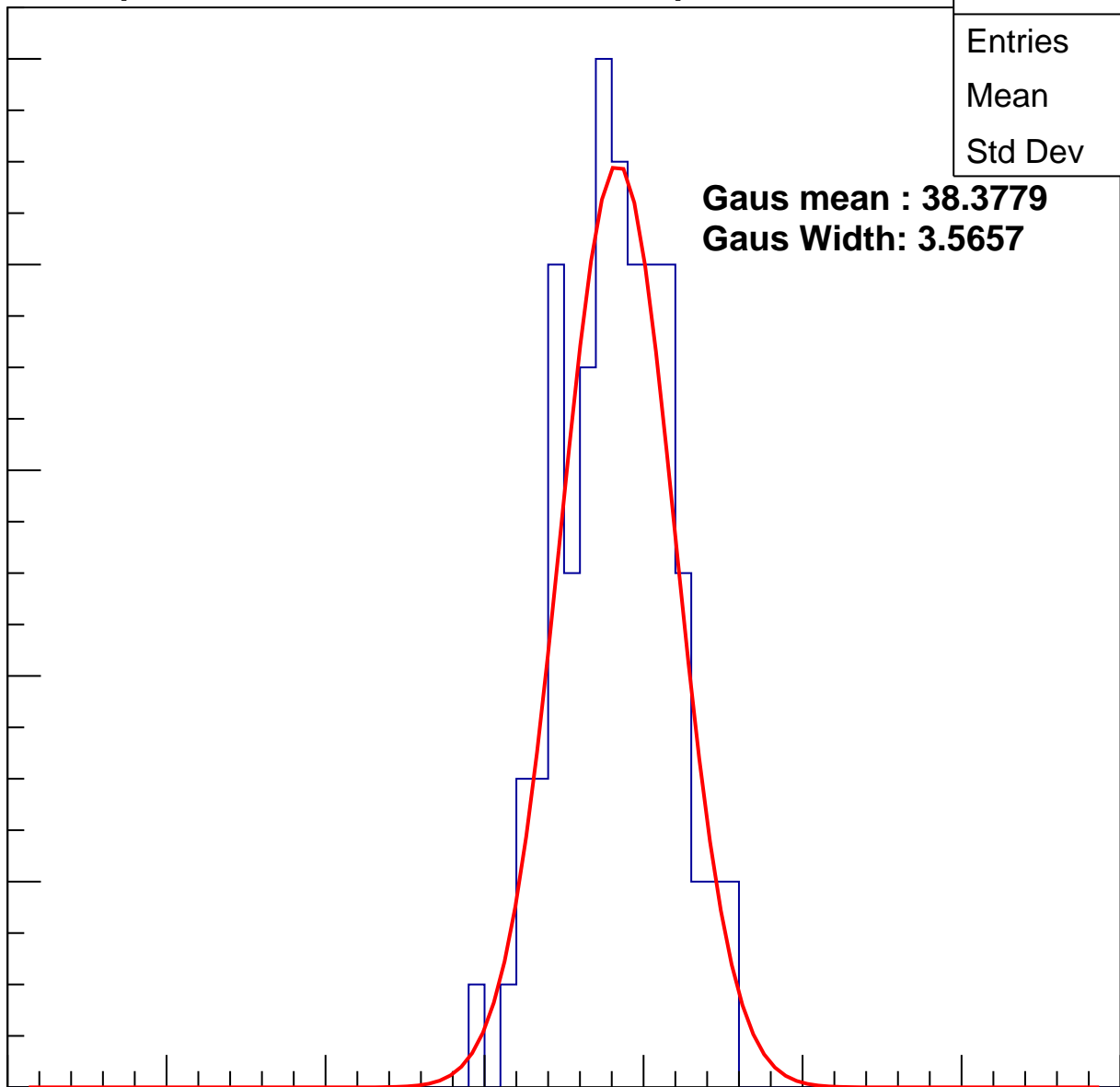
Gaus Width: 3.5657

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch7, adc2

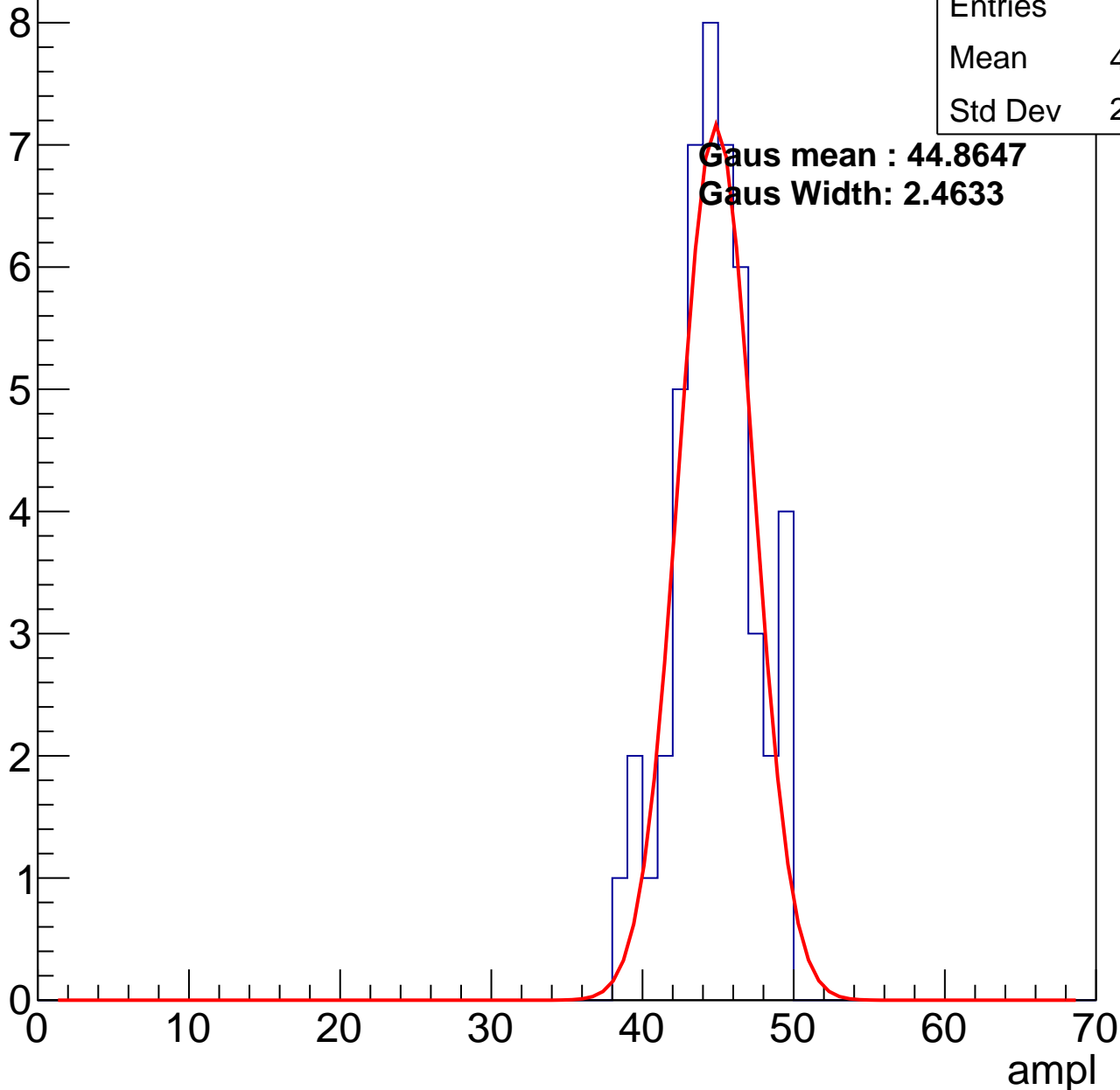
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	44.27
Std Dev	2.636

Gaus mean : 44.8647

Gaus Width: 2.4633

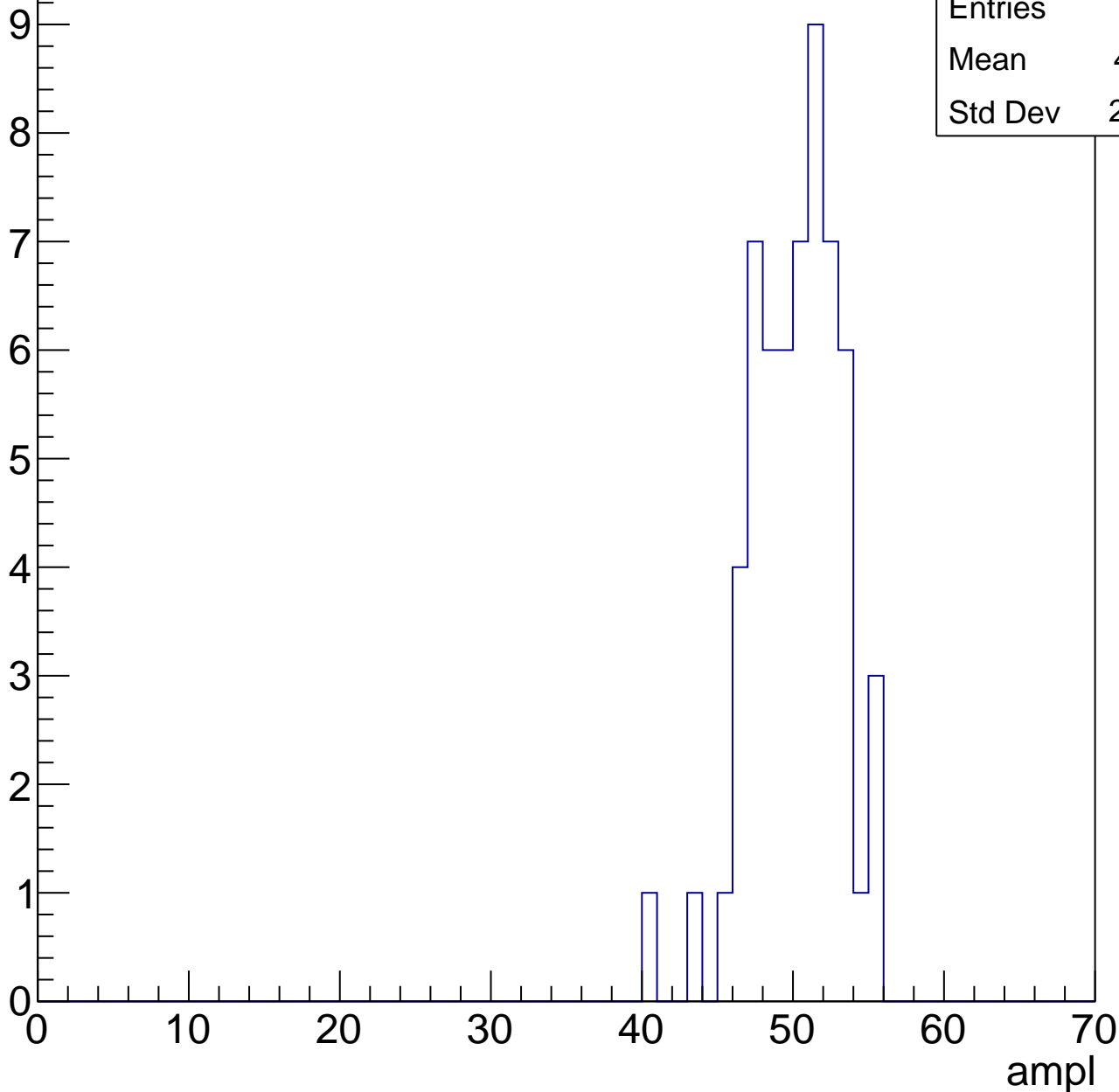


B1L103S, U12-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

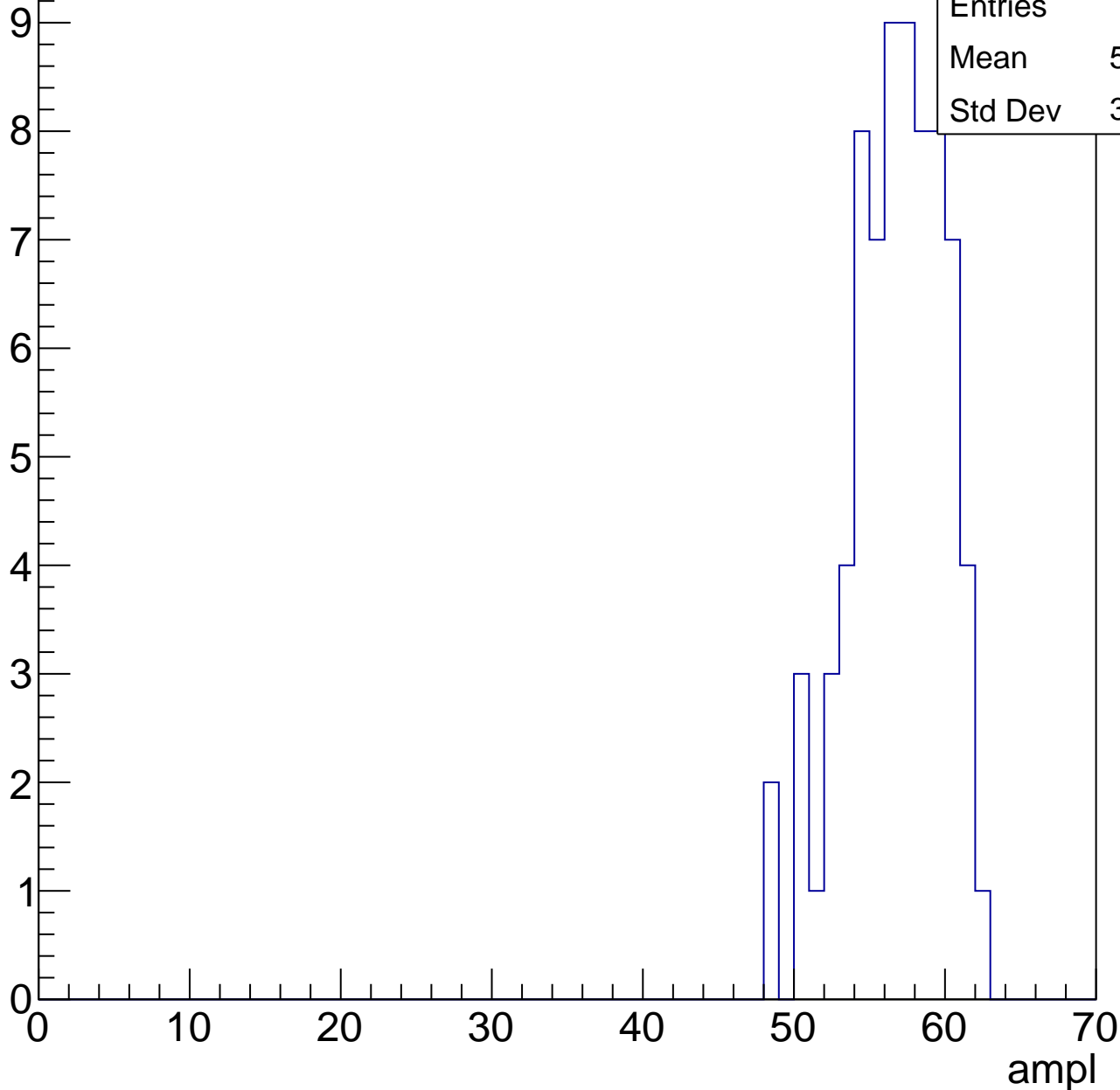
Entries	59
Mean	49.71
Std Dev	2.935



B1L103S, U12-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

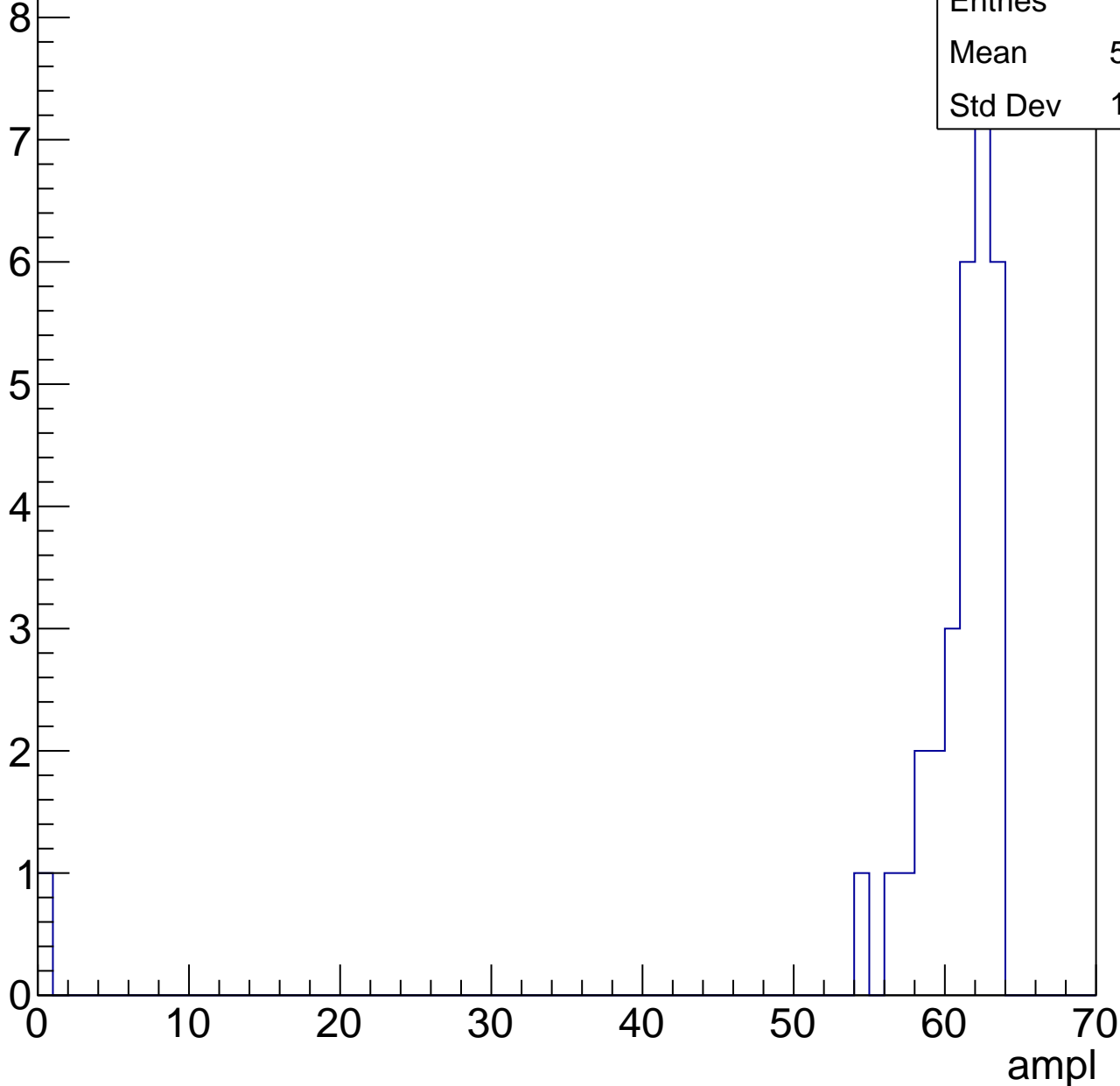


B1L103S, U12-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

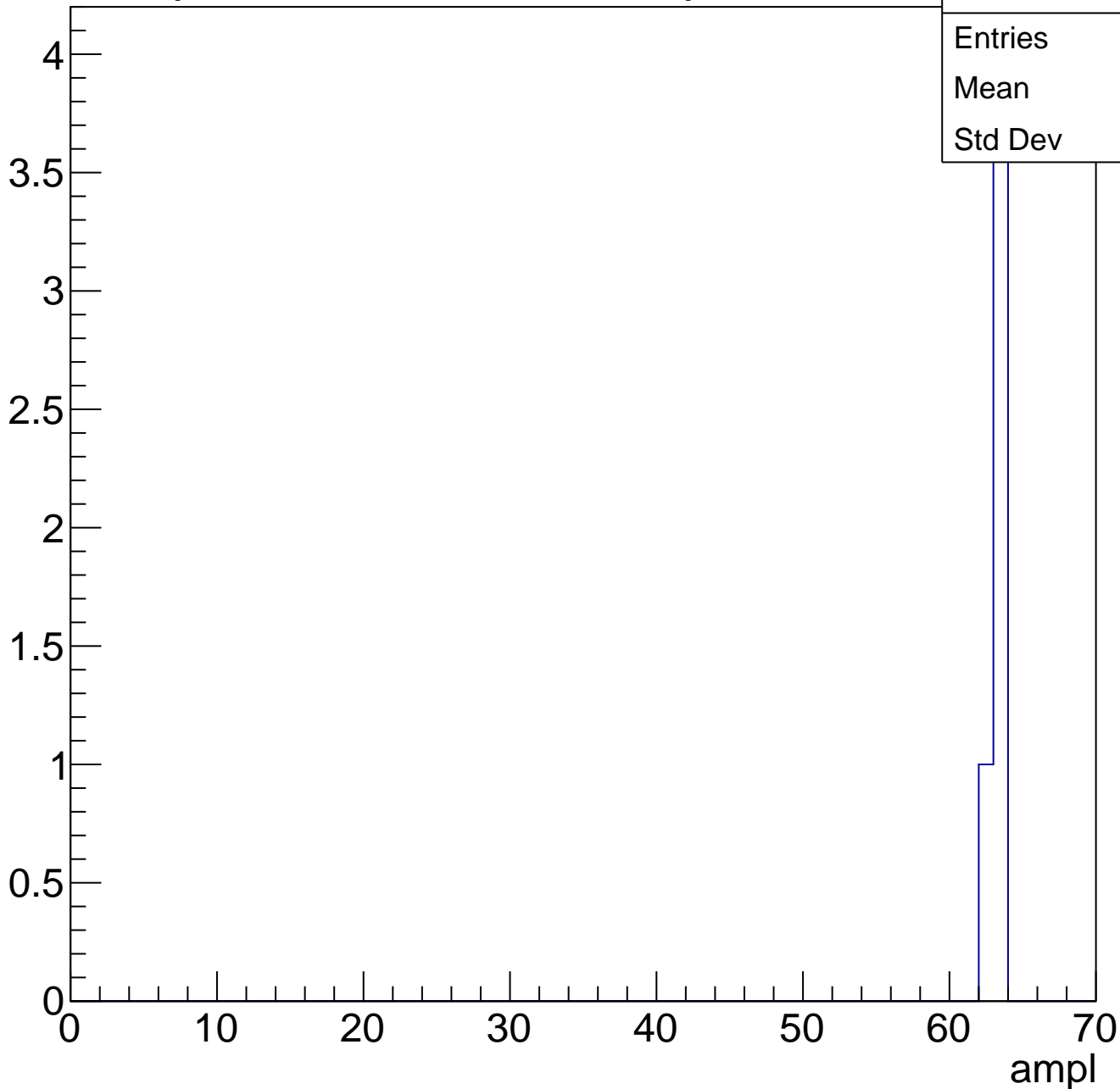
Entries	31
Mean	58.74
Std Dev	10.95



B1L103S, U12-ch7, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch8, adc0

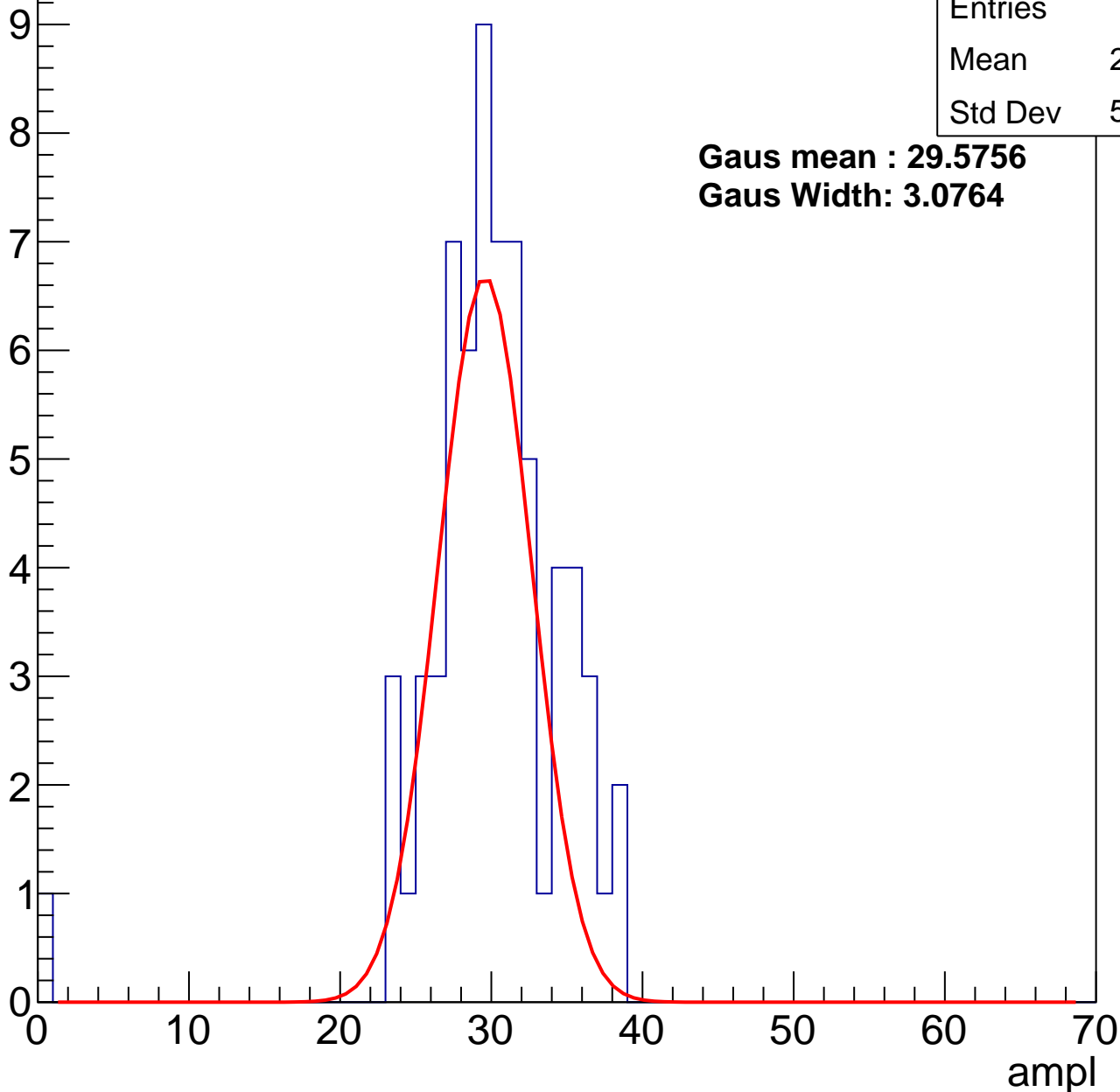
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.57
Std Dev	5.146

Gaus mean : 29.5756

Gaus Width: 3.0764



B1L103S, U12-ch8, adc1

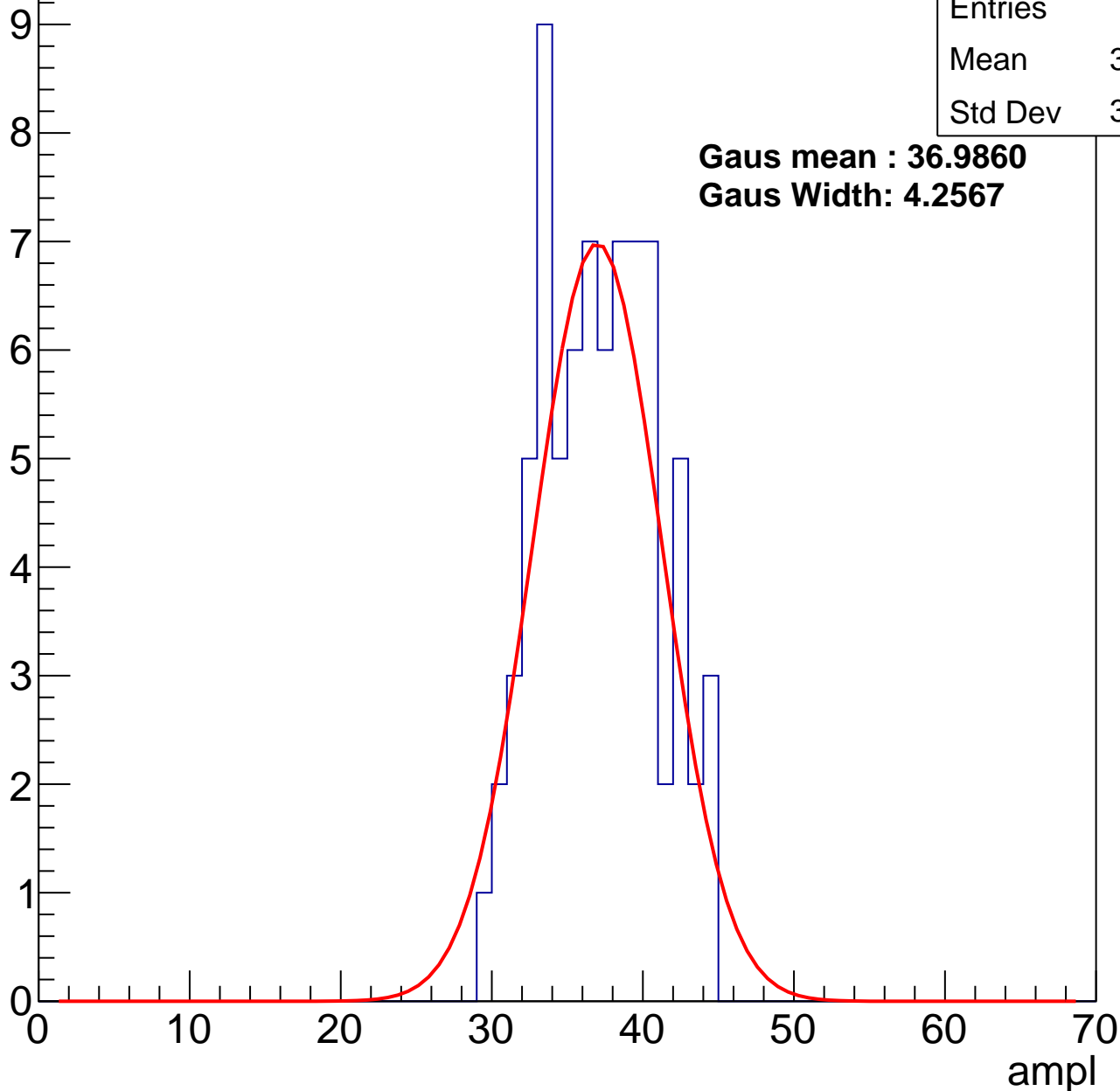
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.65
Std Dev	3.744

Gaus mean : 36.9860

Gaus Width: 4.2567



B1L103S, U12-ch8, adc2

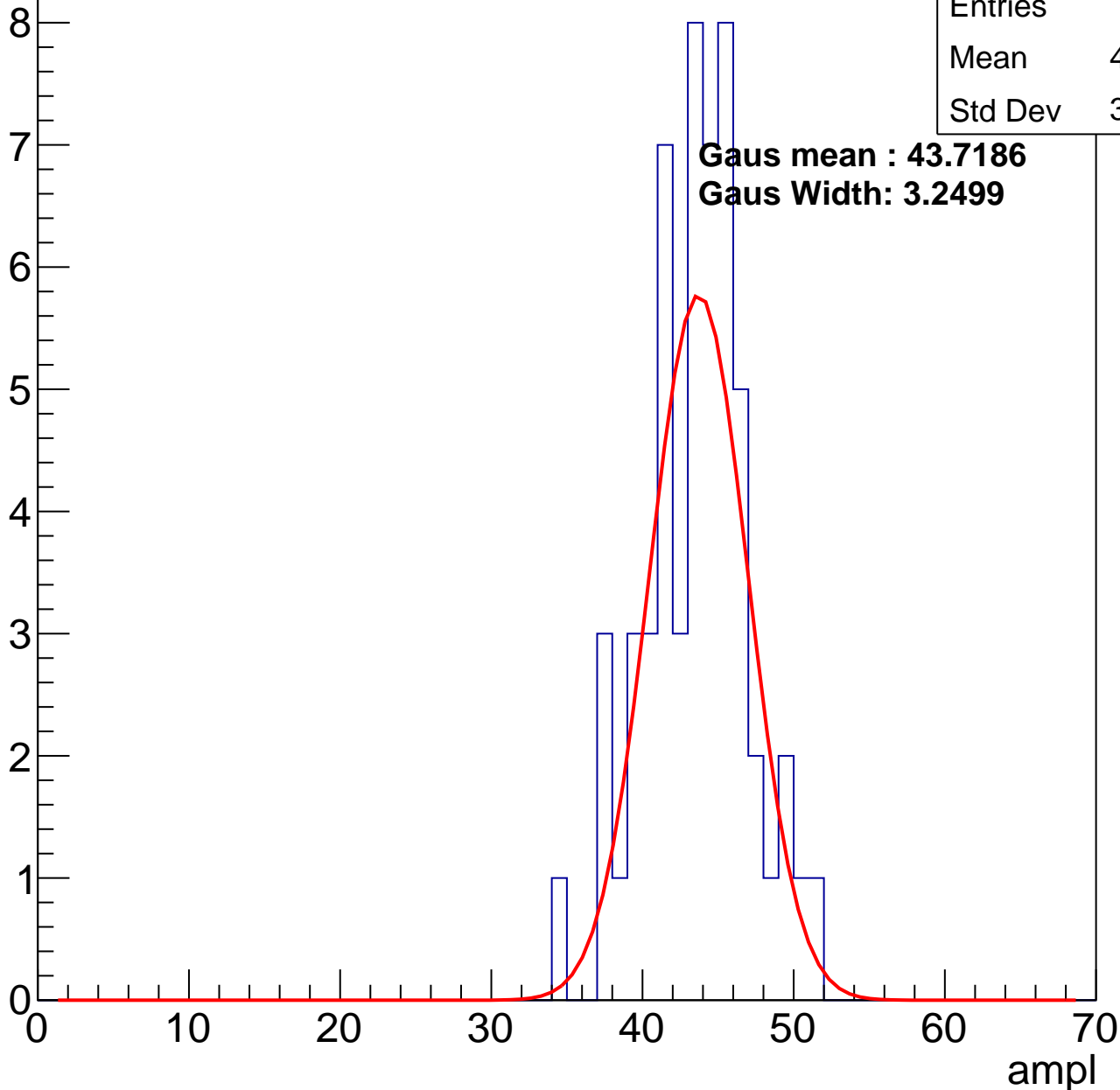
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.14
Std Dev	3.393

Gaus mean : 43.7186

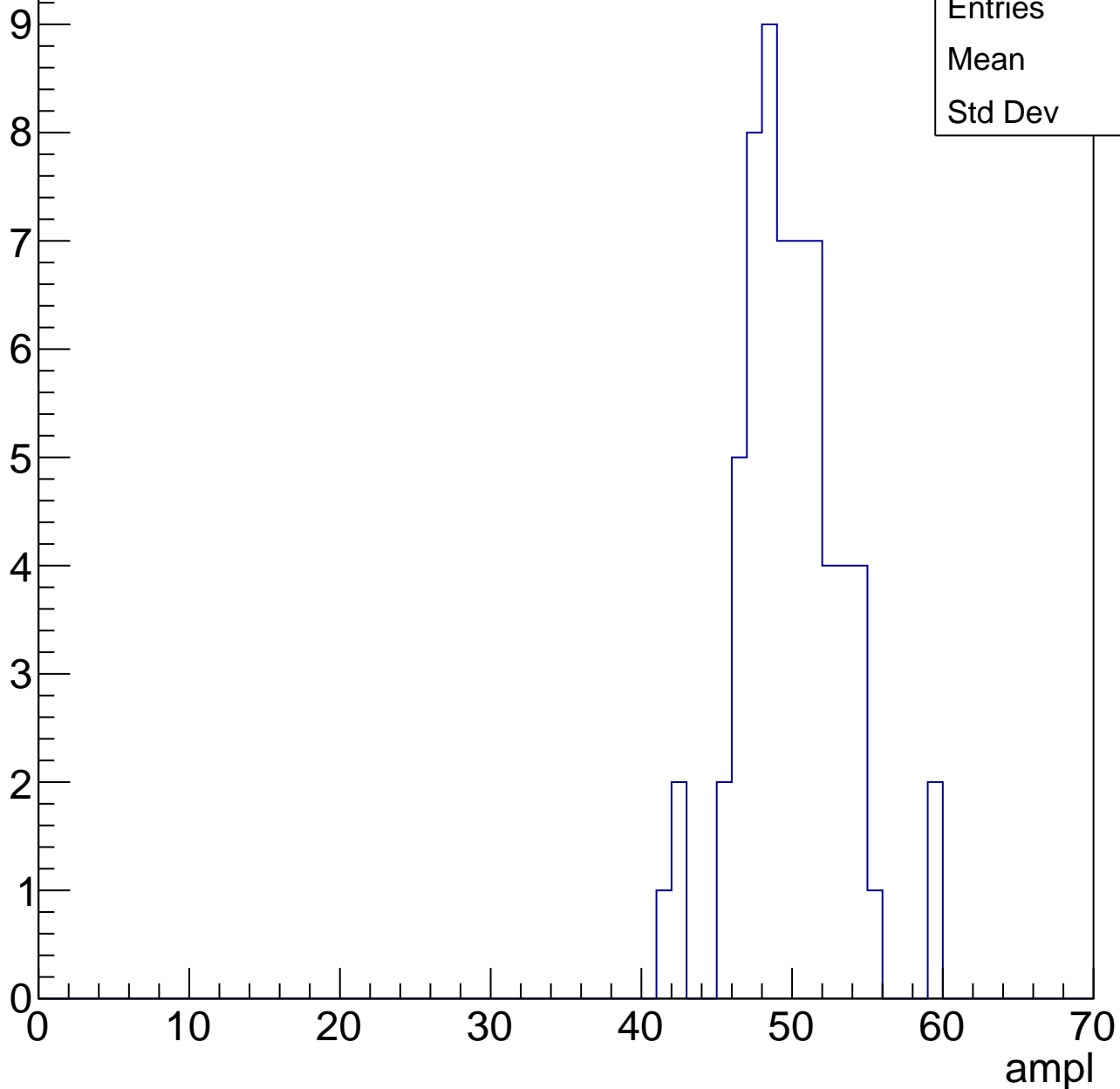
Gaus Width: 3.2499



B1L103S, U12-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

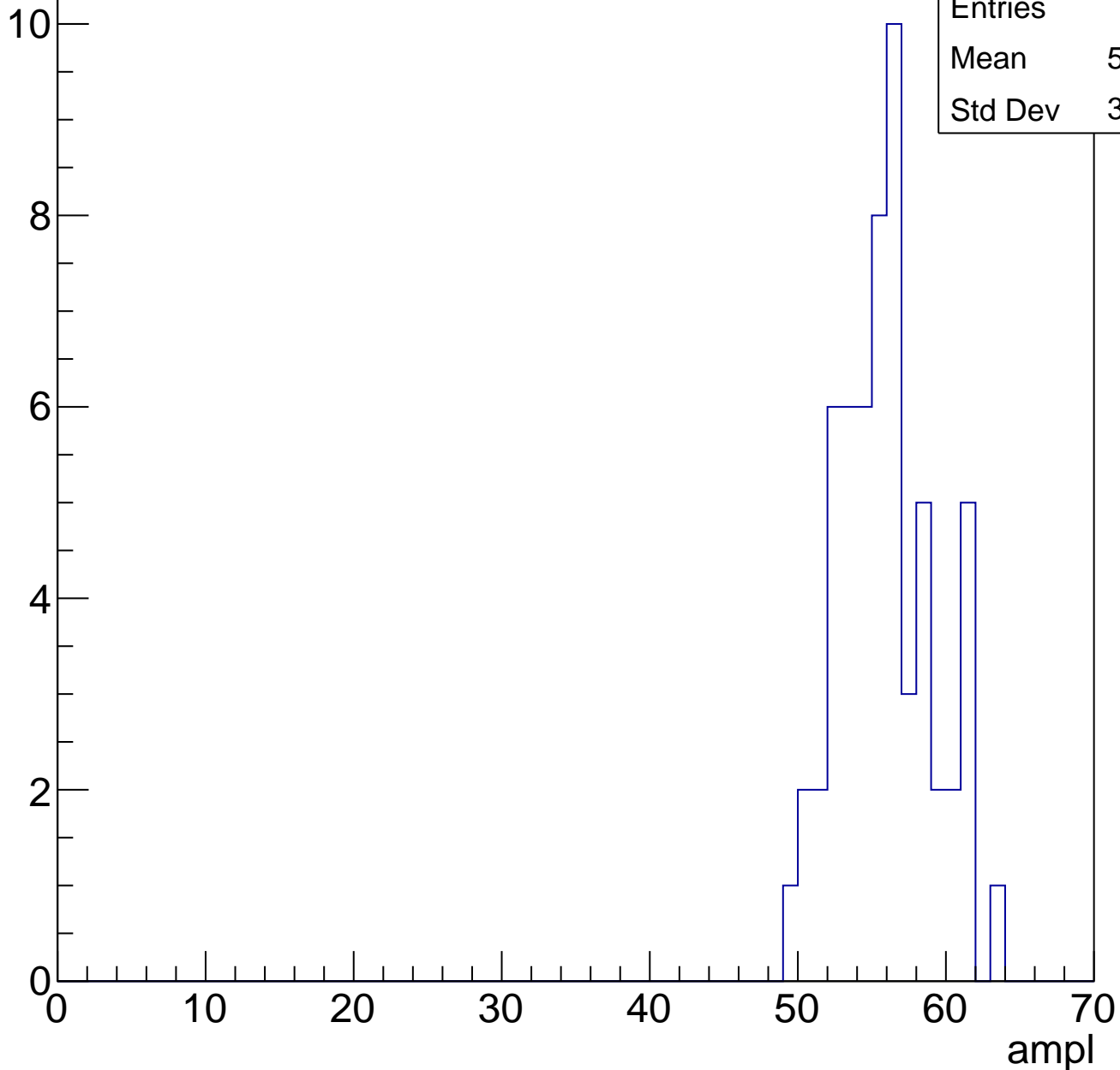


B1L103S, U12-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	59
Mean	55.46
Std Dev	3.143

Entry

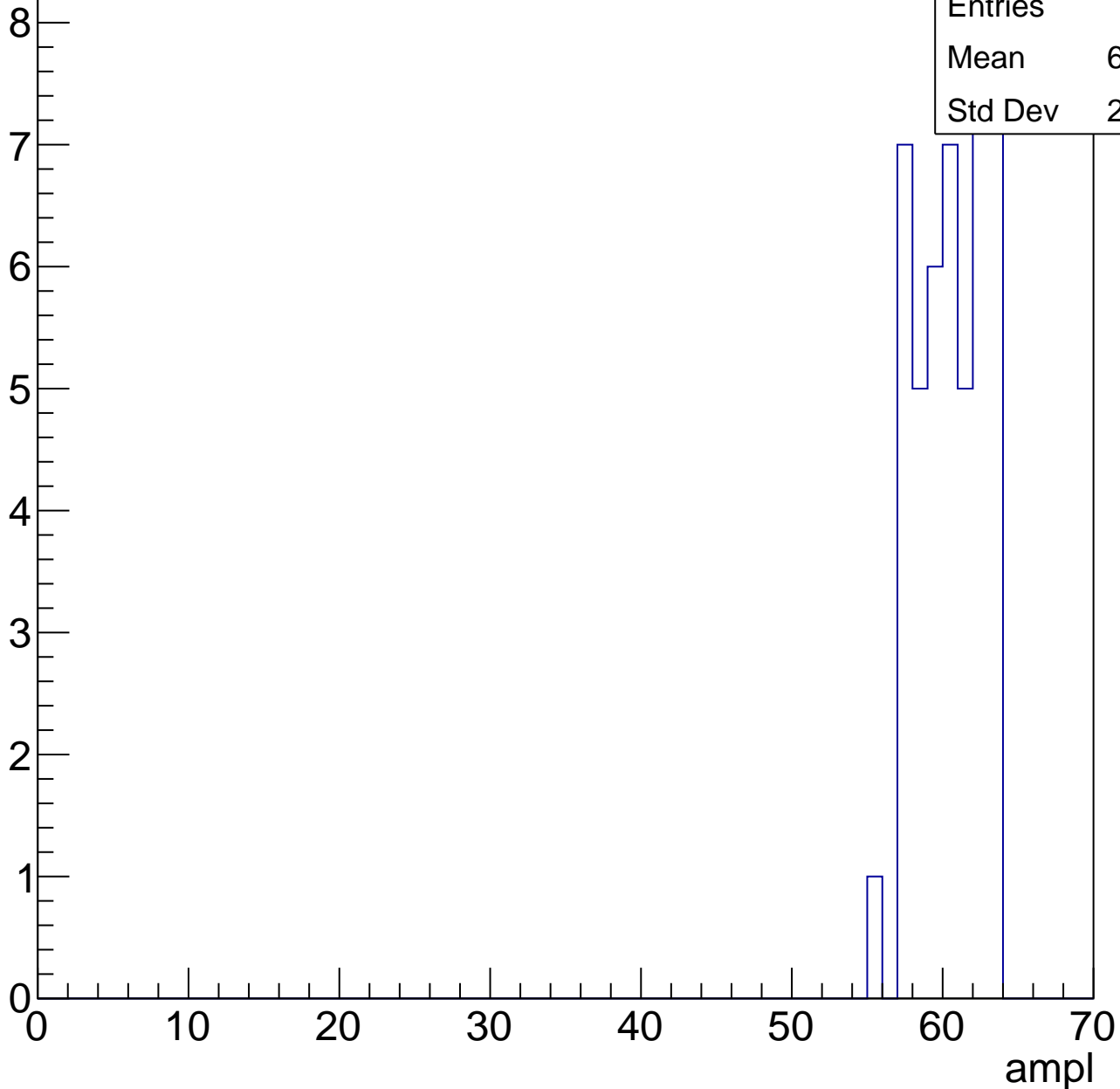


B1L103S, U12-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

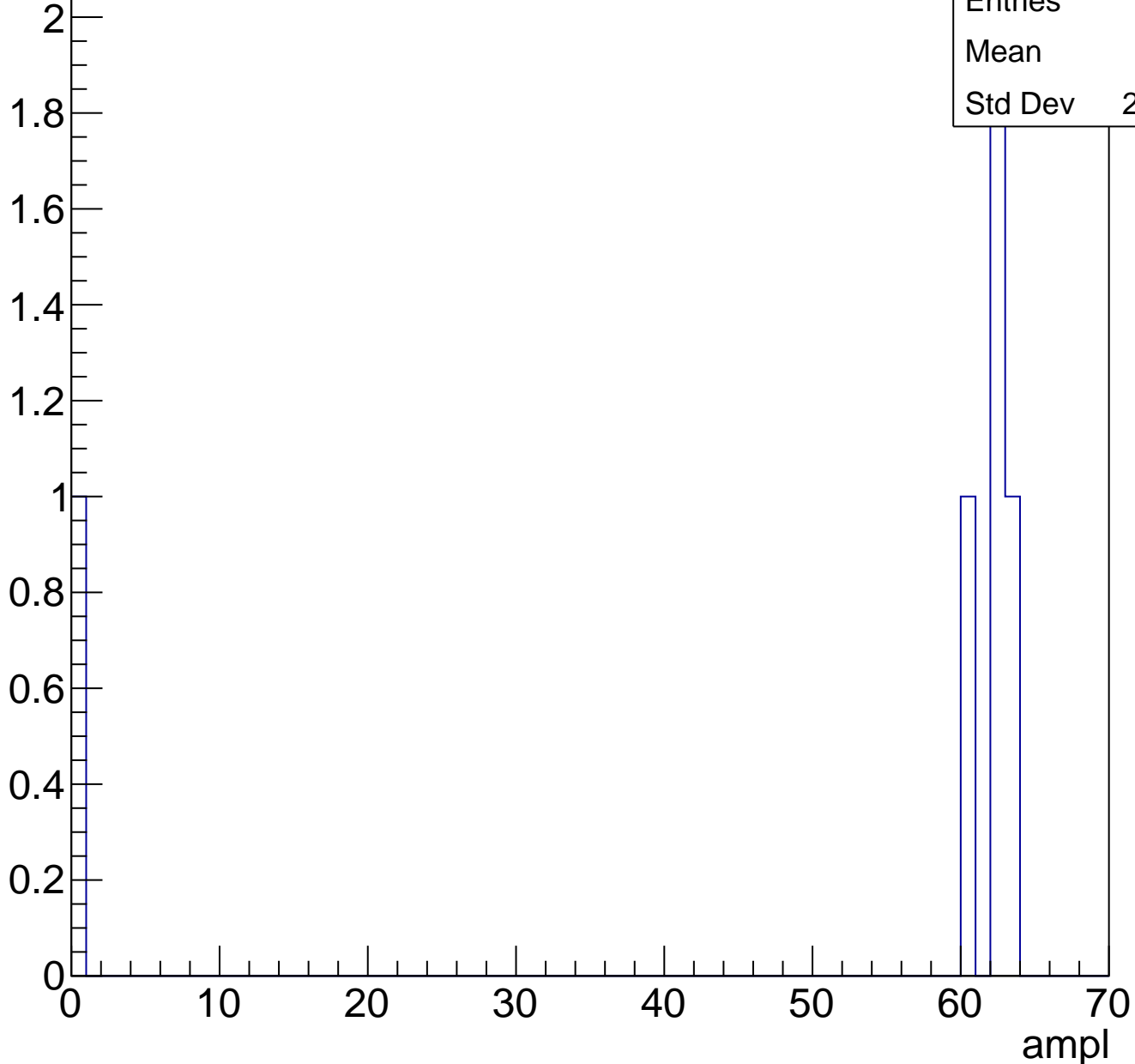
Entries	47
Mean	60.06
Std Dev	2.177



B1L103S, U12-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch9, adc0

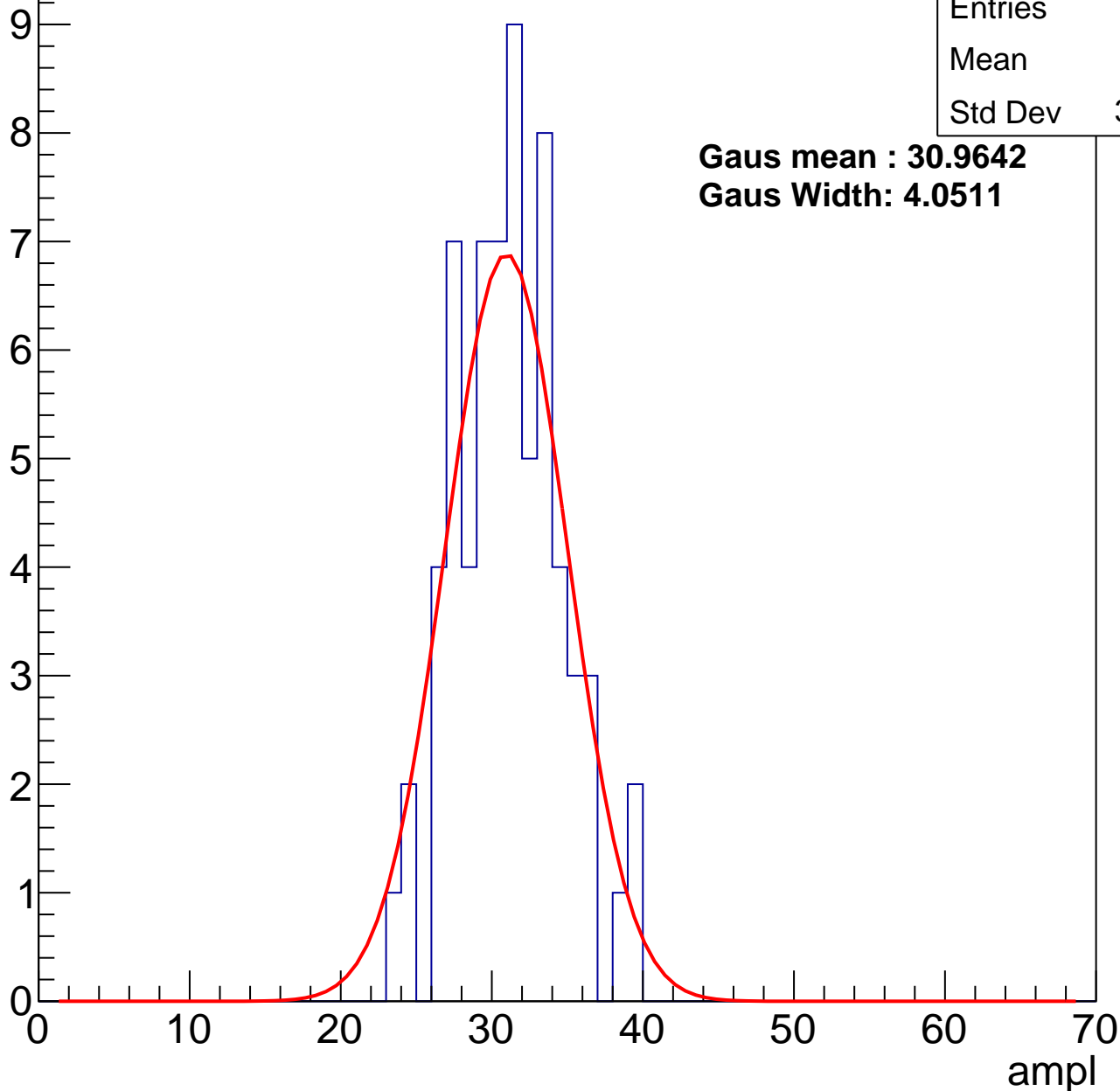
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.7
Std Dev	3.481

Gaus mean : 30.9642

Gaus Width: 4.0511



B1L103S, U12-ch9, adc1

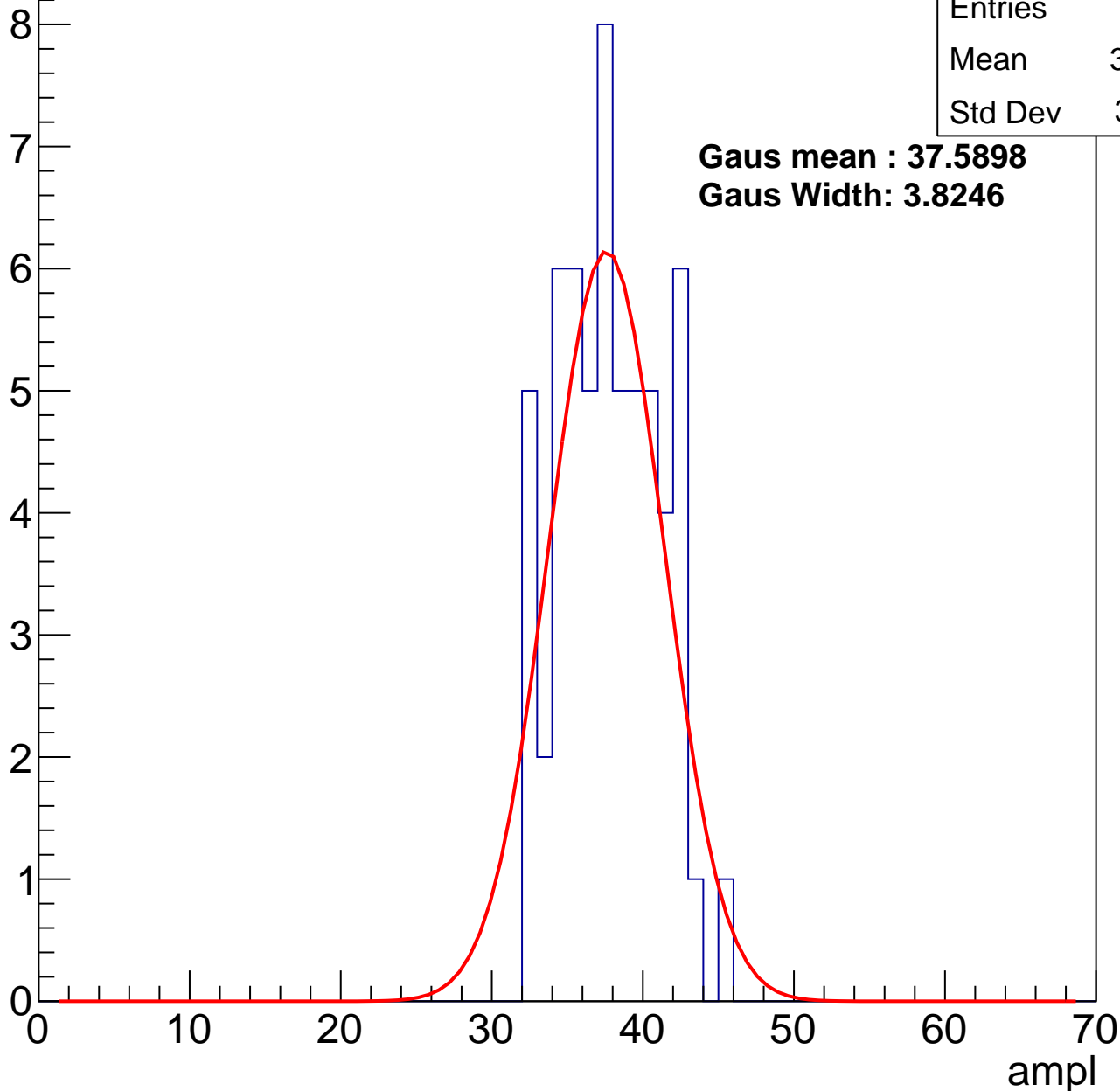
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	37.37
Std Dev	3.231

Gaus mean : 37.5898

Gaus Width: 3.8246



B1L103S, U12-ch9, adc2

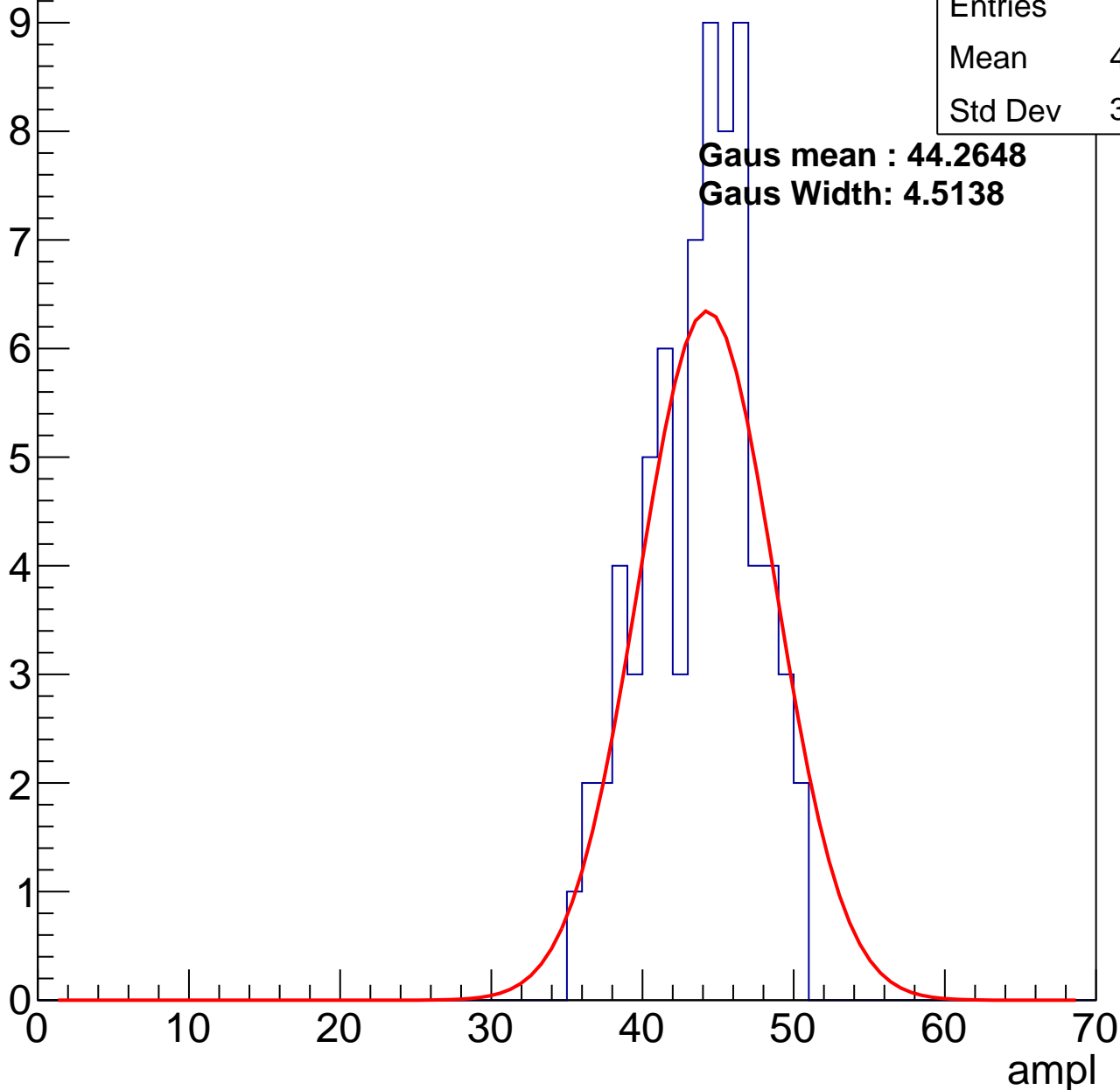
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.33
Std Dev	3.609

Gaus mean : 44.2648

Gaus Width: 4.5138

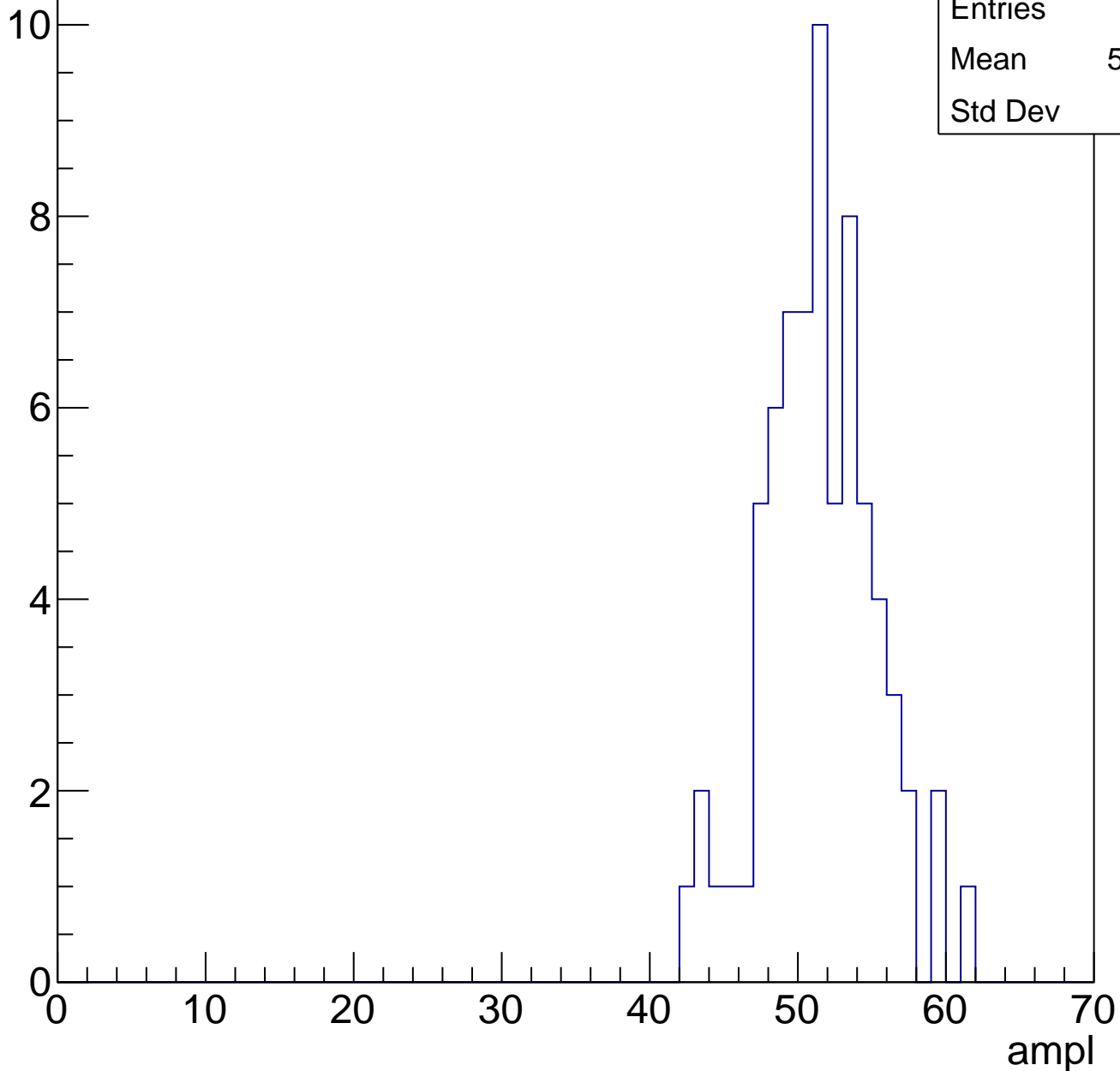


B1L103S, U12-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

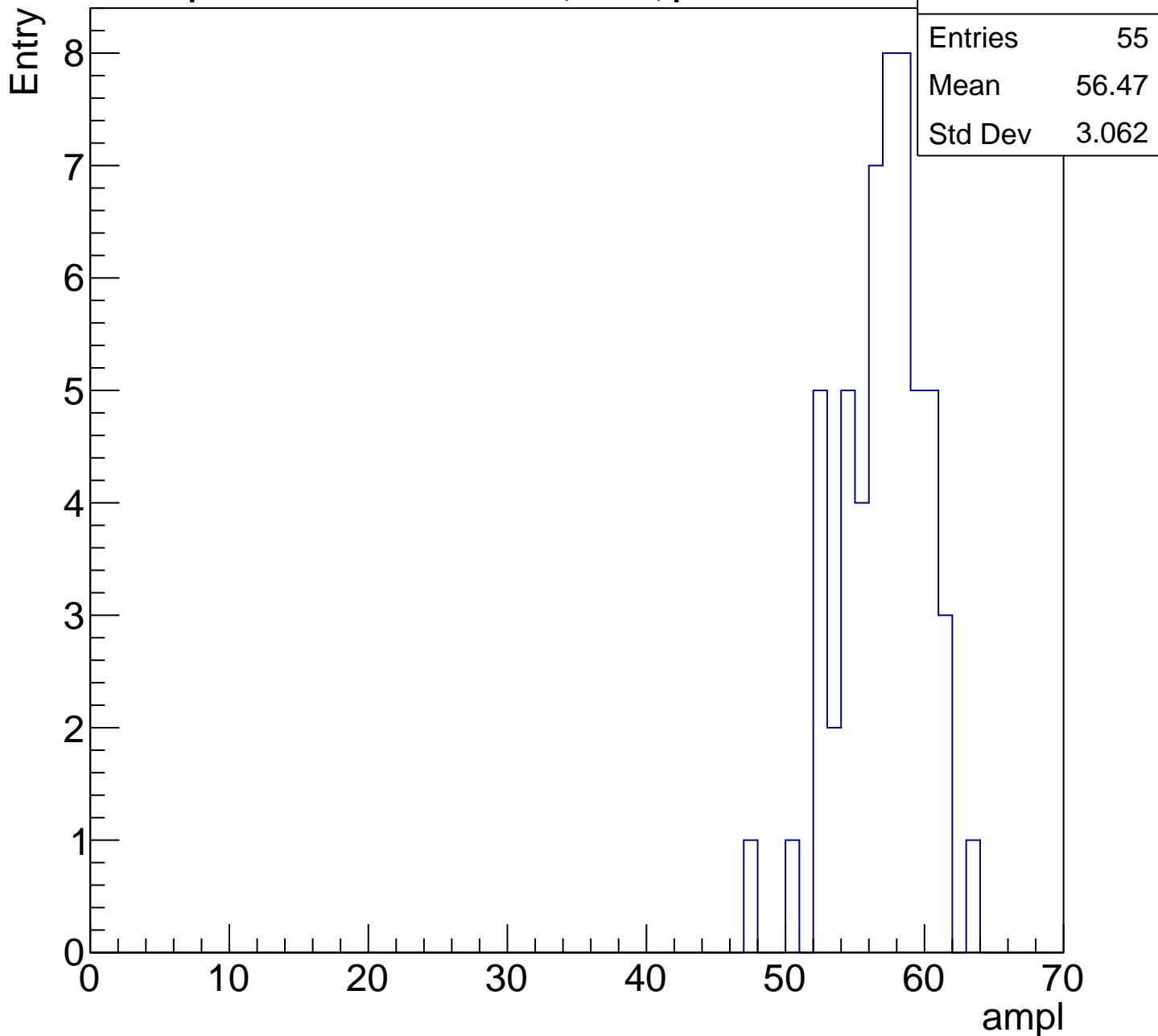
Entries	71
Mean	51.04
Std Dev	3.77

Entry



B1L103S, U12-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

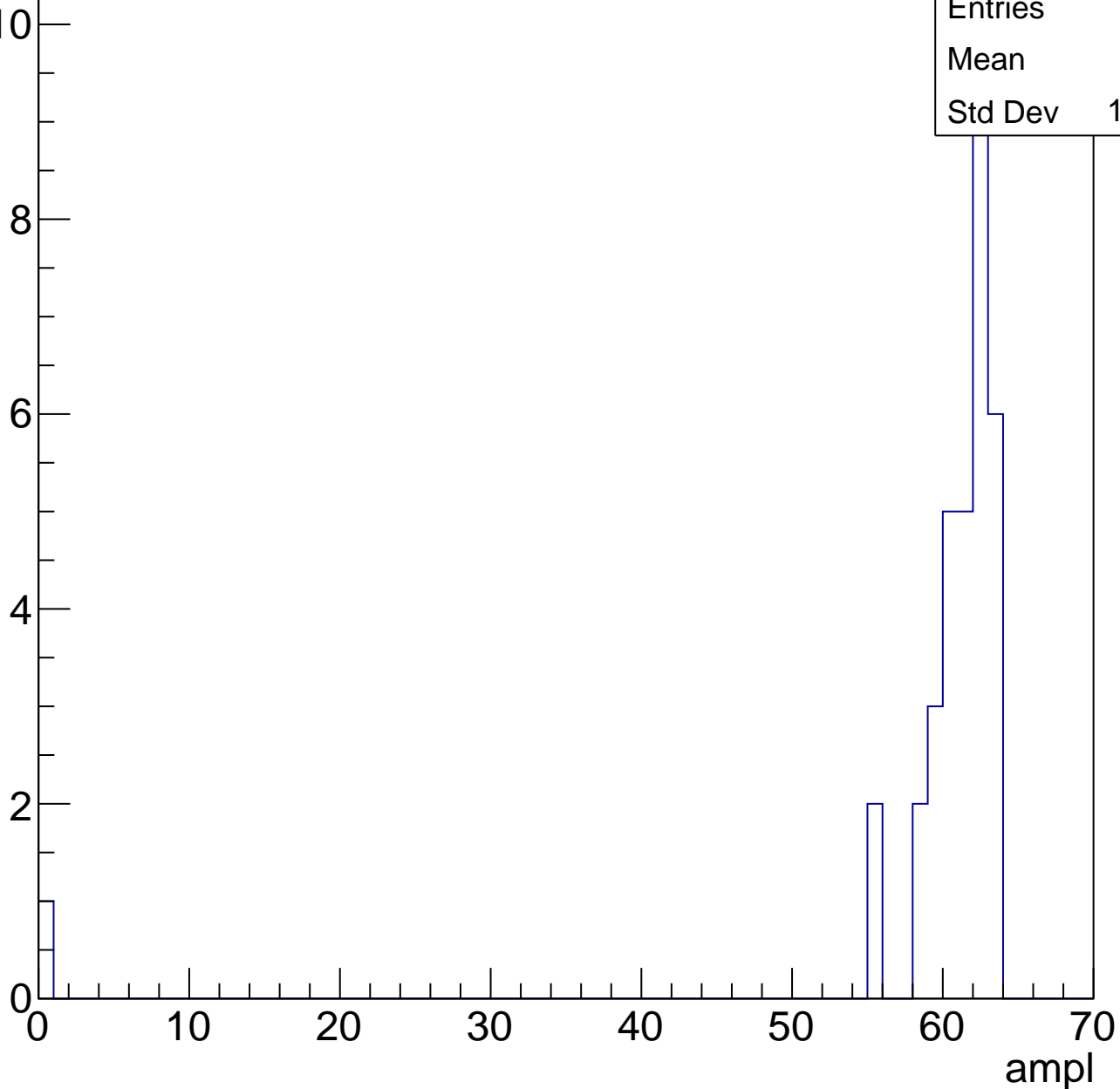


B1L103S, U12-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

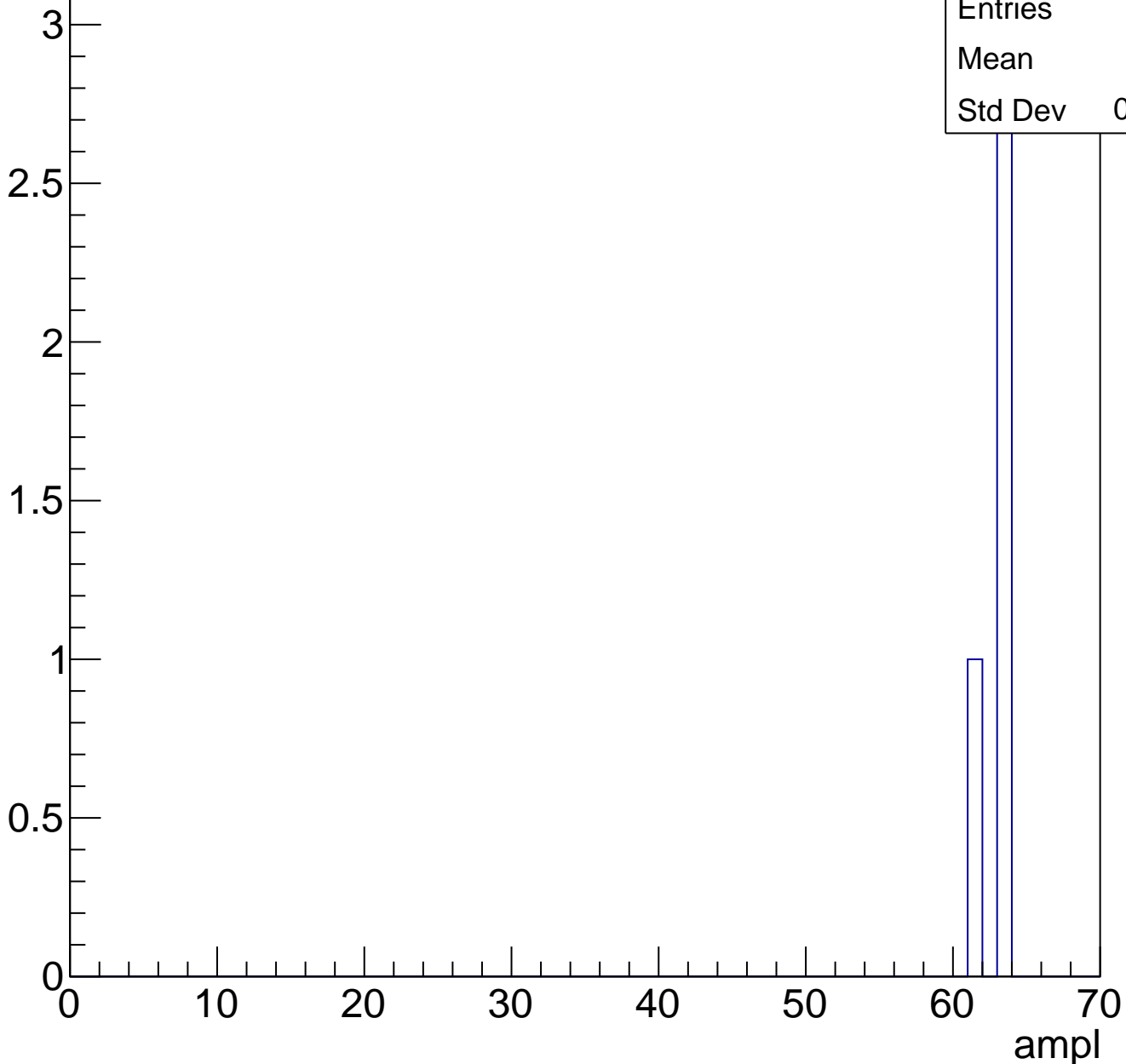
Entries	34
Mean	59
Std Dev	10.47



B1L103S, U12-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch10, adc0

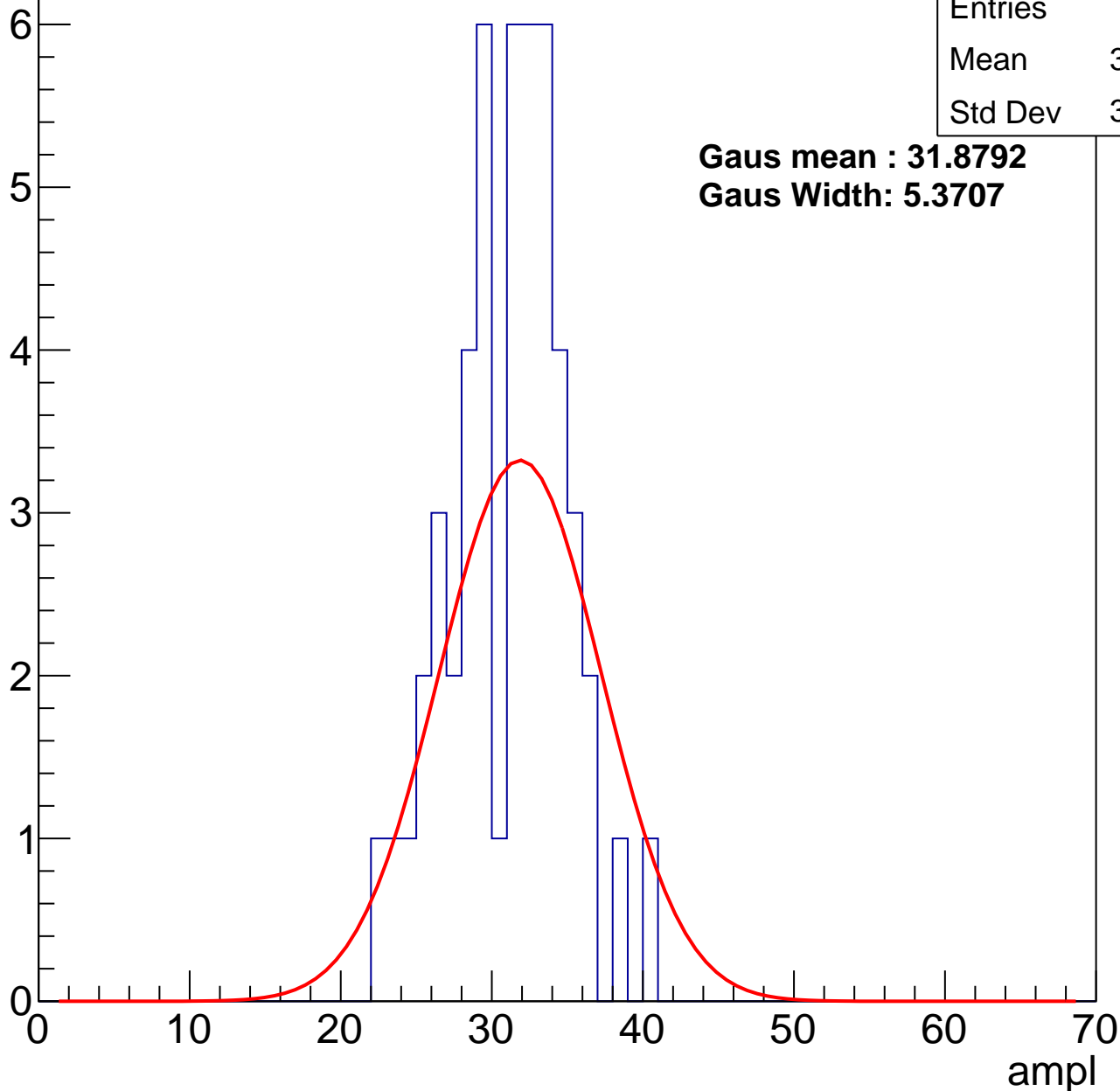
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	30.68
Std Dev	3.802

Gaus mean : 31.8792

Gaus Width: 5.3707



B1L103S, U12-ch10, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	36.91
Std Dev	3.792

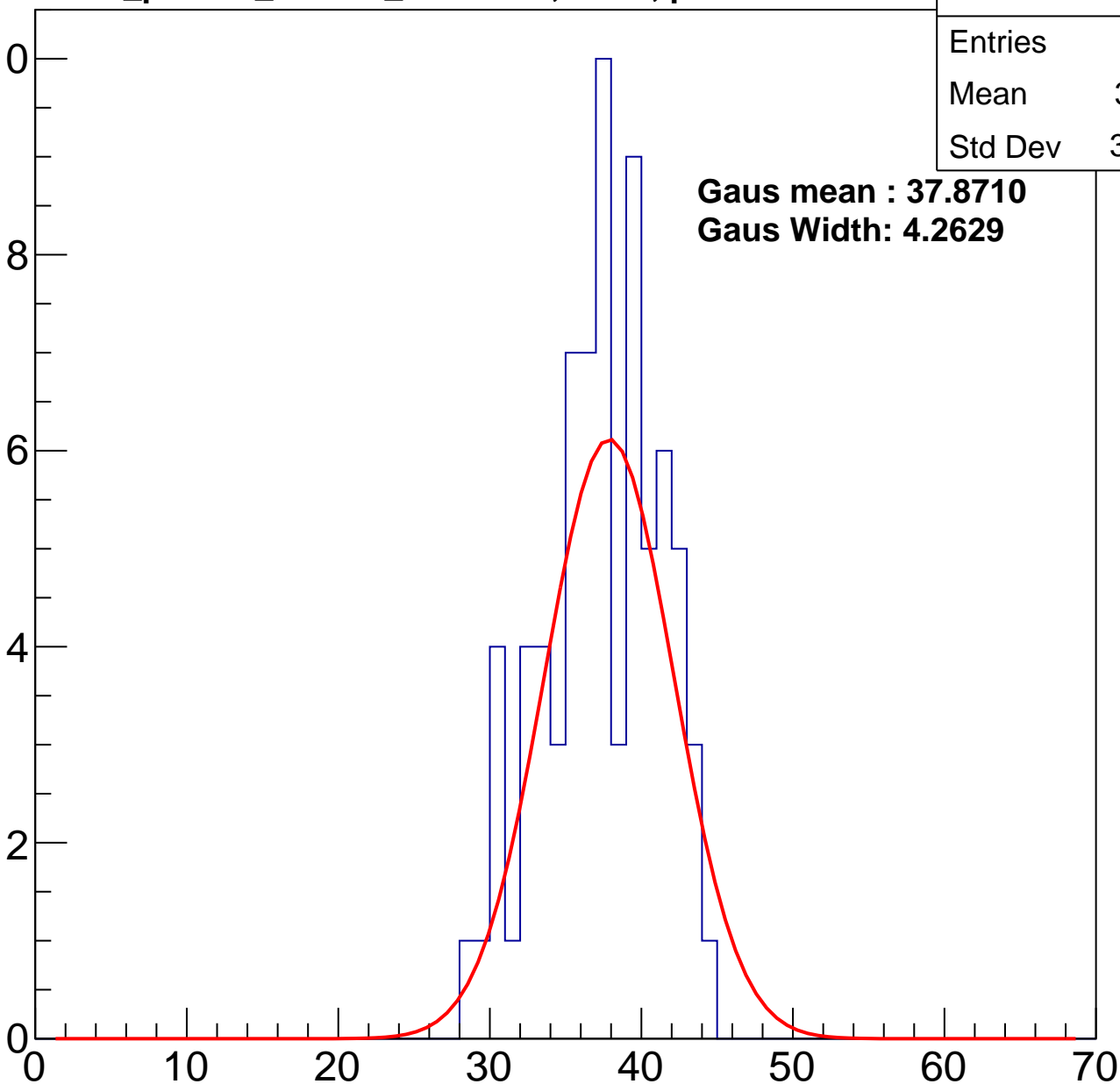
Gaus mean : 37.8710

Gaus Width: 4.2629

Entry

10
8
6
4
2
0

ampl



B1L103S, U12-ch10, adc2

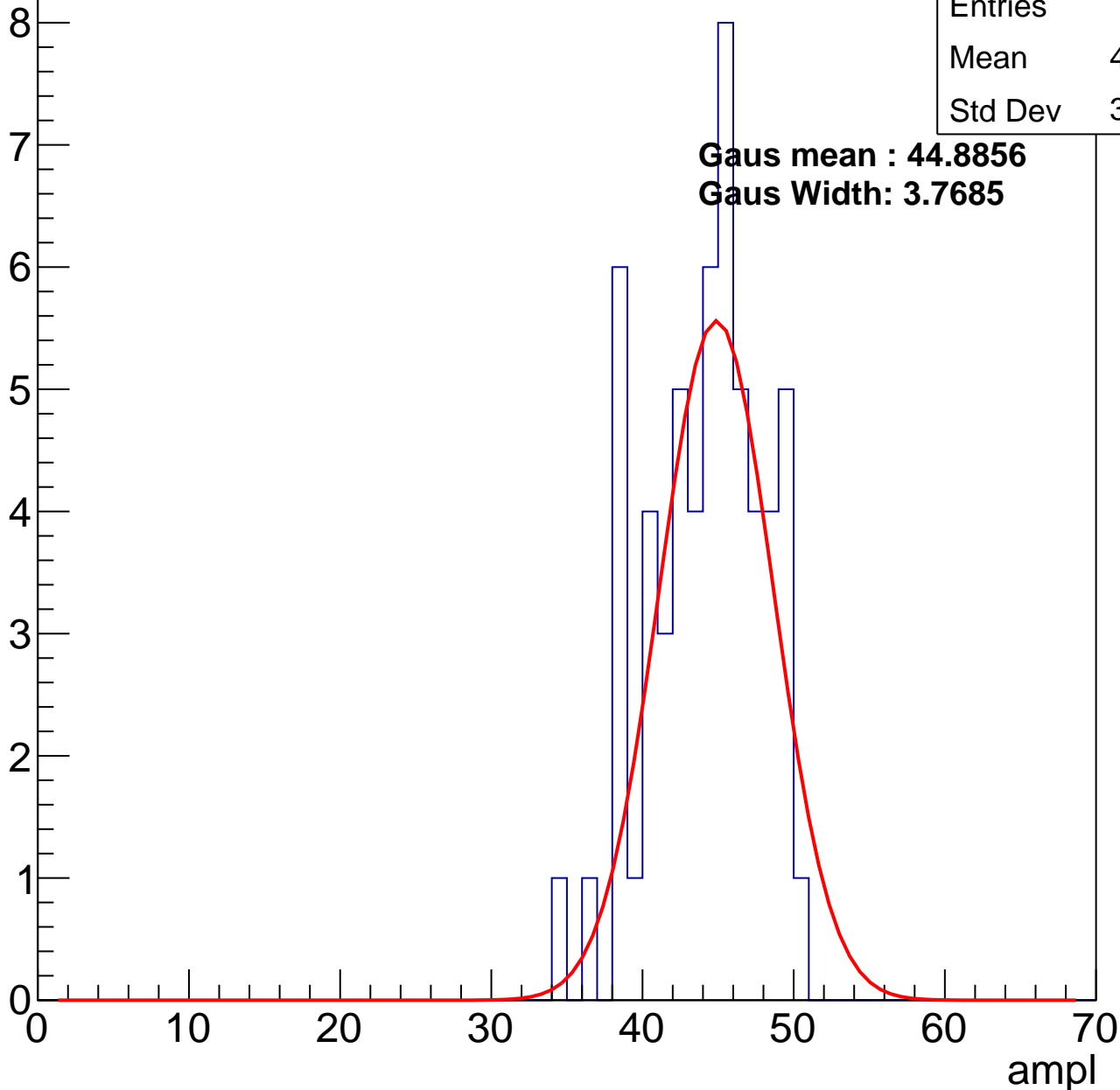
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.64
Std Dev	3.717

Gaus mean : 44.8856

Gaus Width: 3.7685

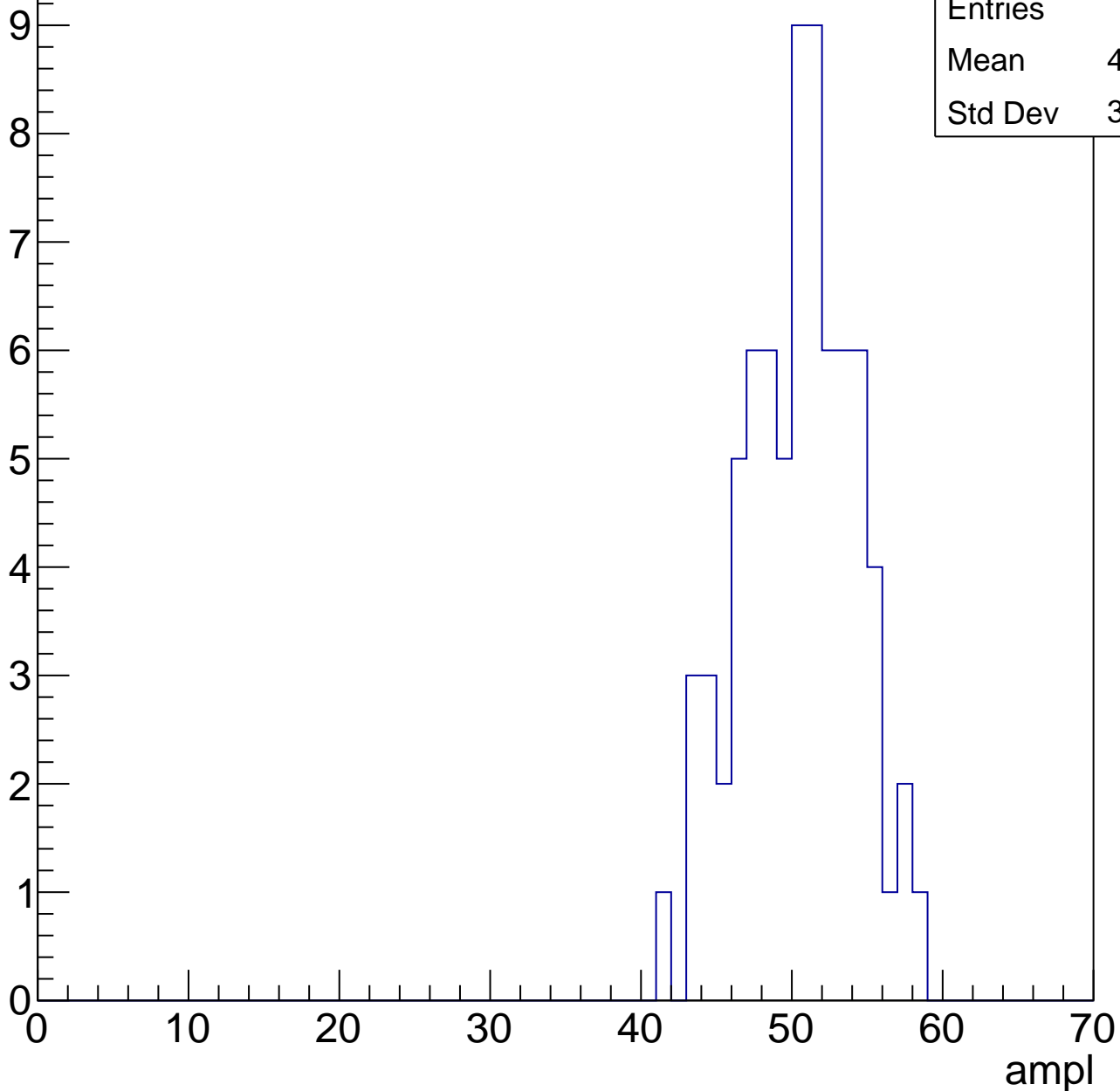


B1L103S, U12-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	49.97
Std Dev	3.706

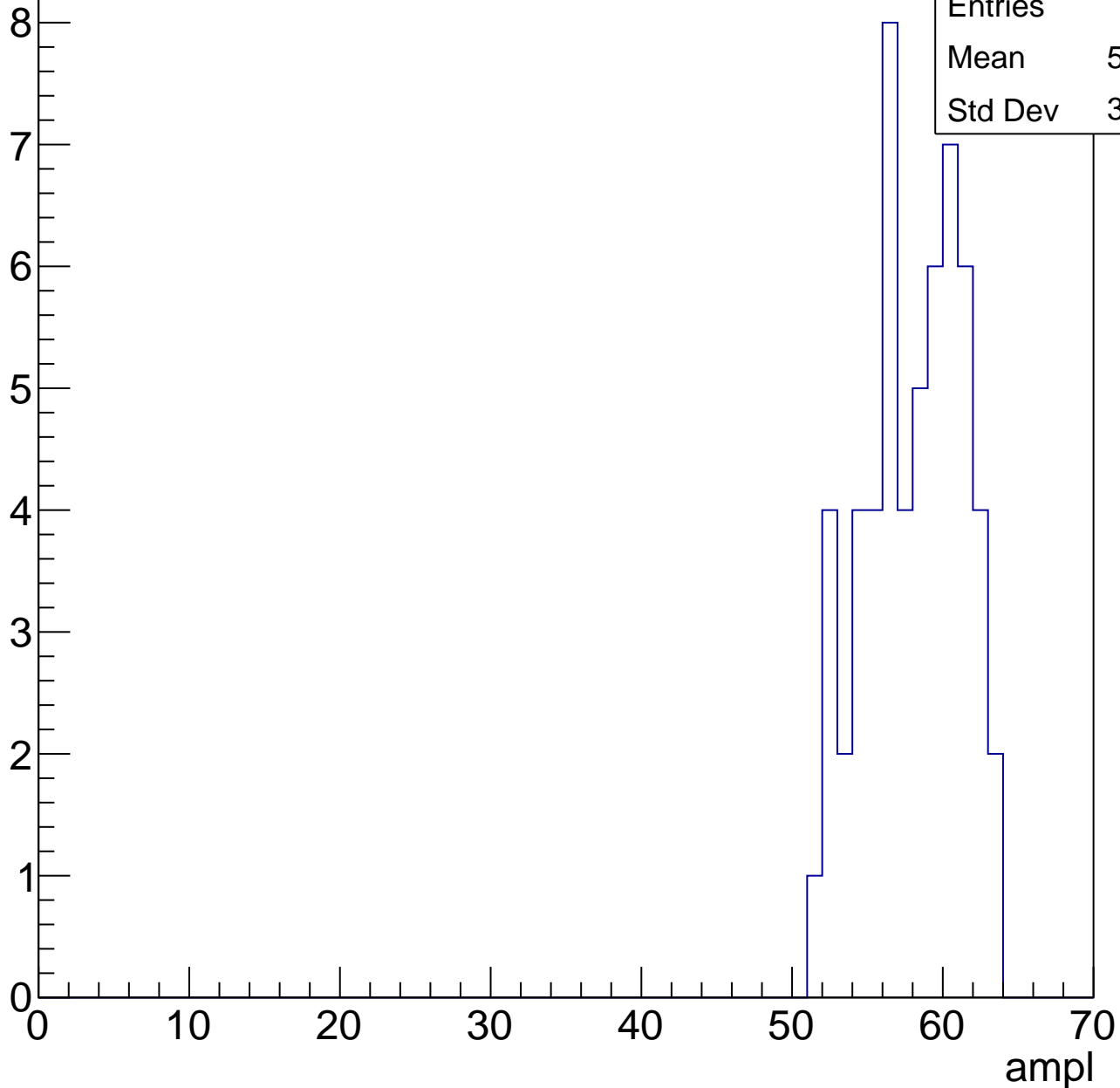


B1L103S, U12-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	57.56
Std Dev	3.162

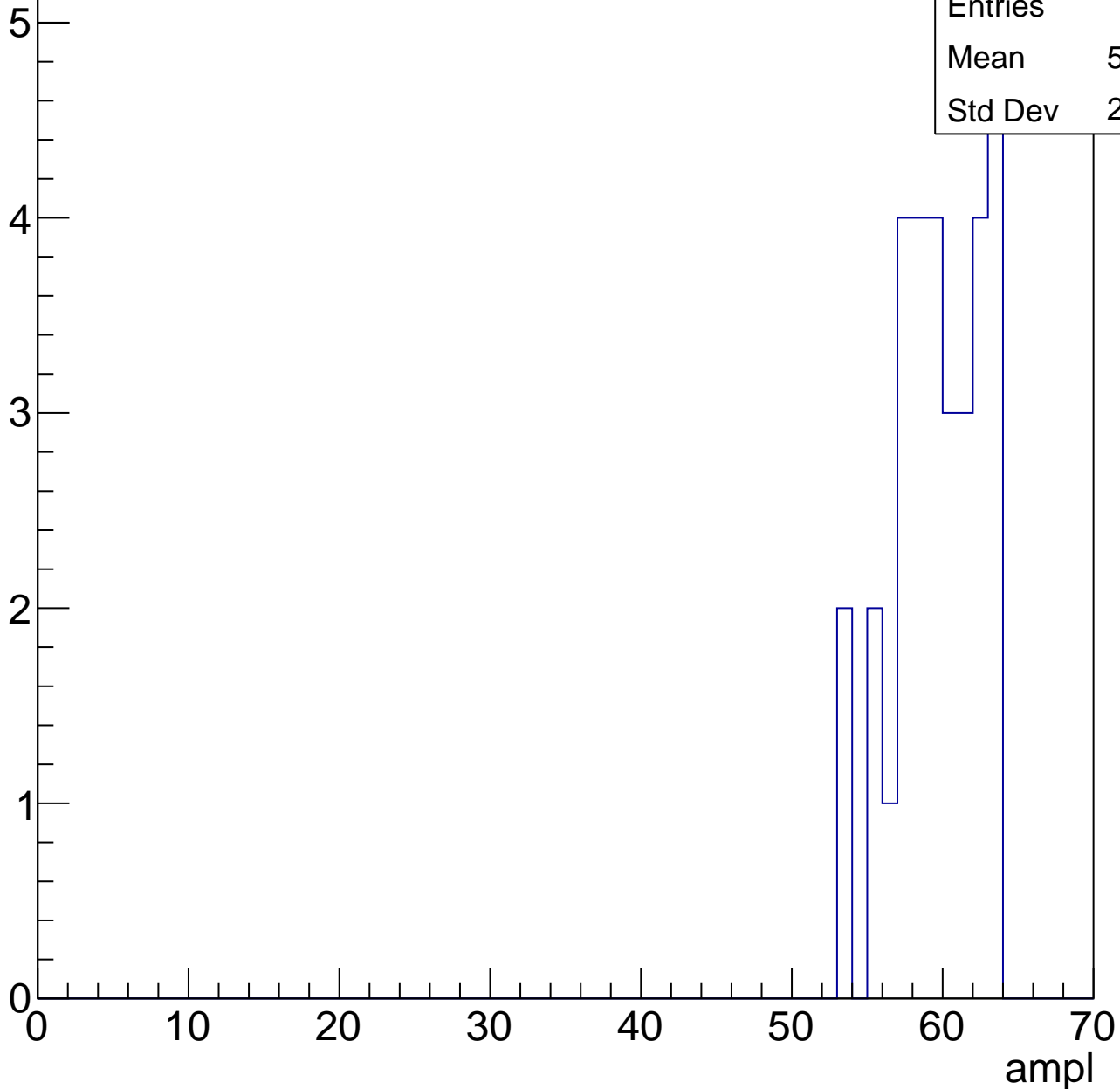


B1L103S, U12-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	59.19
Std Dev	2.866



B1L103S, U12-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch11, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	30.77
Std Dev	3.547

Gaus mean : 31.0524

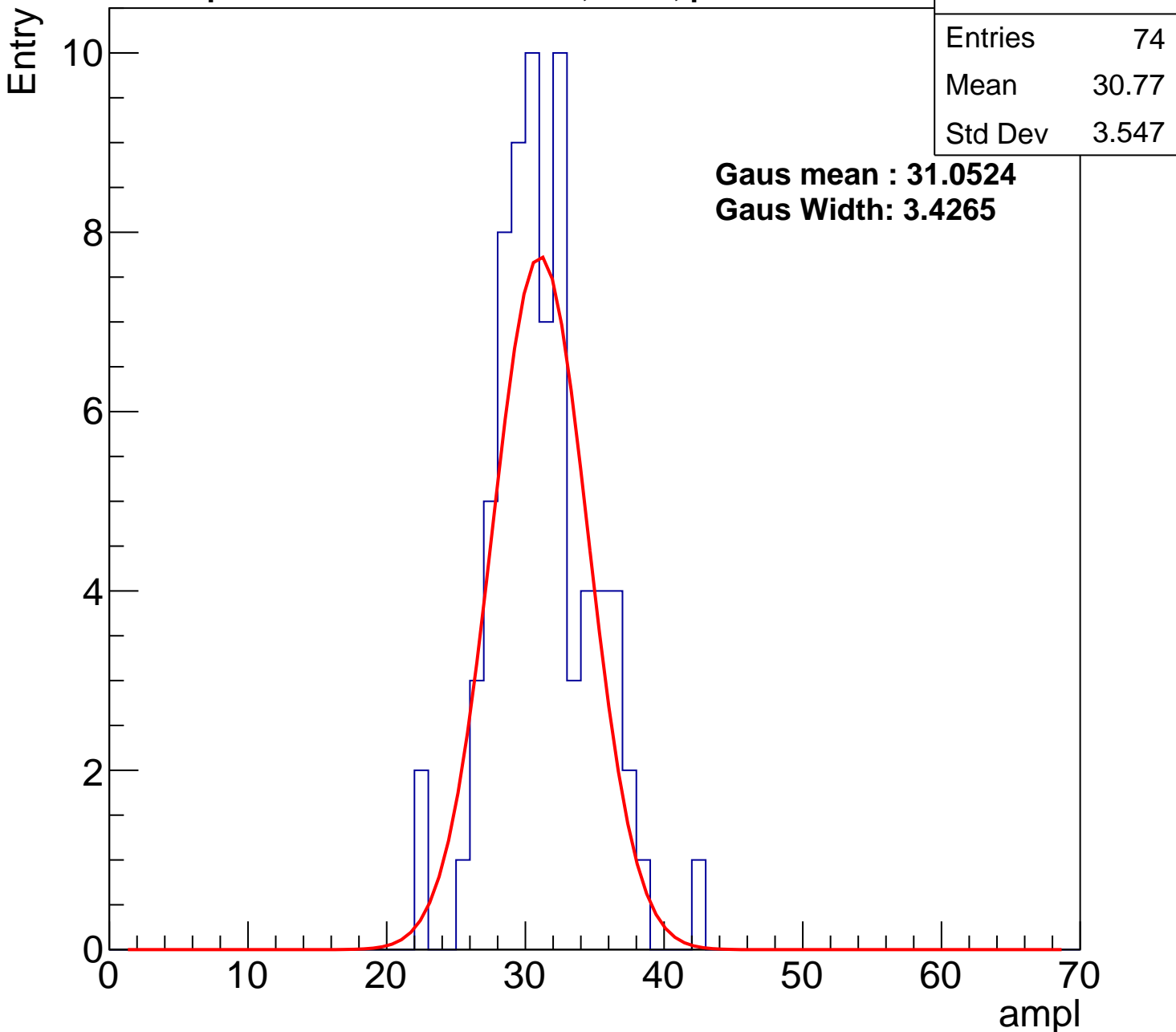
Gaus Width: 3.4265

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



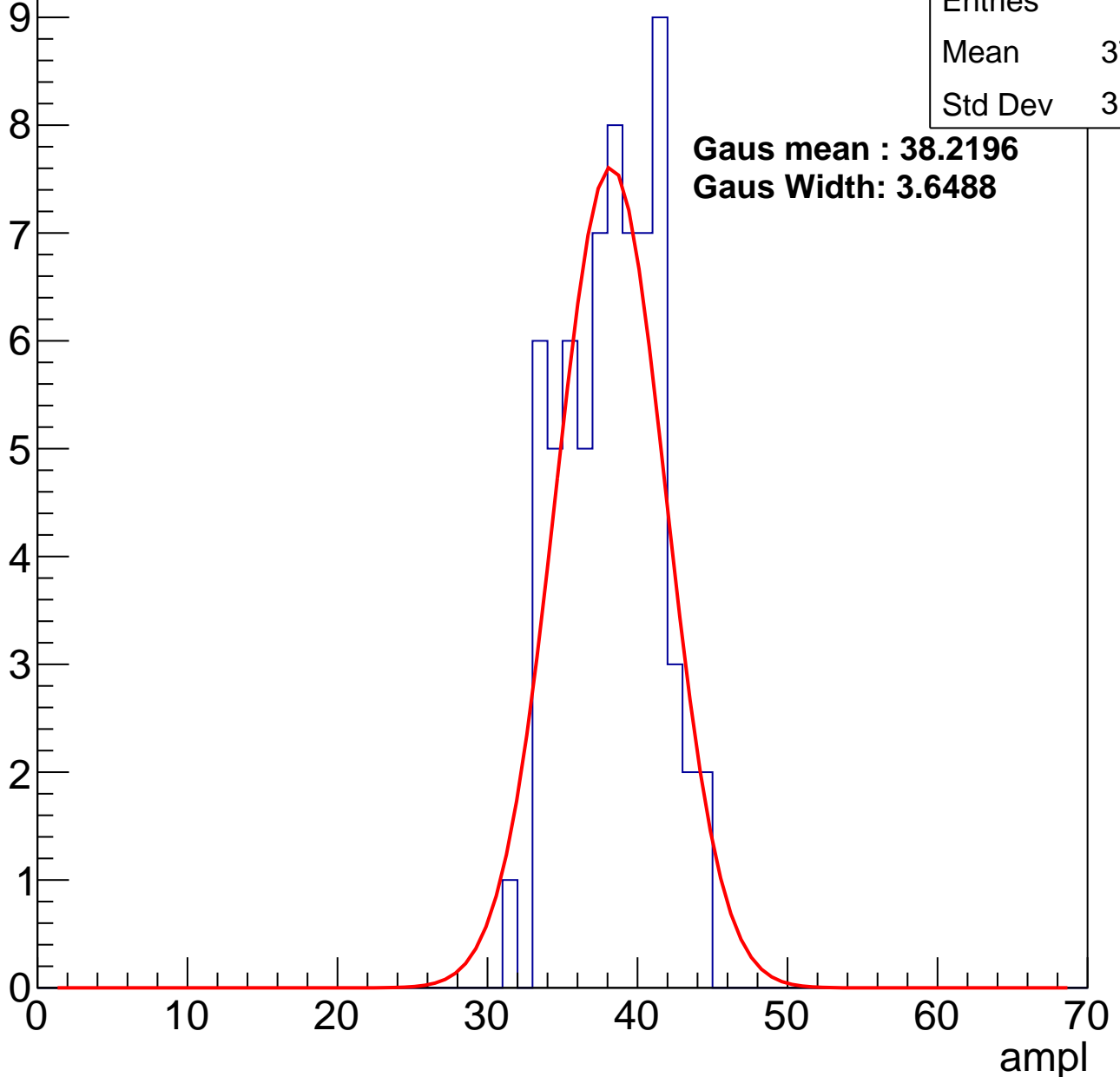
B1L103S, U12-ch11, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.85
Std Dev	3.079

Gaus mean : 38.2196
Gaus Width: 3.6488



B1L103S, U12-ch11, adc2

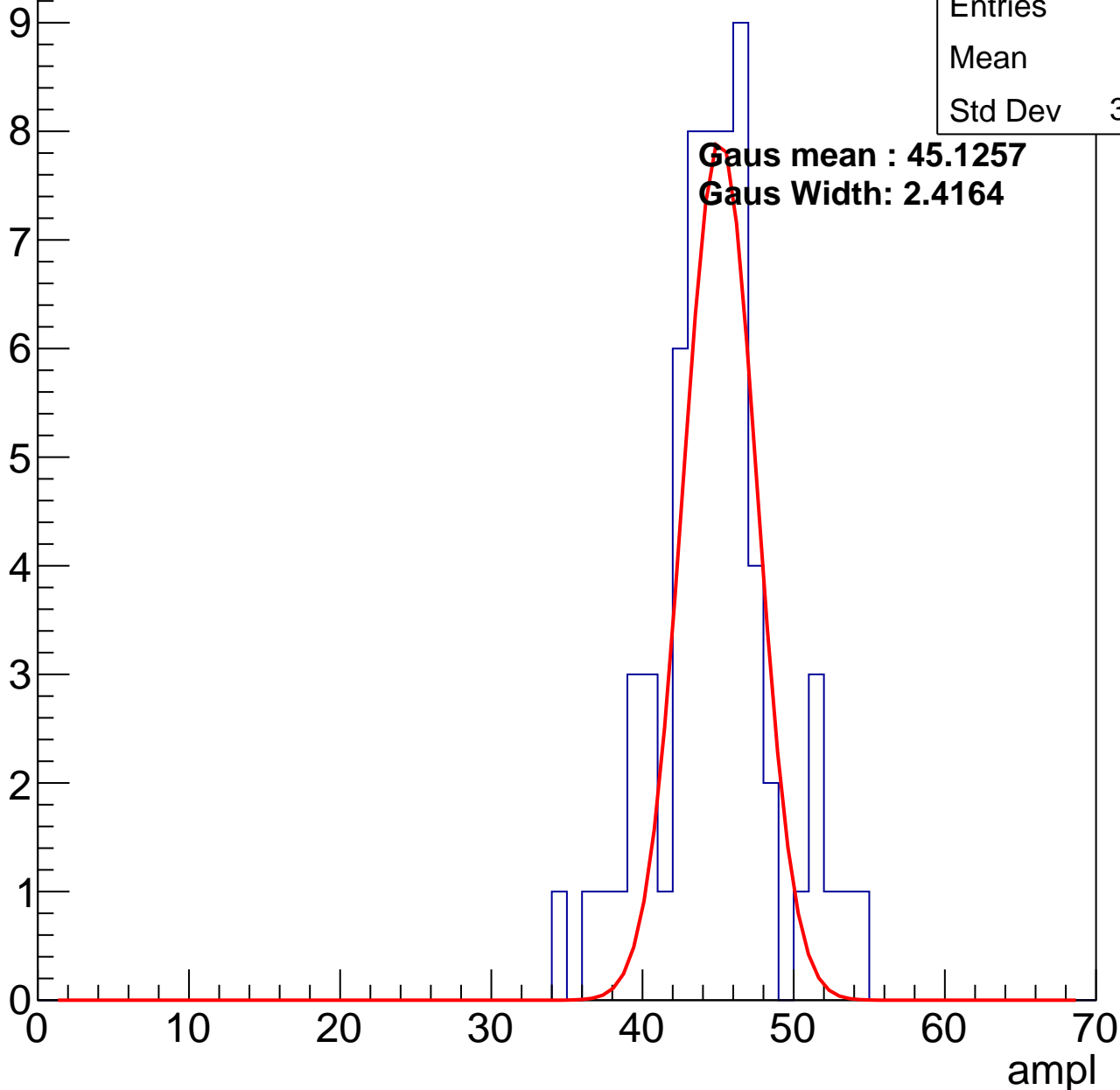
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.3
Std Dev	3.878

Gaus mean : 45.1257

Gaus Width: 2.4164



B1L103S, U12-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

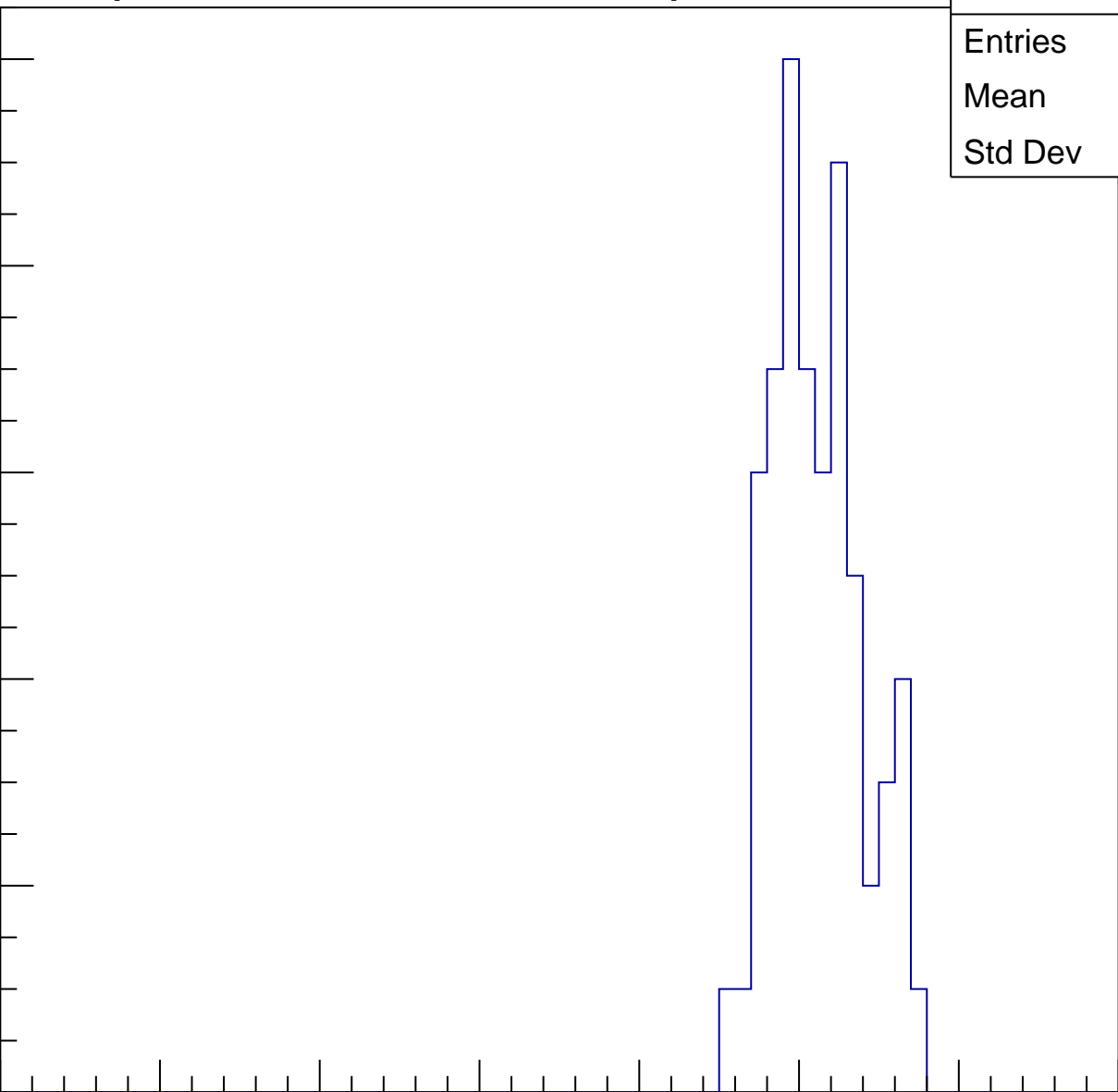
Entries	62
Mean	50.68
Std Dev	2.816

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

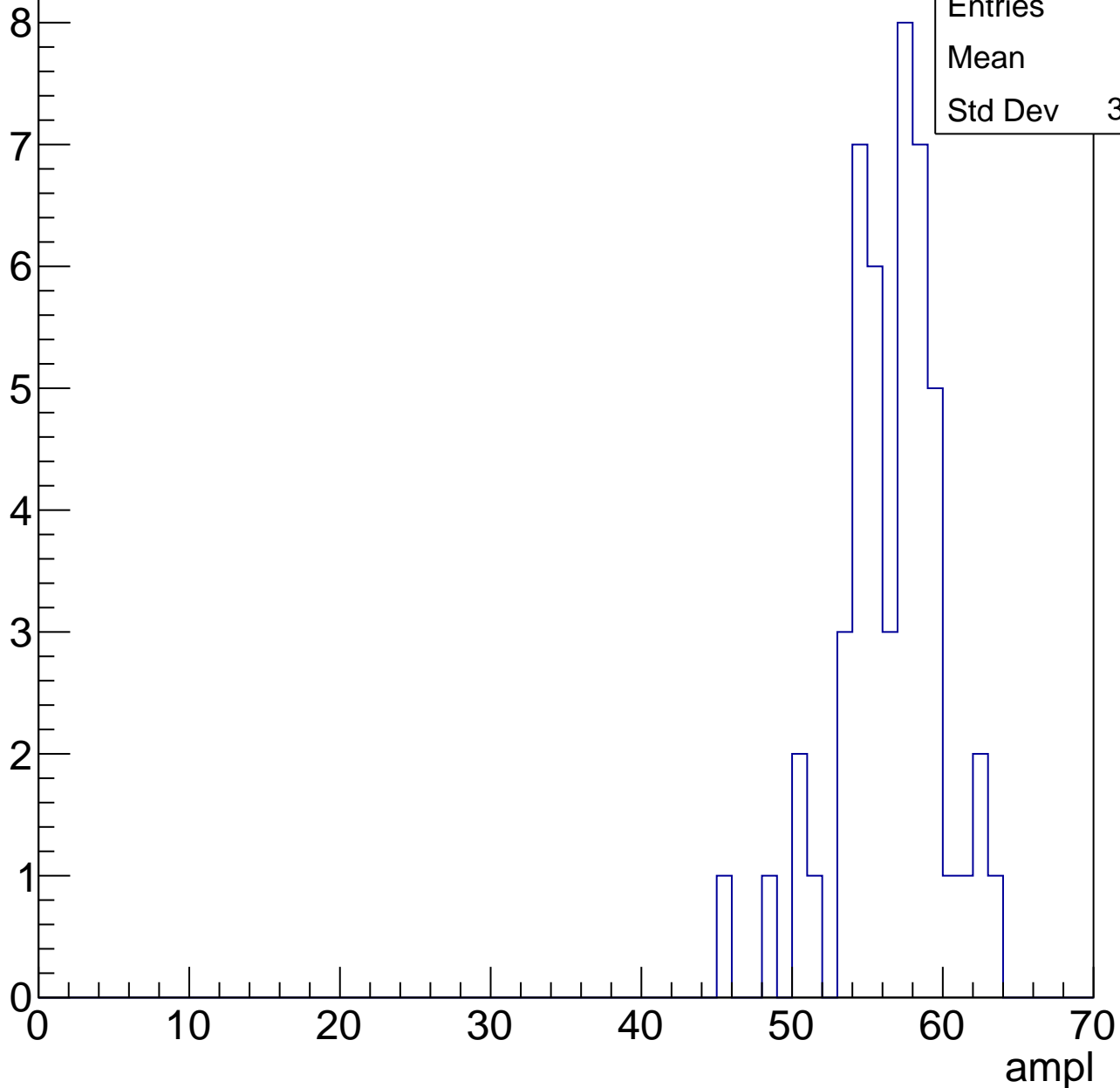
ampl



B1L103S, U12-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

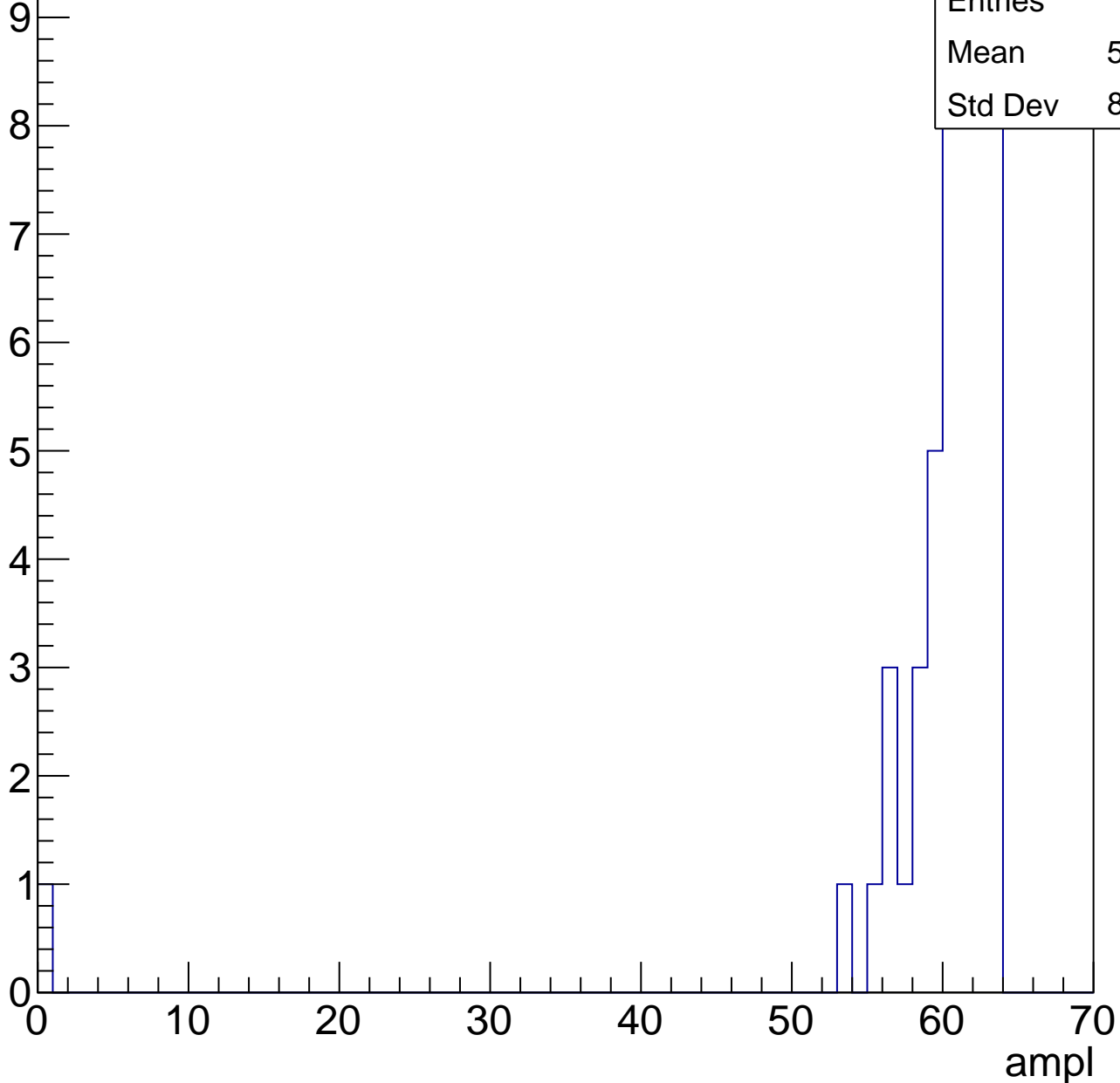


B1L103S, U12-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.04
Std Dev	8.834



B1L103S, U12-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch12, adc0

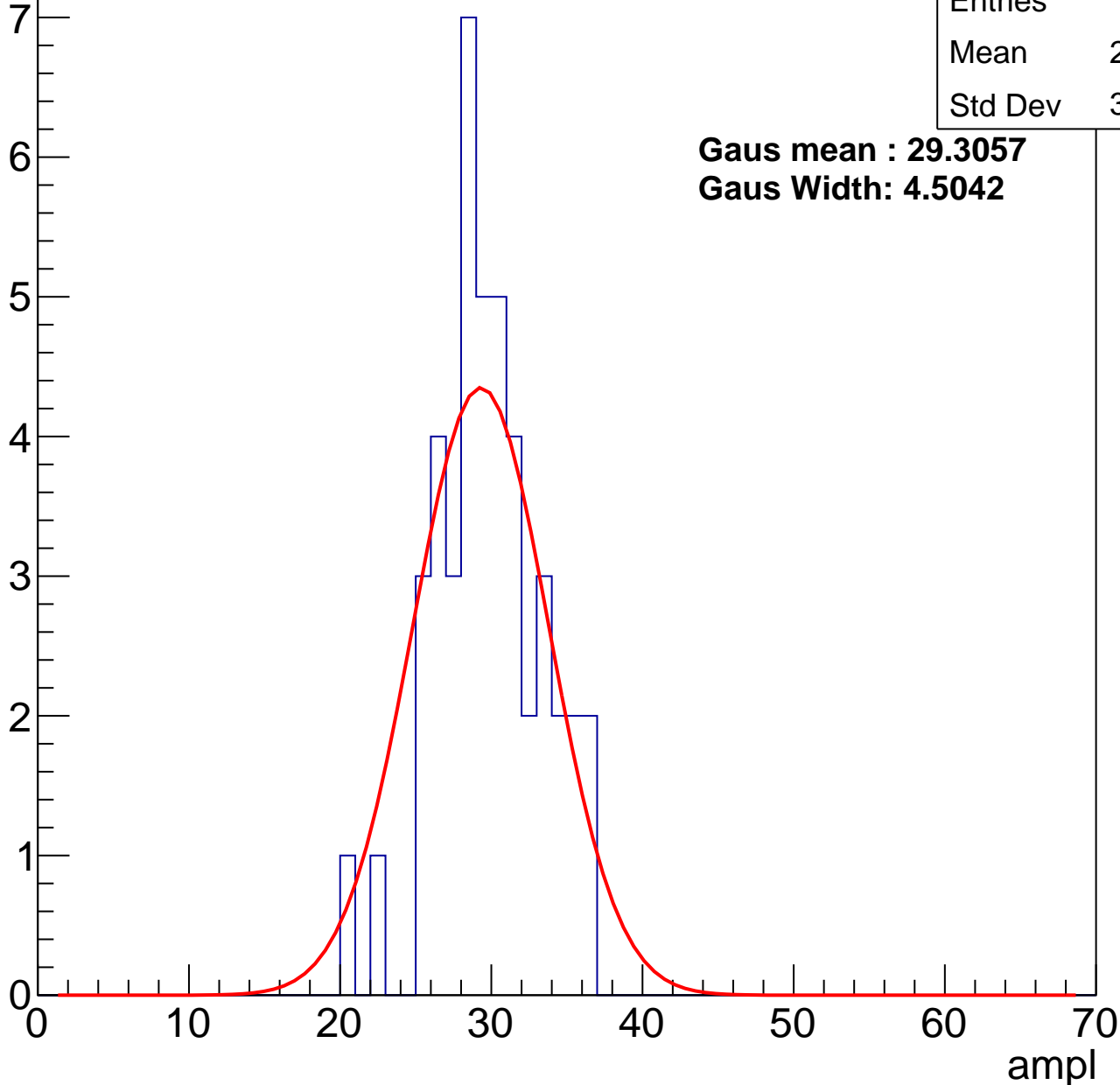
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	29.32
Std Dev	3.489

Gaus mean : 29.3057

Gaus Width: 4.5042



B1L103S, U12-ch12, adc1

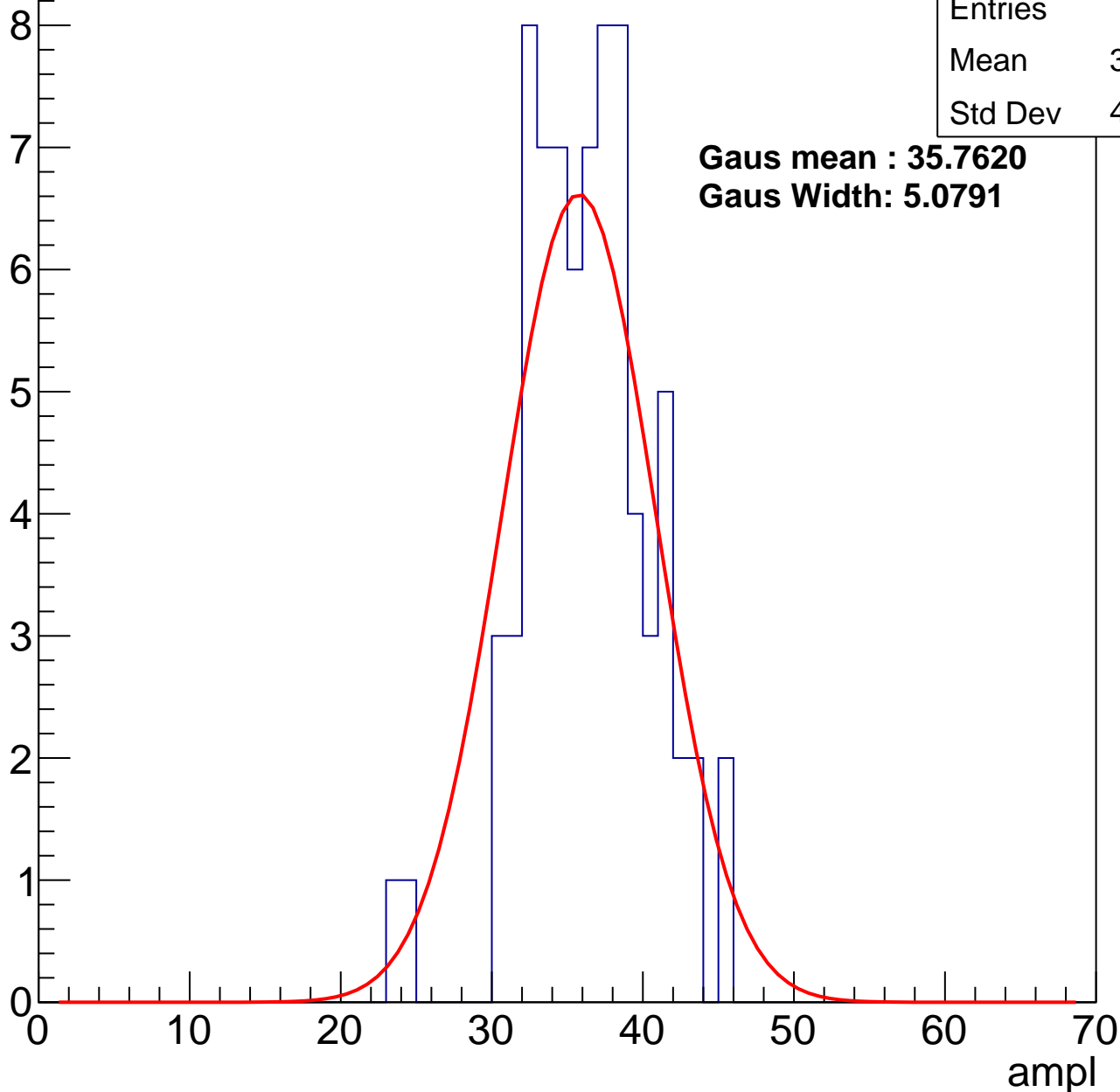
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	35.82
Std Dev	4.108

Gaus mean : 35.7620

Gaus Width: 5.0791



B1L103S, U12-ch12, adc2

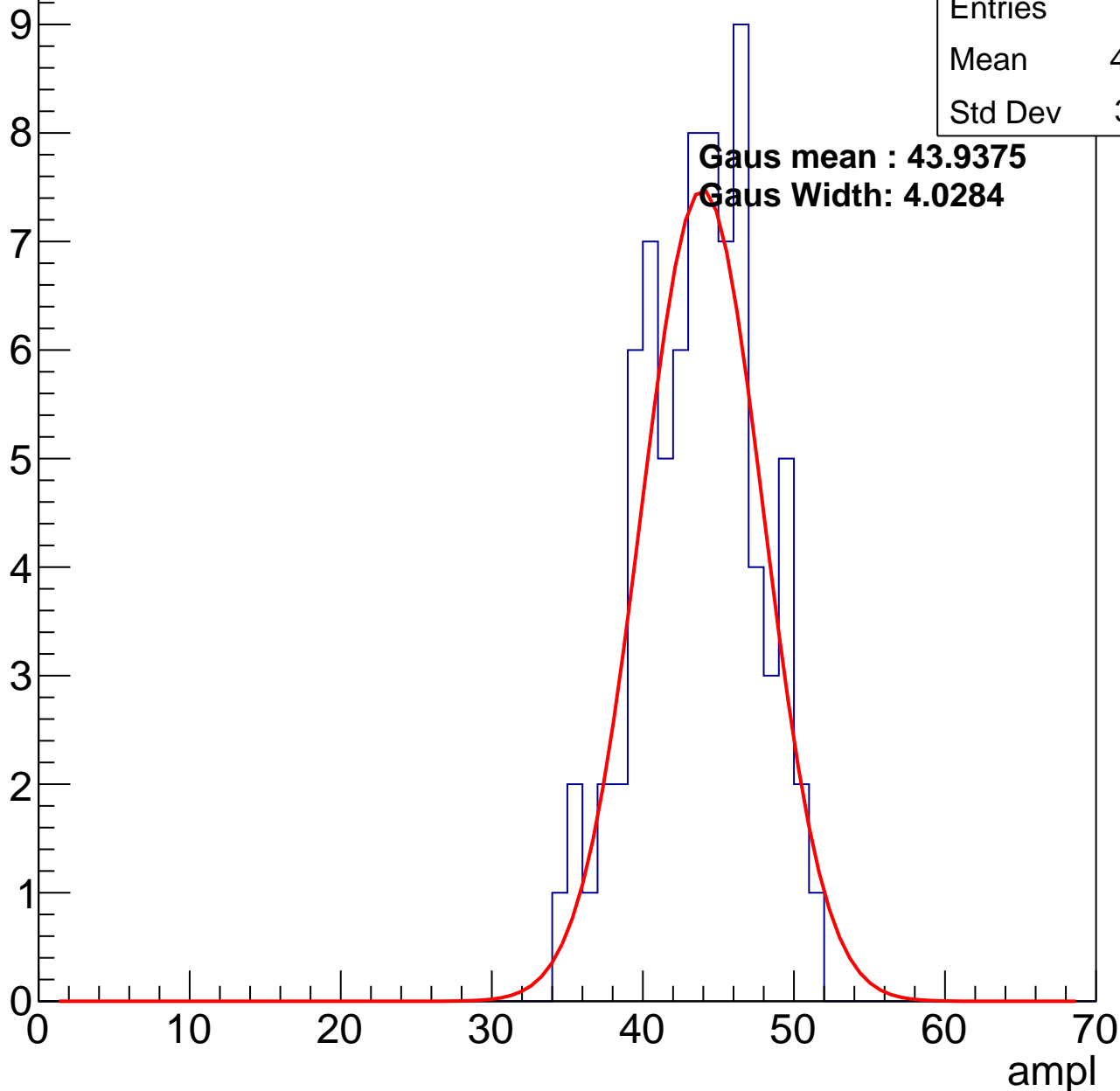
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	43.22
Std Dev	3.831

Gaus mean : 43.9375

Gaus Width: 4.0284

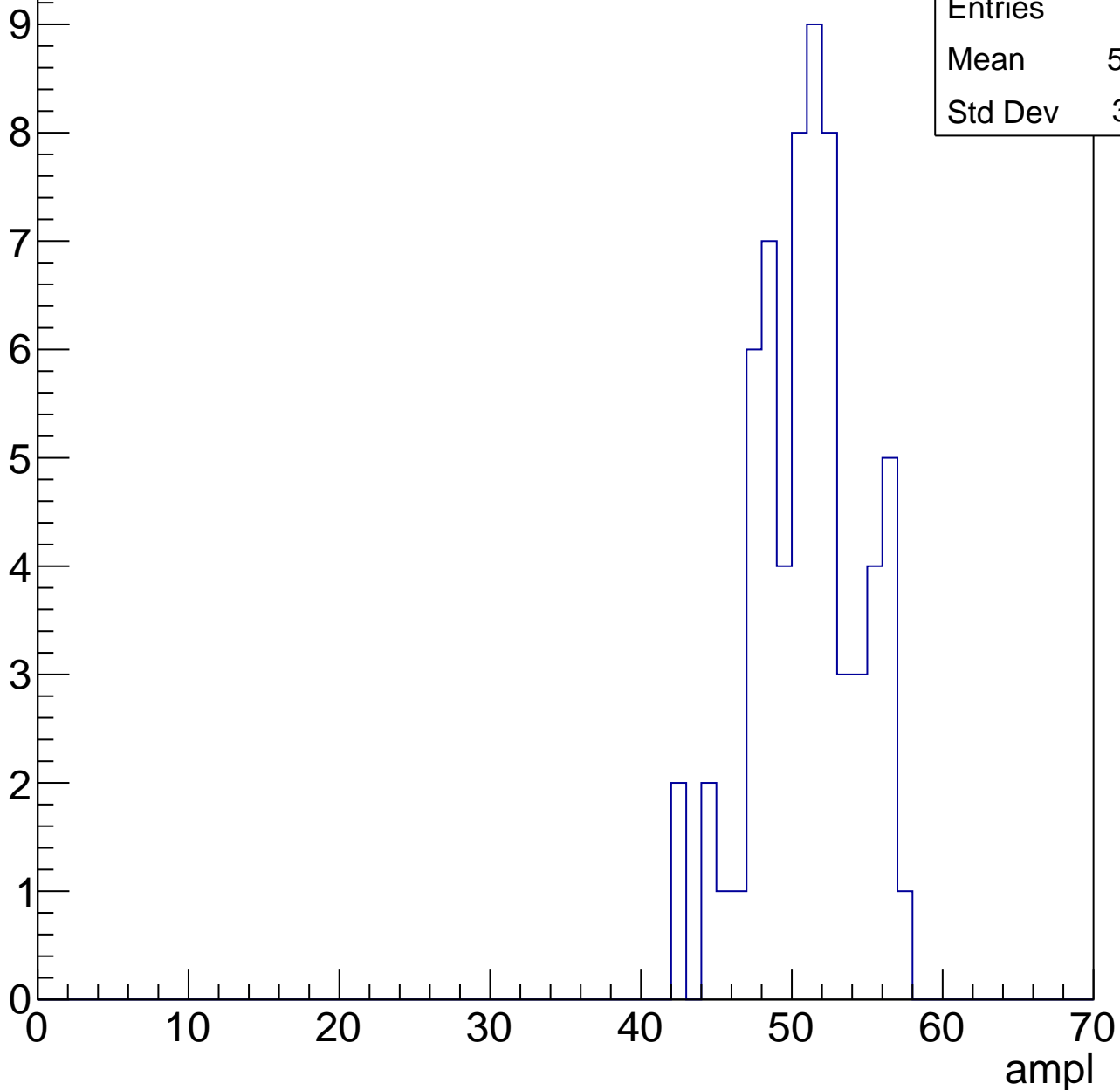


B1L103S, U12-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.47
Std Dev	3.441

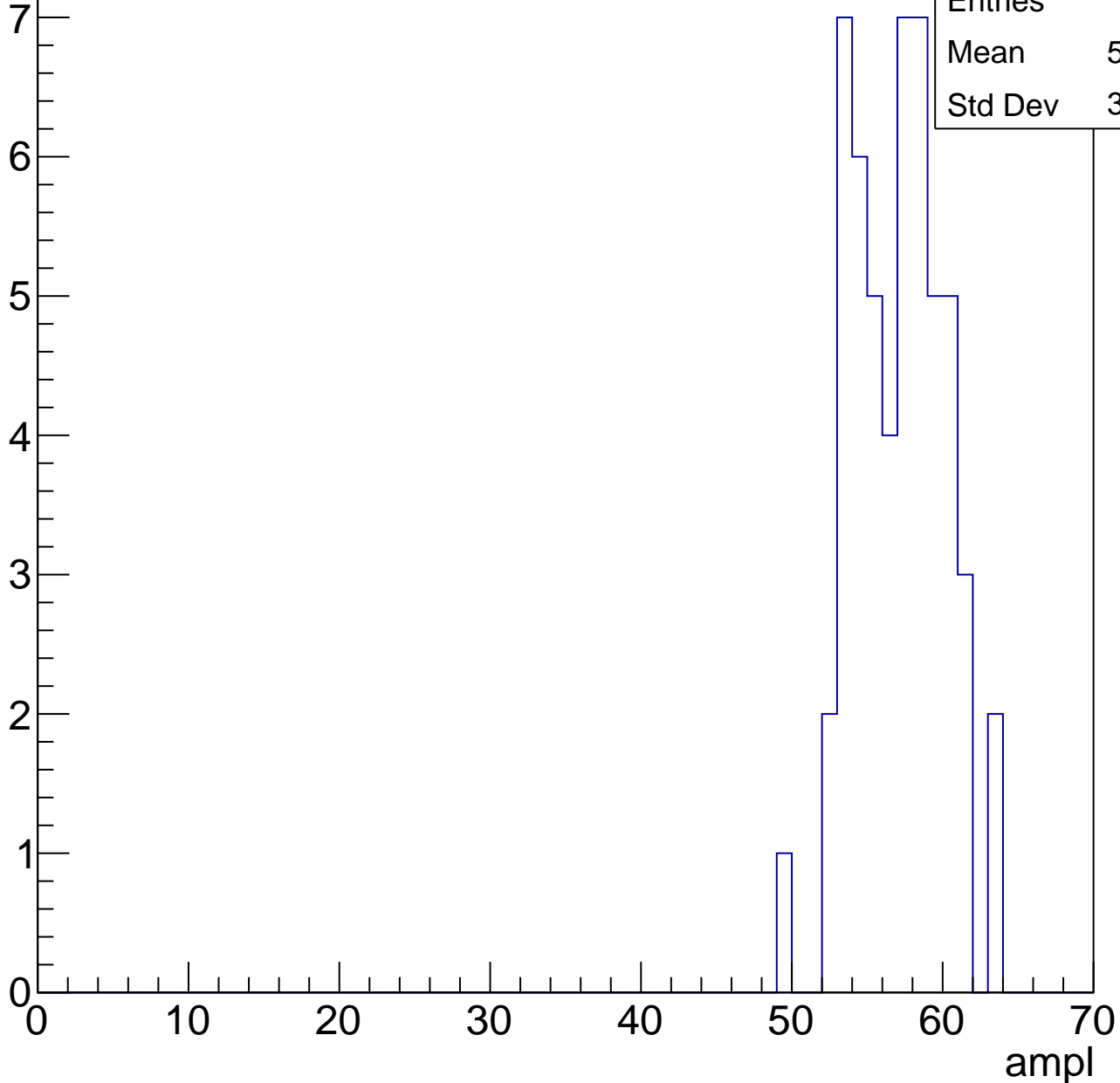


B1L103S, U12-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	56.59
Std Dev	3.003

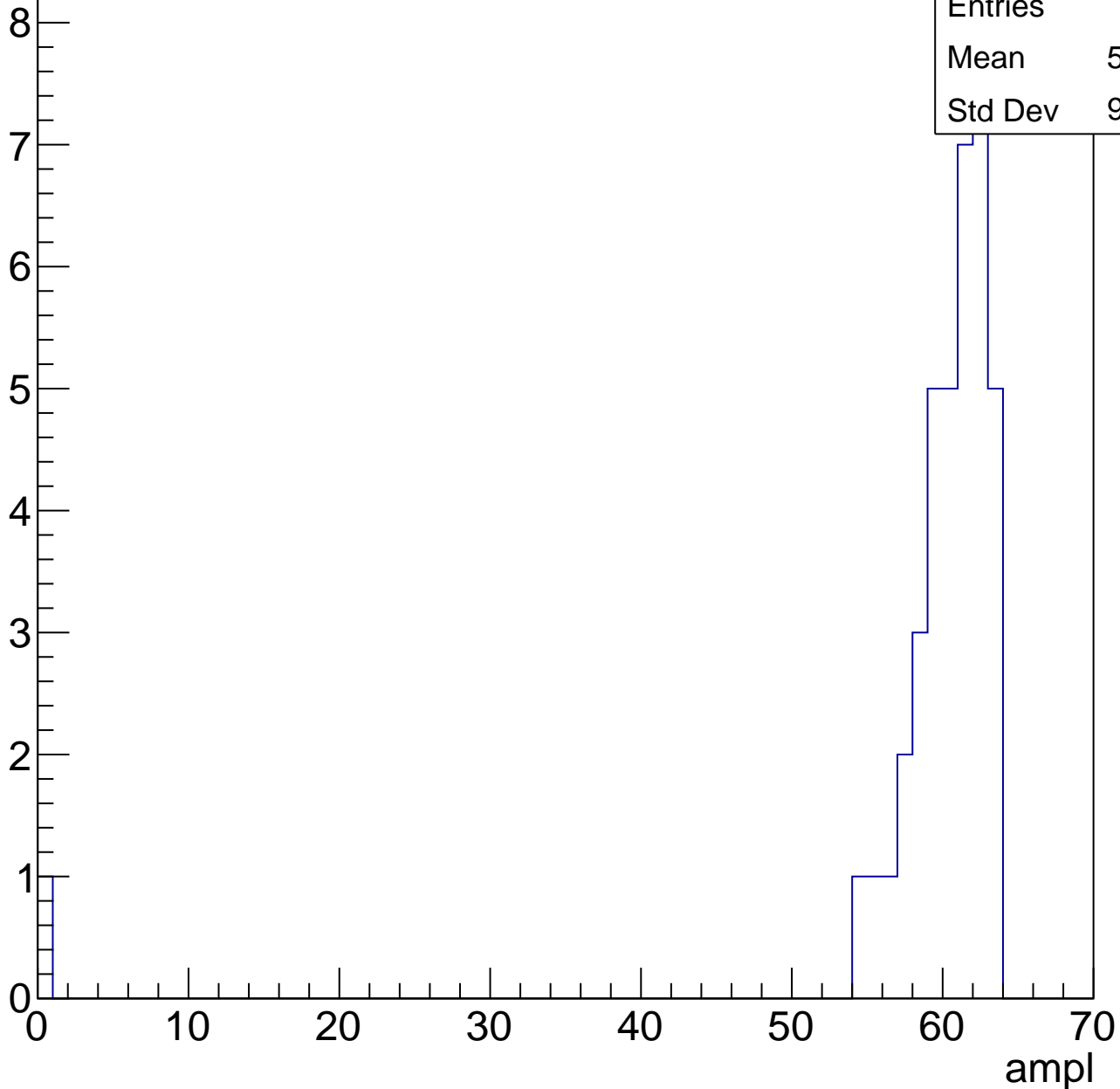


B1L103S, U12-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.62
Std Dev	9.768



B1L103S, U12-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch13, adc0

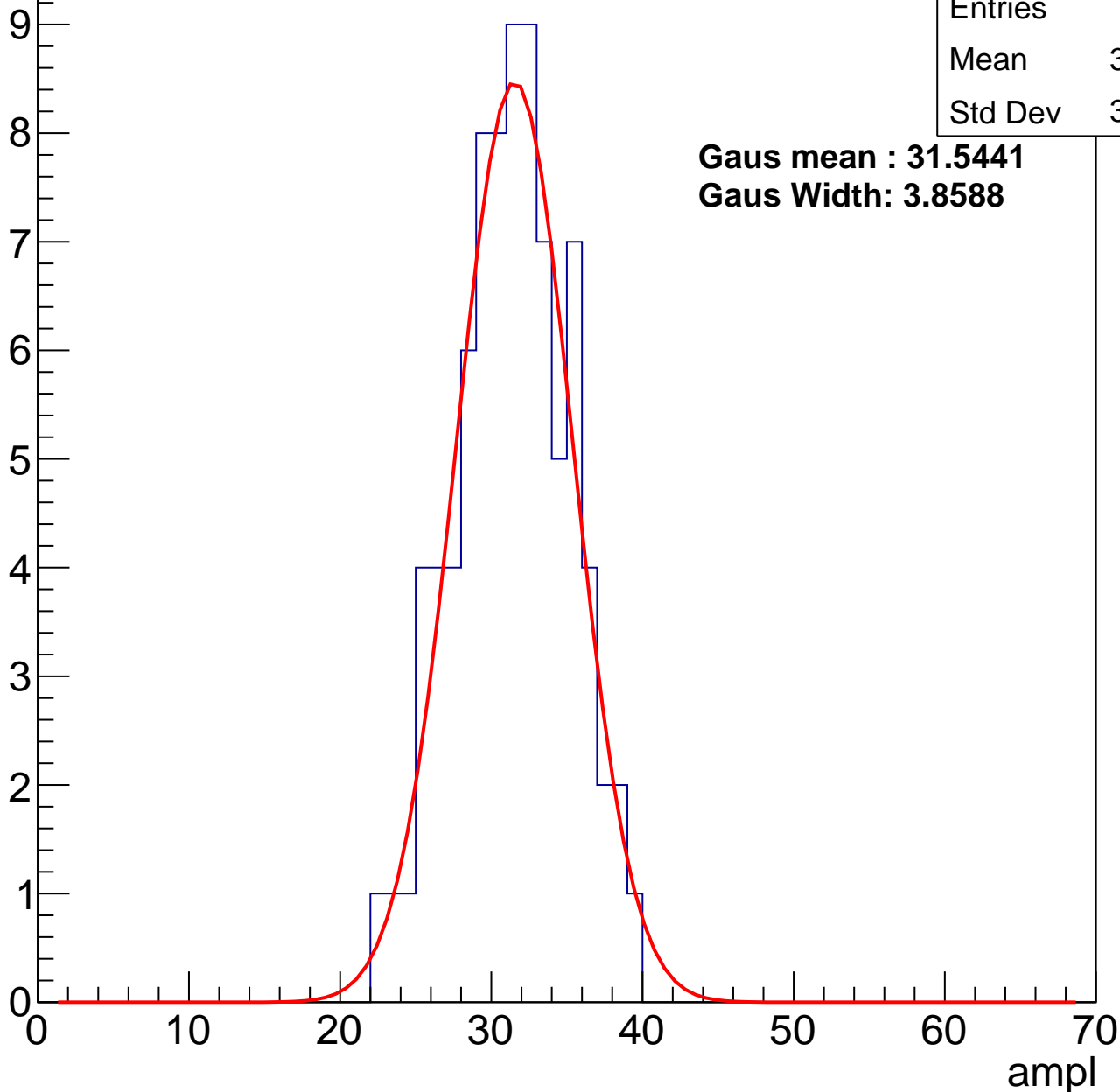
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	30.93
Std Dev	3.679

Gaus mean : 31.5441

Gaus Width: 3.8588



B1L103S, U12-ch13, adc1

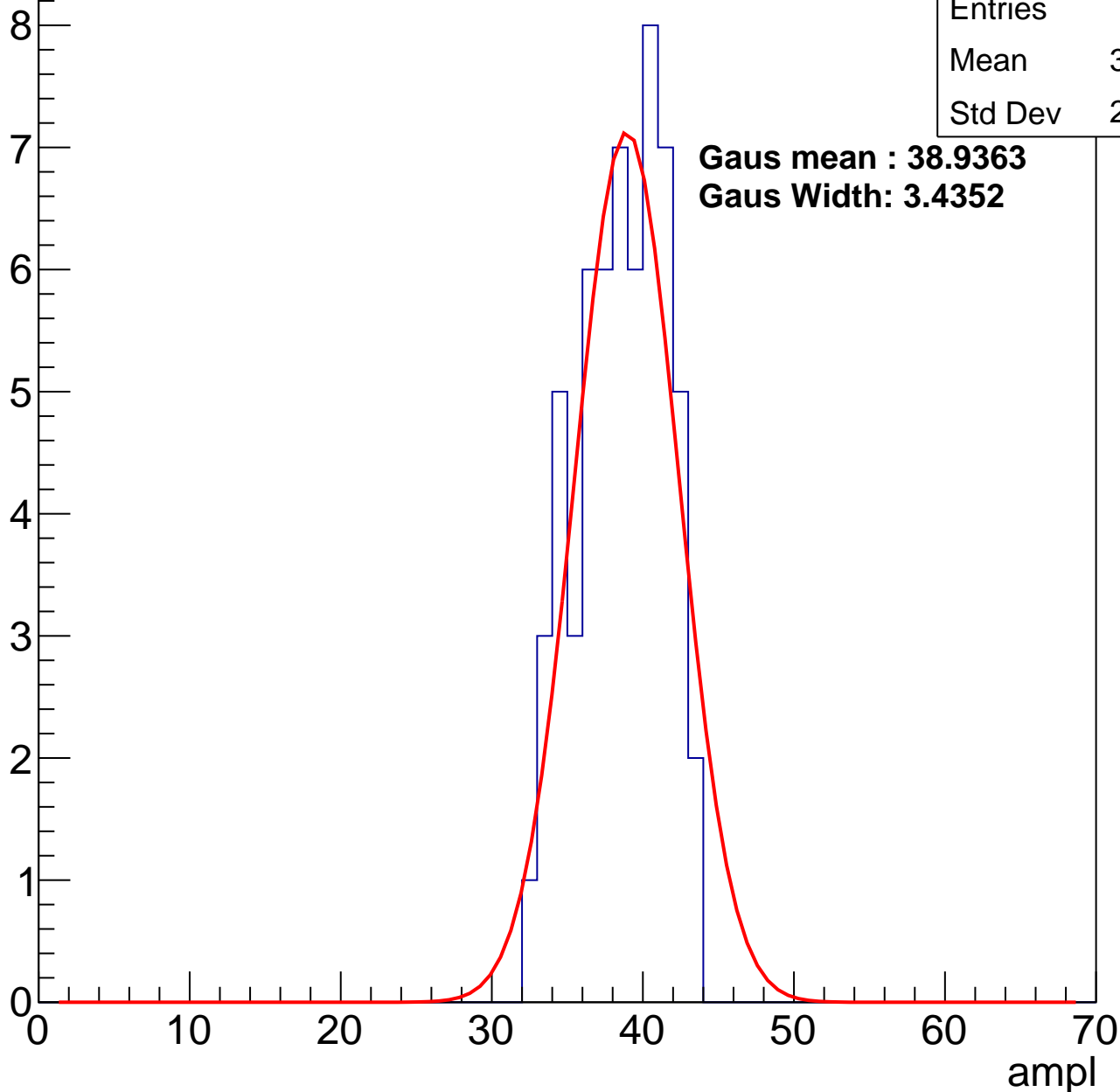
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	38.08
Std Dev	2.848

Gaus mean : 38.9363

Gaus Width: 3.4352



B1L103S, U12-ch13, adc2

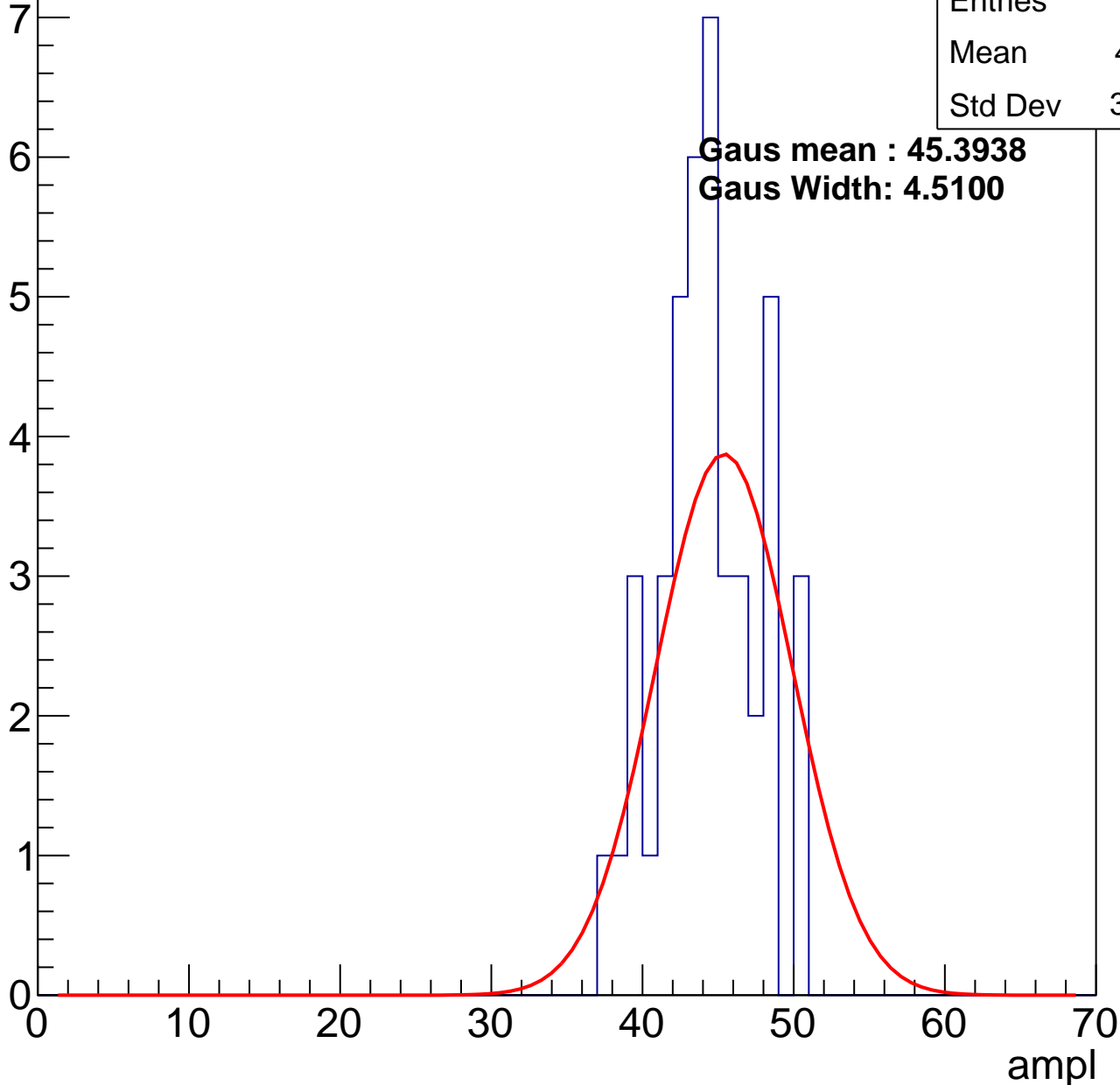
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	43.91
Std Dev	3.234

Gaus mean : 45.3938

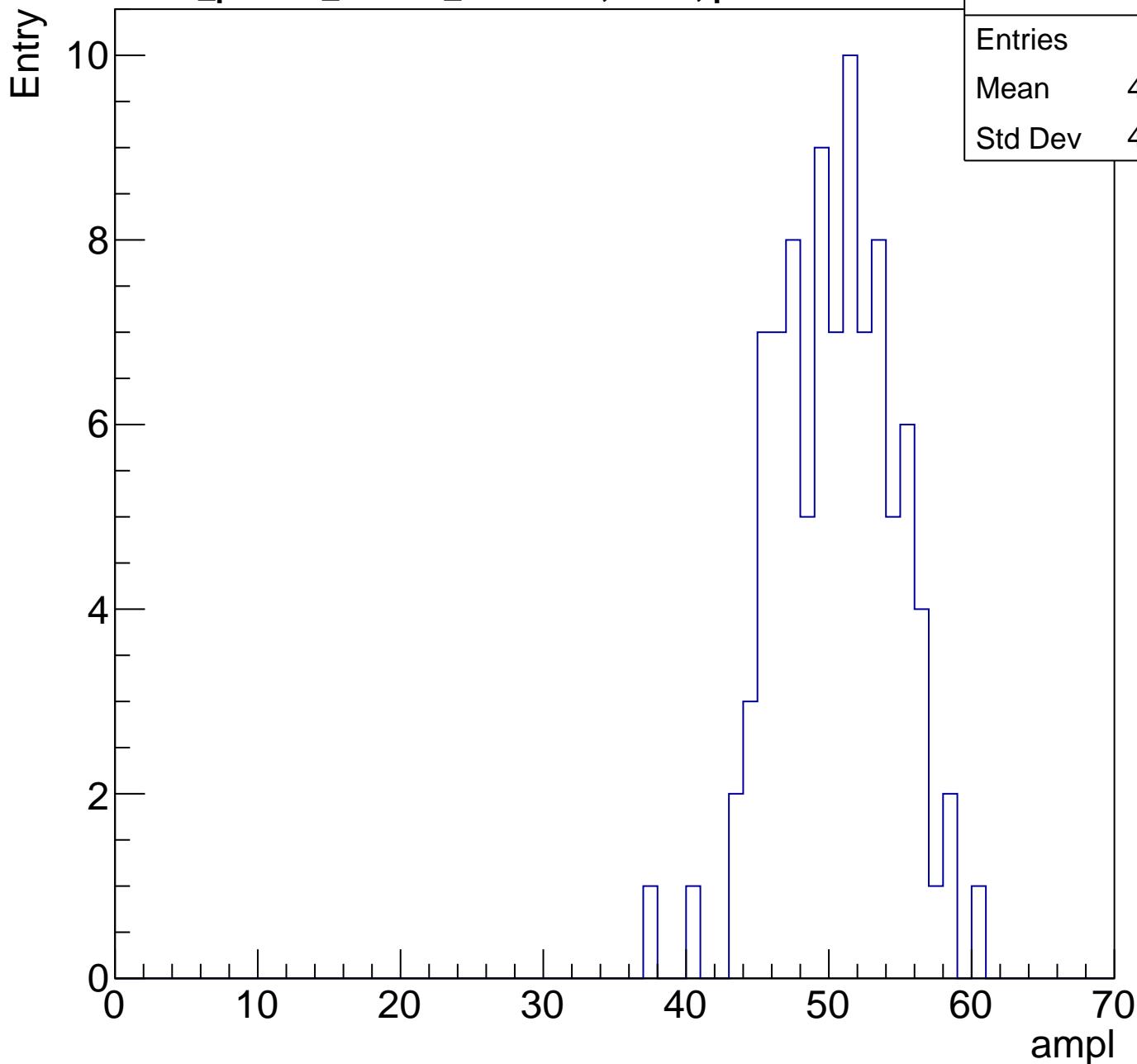
Gaus Width: 4.5100



B1L103S, U12-ch13, adc3

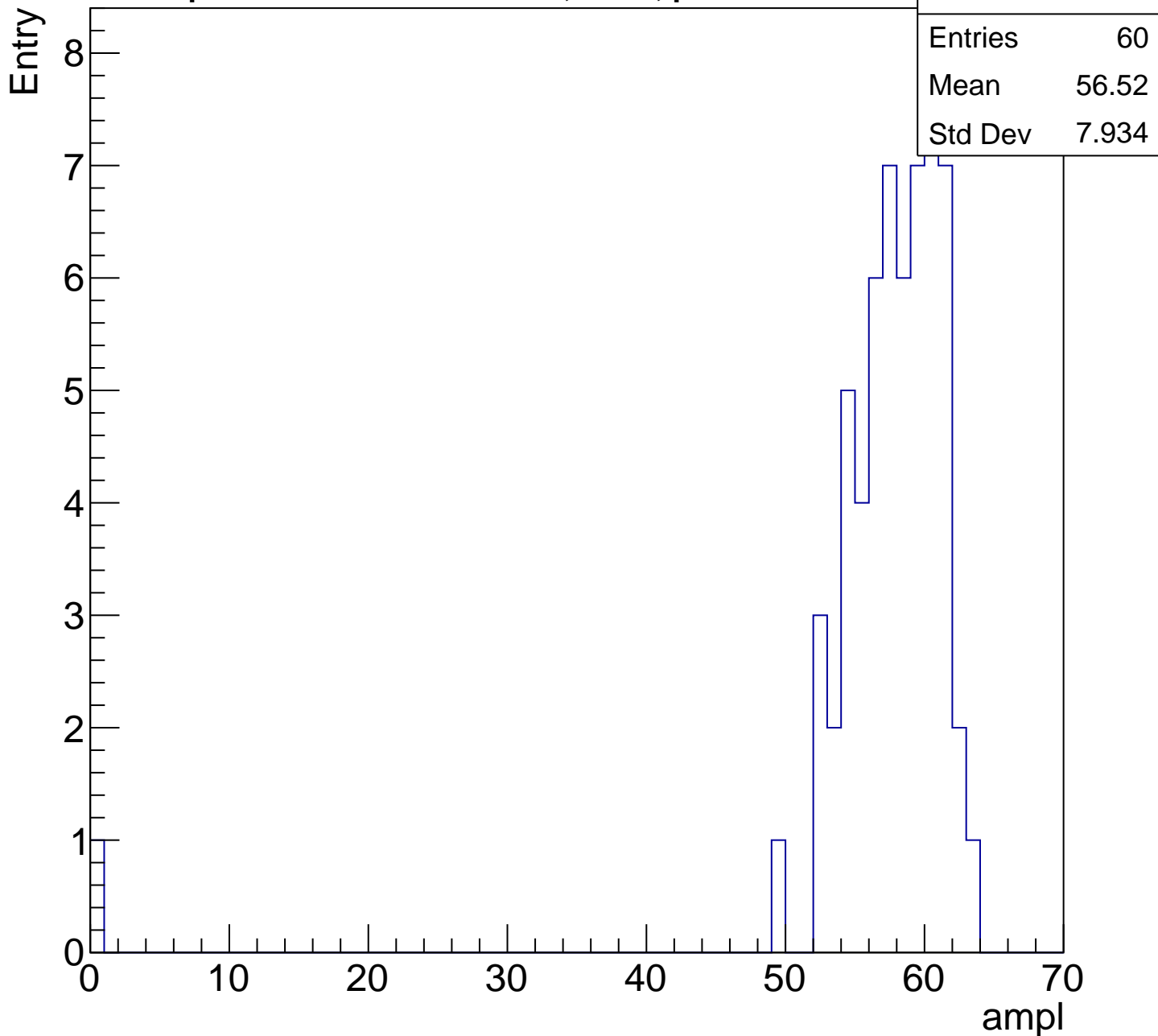
calib_packv5_042523_0143.root, FC#7, port C2

Entries	94
Mean	49.94
Std Dev	4.156



B1L103S, U12-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

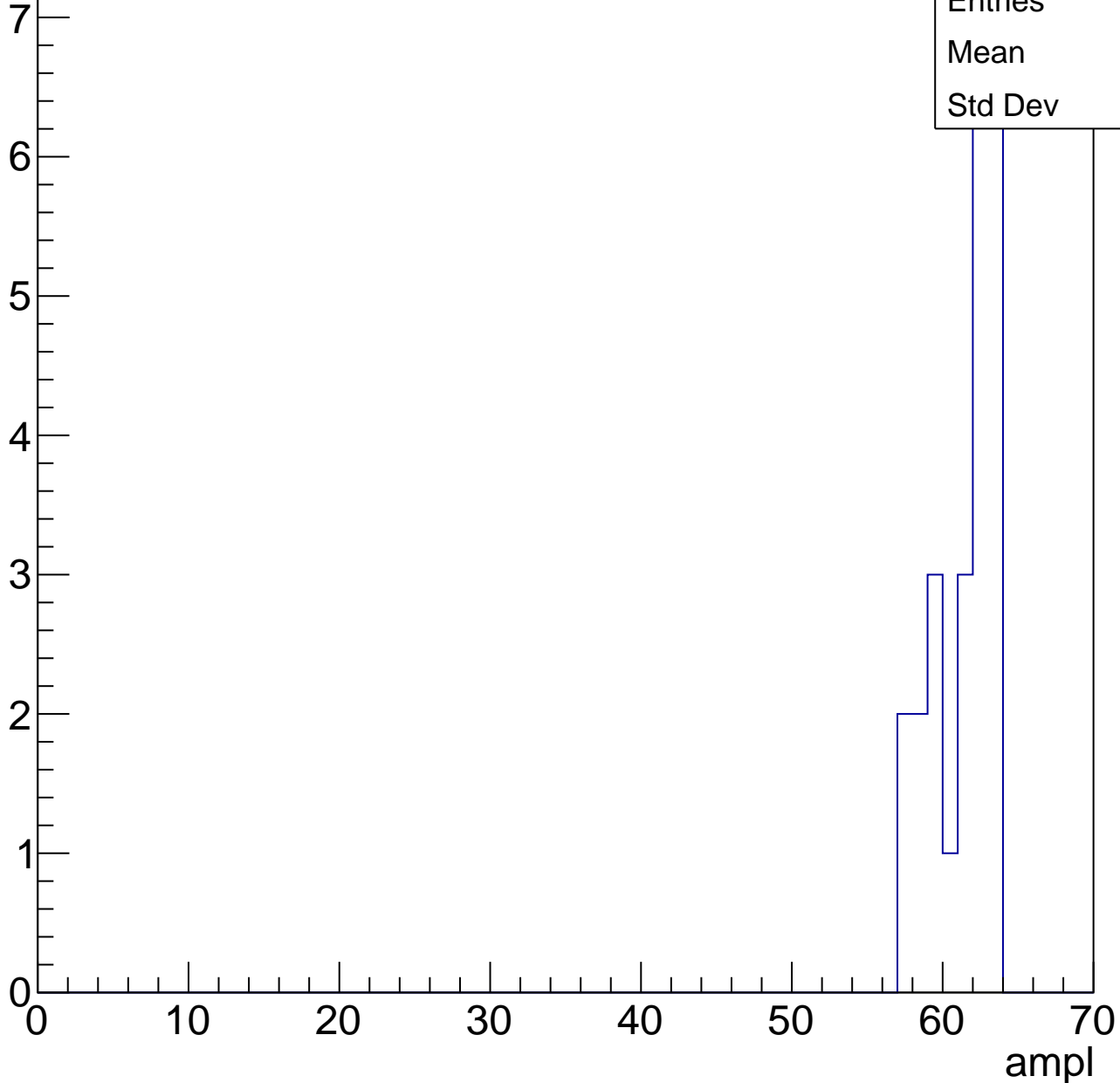


B1L103S, U12-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	61
Std Dev	1.98



B1L103S, U12-ch13, adc6

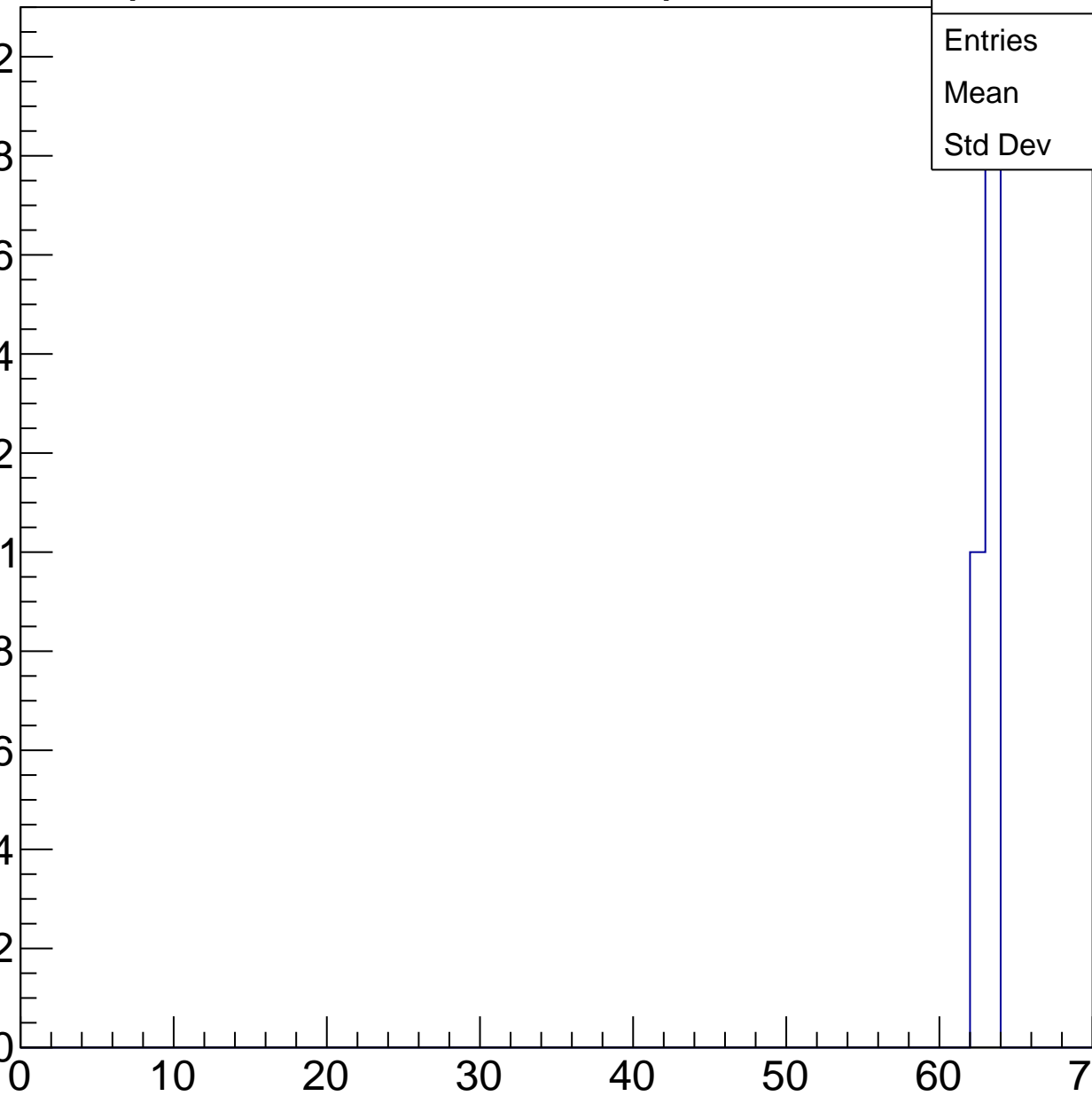
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L103S, U12-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch14, adc0

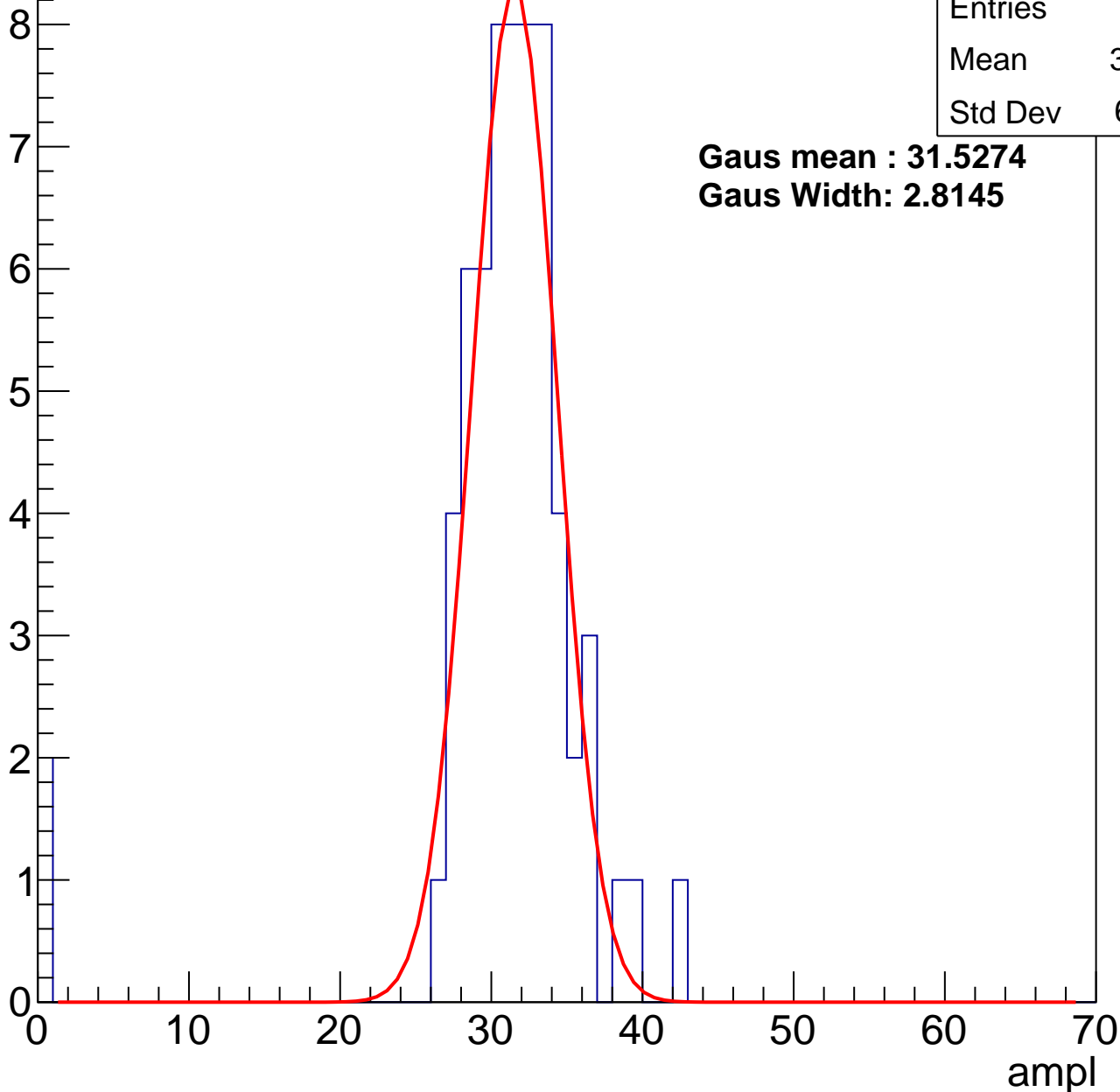
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	30.43
Std Dev	6.291

Gaus mean : 31.5274

Gaus Width: 2.8145



B1L103S, U12-ch14, adc1

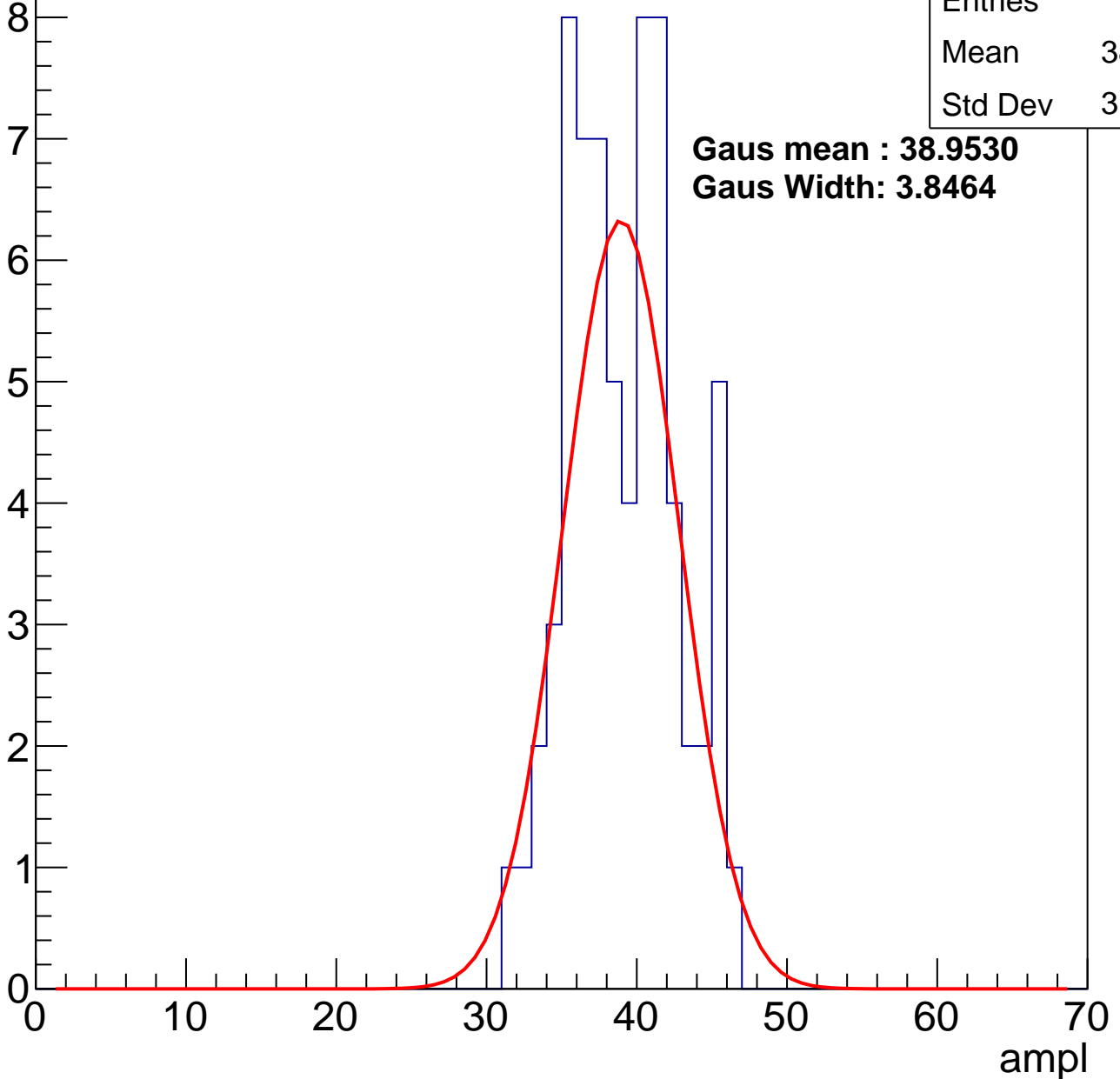
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	38.66
Std Dev	3.563

Gaus mean : 38.9530

Gaus Width: 3.8464



B1L103S, U12-ch14, adc2

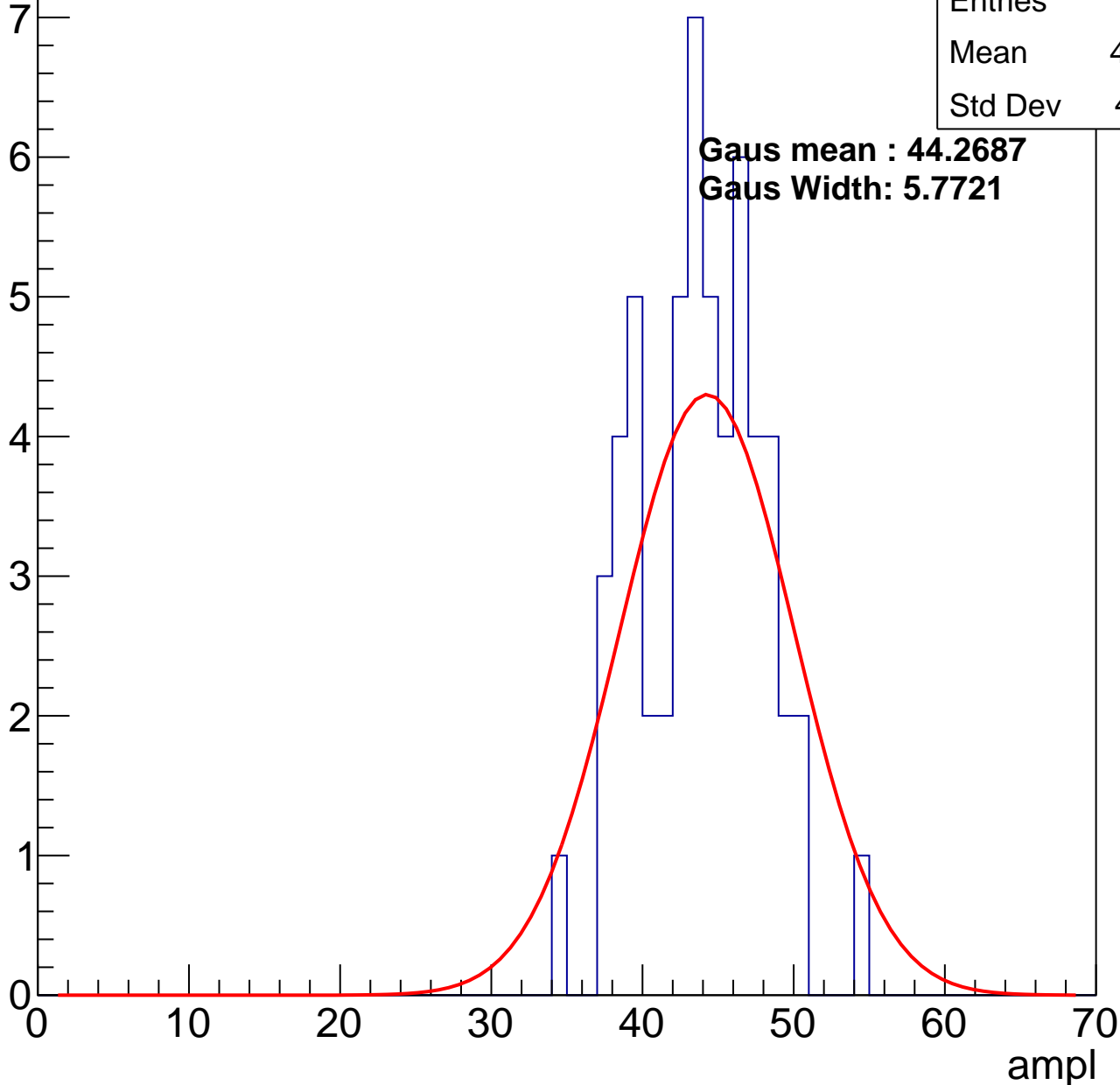
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.39
Std Dev	4.021

Gaus mean : 44.2687

Gaus Width: 5.7721

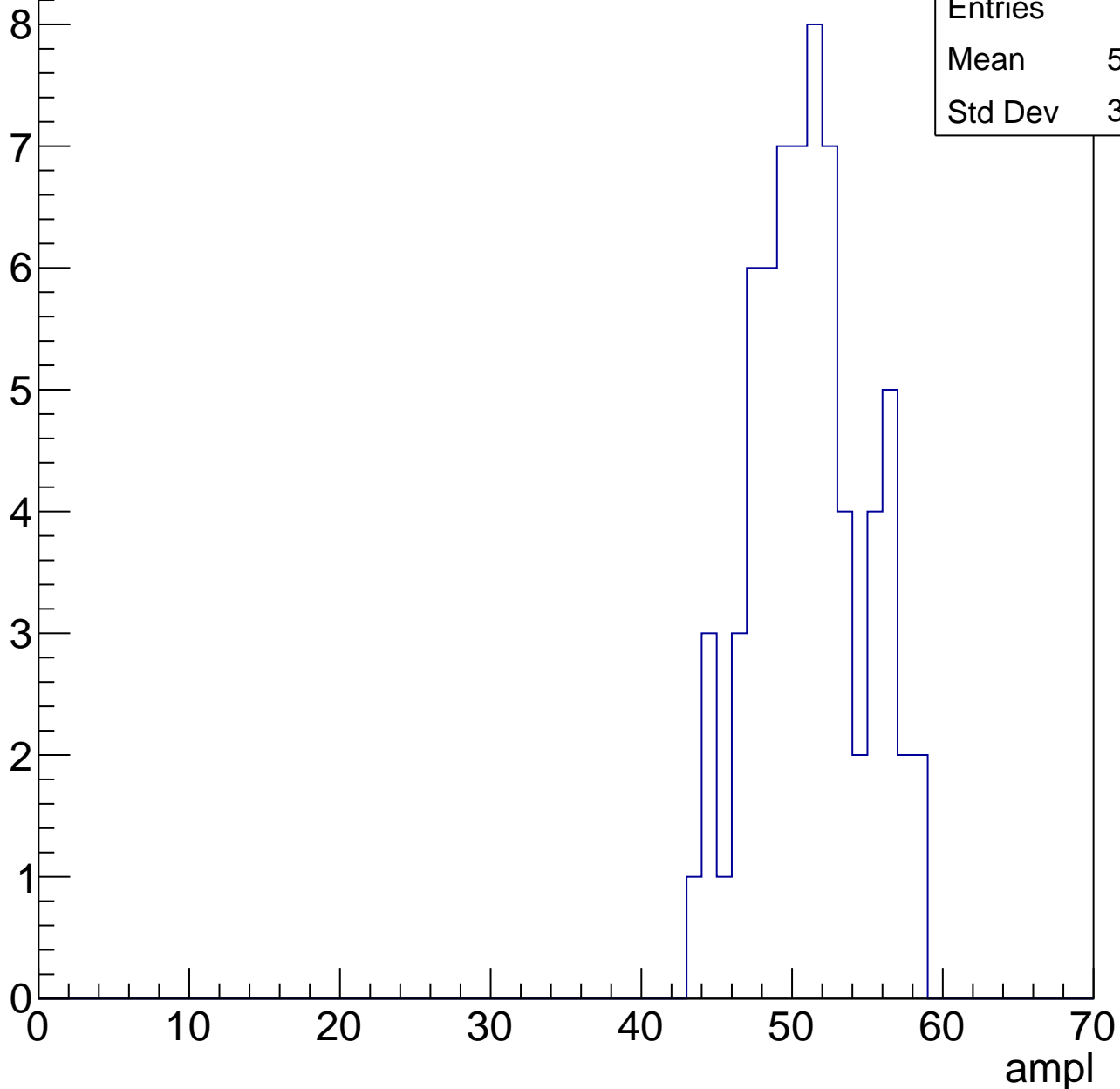


B1L103S, U12-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	50.63
Std Dev	3.634

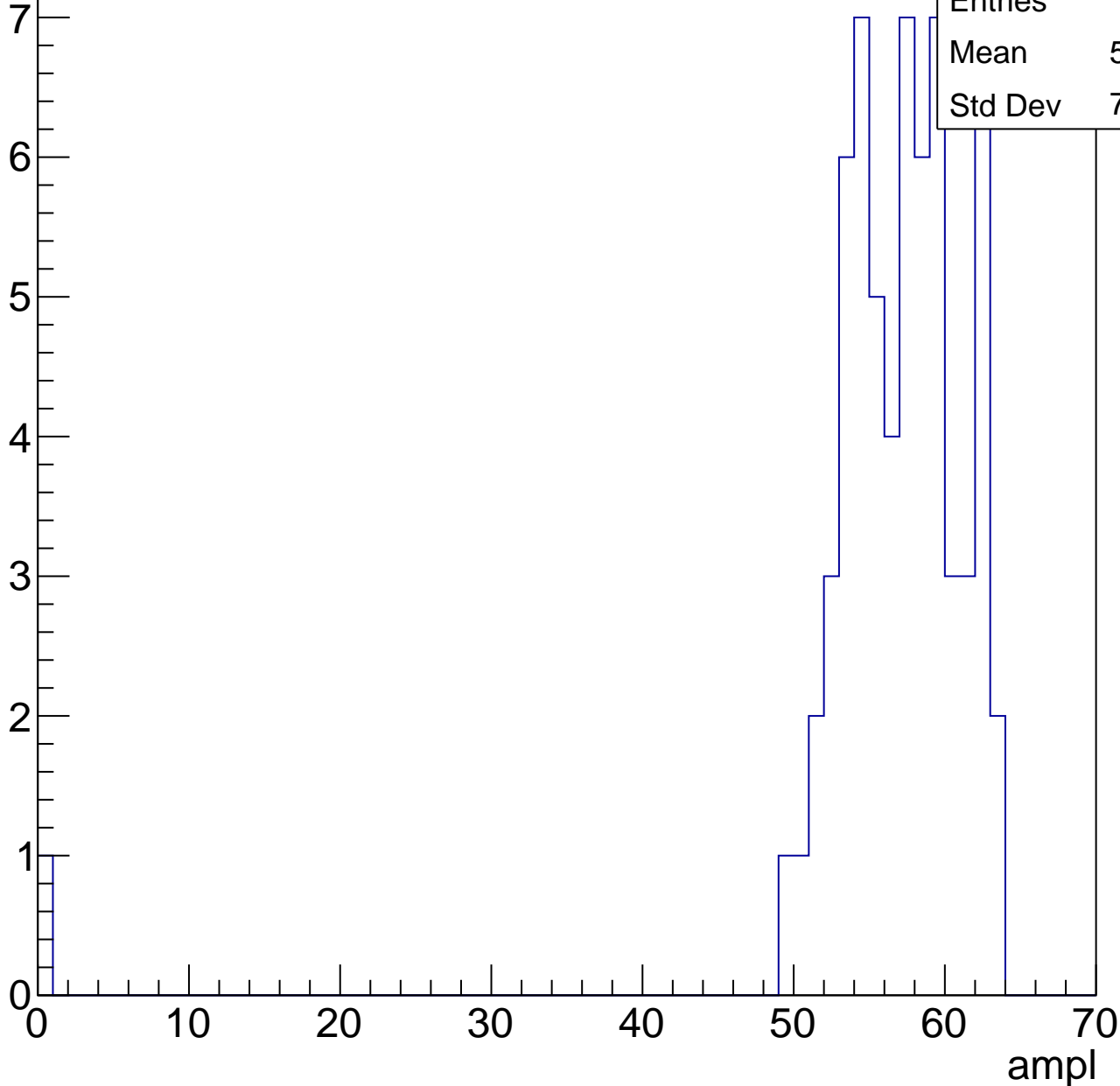


B1L103S, U12-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

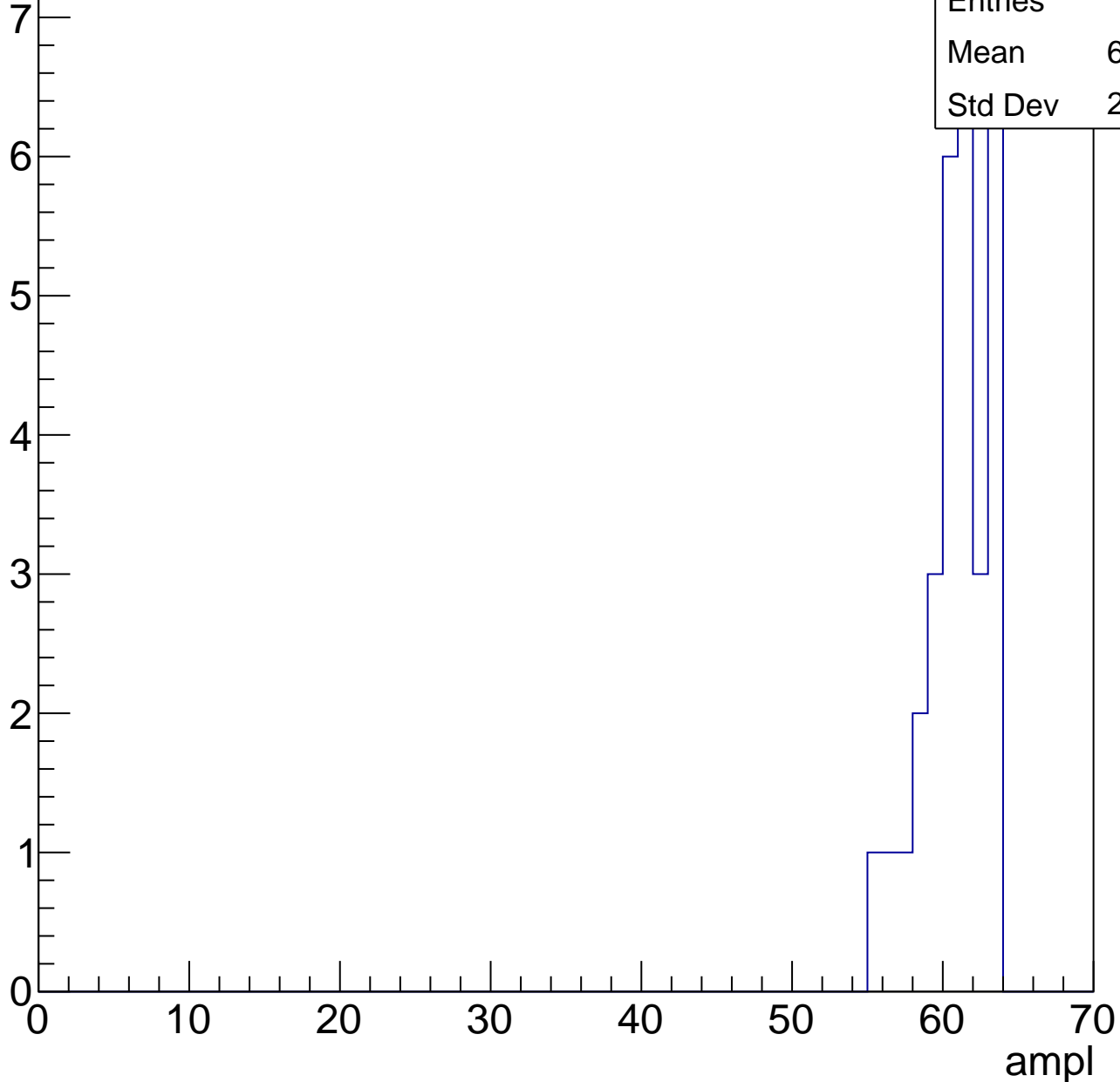
Entries	65
Mean	55.92
Std Dev	7.818



B1L103S, U12-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	31
Mean	60.48
Std Dev	2.093

B1L103S, U12-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch15, adc0

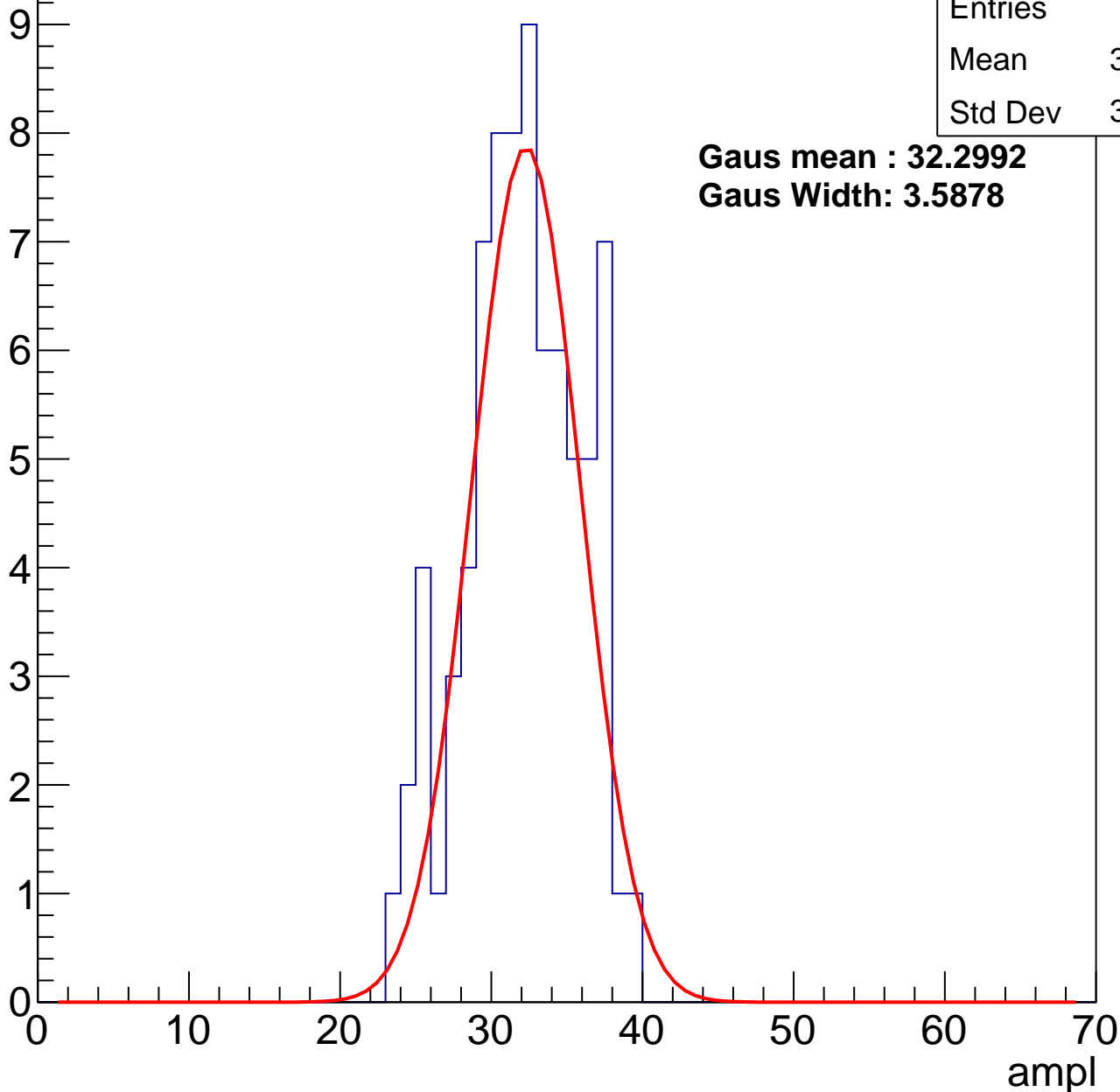
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	31.56
Std Dev	3.727

Gaus mean : 32.2992

Gaus Width: 3.5878



B1L103S, U12-ch15, adc1

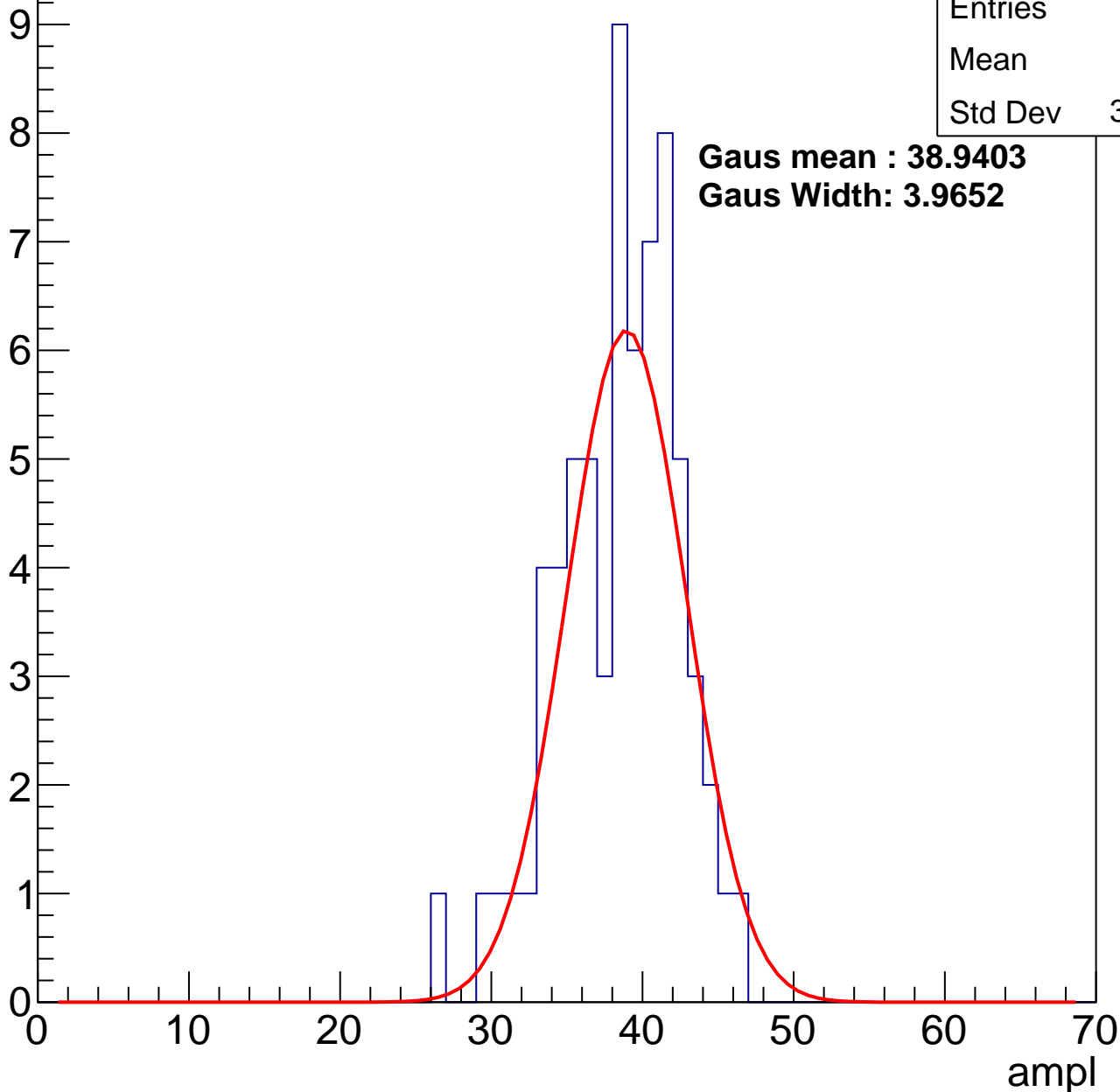
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	38
Std Dev	3.933

Gaus mean : 38.9403

Gaus Width: 3.9652



B1L103S, U12-ch15, adc2

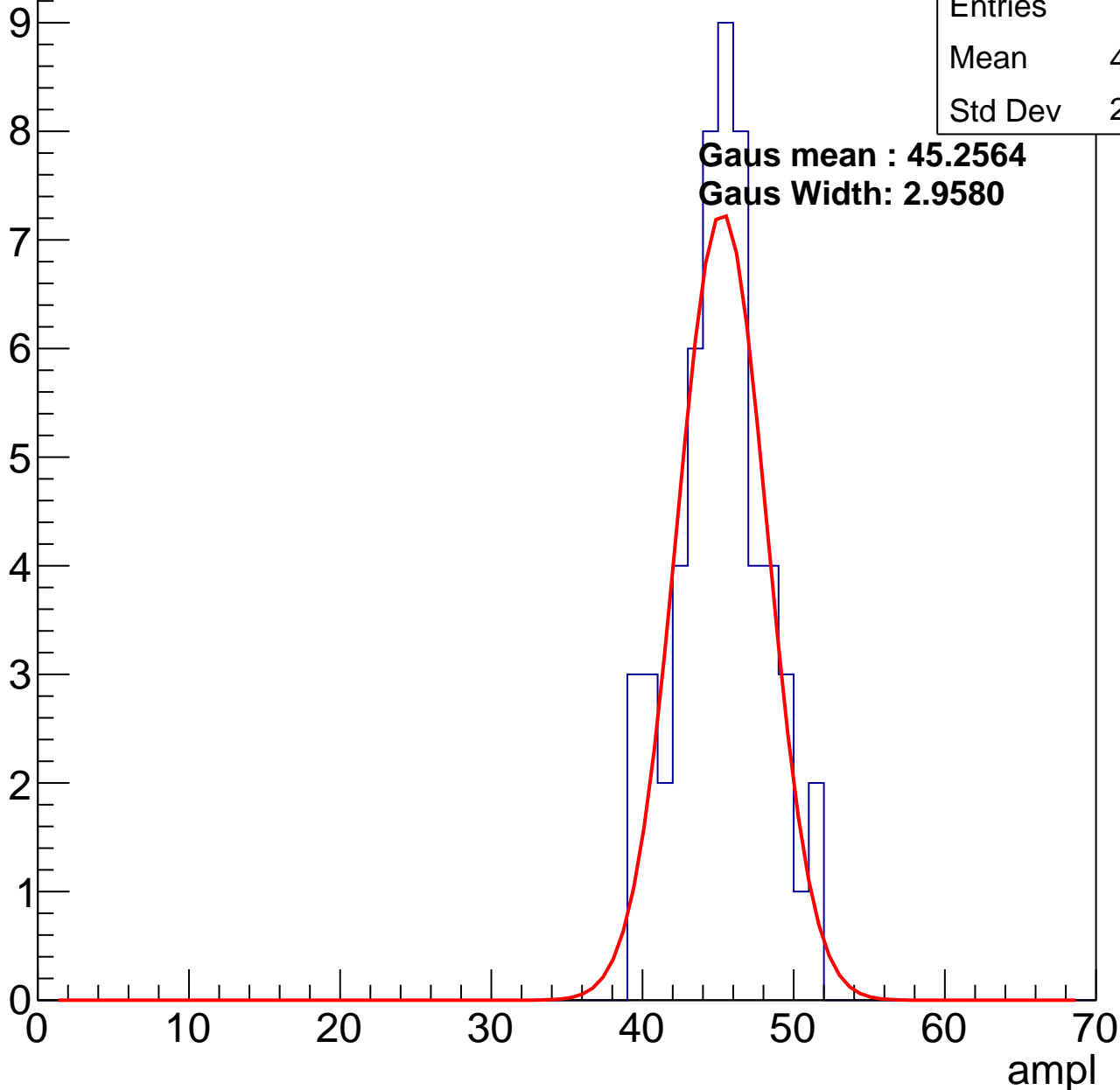
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	44.72
Std Dev	2.912

Gaus mean : 45.2564

Gaus Width: 2.9580

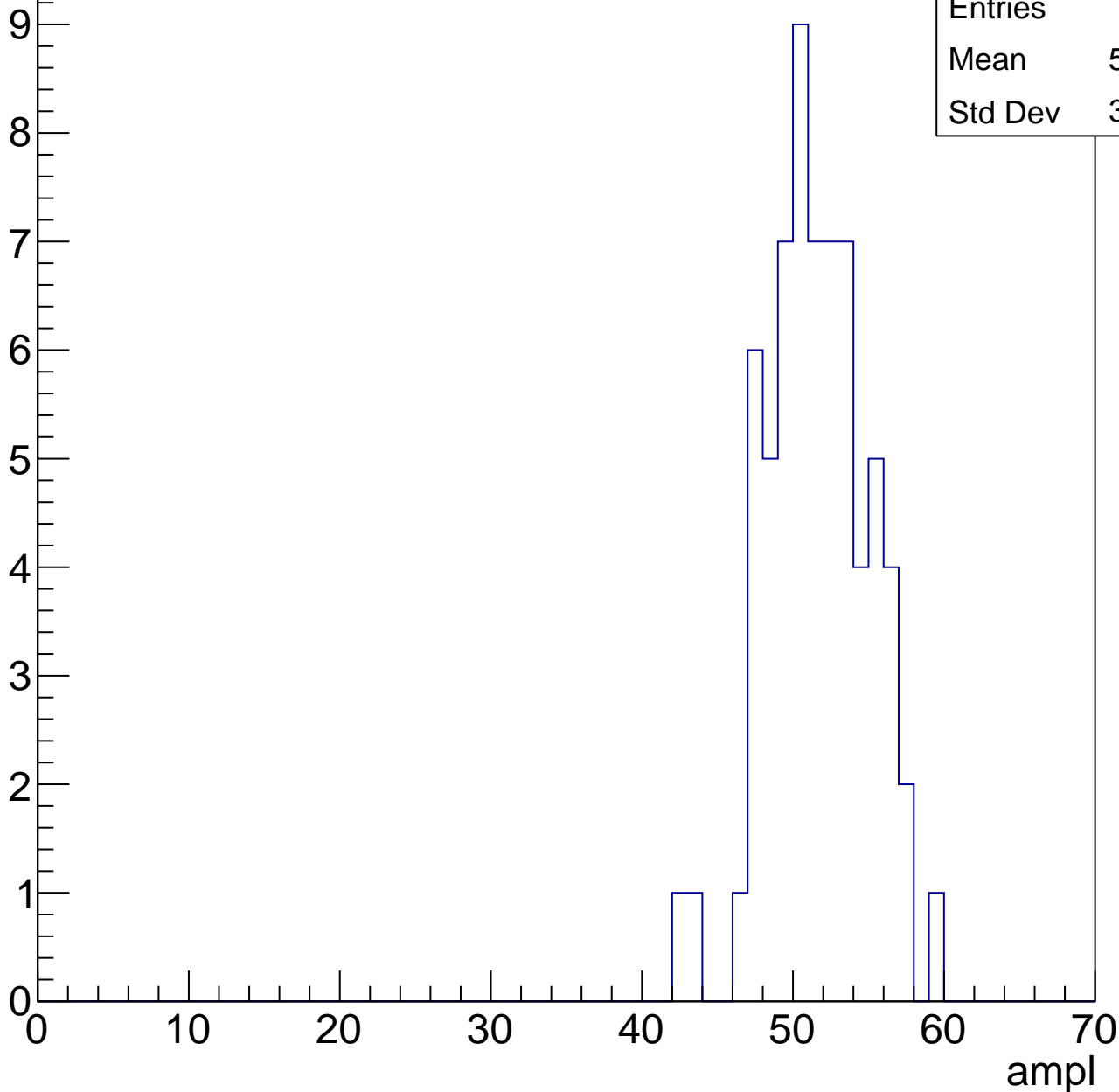


B1L103S, U12-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	51.13
Std Dev	3.309

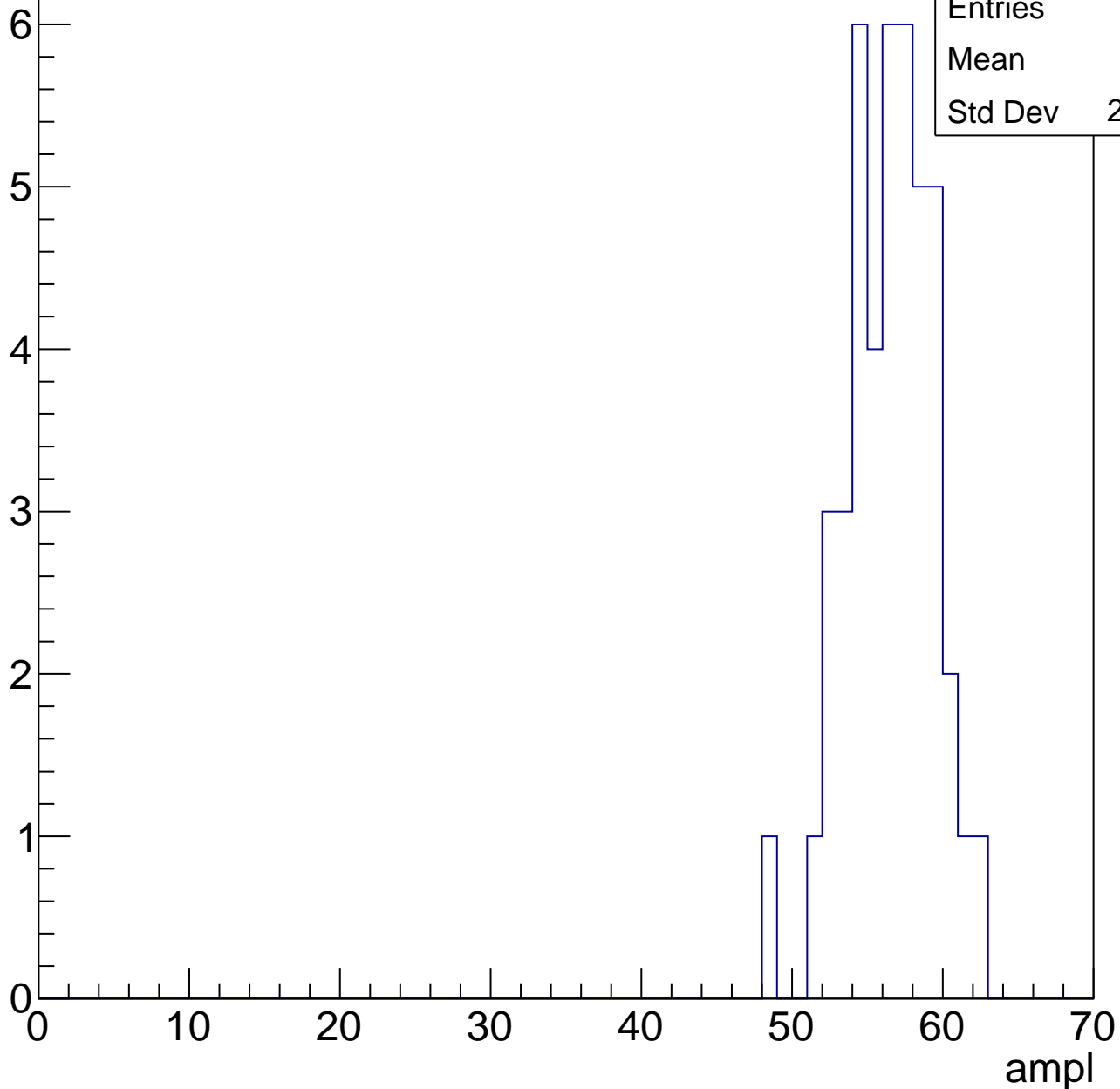


B1L103S, U12-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

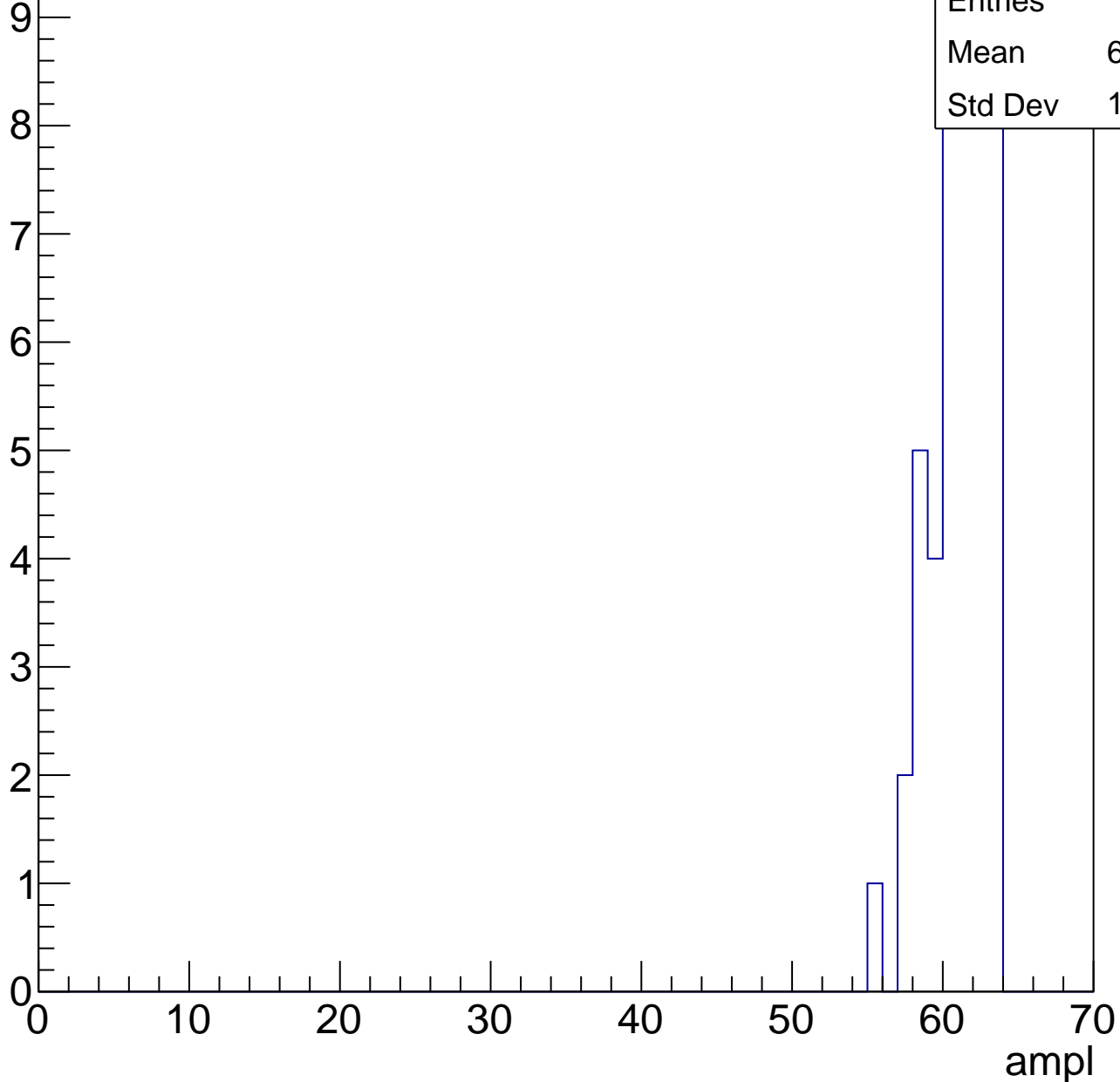
Entries	44
Mean	56
Std Dev	2.844



B1L103S, U12-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L103S, U12-ch16, adc0

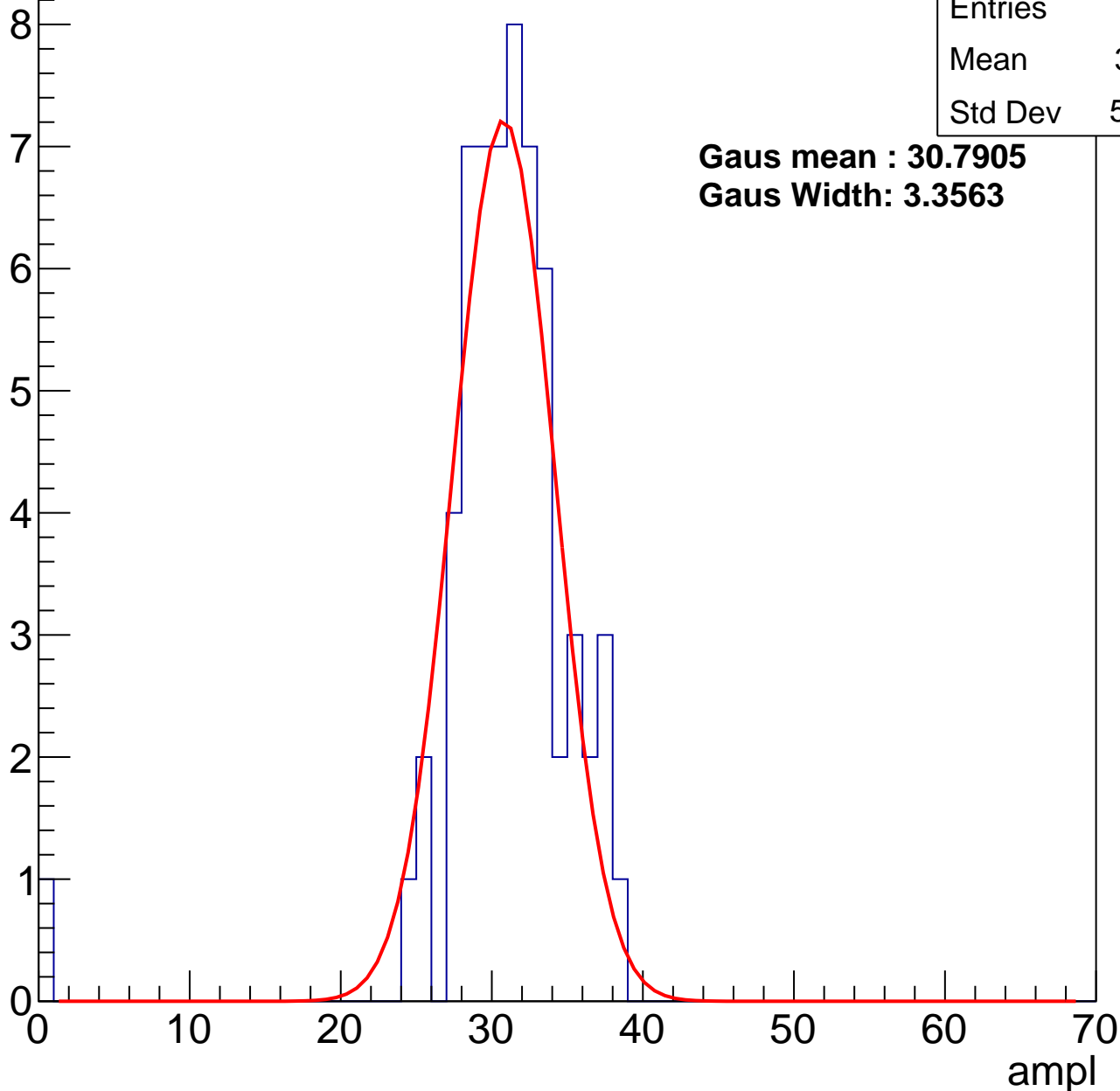
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	30.41
Std Dev	5.003

Gaus mean : 30.7905

Gaus Width: 3.3563



B1L103S, U12-ch16, adc1

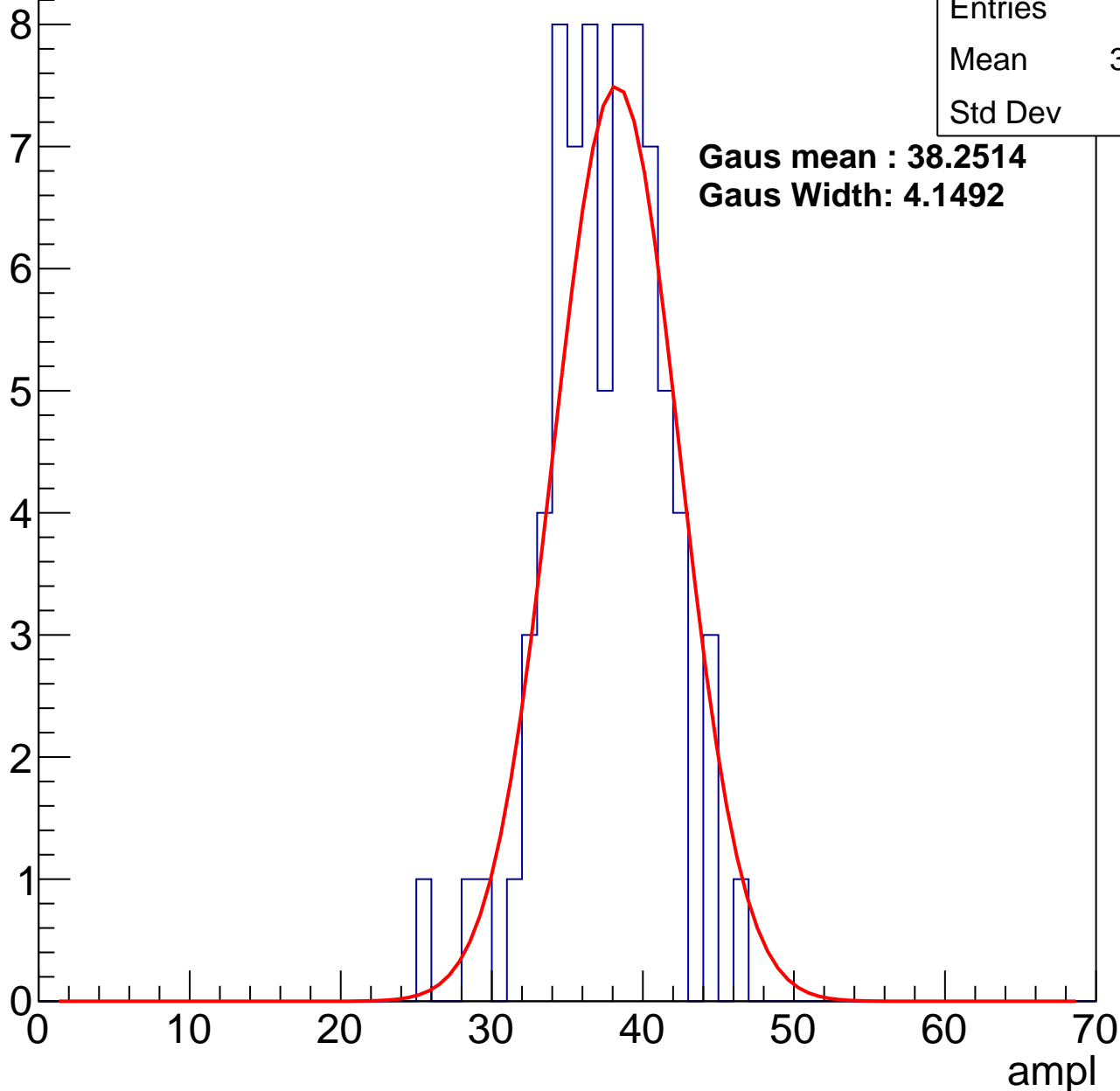
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.04
Std Dev	3.8

Gaus mean : 38.2514

Gaus Width: 4.1492



B1L103S, U12-ch16, adc2

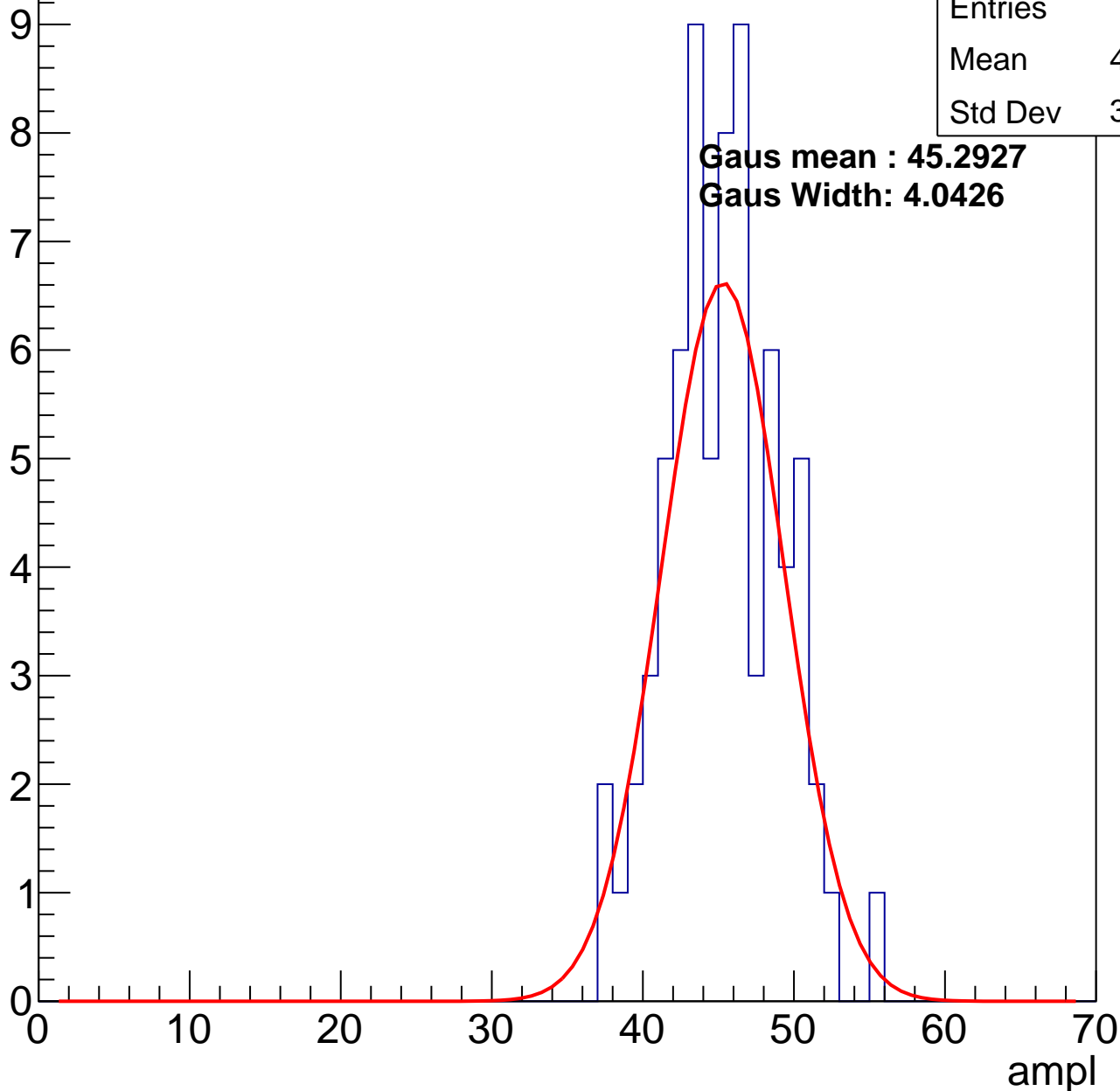
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	44.89
Std Dev	3.695

Gaus mean : 45.2927

Gaus Width: 4.0426

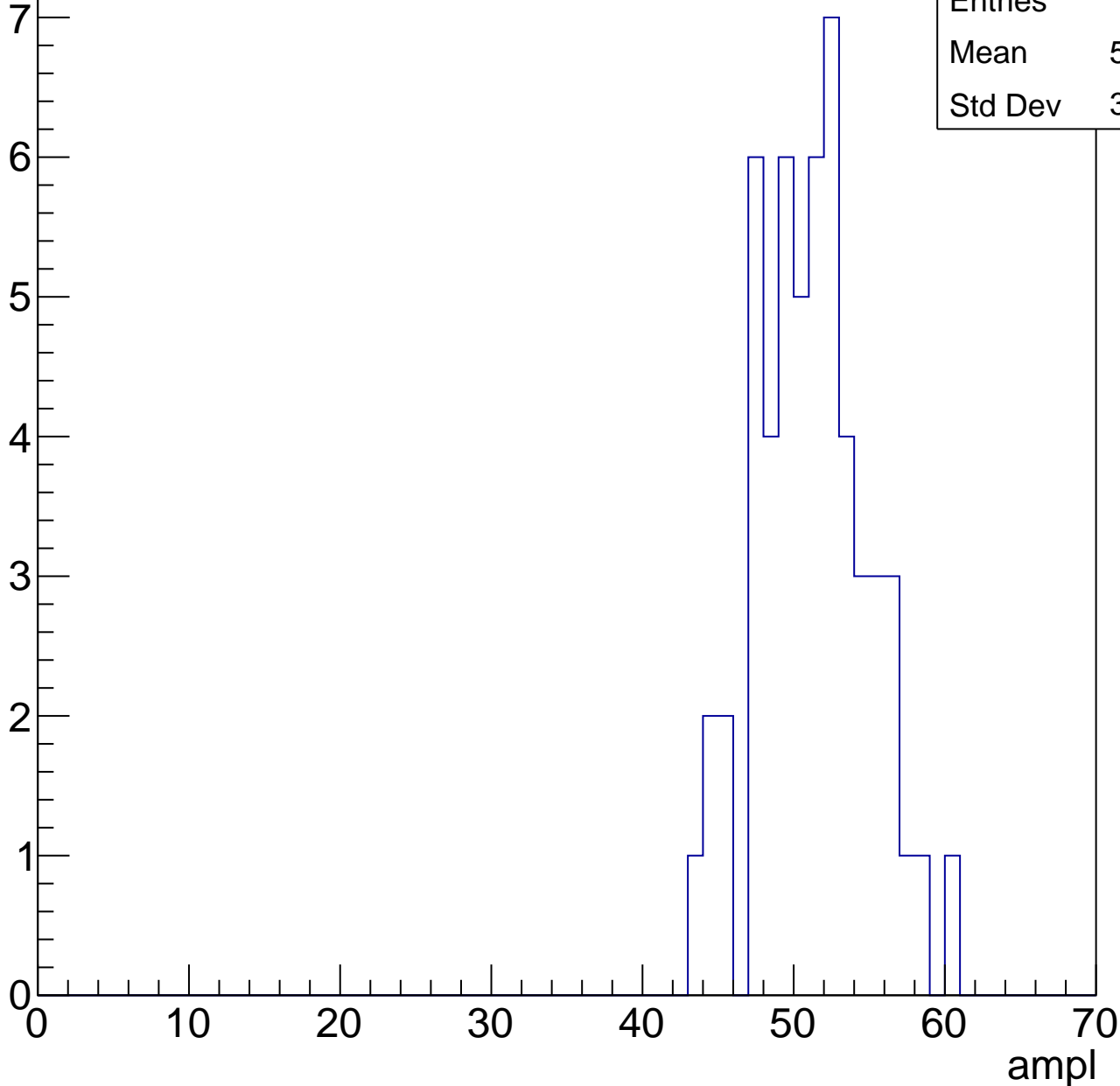


B1L103S, U12-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	50.75
Std Dev	3.644

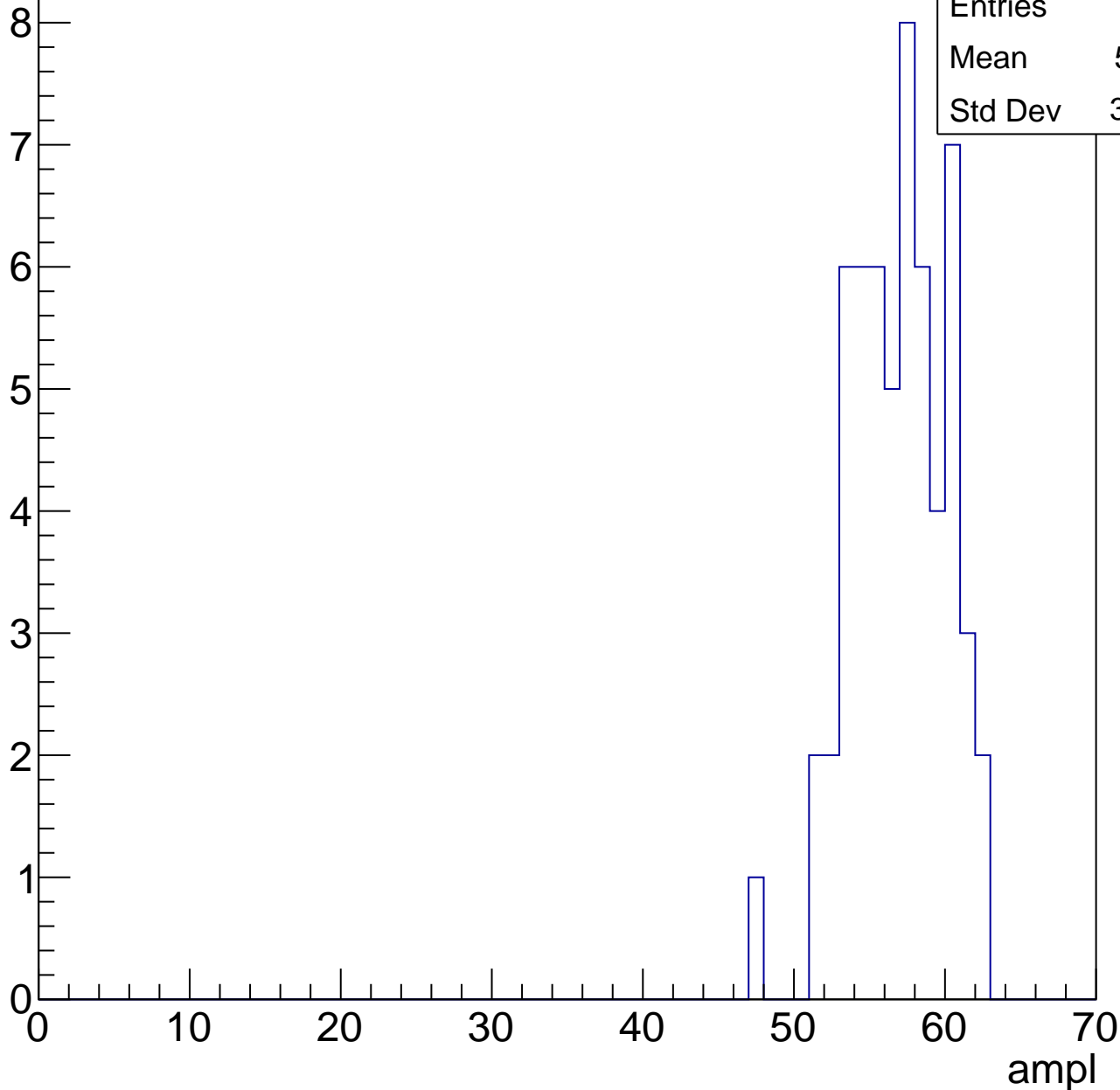


B1L103S, U12-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.41
Std Dev	3.119

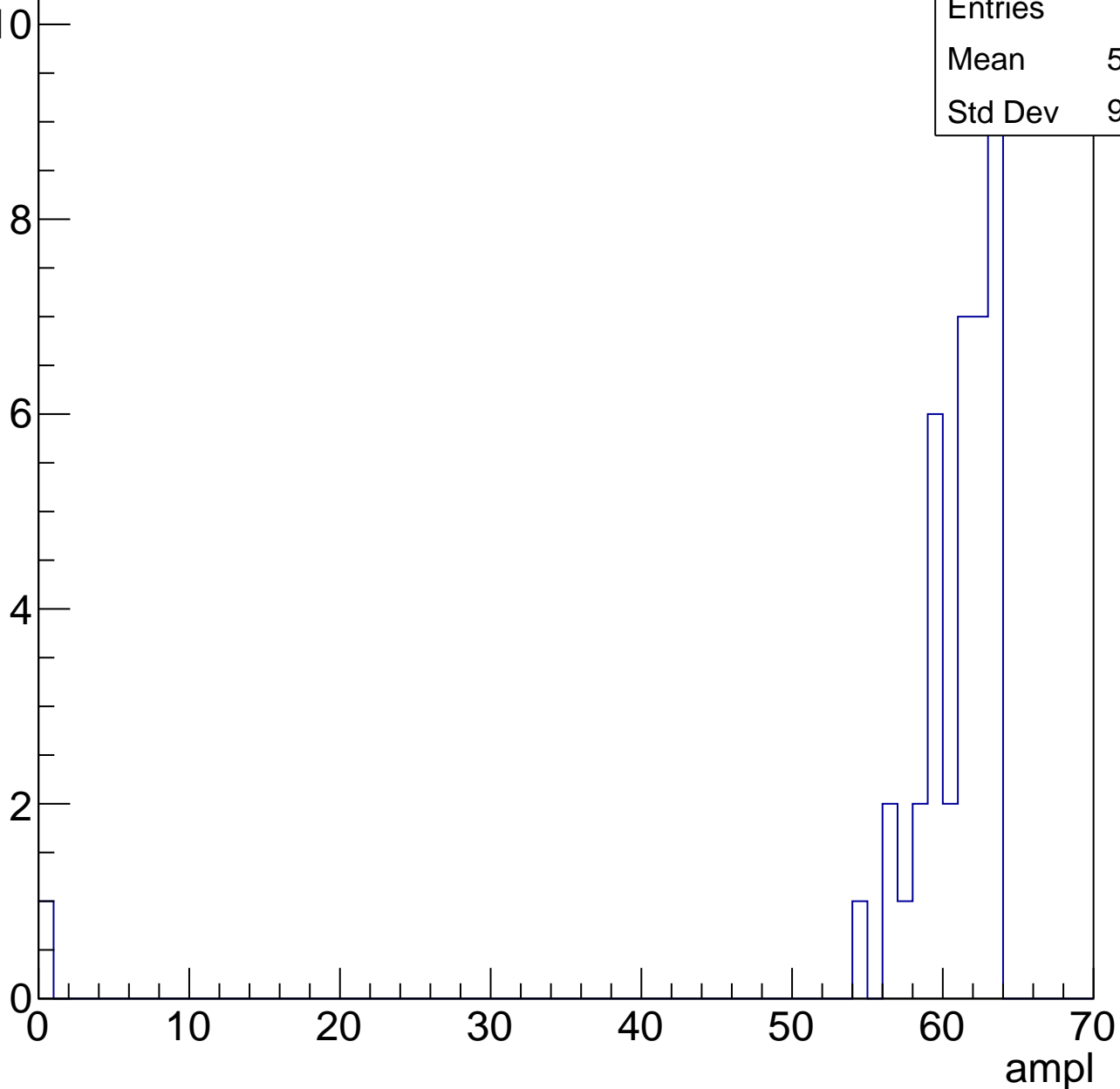


B1L103S, U12-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

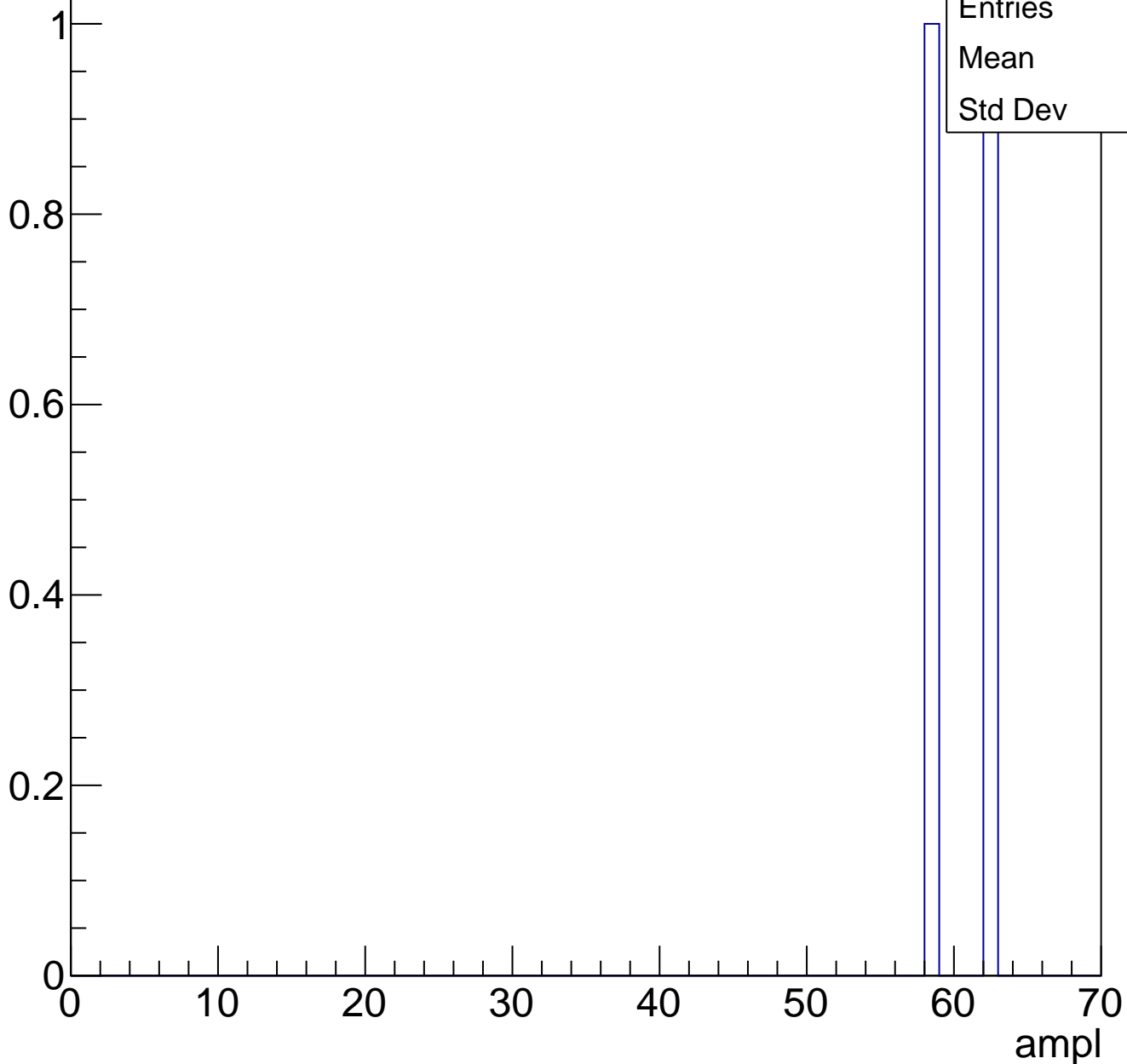
Entries	39
Mean	59.08
Std Dev	9.849



B1L103S, U12-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch17, adc0

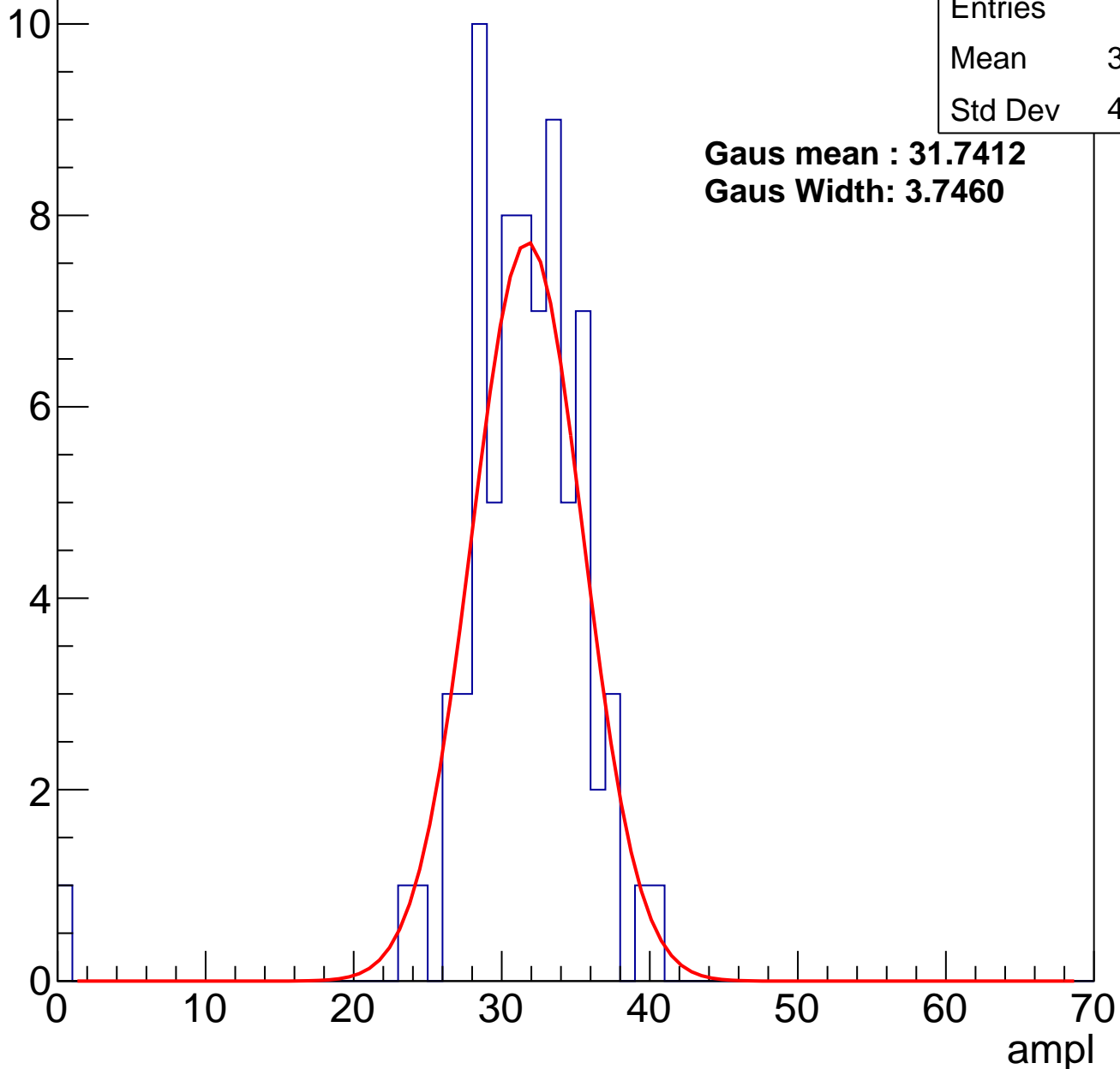
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	30.89
Std Dev	4.922

Gaus mean : 31.7412

Gaus Width: 3.7460

Entry



B1L103S, U12-ch17, adc1

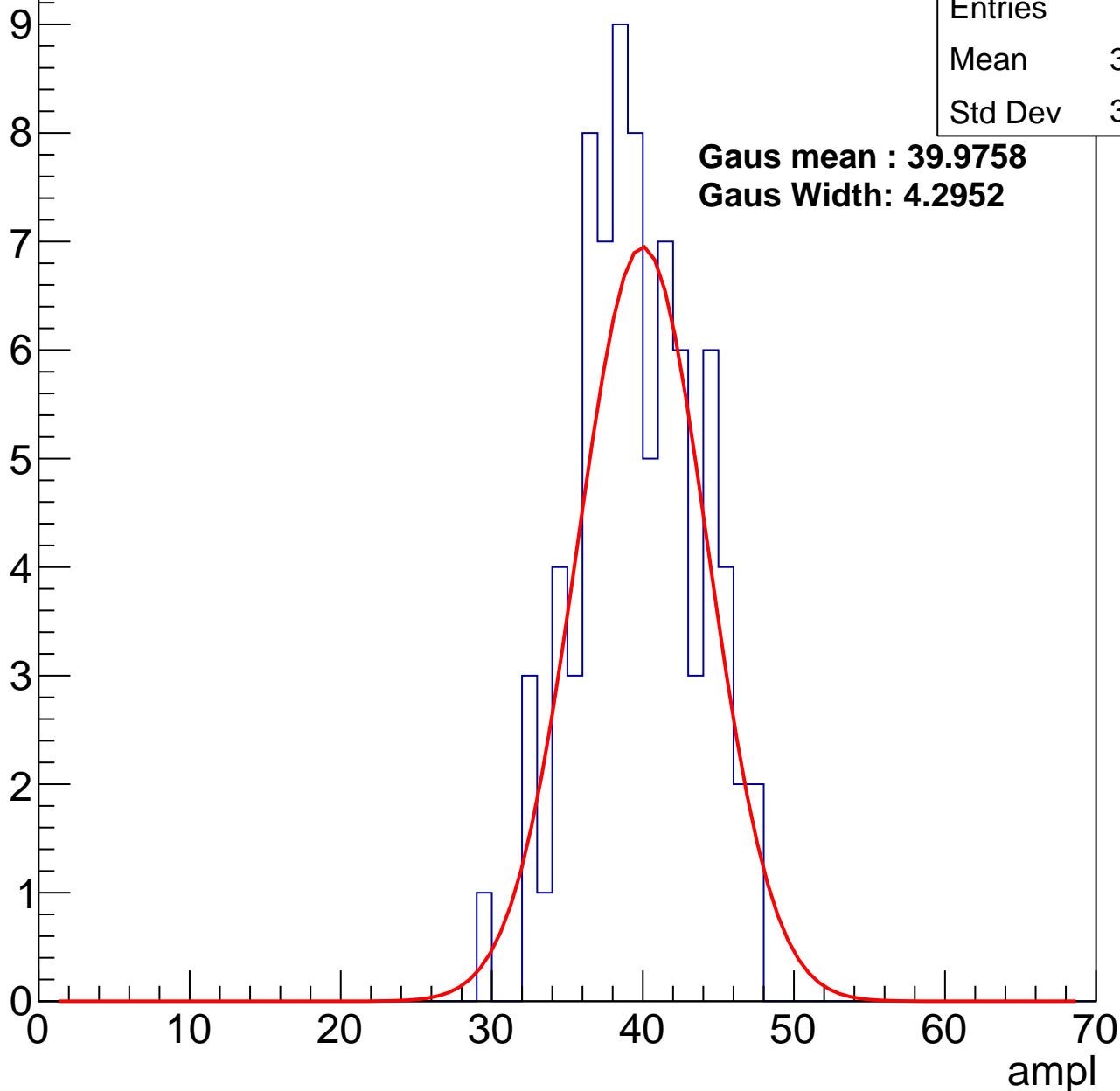
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	39.22
Std Dev	3.883

Gaus mean : 39.9758

Gaus Width: 4.2952



B1L103S, U12-ch17, adc2

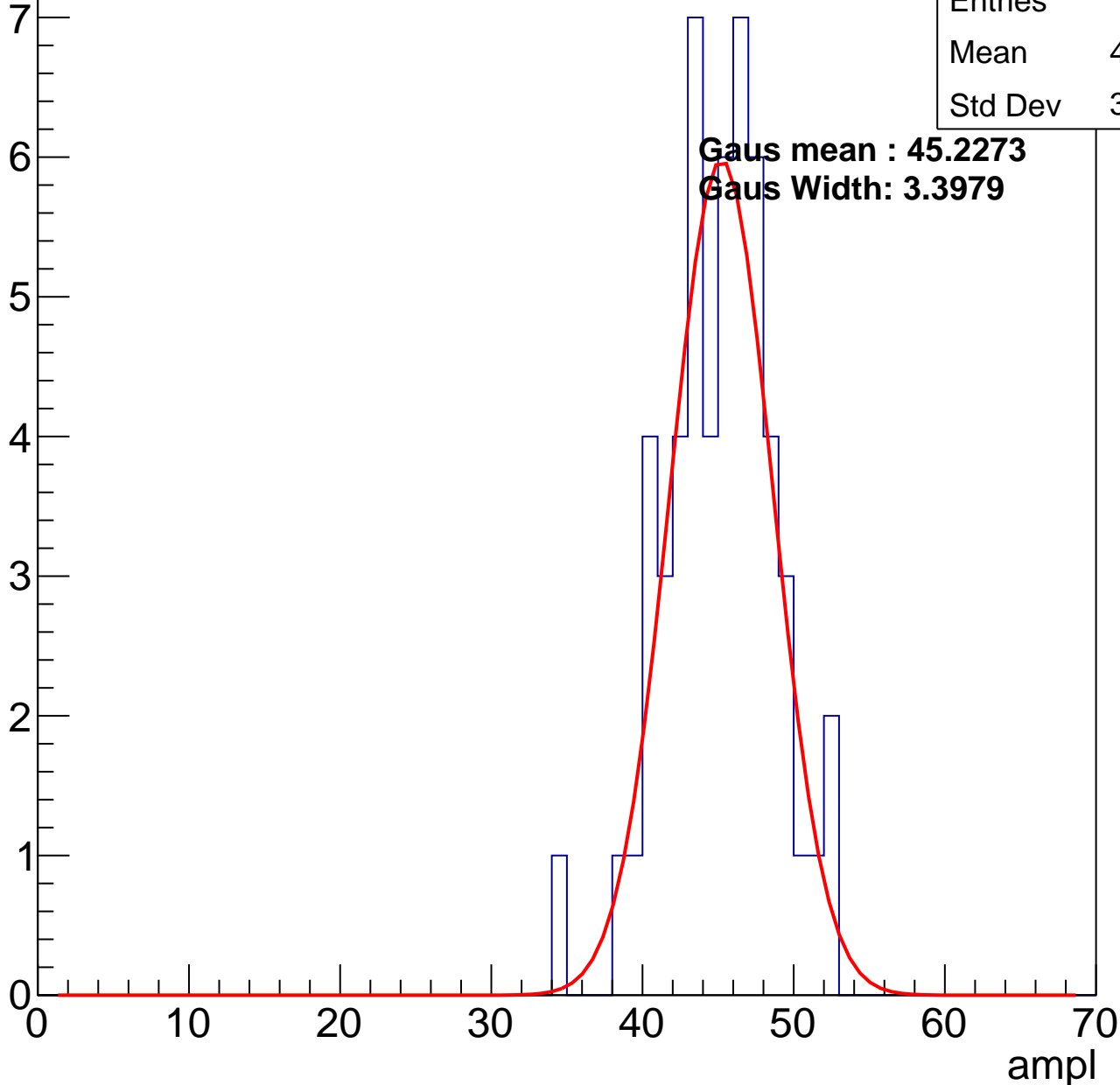
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	44.67
Std Dev	3.542

Gaus mean : 45.2273

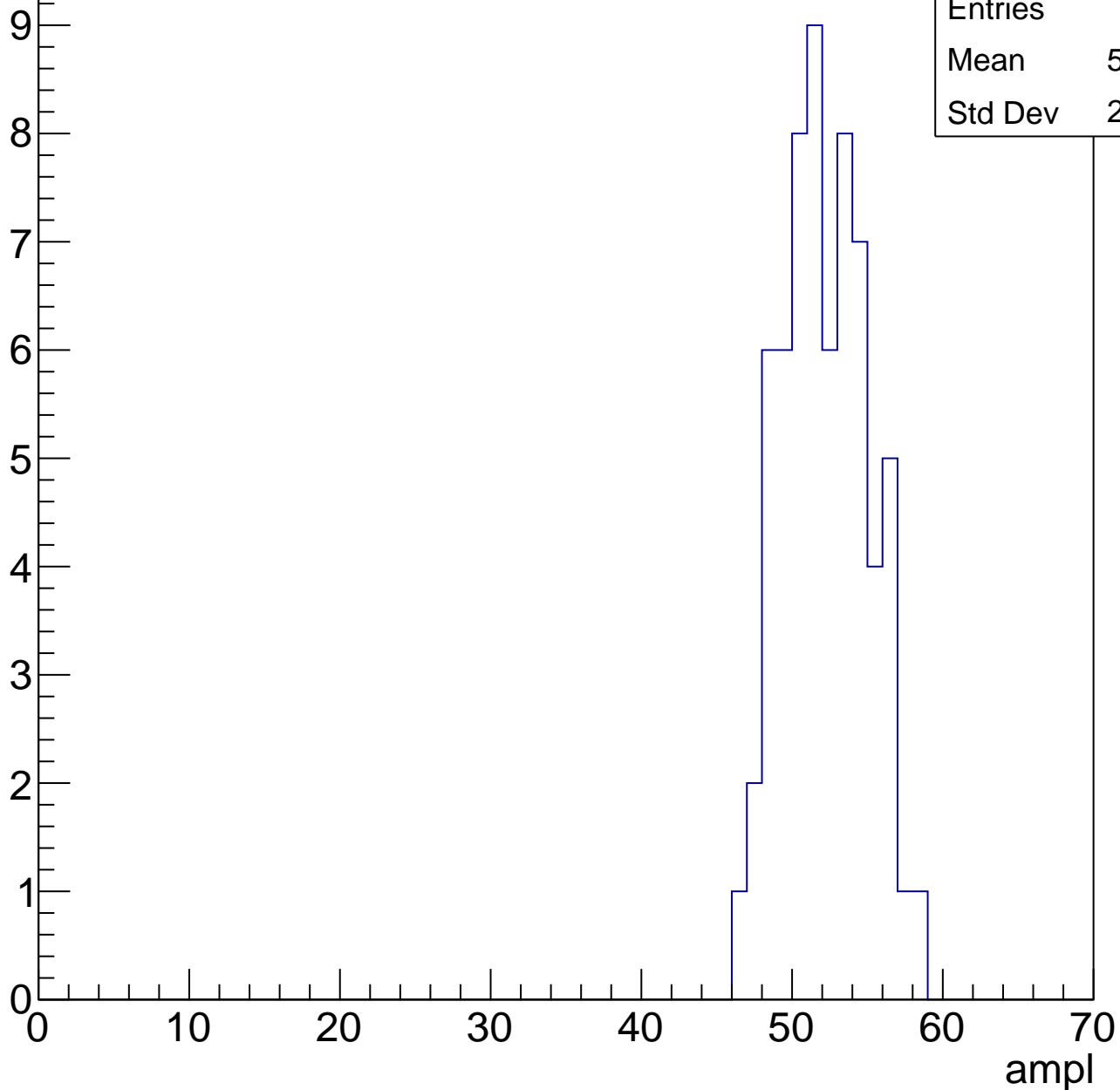
Gaus Width: 3.3979



B1L103S, U12-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

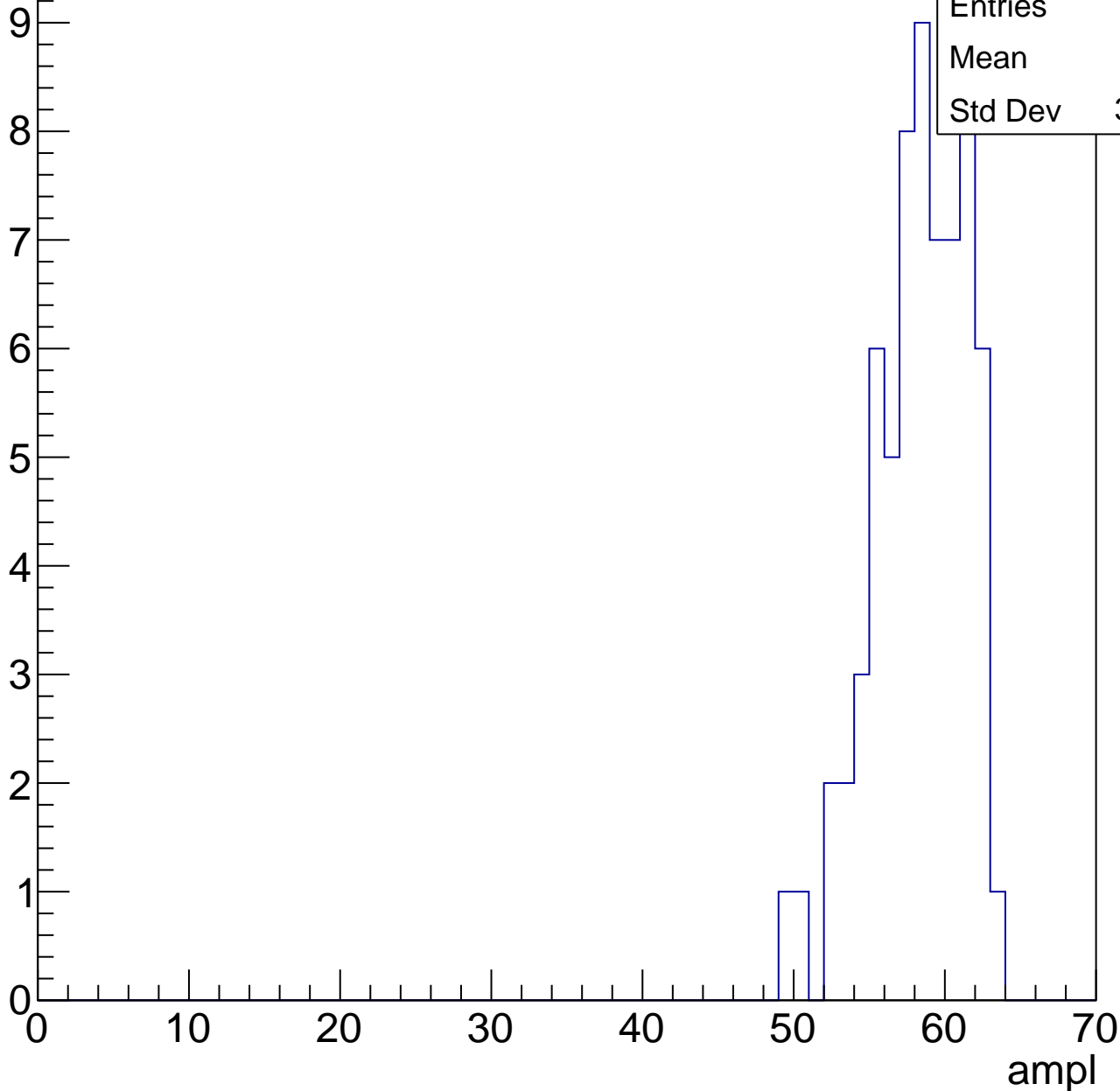


B1L103S, U12-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	57.8
Std Dev	3.071

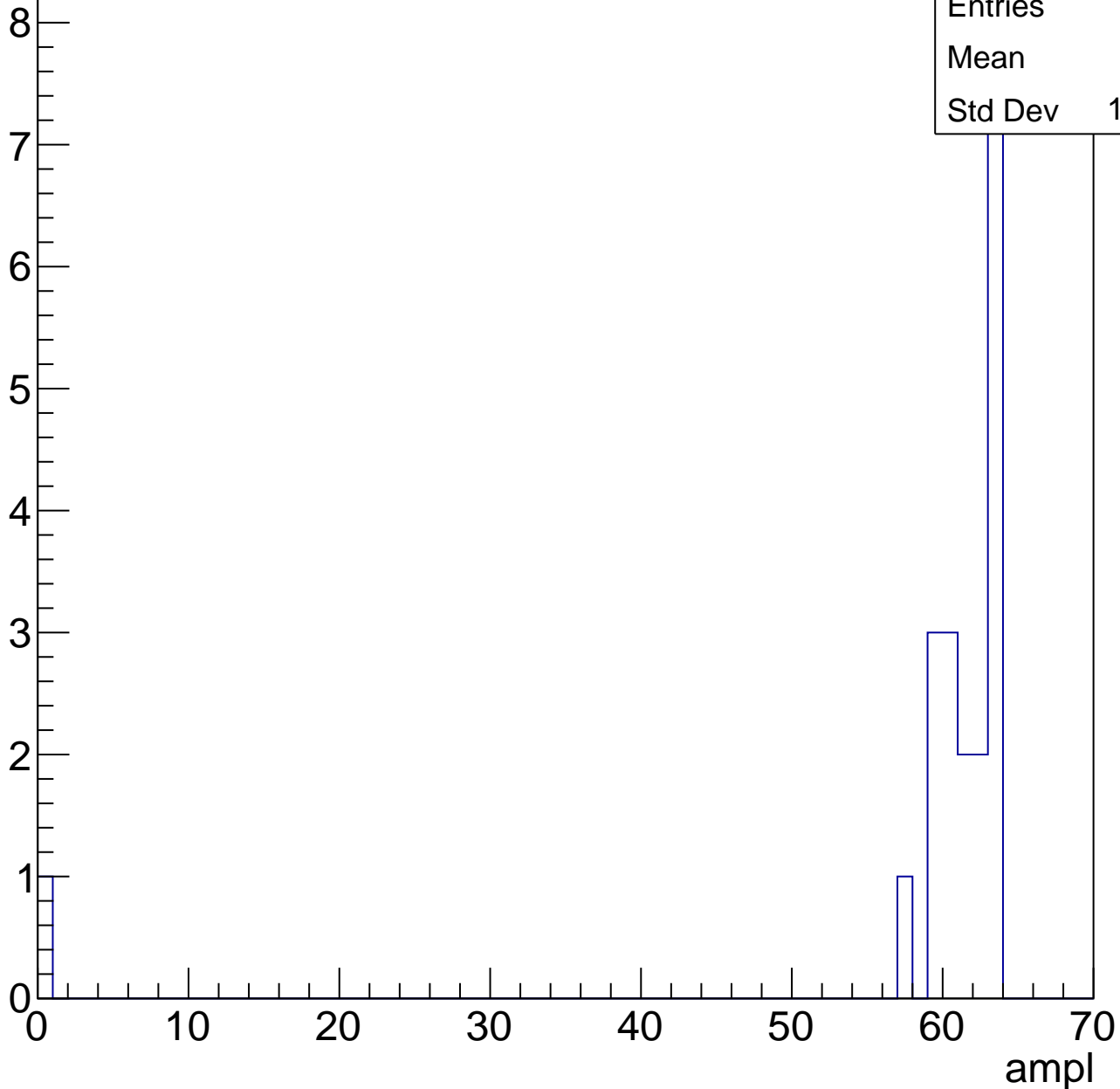


B1L103S, U12-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	58.2
Std Dev	13.47



B1L103S, U12-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L103S, U12-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch18, adc0

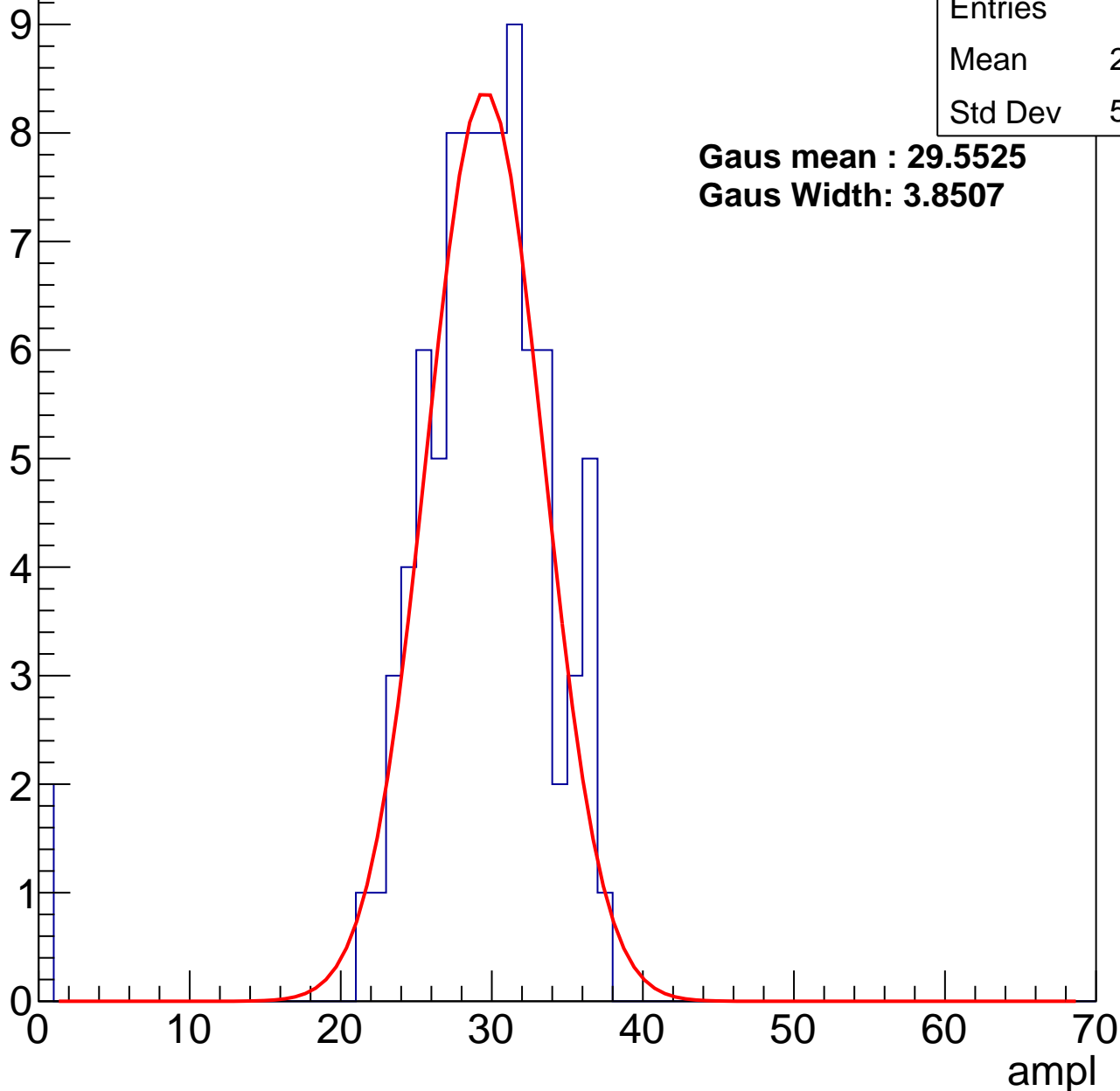
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	28.59
Std Dev	5.737

Gaus mean : 29.5525

Gaus Width: 3.8507



B1L103S, U12-ch18, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	61
Mean	36.67
Std Dev	3.263

Gaus mean : 37.5469

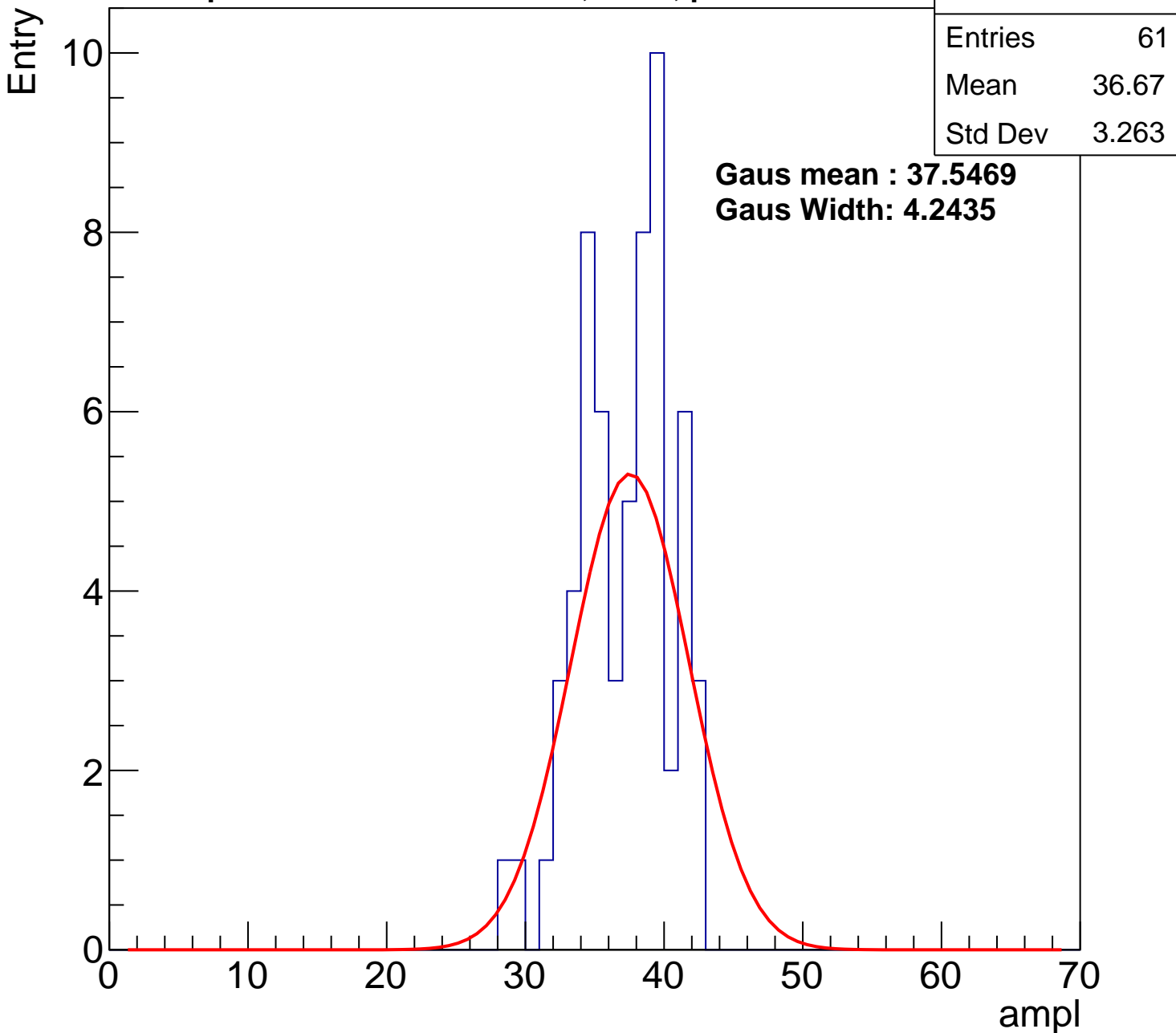
Gaus Width: 4.2435

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch18, adc2

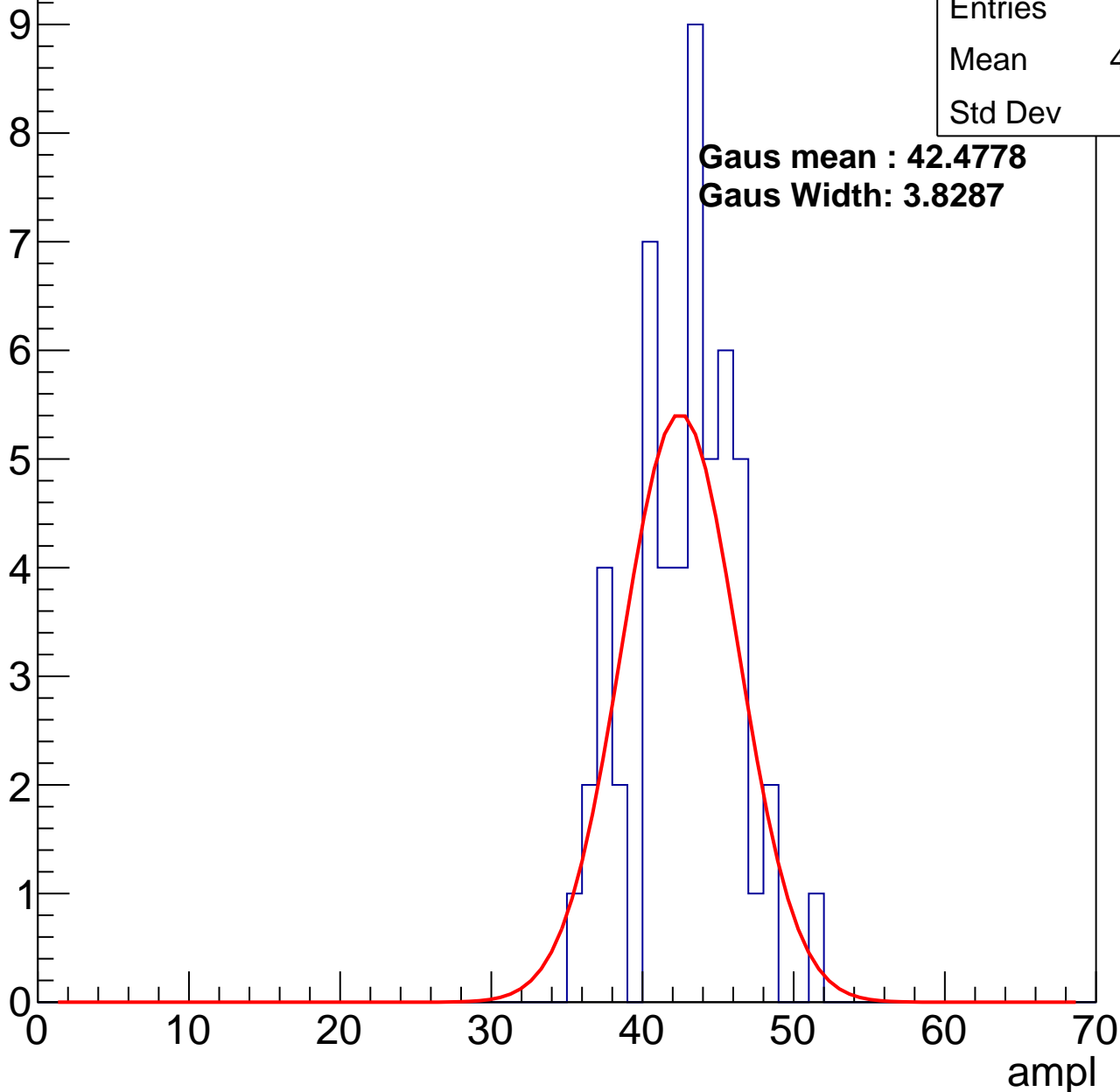
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.34
Std Dev	3.42

Gaus mean : 42.4778

Gaus Width: 3.8287

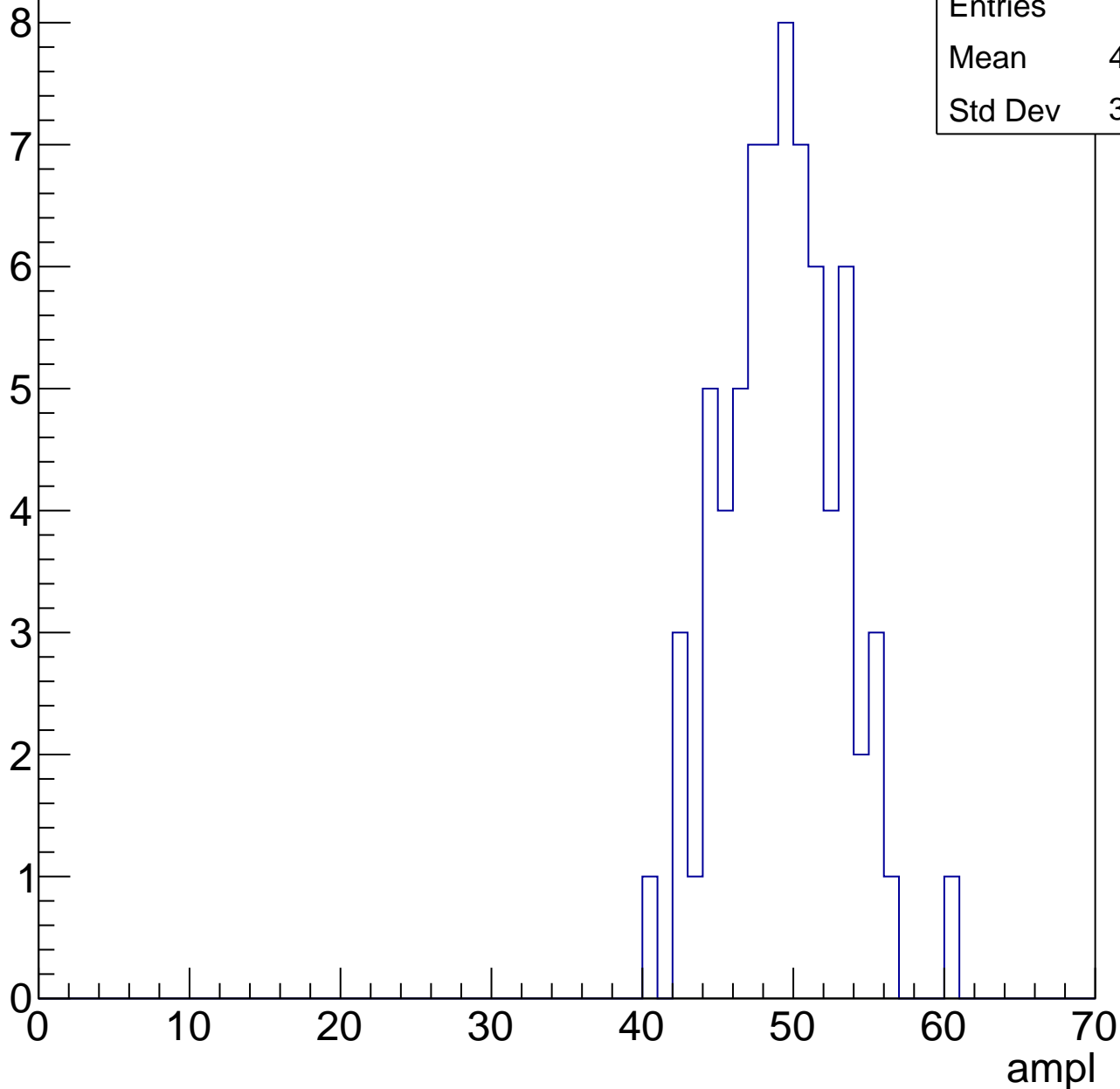


B1L103S, U12-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	48.83
Std Dev	3.794

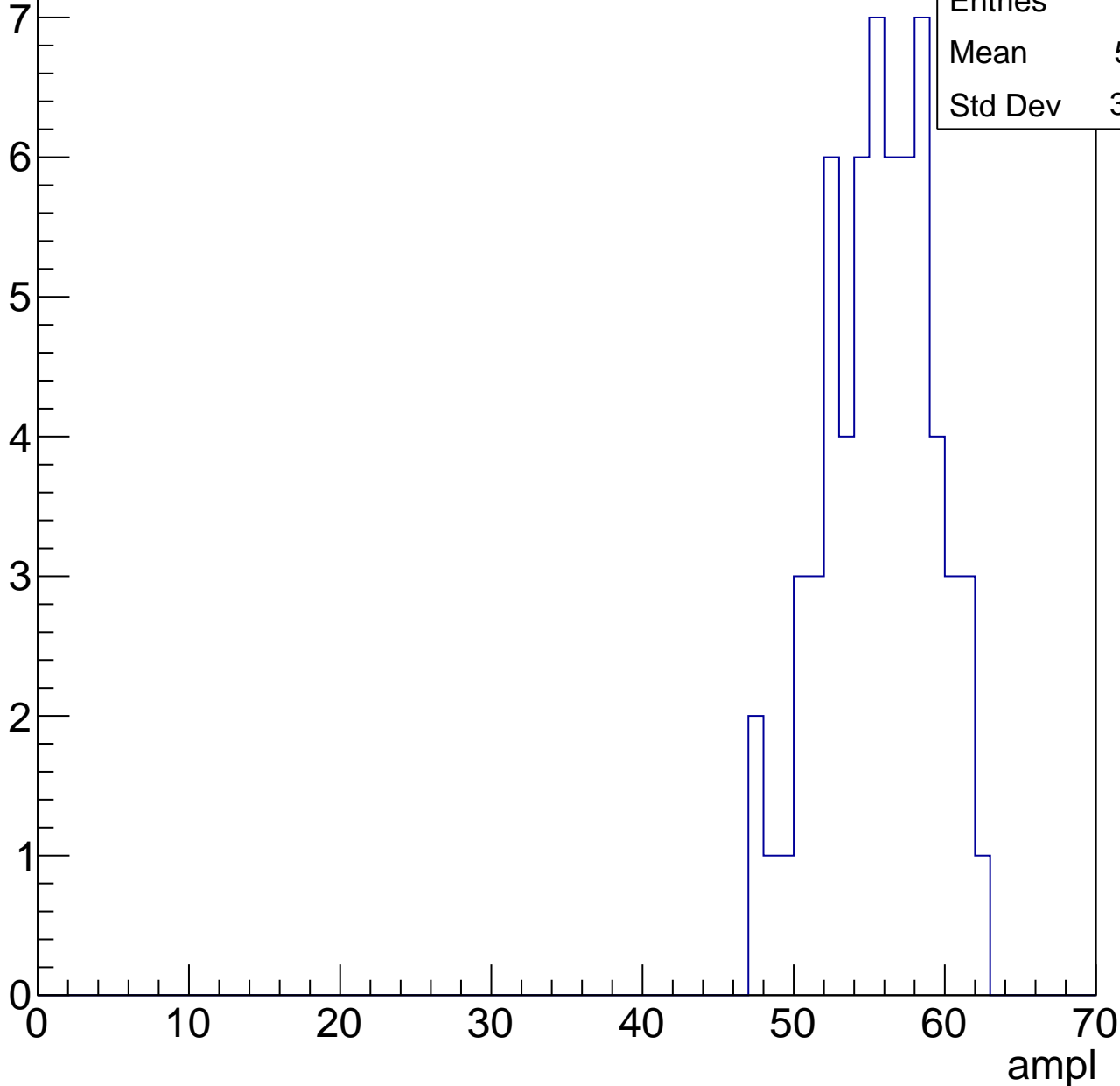


B1L103S, U12-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

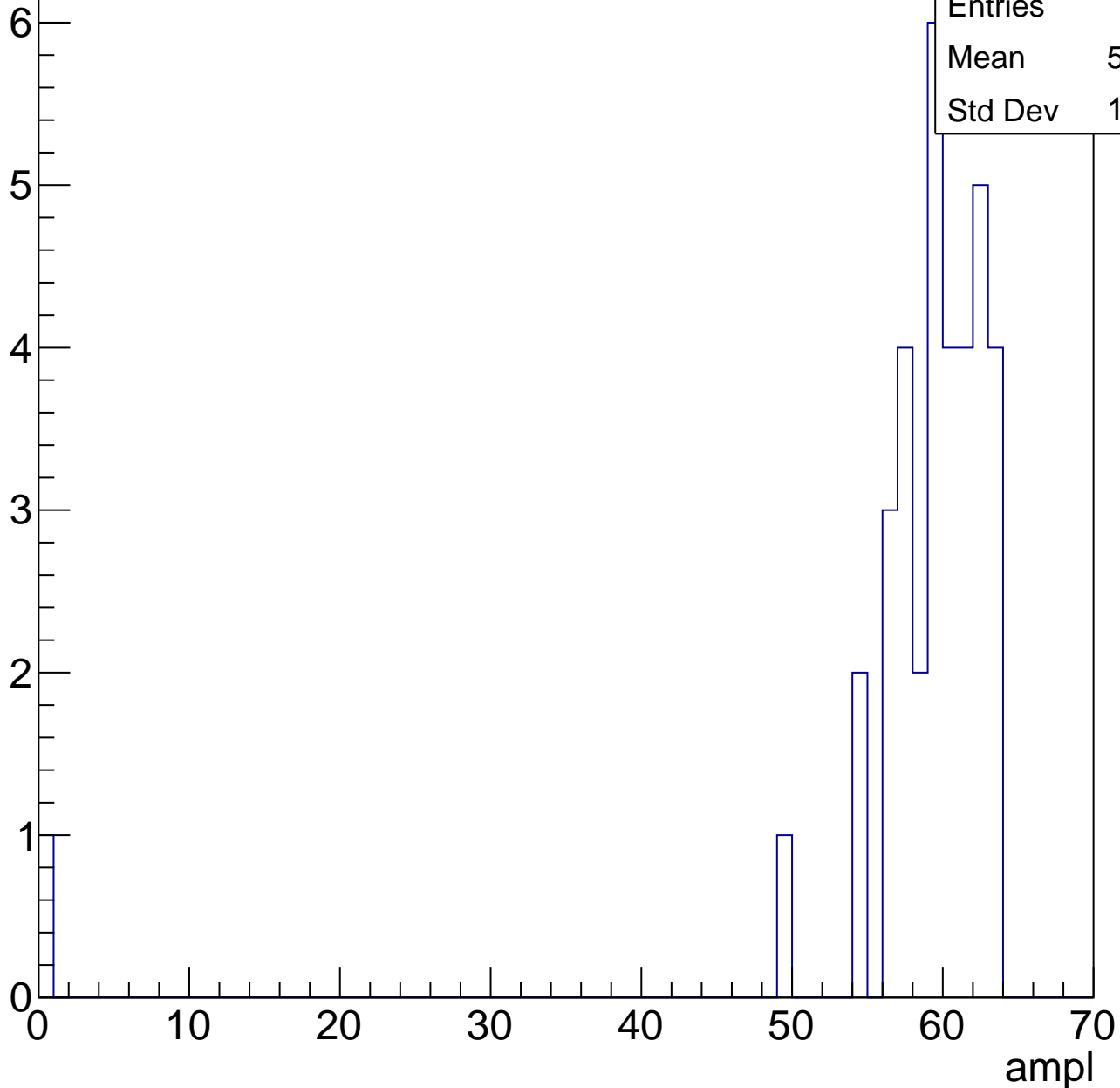
Entries	63
Mean	55.11
Std Dev	3.564



B1L103S, U12-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

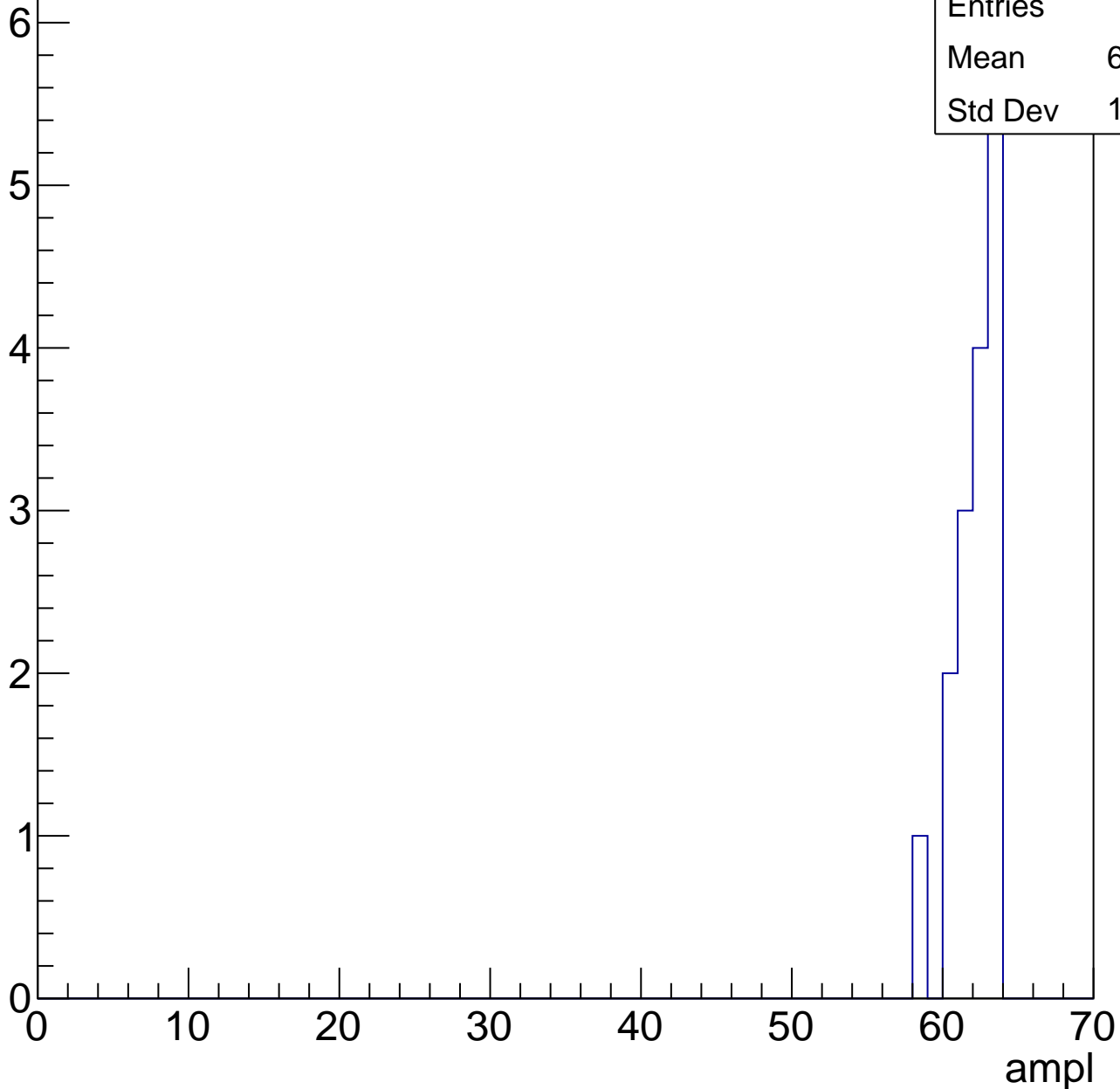


B1L103S, U12-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.69
Std Dev	1.402



B1L103S, U12-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch19, adc0

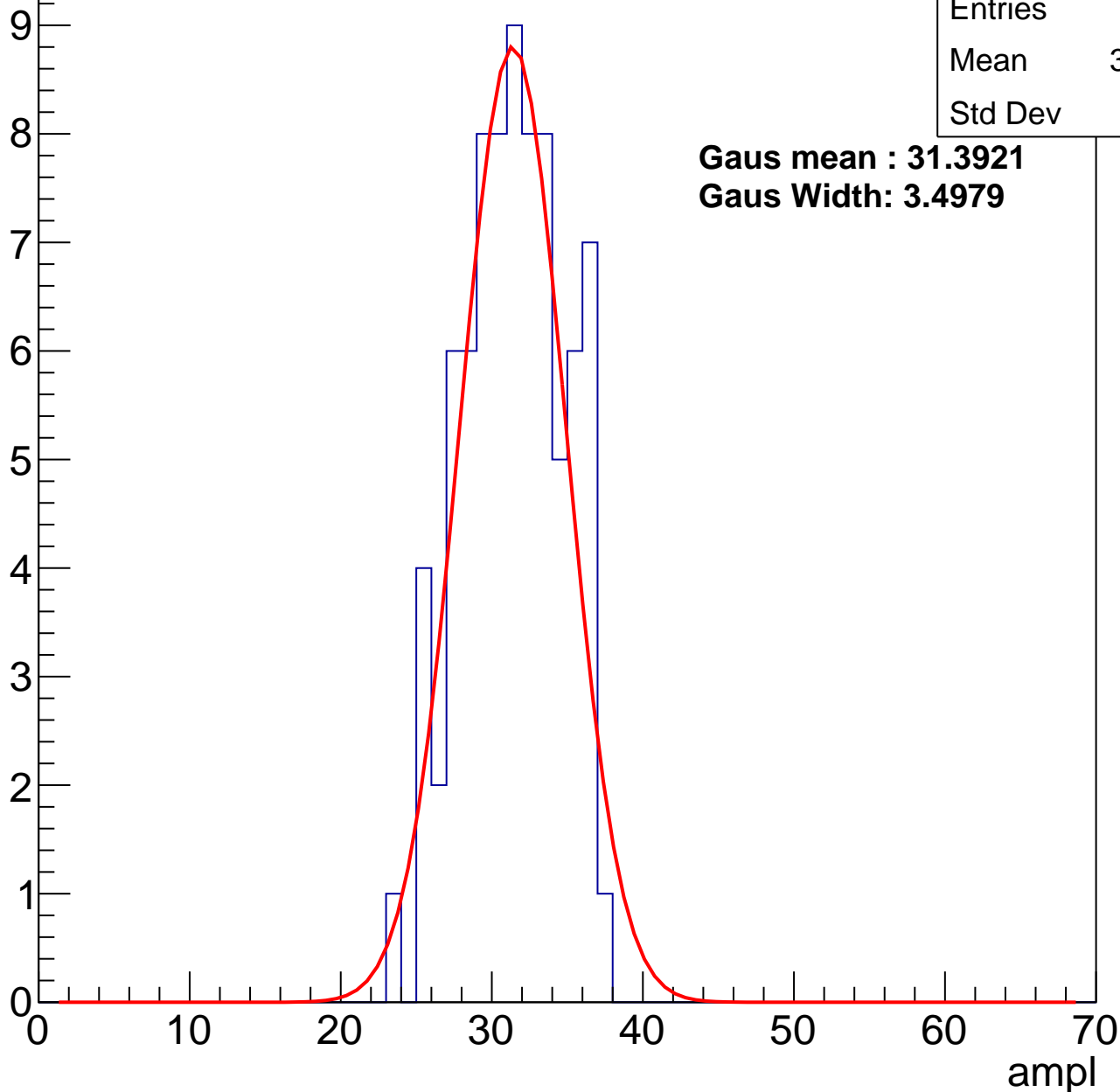
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	30.95
Std Dev	3.26

Gaus mean : 31.3921

Gaus Width: 3.4979



B1L103S, U12-ch19, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	38.3
Std Dev	3.329

Gaus mean : 39.0156

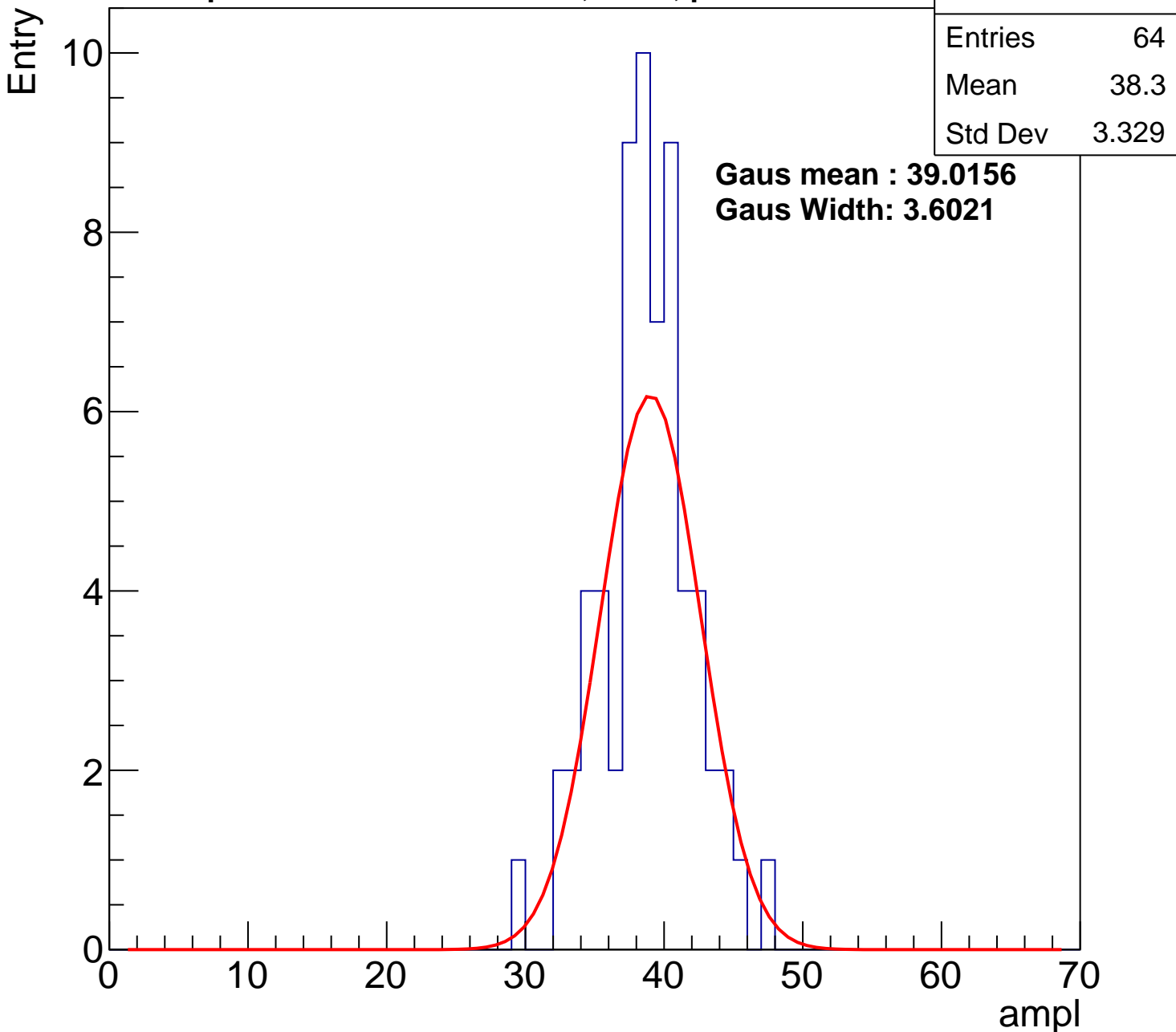
Gaus Width: 3.6021

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch19, adc2

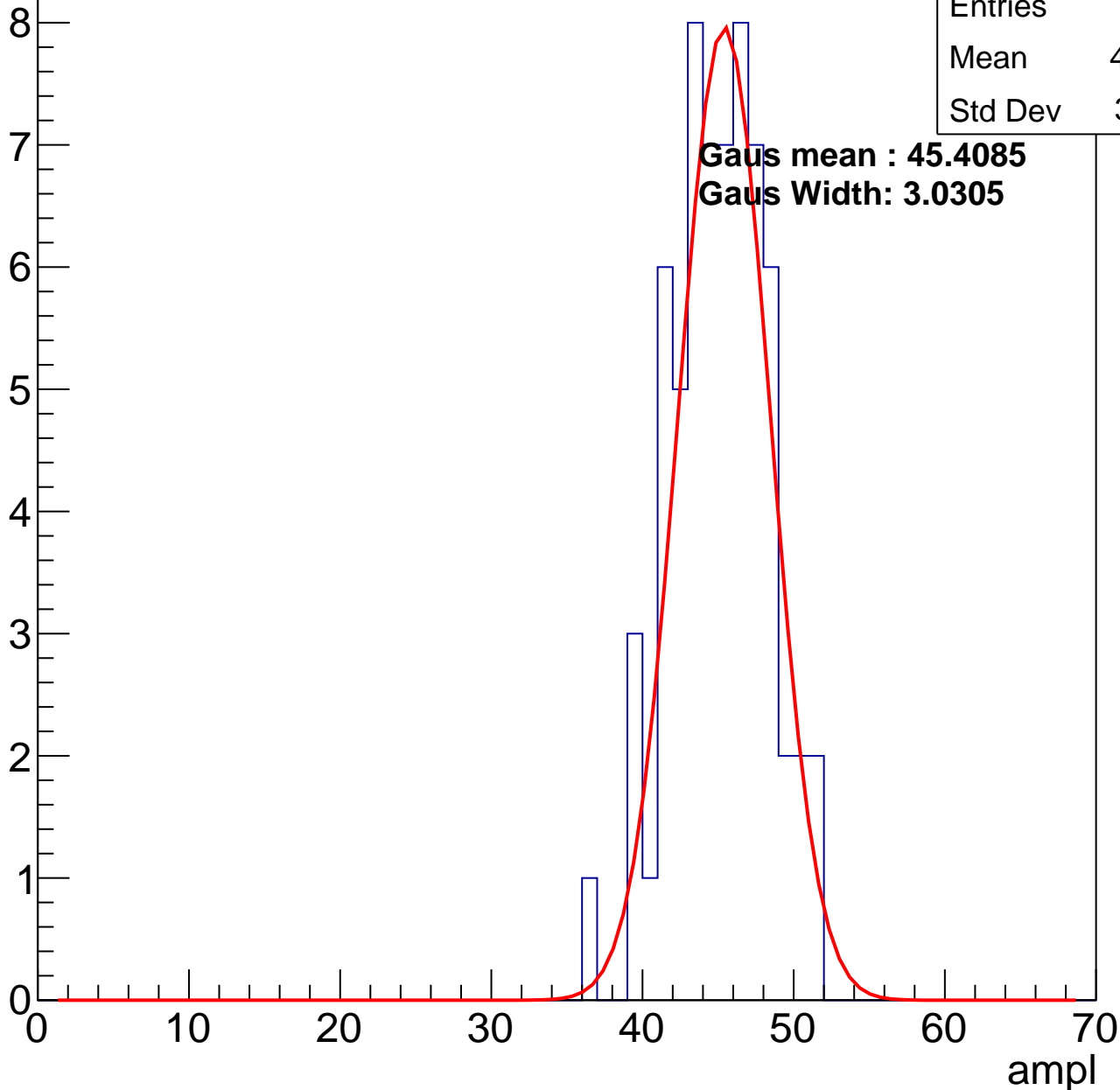
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.63
Std Dev	3.111

Gaus mean : 45.4085

Gaus Width: 3.0305

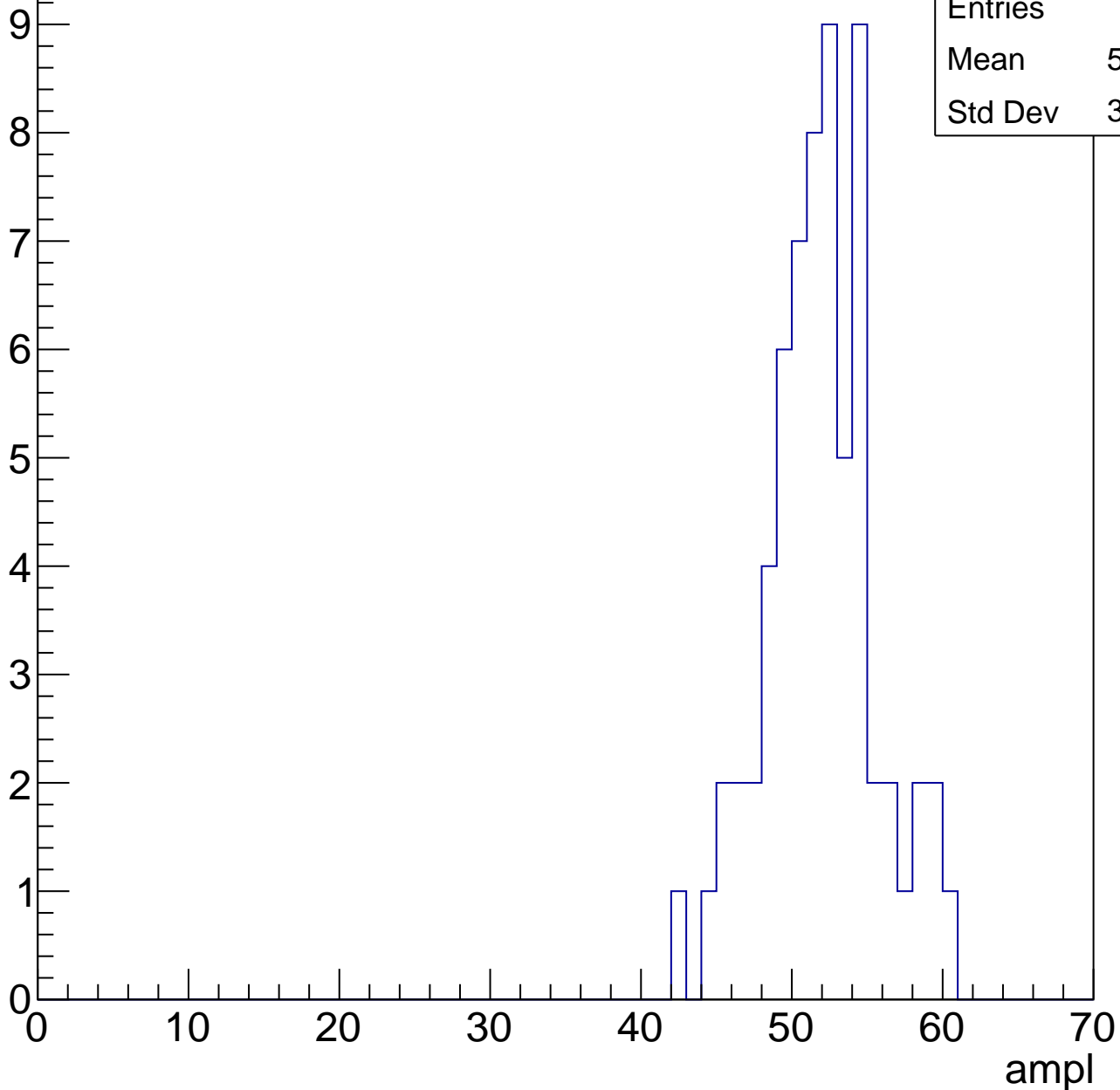


B1L103S, U12-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	51.48
Std Dev	3.632

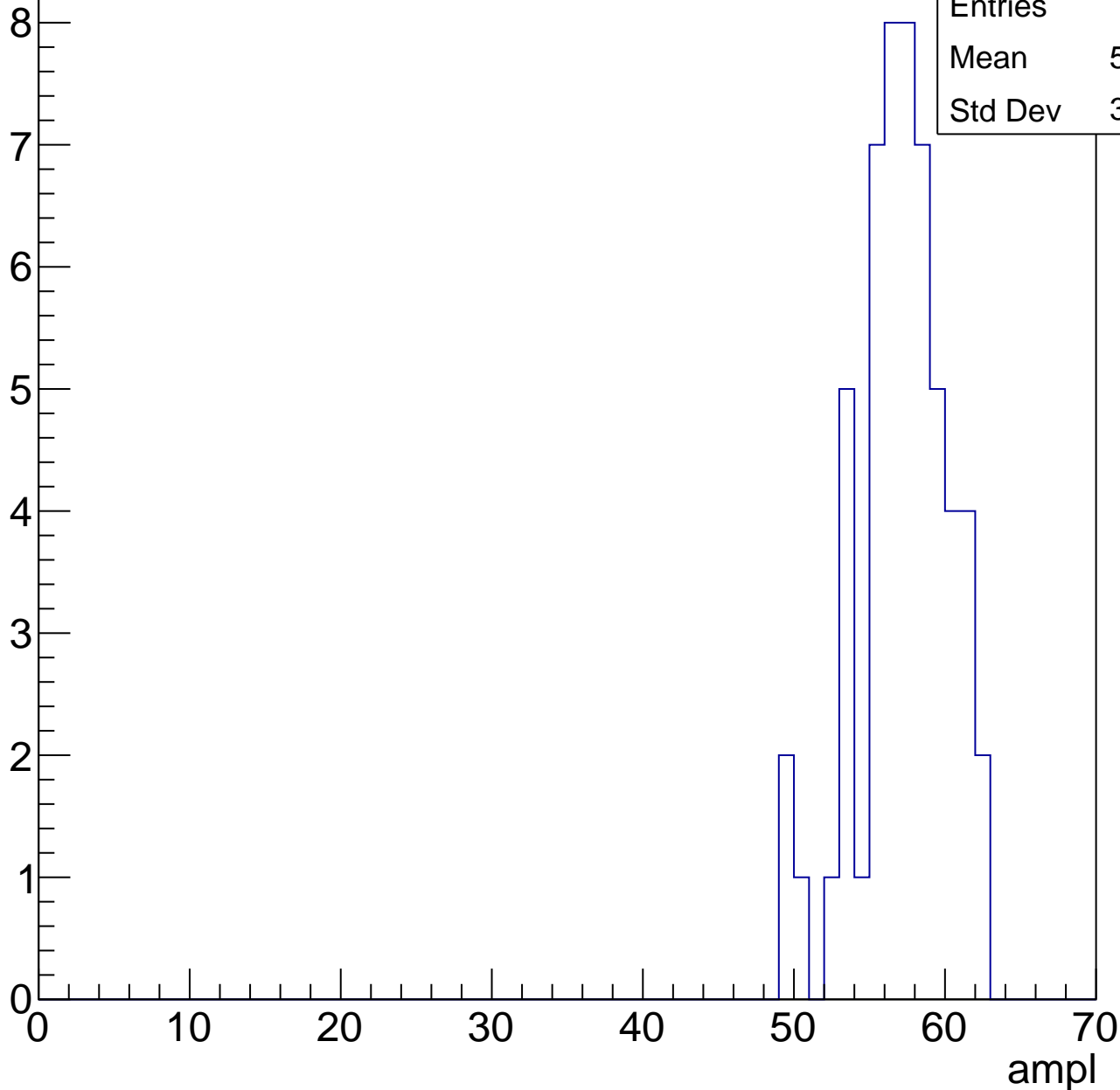


B1L103S, U12-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.67
Std Dev	3.009

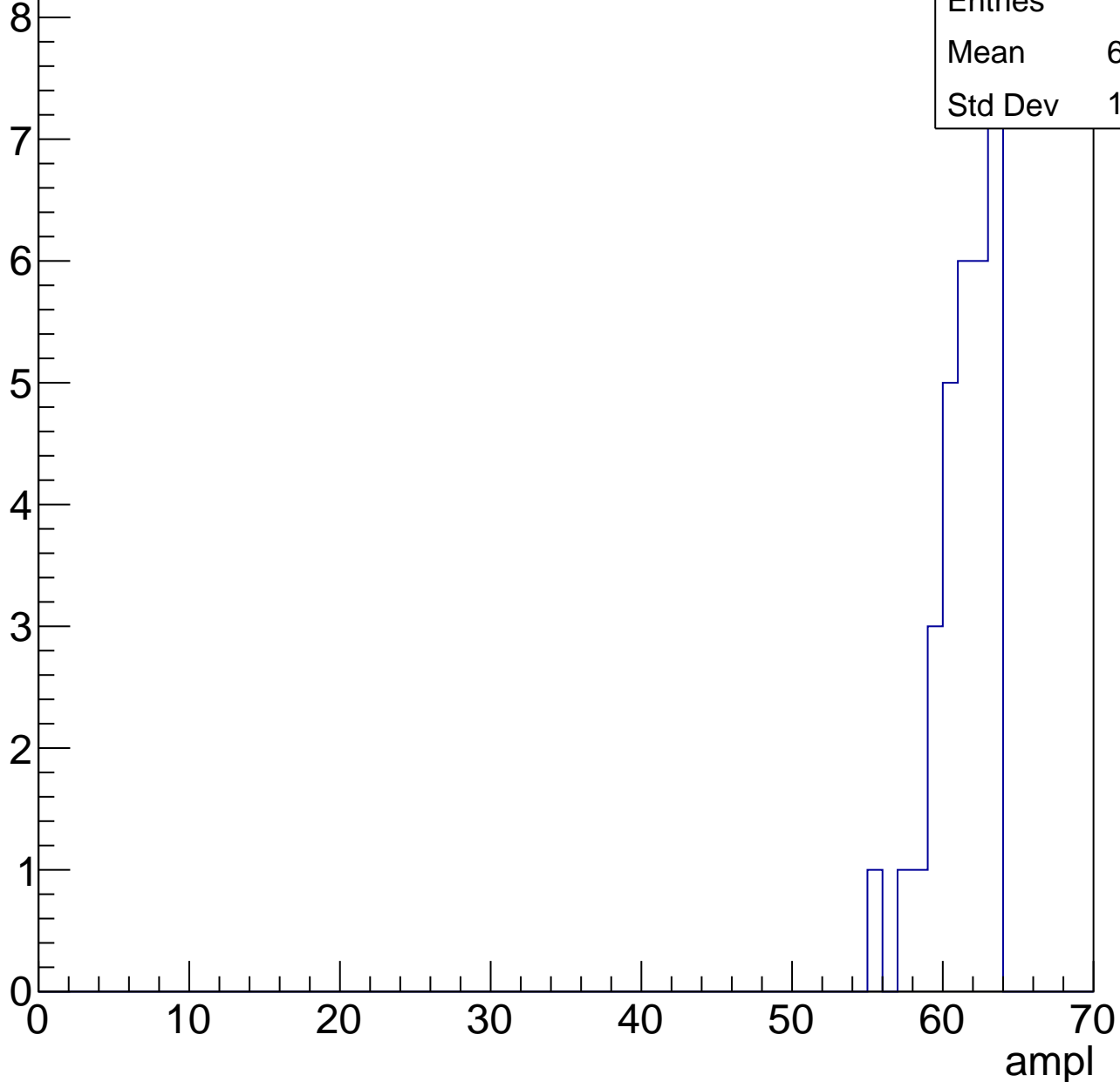


B1L103S, U12-ch19, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	31
Mean	60.94
Std Dev	1.933



B1L103S, U12-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch20, adc0

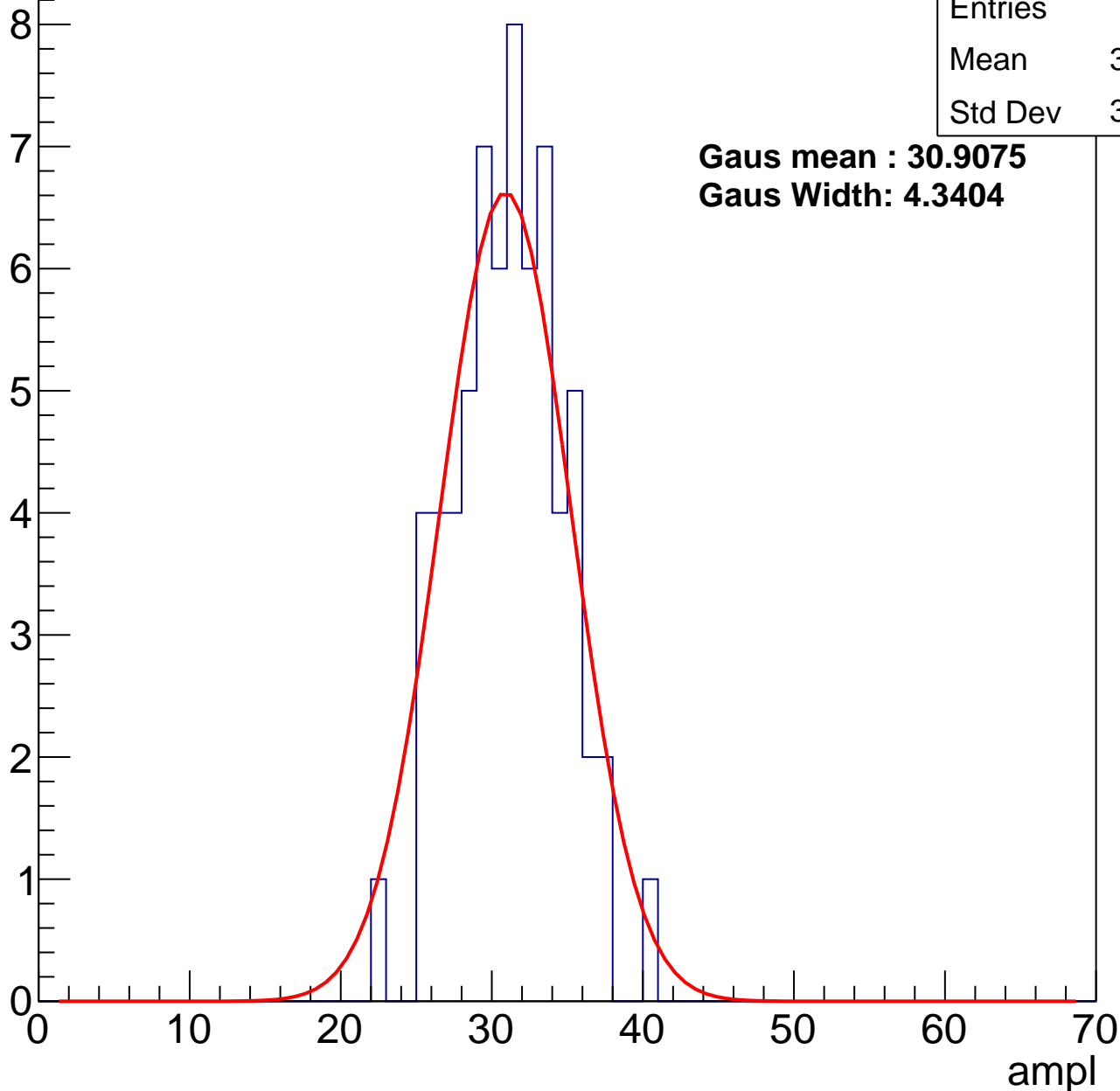
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	30.68
Std Dev	3.513

Gaus mean : 30.9075

Gaus Width: 4.3404



B1L103S, U12-ch20, adc1

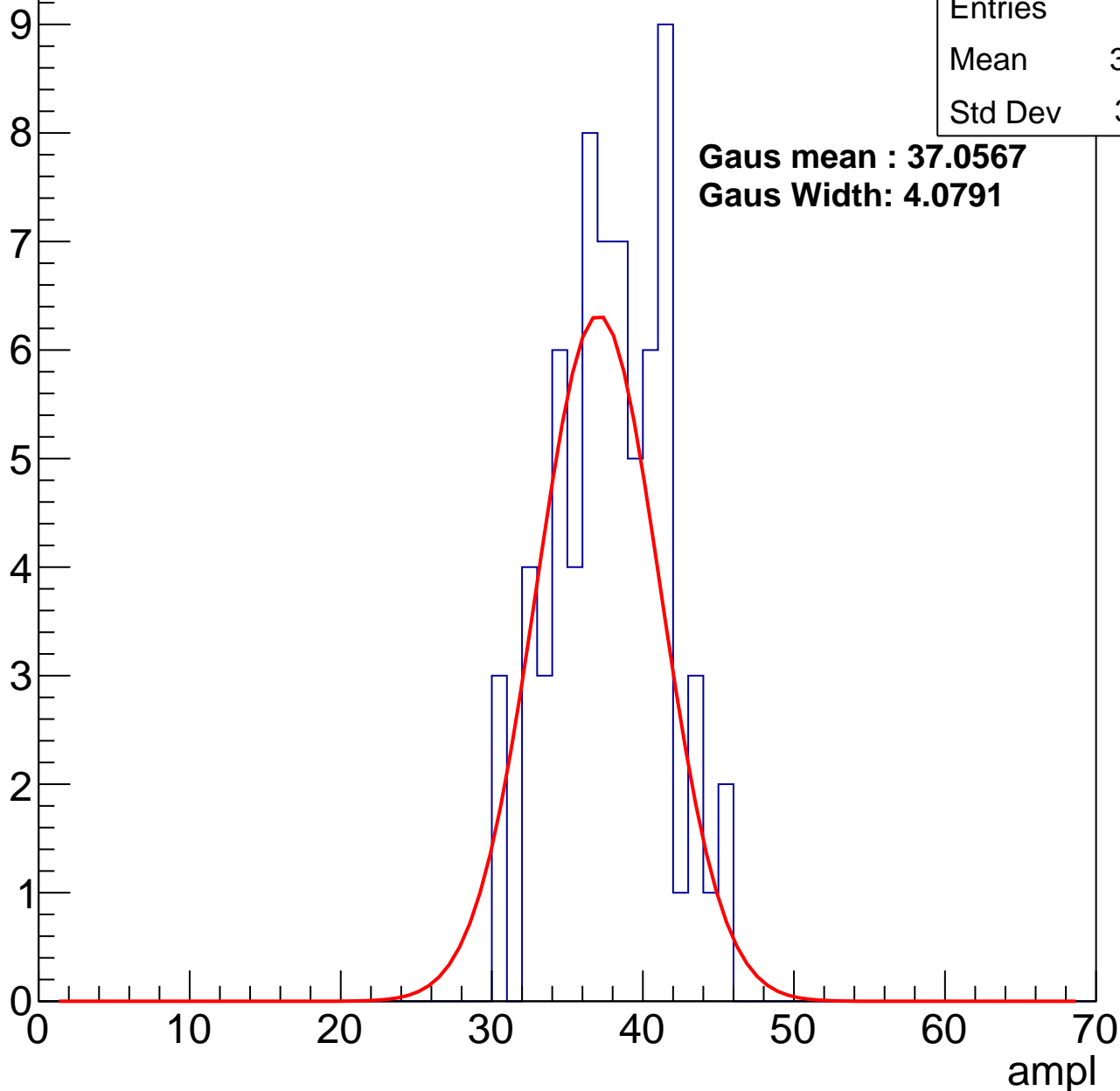
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.43
Std Dev	3.601

Gaus mean : 37.0567

Gaus Width: 4.0791



B1L103S, U12-ch20, adc2

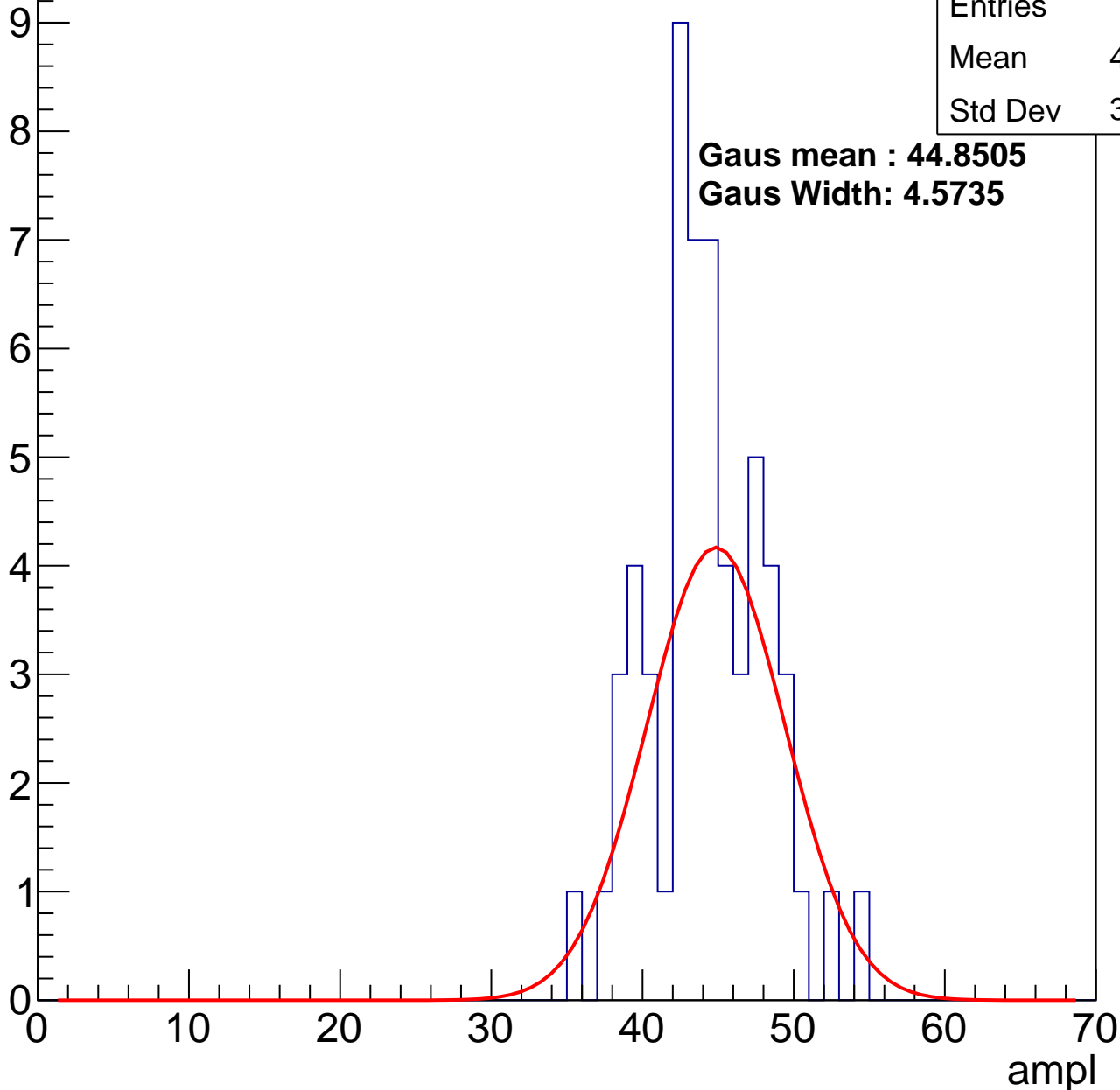
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.76
Std Dev	3.793

Gaus mean : 44.8505

Gaus Width: 4.5735



B1L103S, U12-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	50.47
Std Dev	3.771

Entry

10

8

6

4

2

0

0

10

20

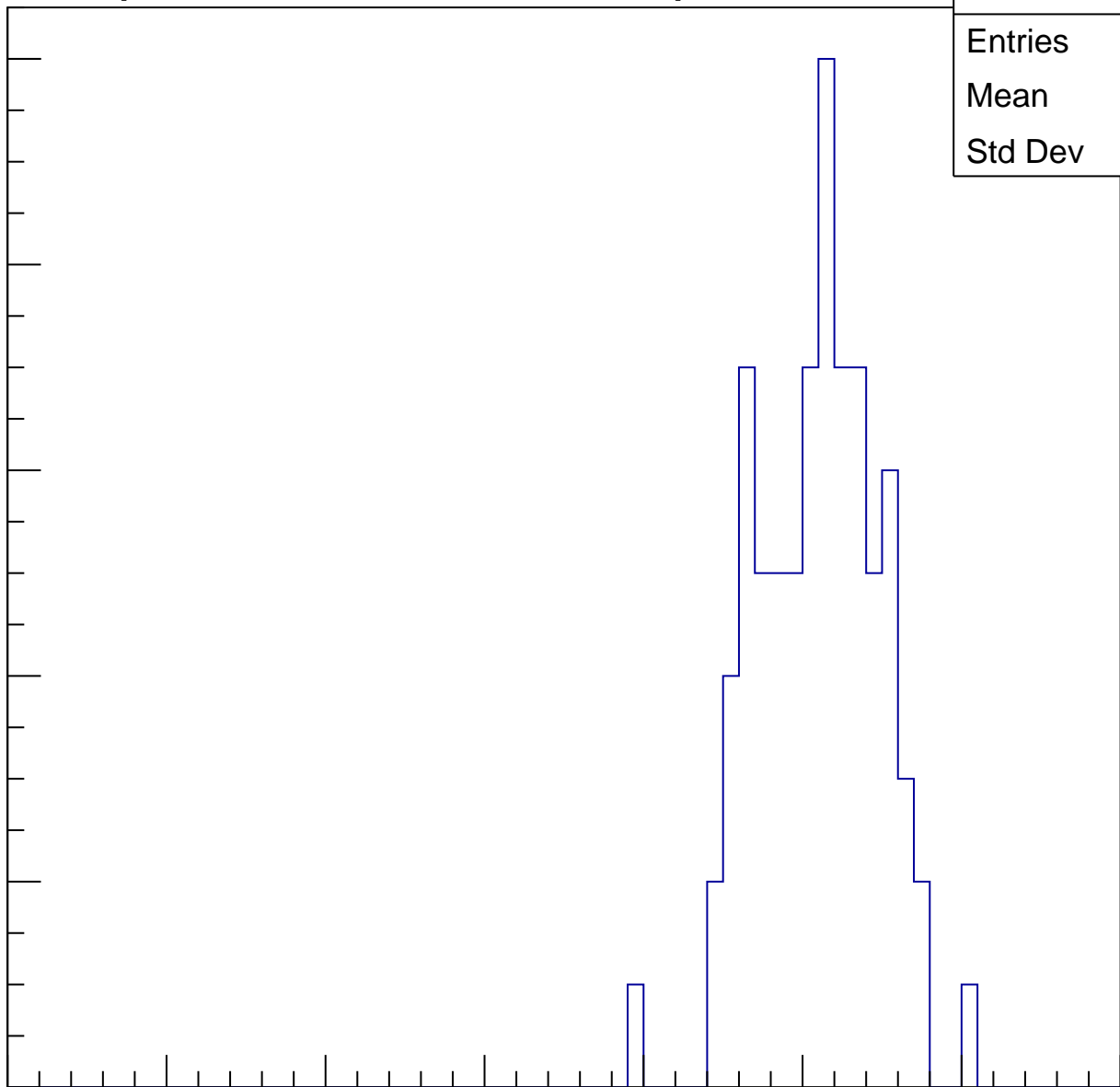
30

40

50

60

ampl

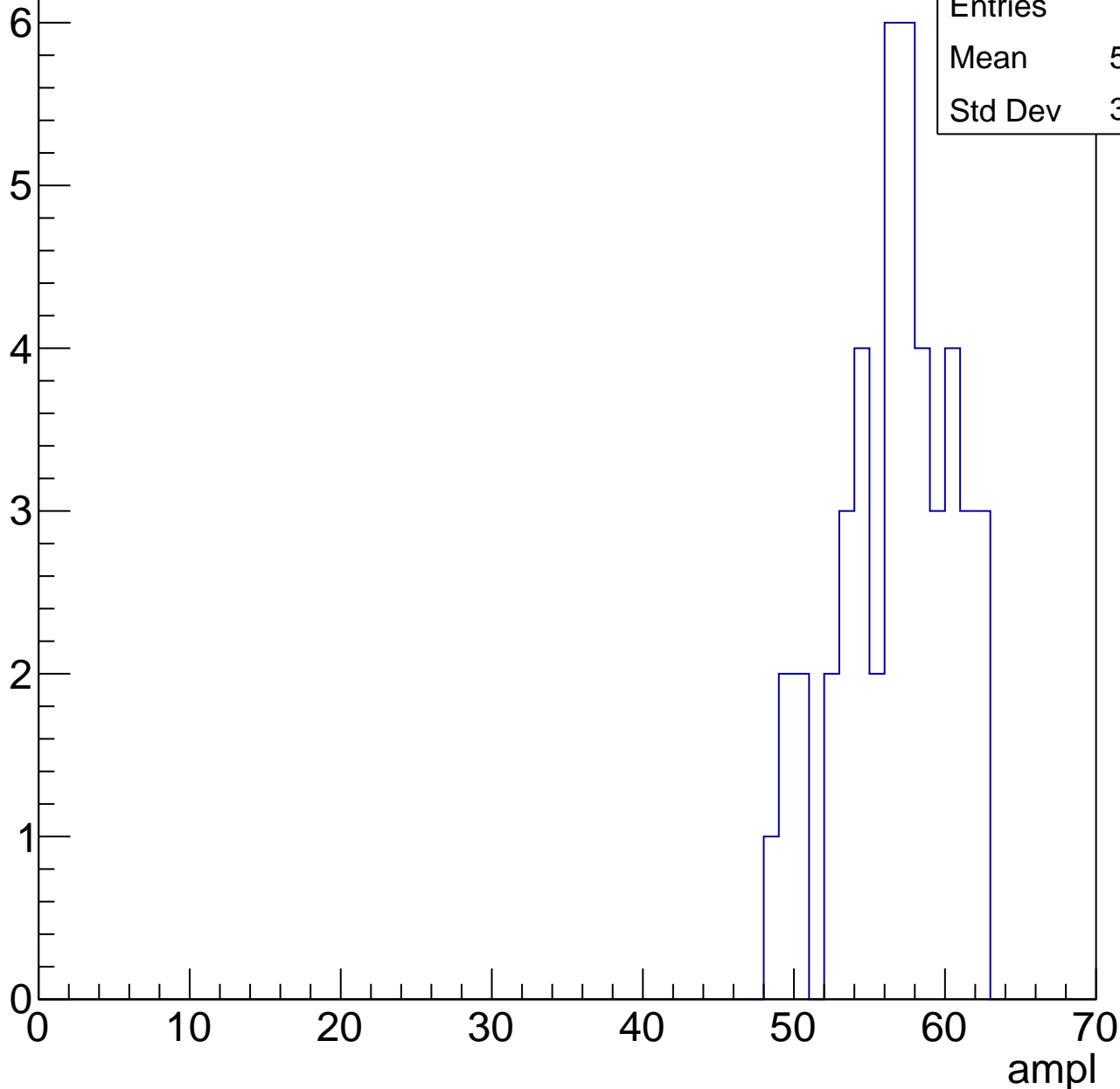


B1L103S, U12-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

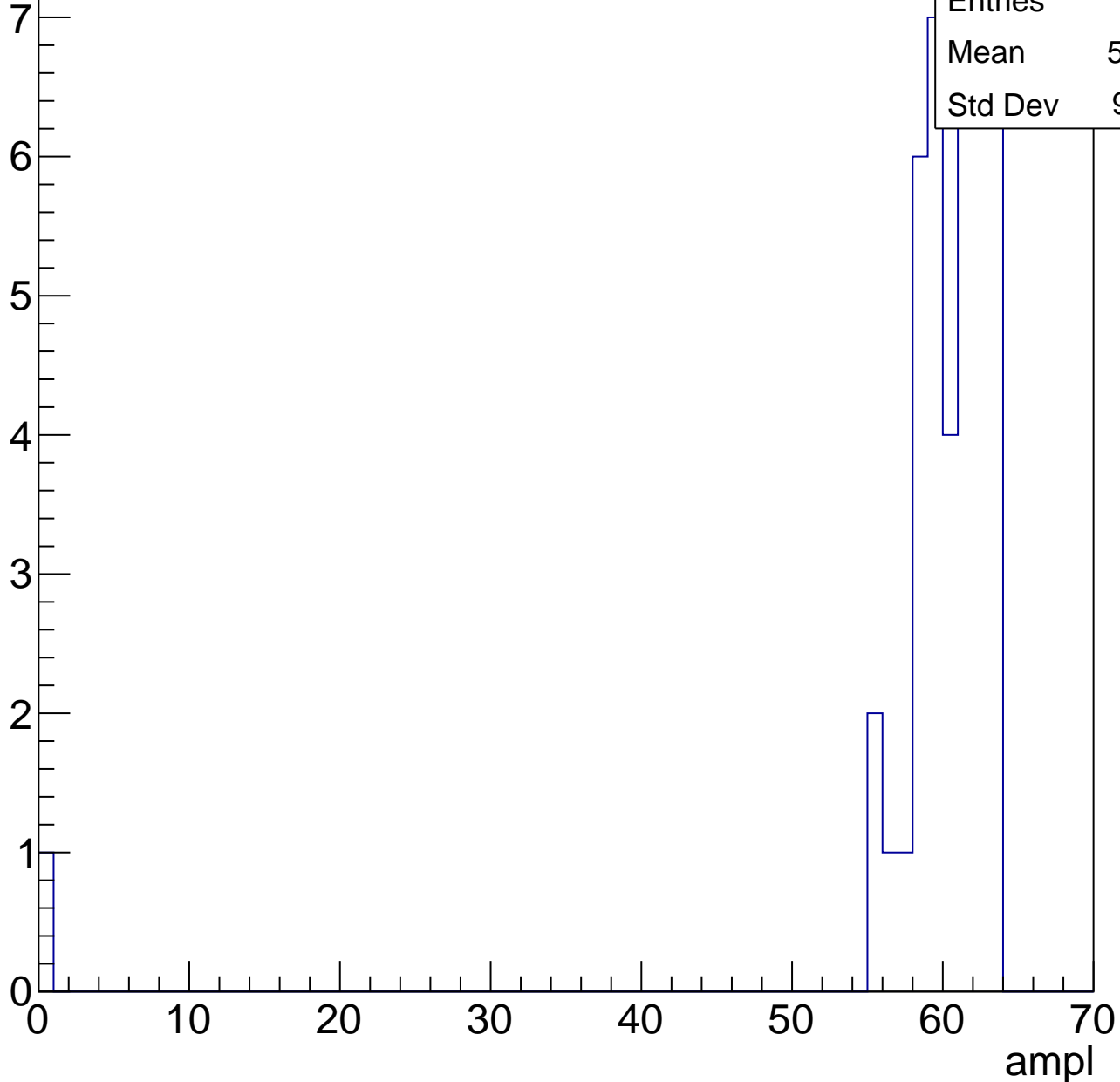
Entries	45
Mean	56.24
Std Dev	3.665



B1L103S, U12-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

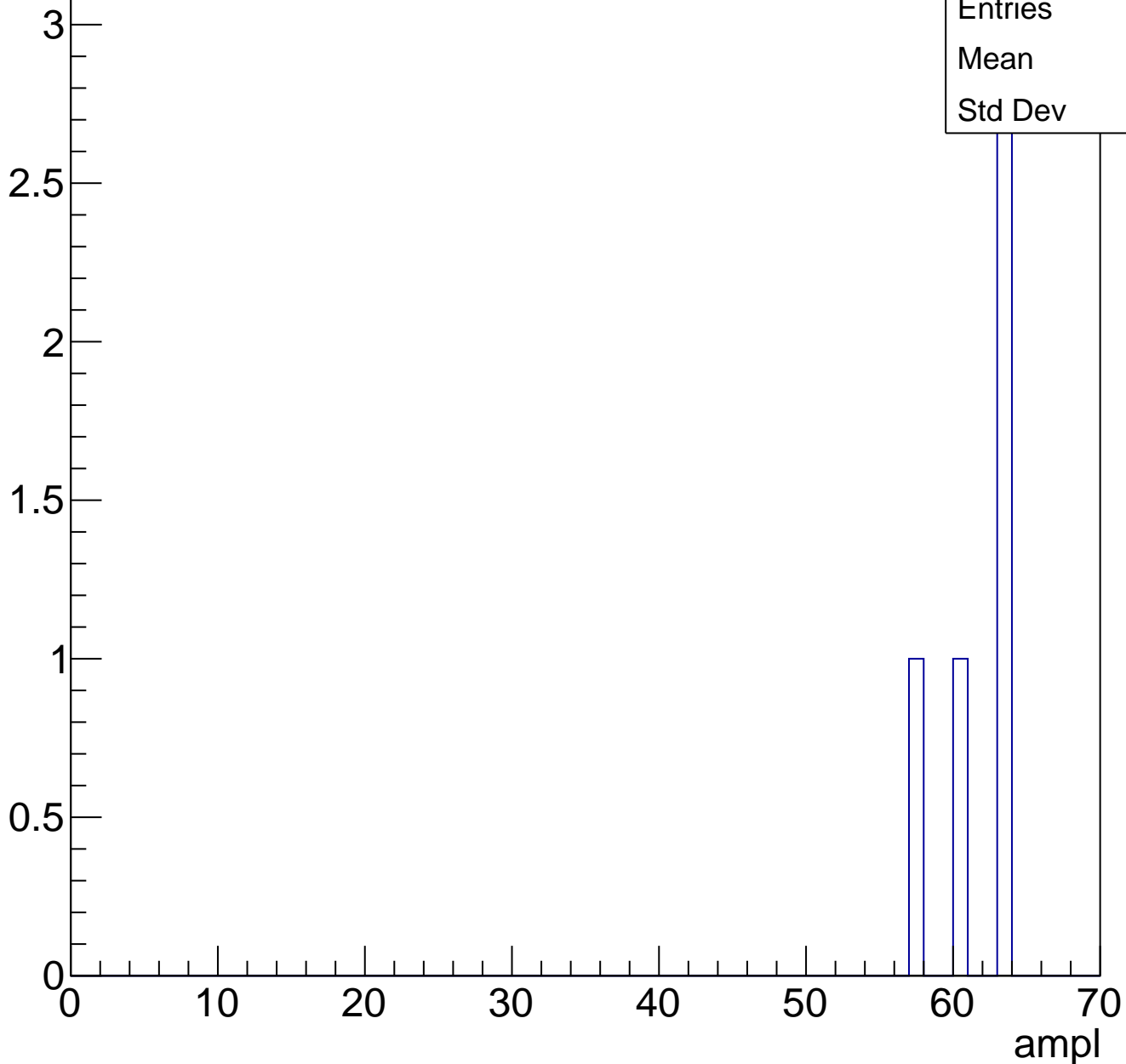
Entry



B1L103S, U12-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch21, adc0

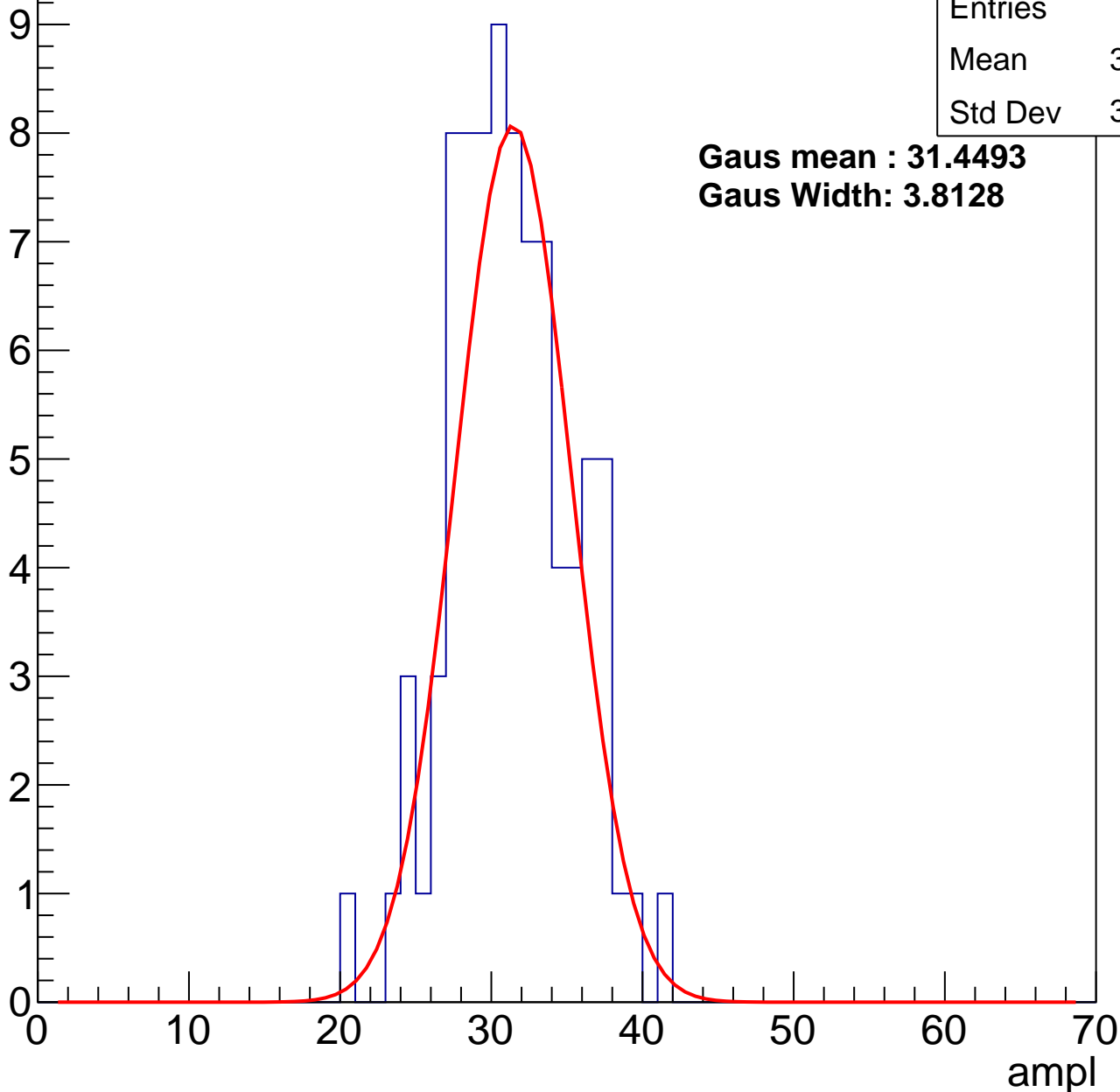
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	30.85
Std Dev	3.963

Gaus mean : 31.4493

Gaus Width: 3.8128



B1L103S, U12-ch21, adc1

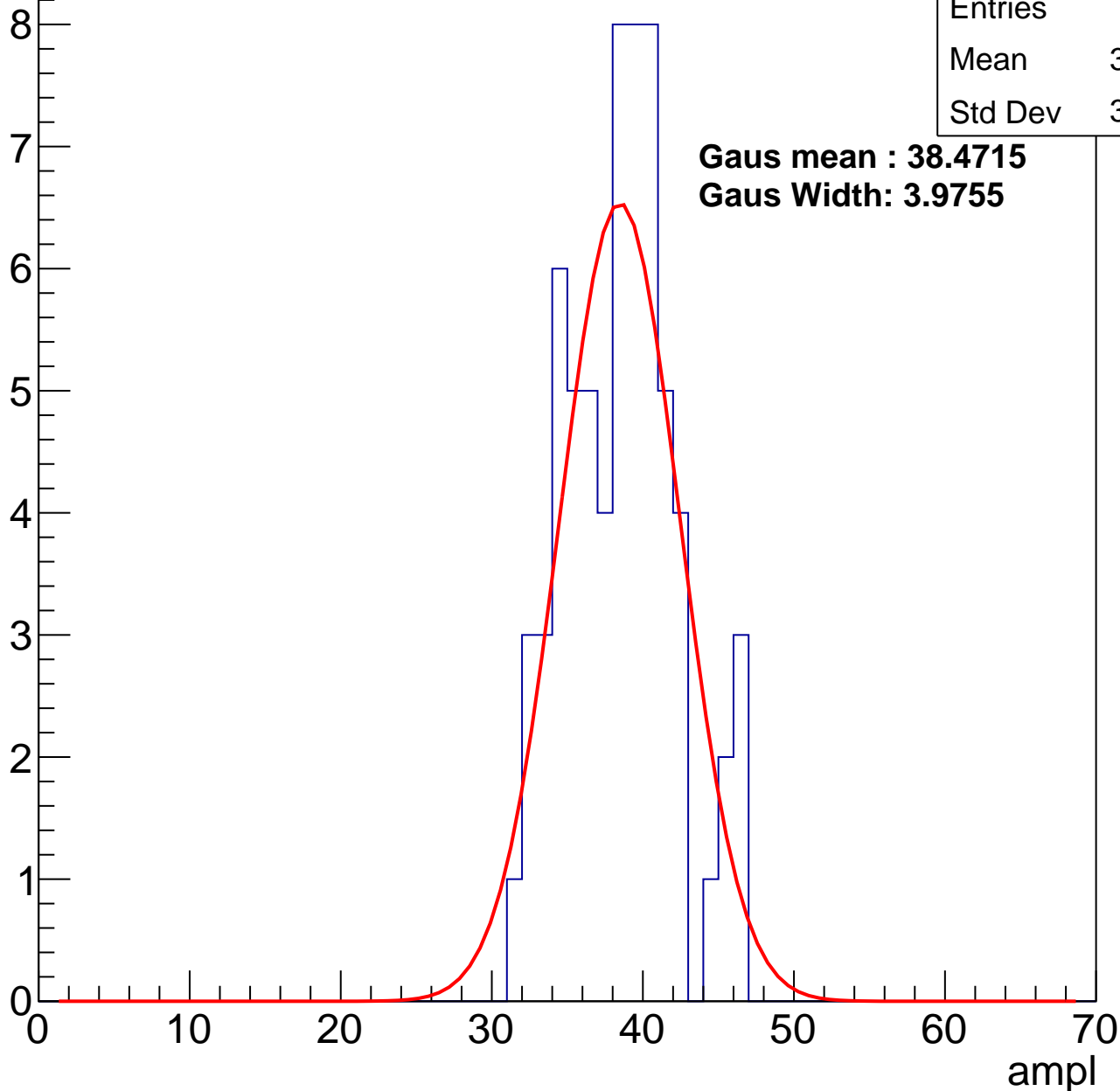
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	38.09
Std Dev	3.634

Gaus mean : 38.4715

Gaus Width: 3.9755



B1L103S, U12-ch21, adc2

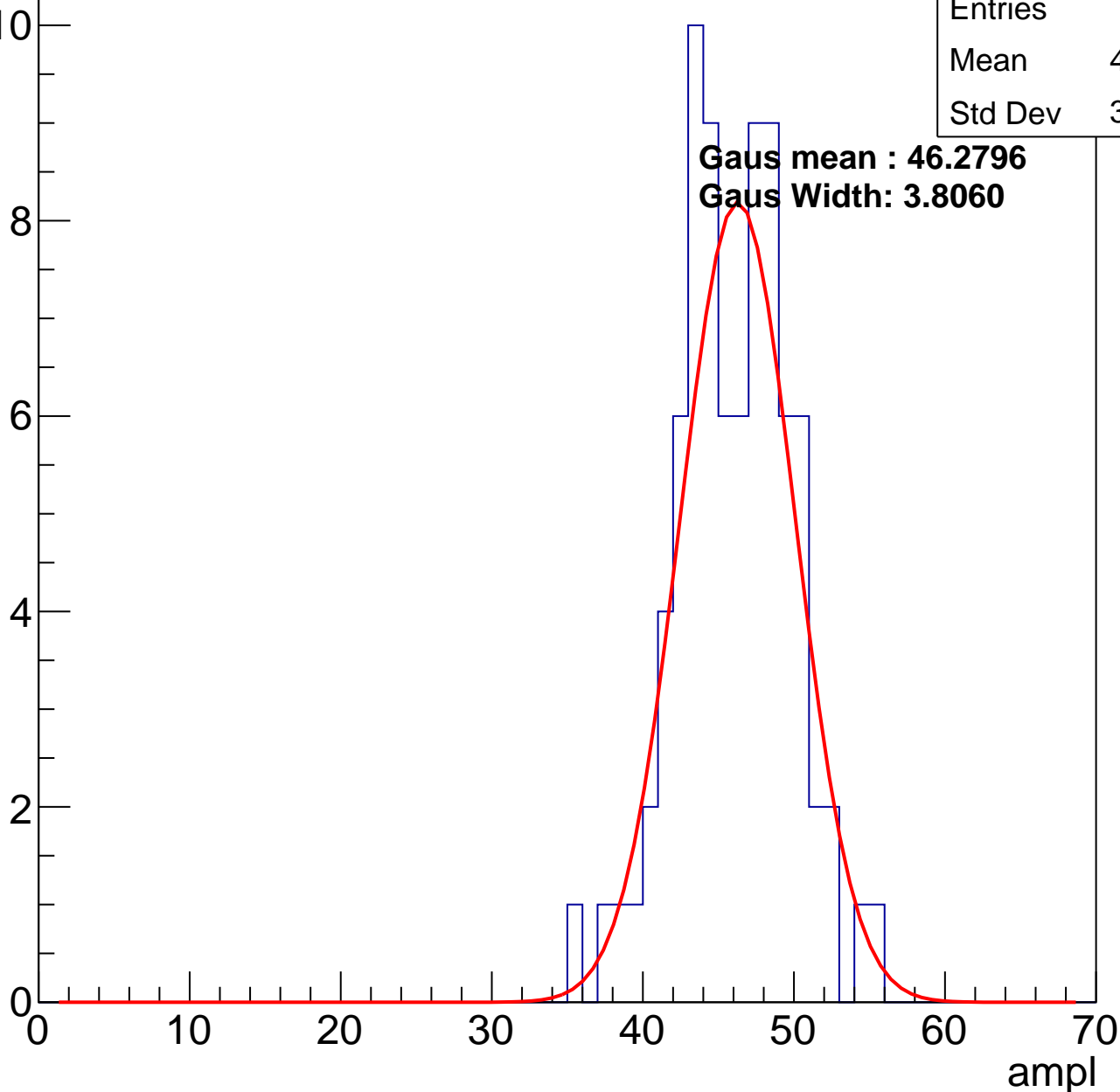
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	45.55
Std Dev	3.739

Gaus mean : 46.2796

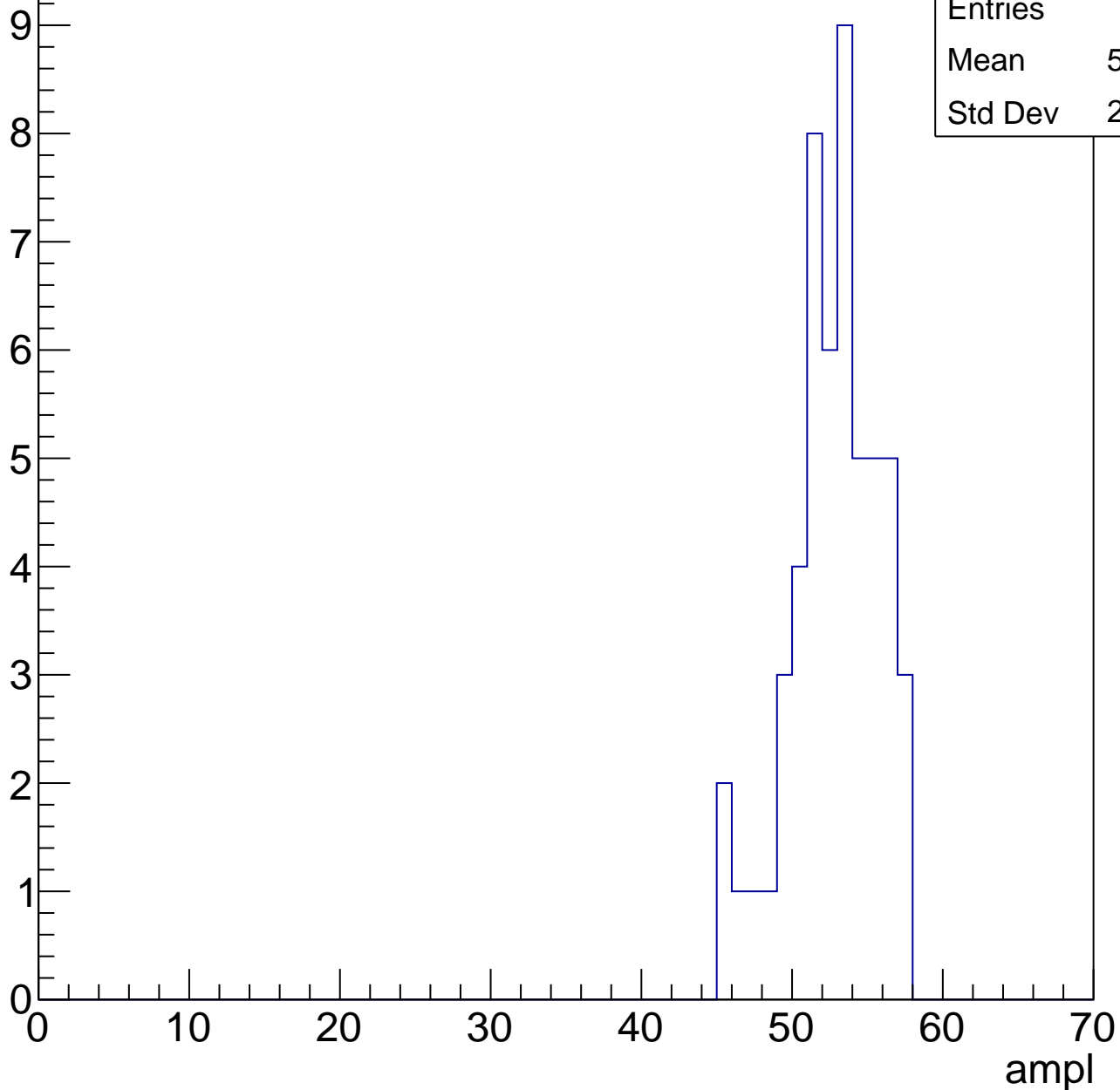
Gaus Width: 3.8060



B1L103S, U12-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



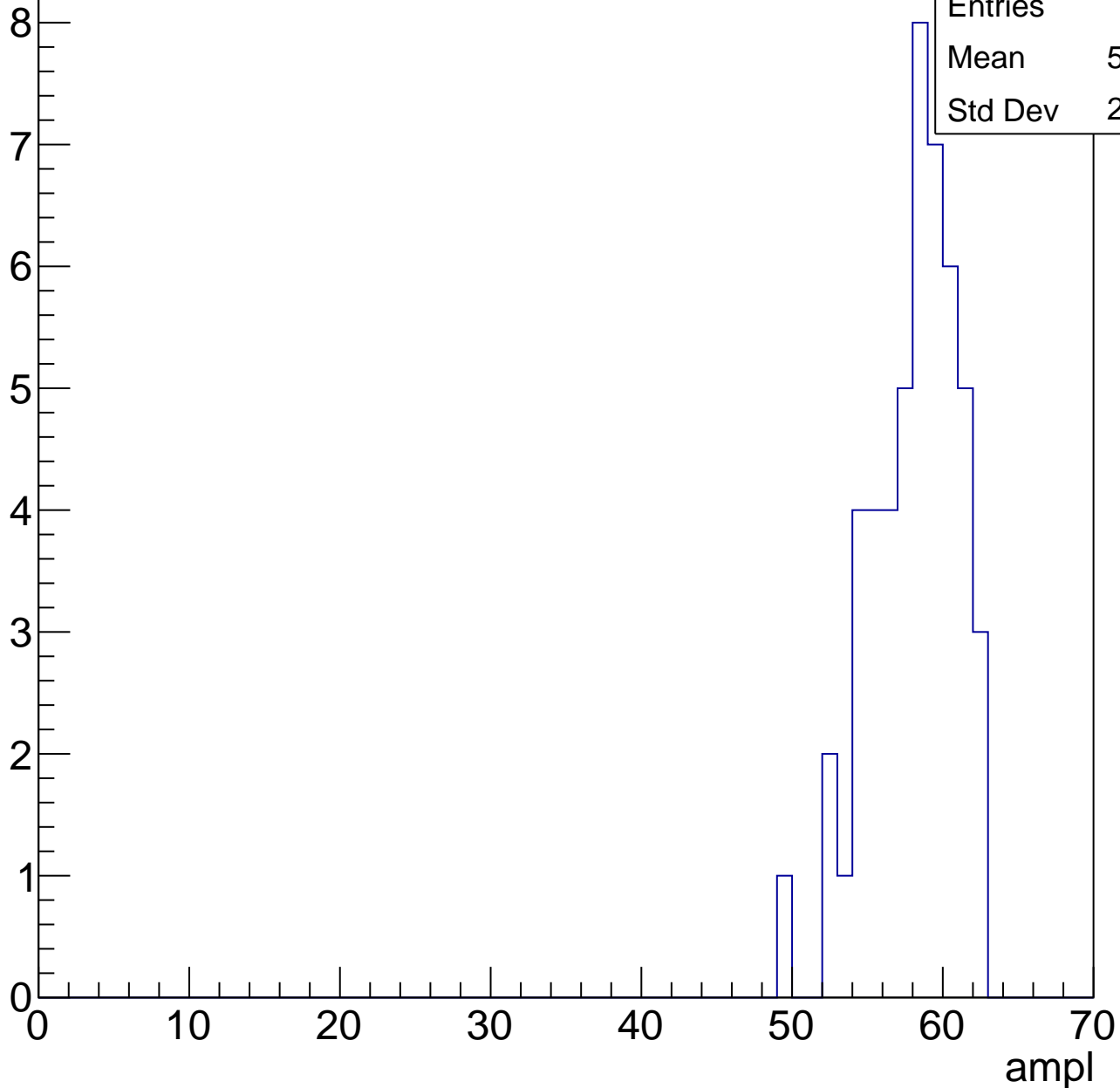
Entries	53
Mean	52.28
Std Dev	2.916

B1L103S, U12-ch21, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	57.58
Std Dev	2.878

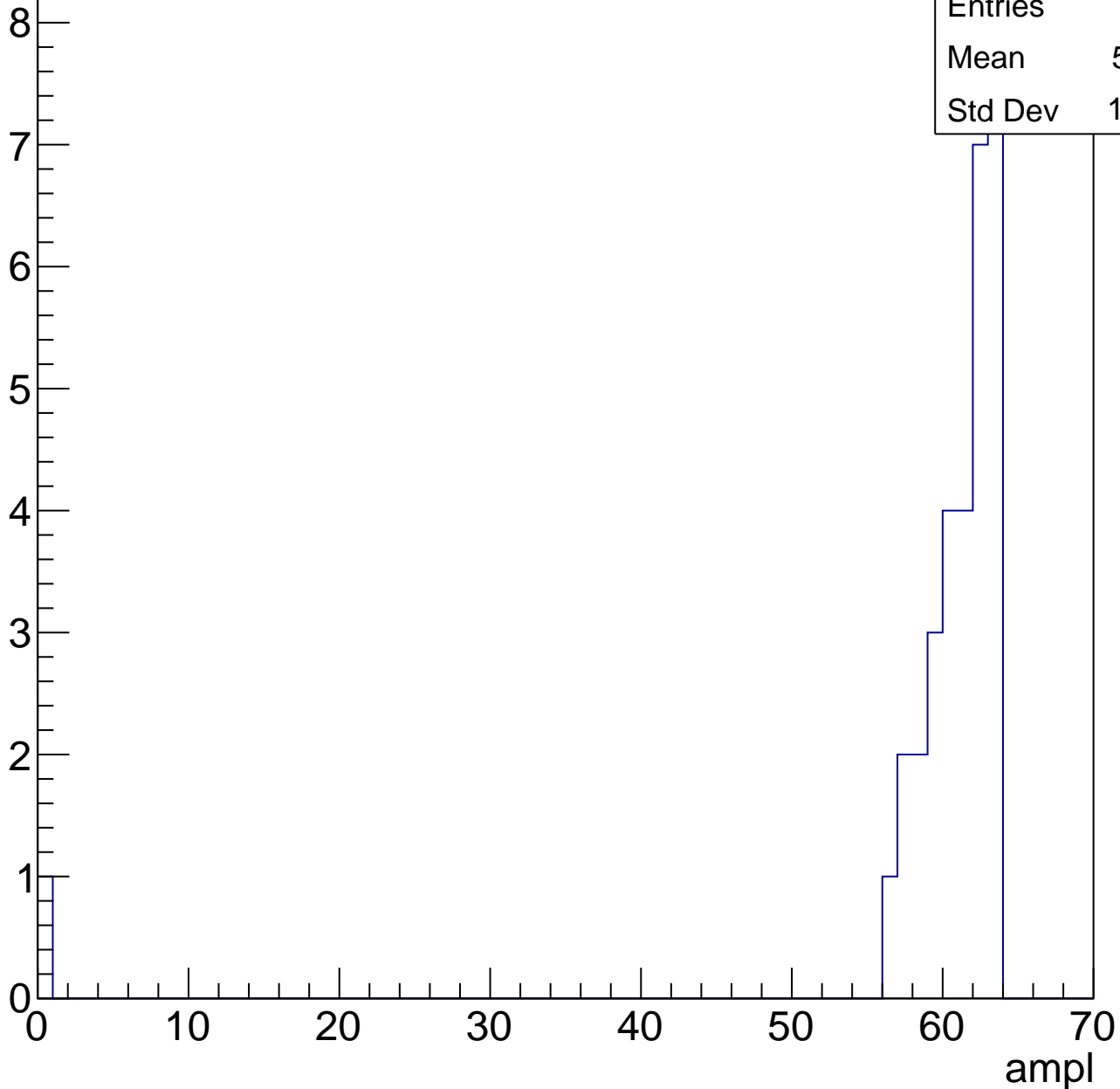


B1L103S, U12-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.91
Std Dev	10.77



B1L103S, U12-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch22, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	29.61
Std Dev	5.973

Gaus mean : 31.0822

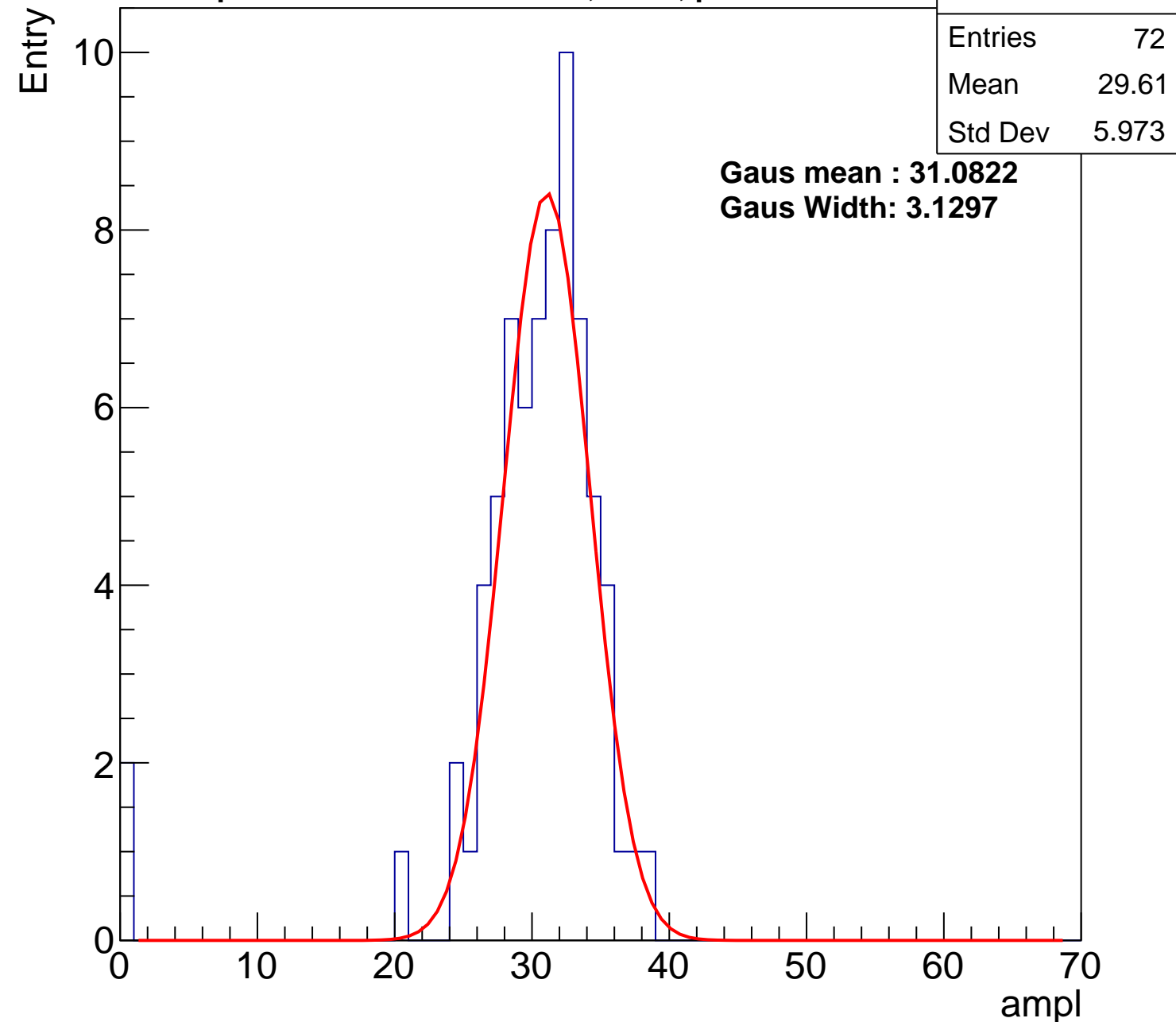
Gaus Width: 3.1297

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



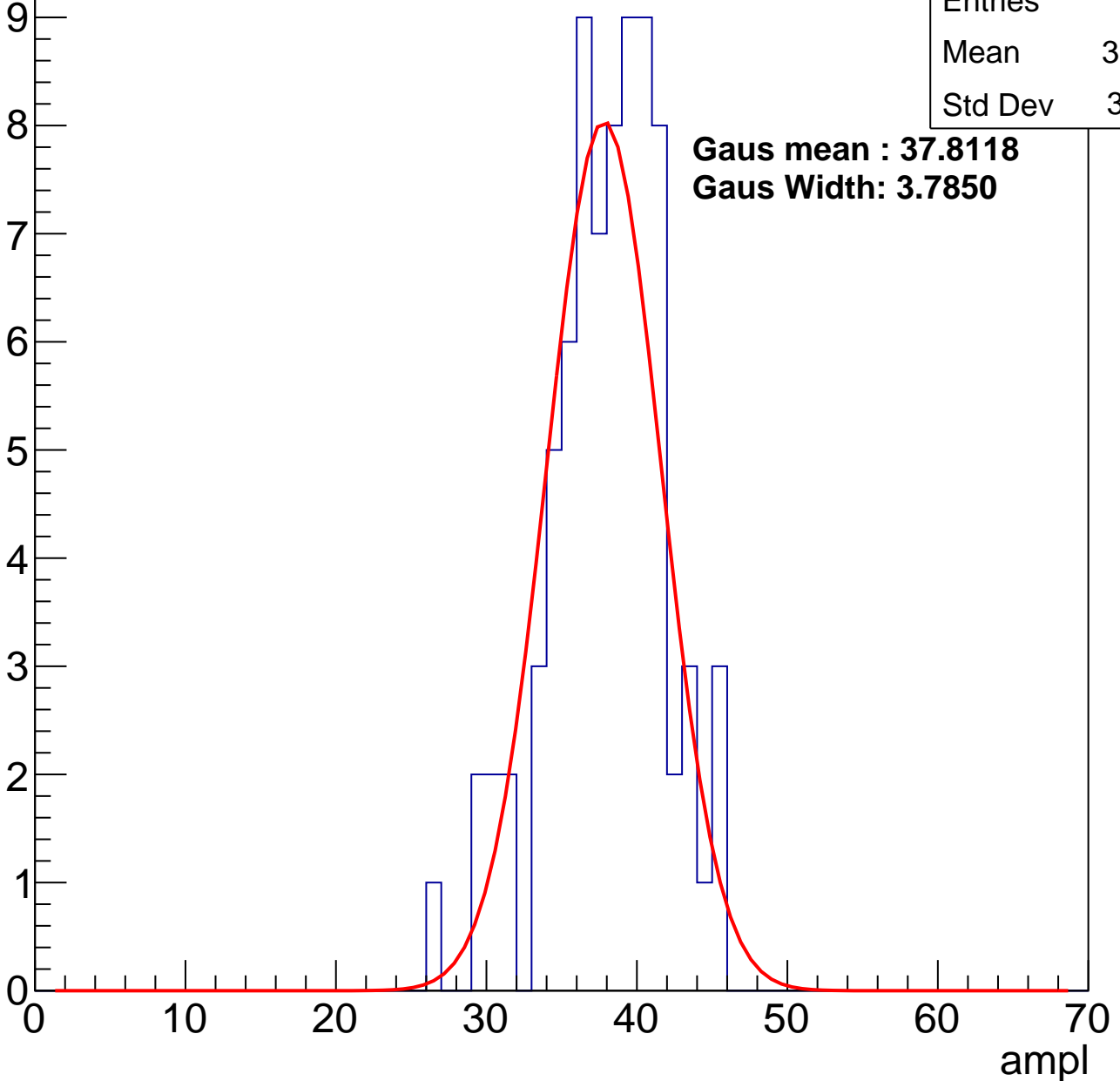
B1L103S, U12-ch22, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	37.54
Std Dev	3.831

Gaus mean : 37.8118
Gaus Width: 3.7850



B1L103S, U12-ch22, adc2

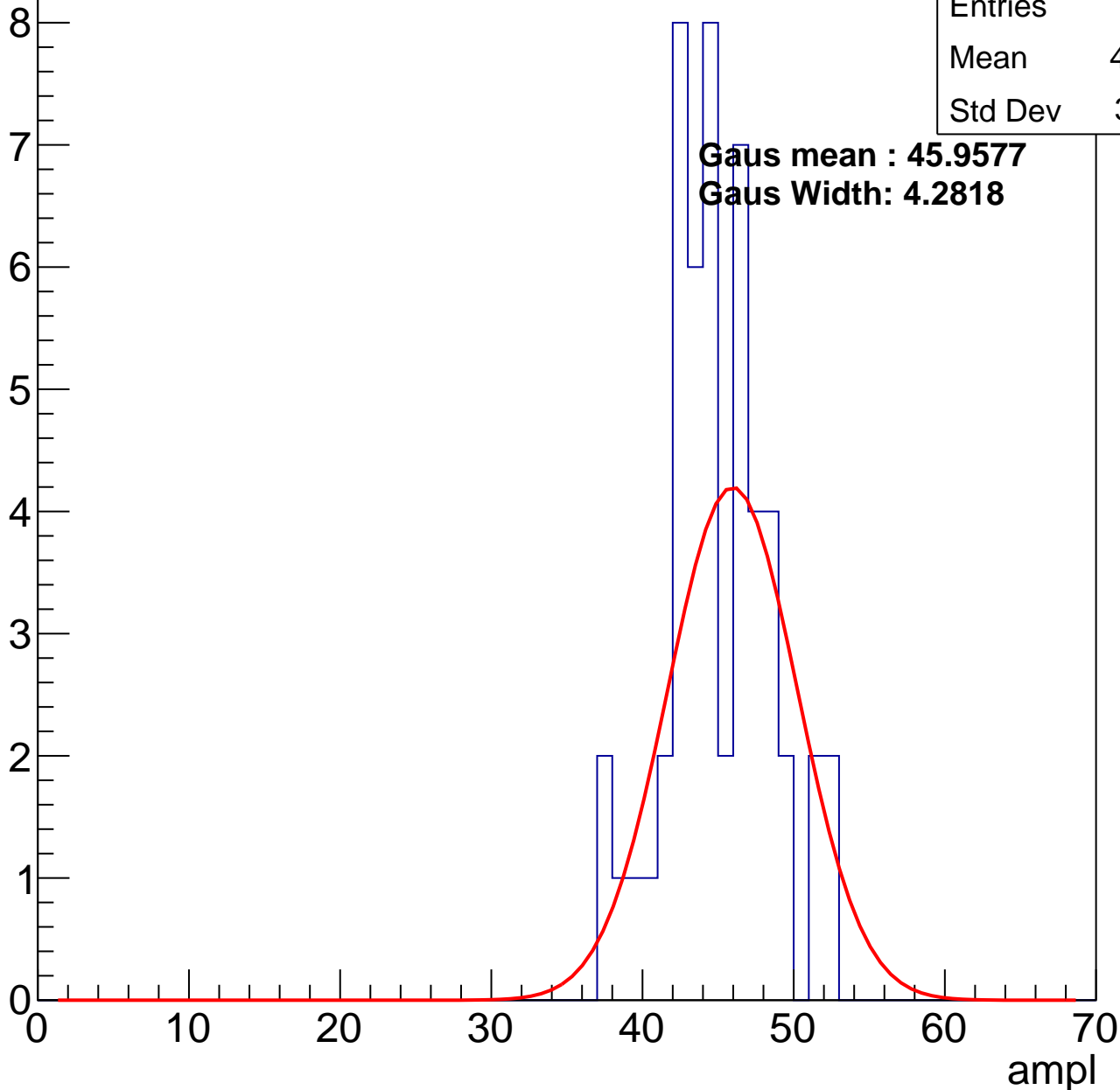
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	44.52
Std Dev	3.461

Gaus mean : 45.9577

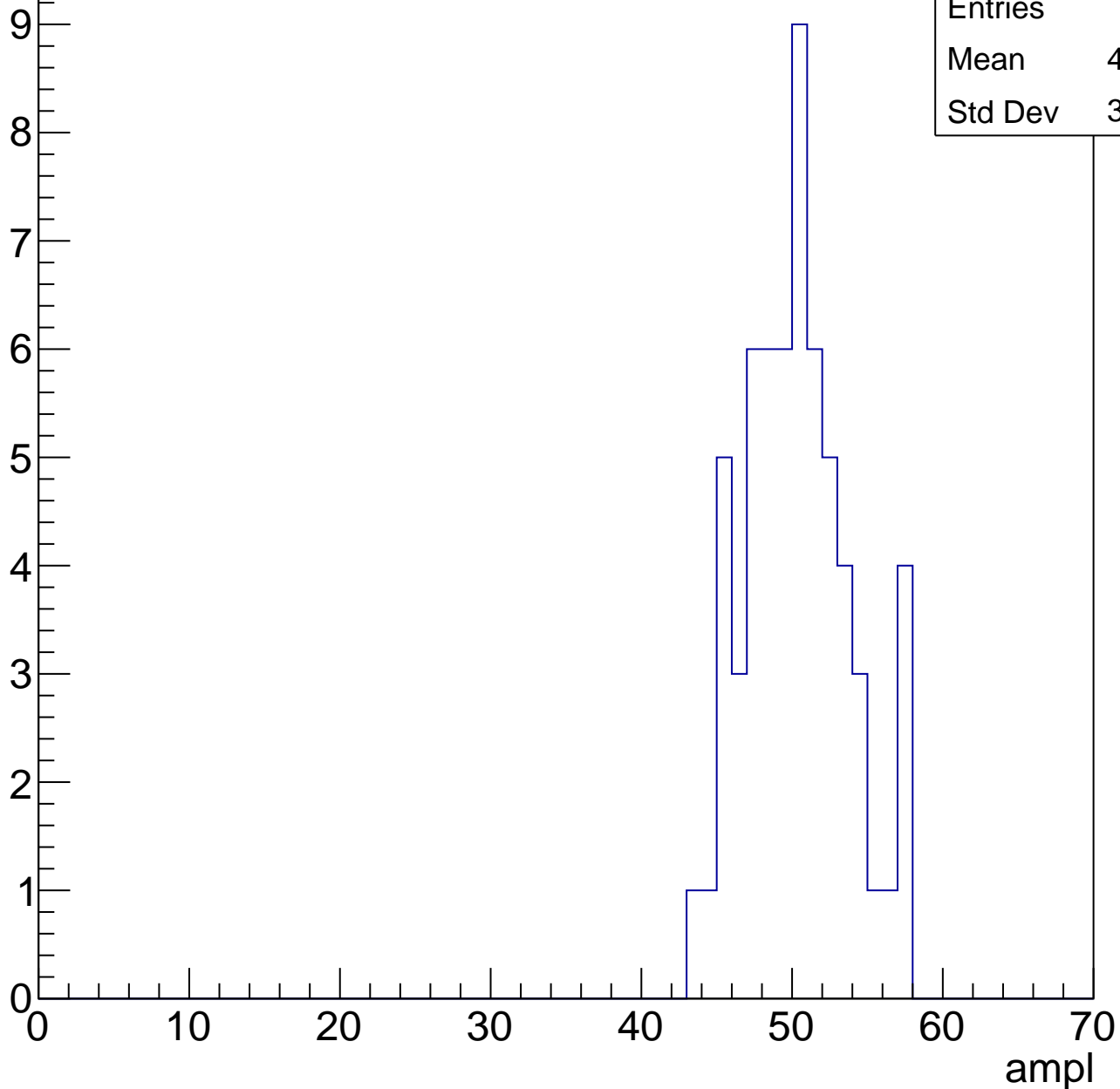
Gaus Width: 4.2818



B1L103S, U12-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

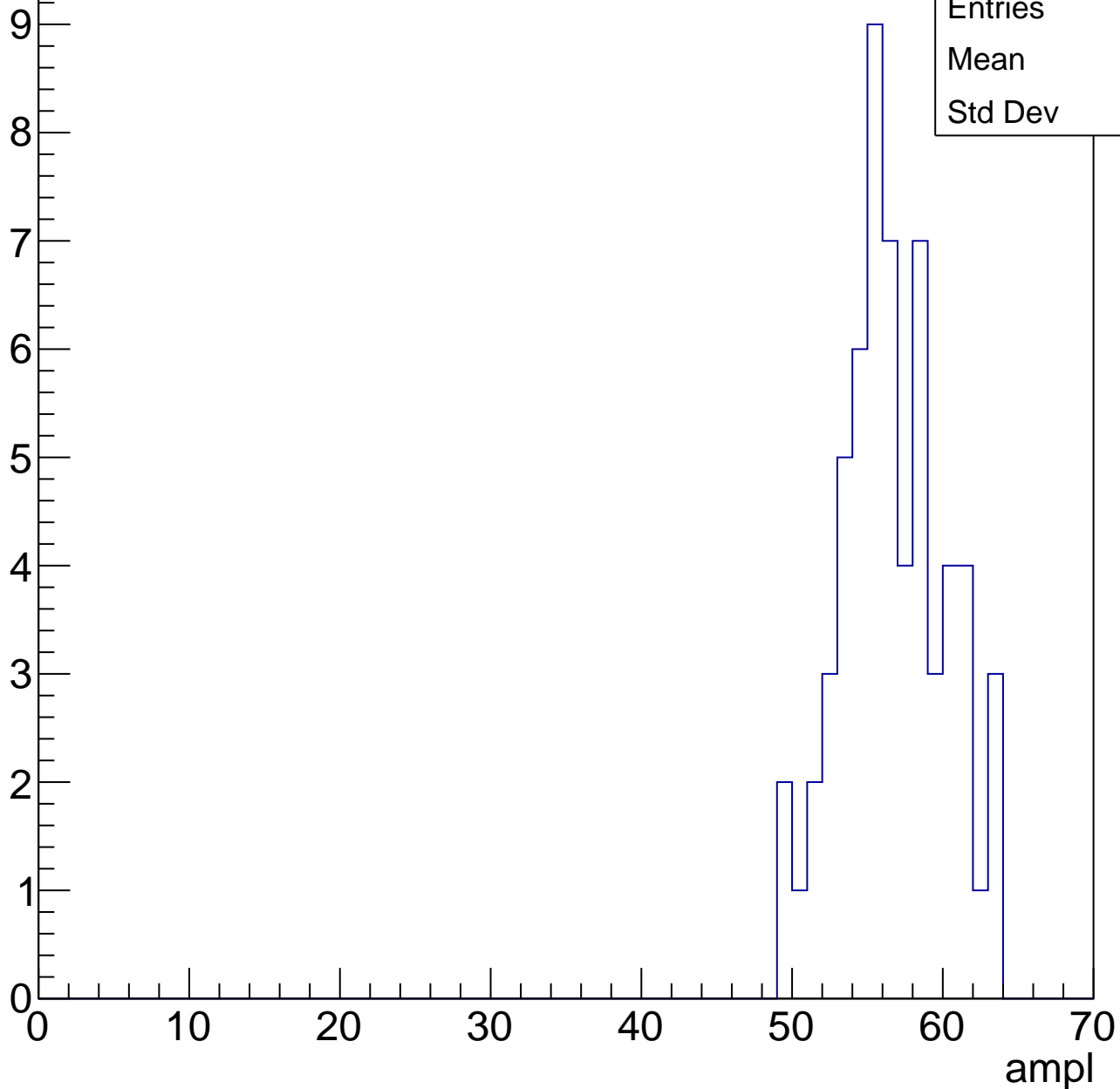
Entry



B1L103S, U12-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



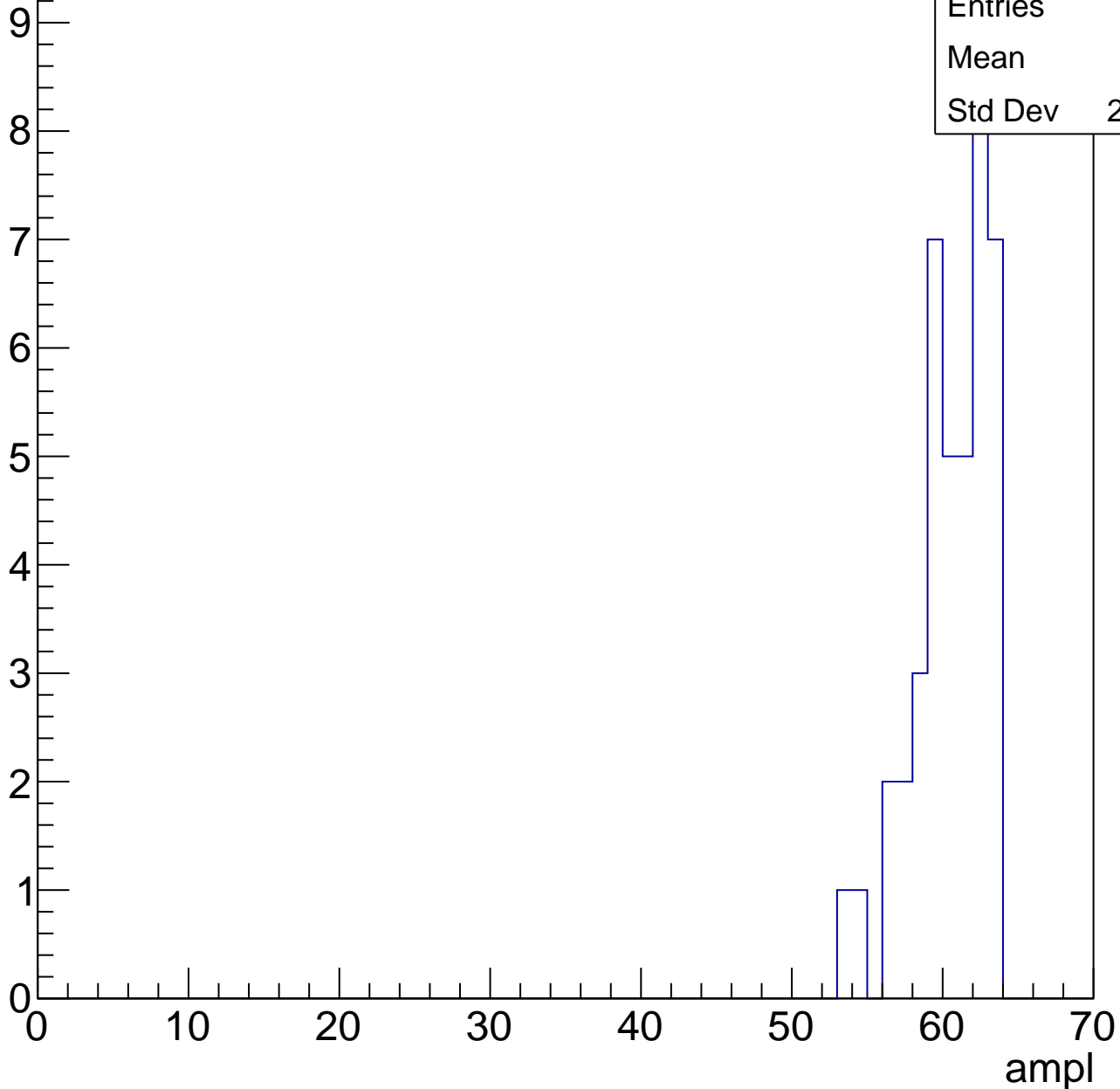
Entries	61
Mean	56.2
Std Dev	3.42

B1L103S, U12-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

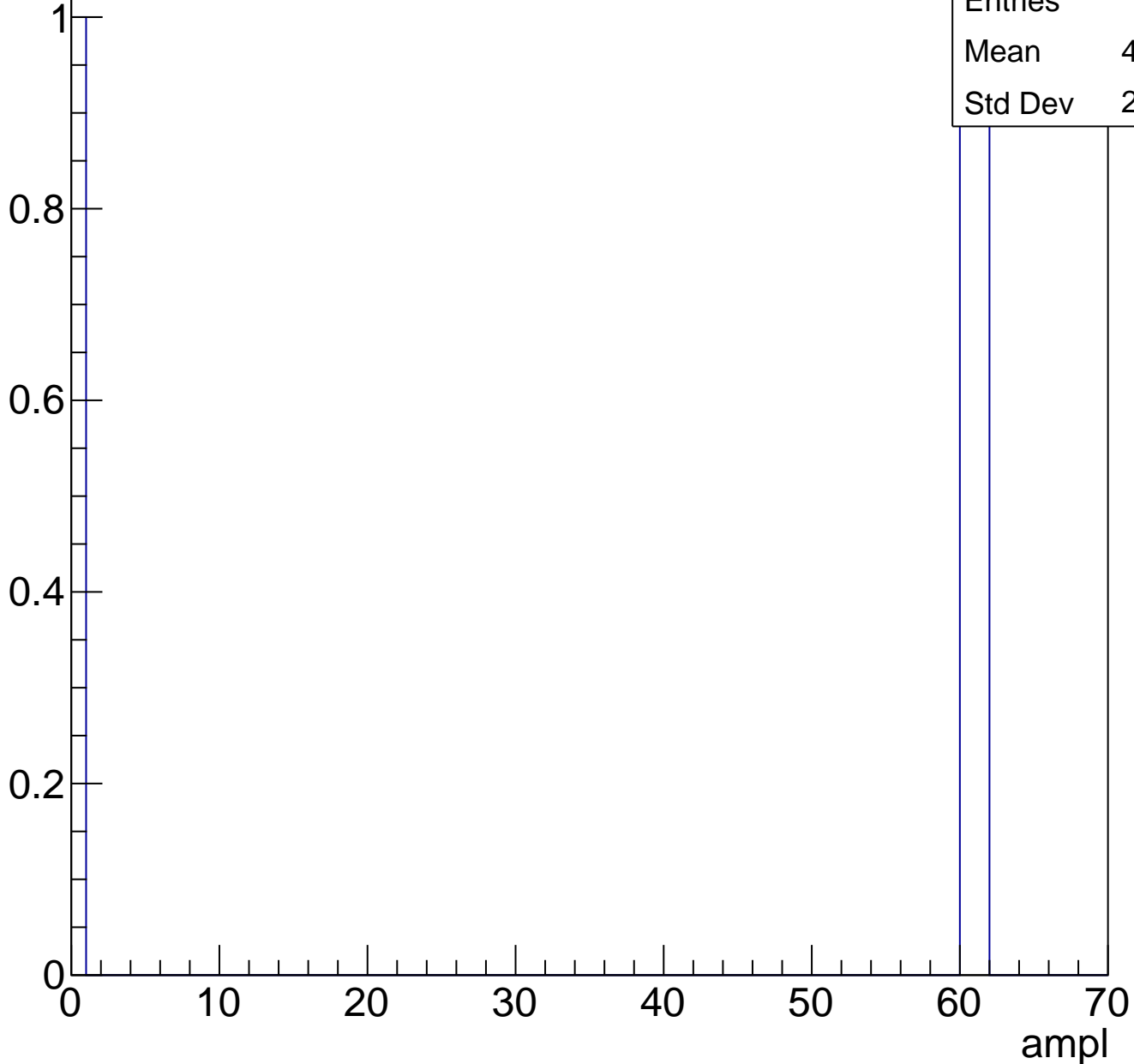
Entries	42
Mean	60.1
Std Dev	2.477



B1L103S, U12-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch23, adc0

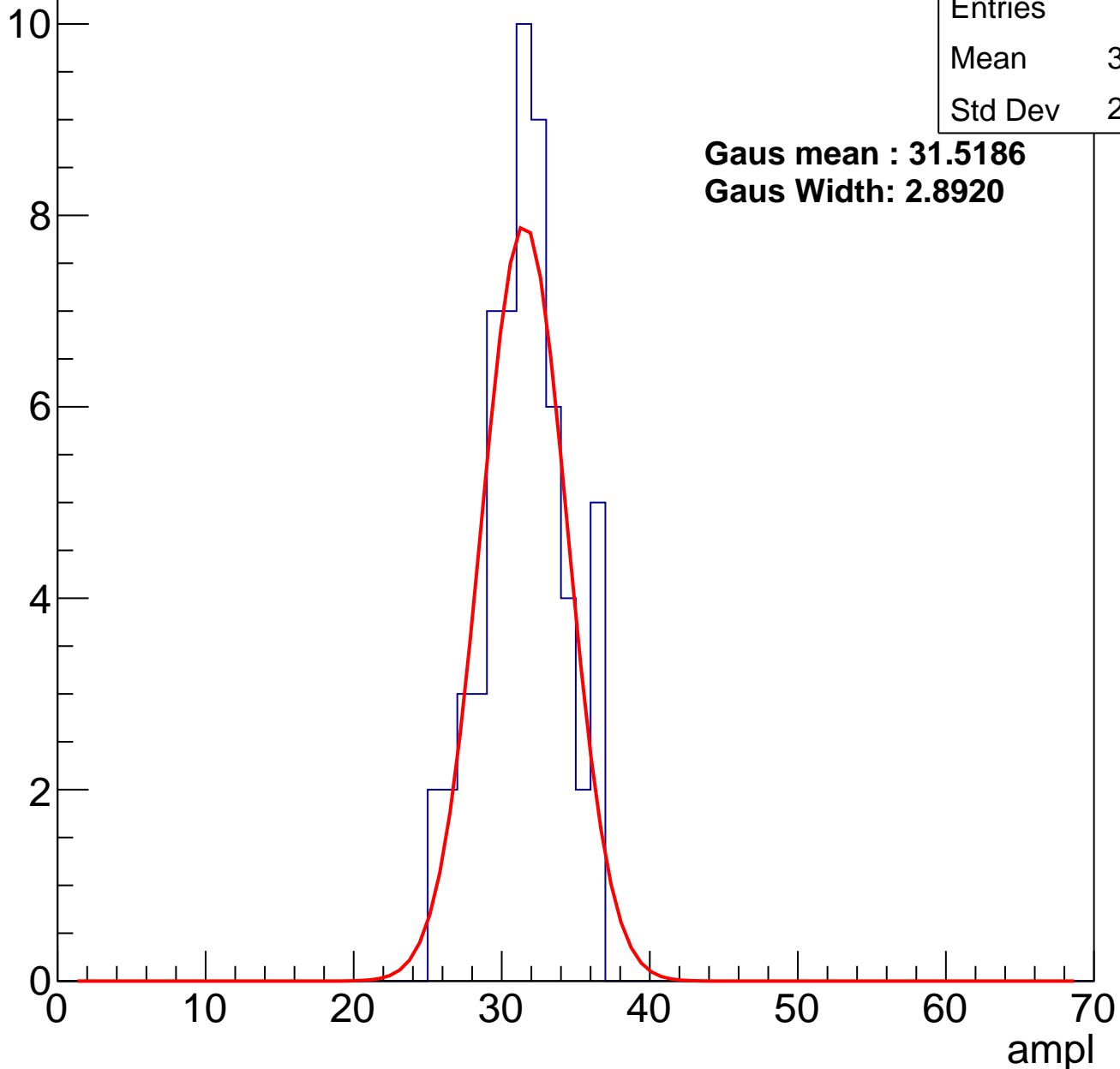
calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	31.03
Std Dev	2.763

Gaus mean : 31.5186

Gaus Width: 2.8920

Entry



B1L103S, U12-ch23, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	38.44
Std Dev	3.798

Gaus mean : 39.0418

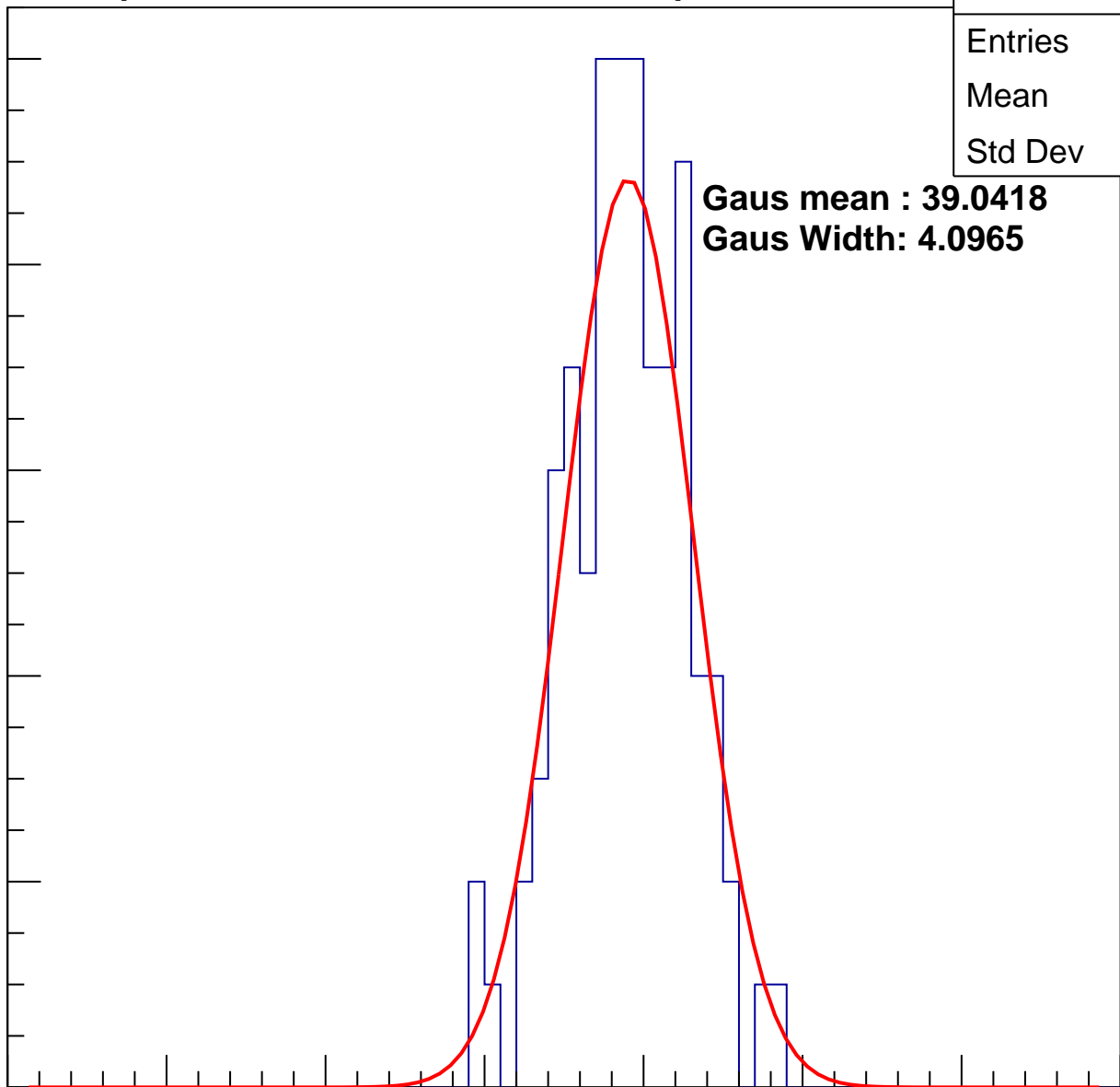
Gaus Width: 4.0965

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch23, adc2

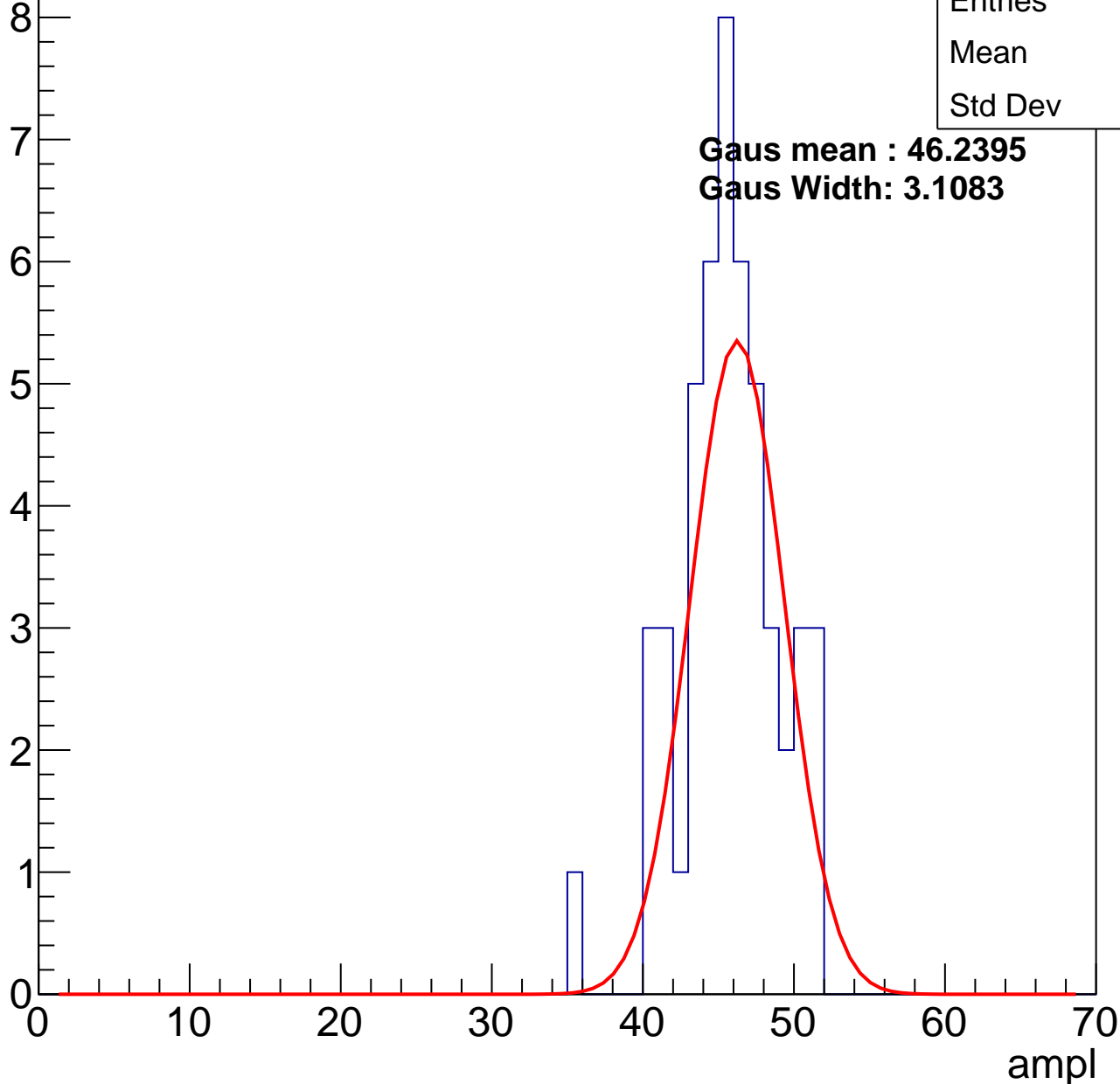
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	45.2
Std Dev	3.27

Gaus mean : 46.2395

Gaus Width: 3.1083

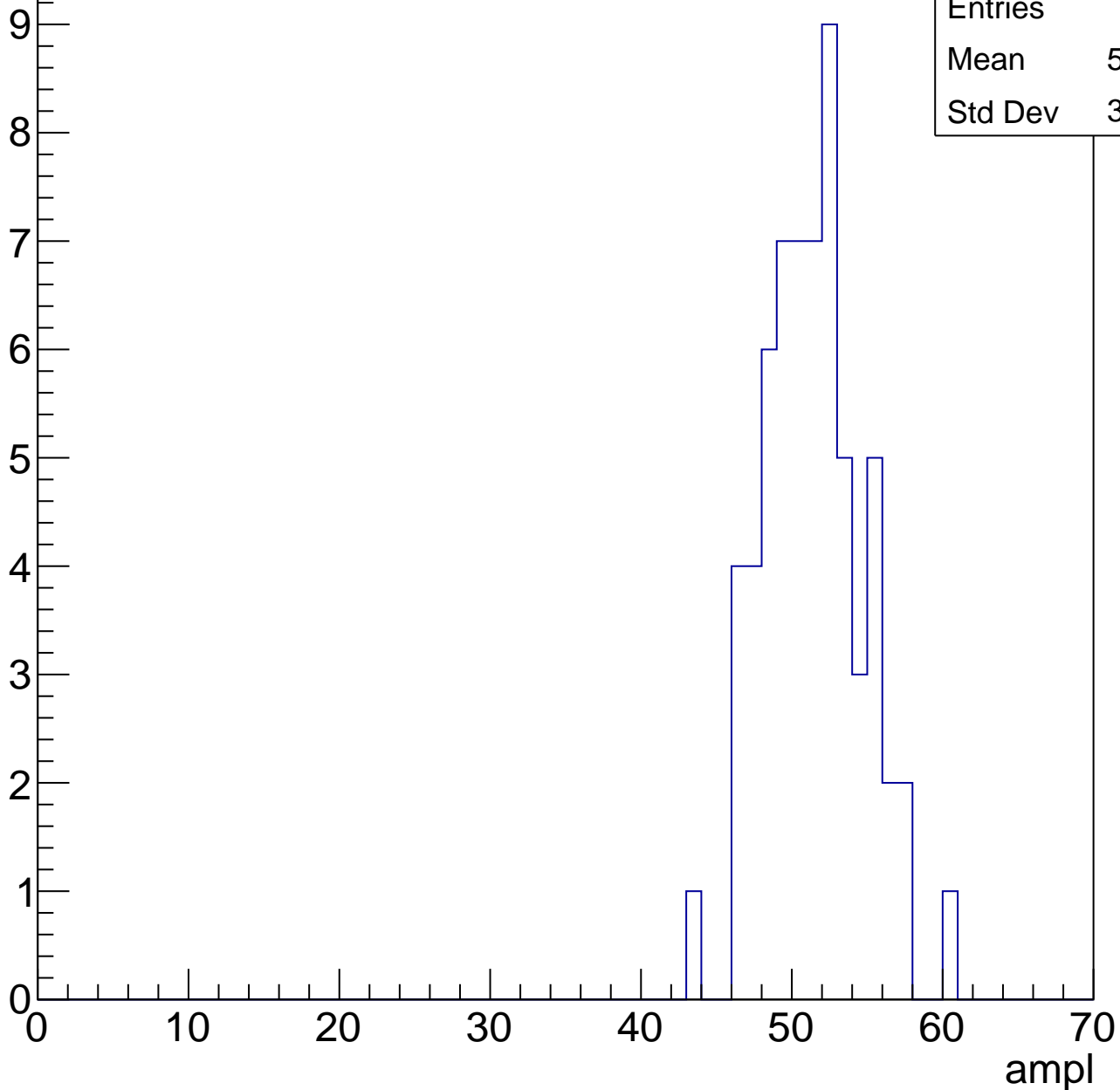


B1L103S, U12-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

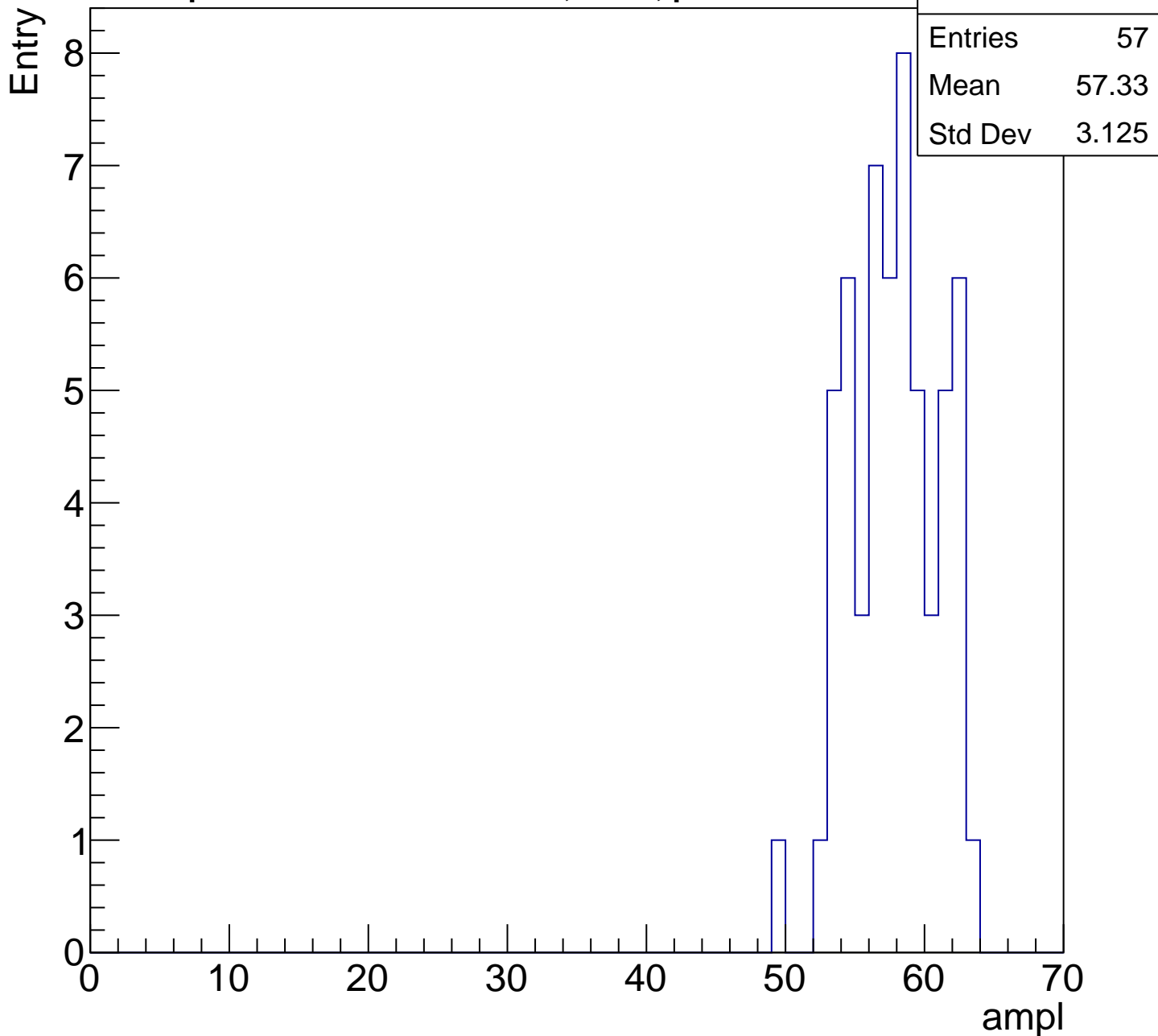
Entry

Entries	63
Mean	50.94
Std Dev	3.226



B1L103S, U12-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

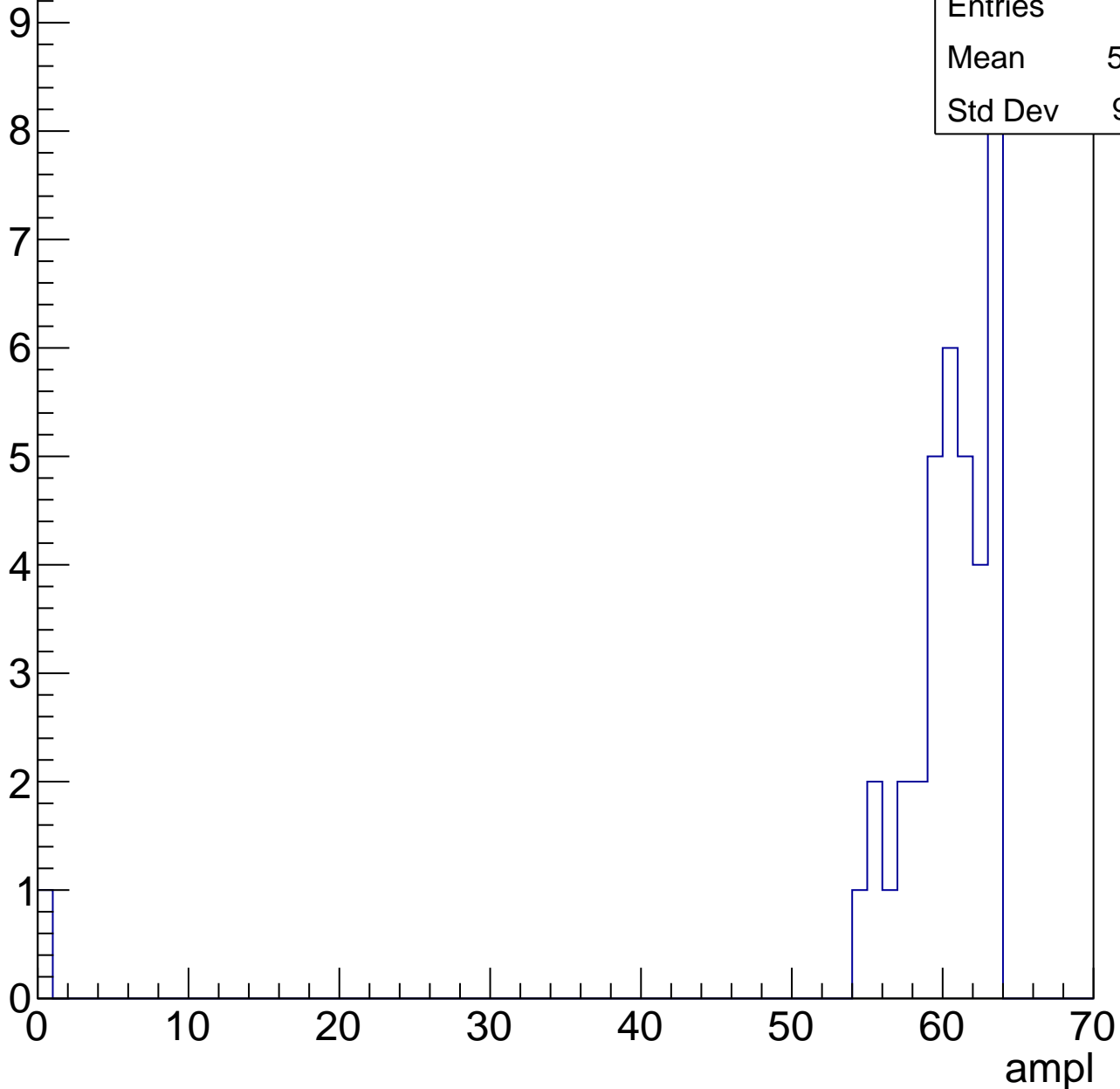


B1L103S, U12-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.55
Std Dev	9.941



B1L103S, U12-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

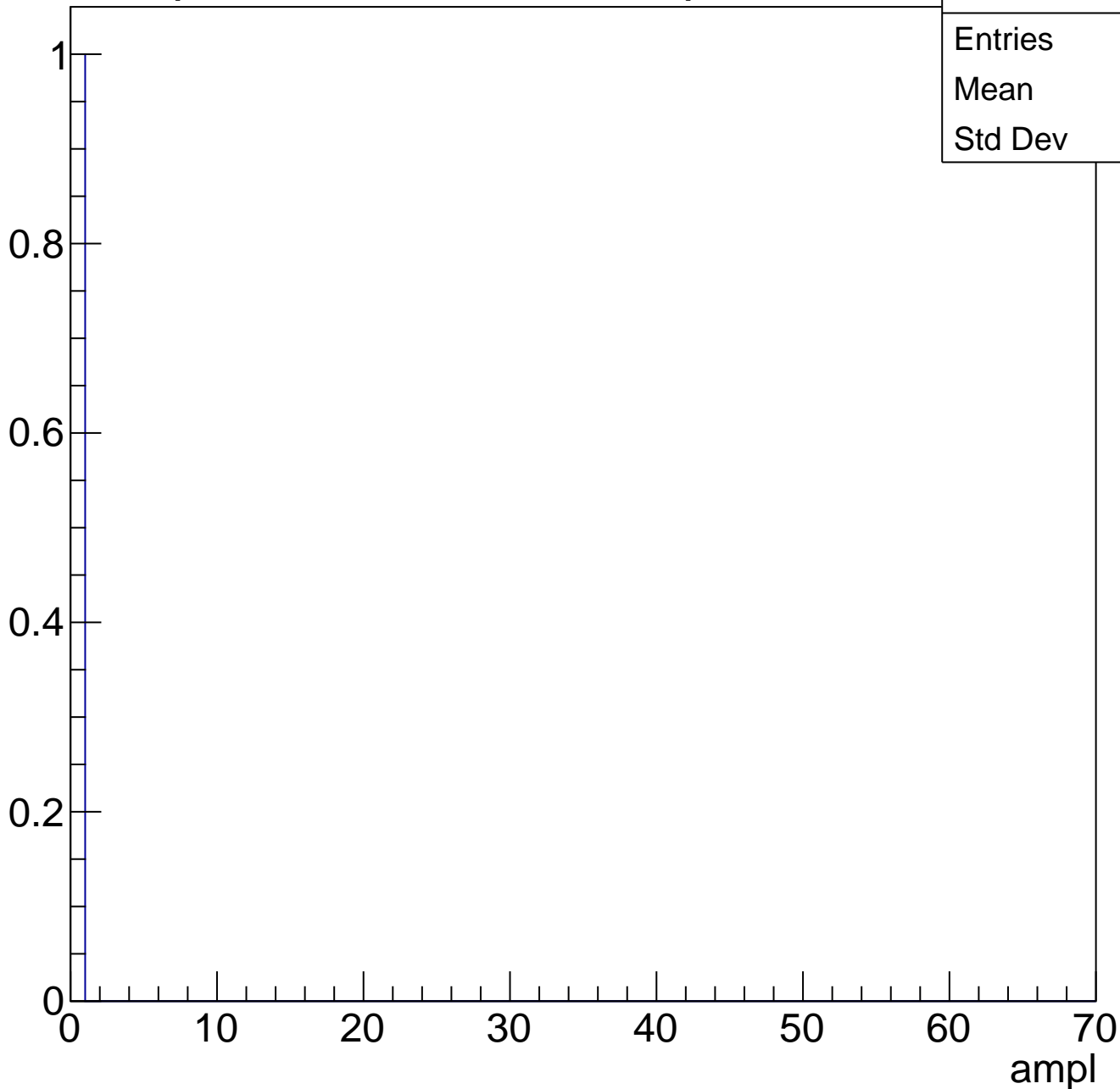


Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch24, adc0

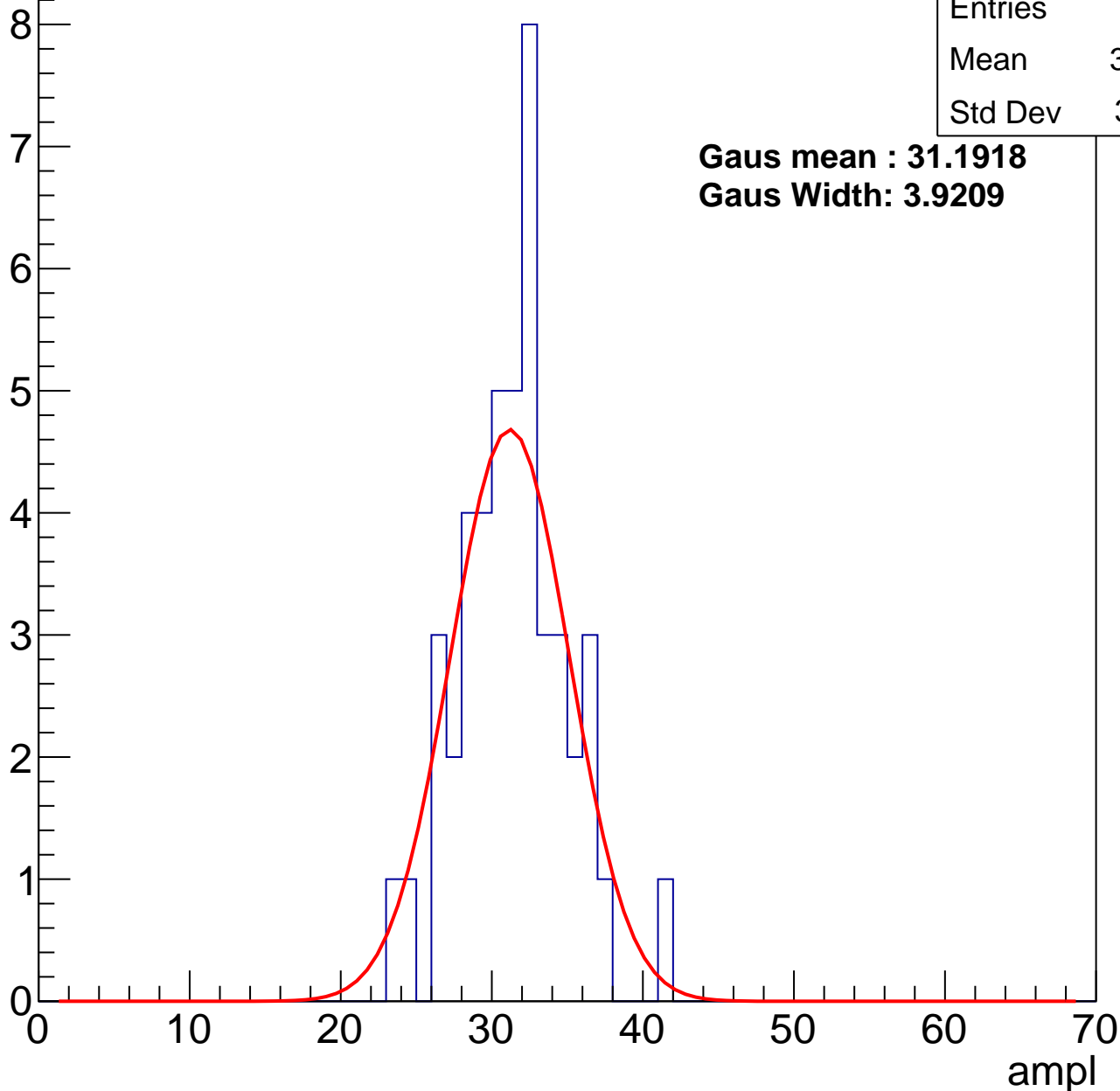
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	30.98
Std Dev	3.511

Gaus mean : 31.1918

Gaus Width: 3.9209



B1L103S, U12-ch24, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	37.05
Std Dev	3.352

Gaus mean : 37.7800

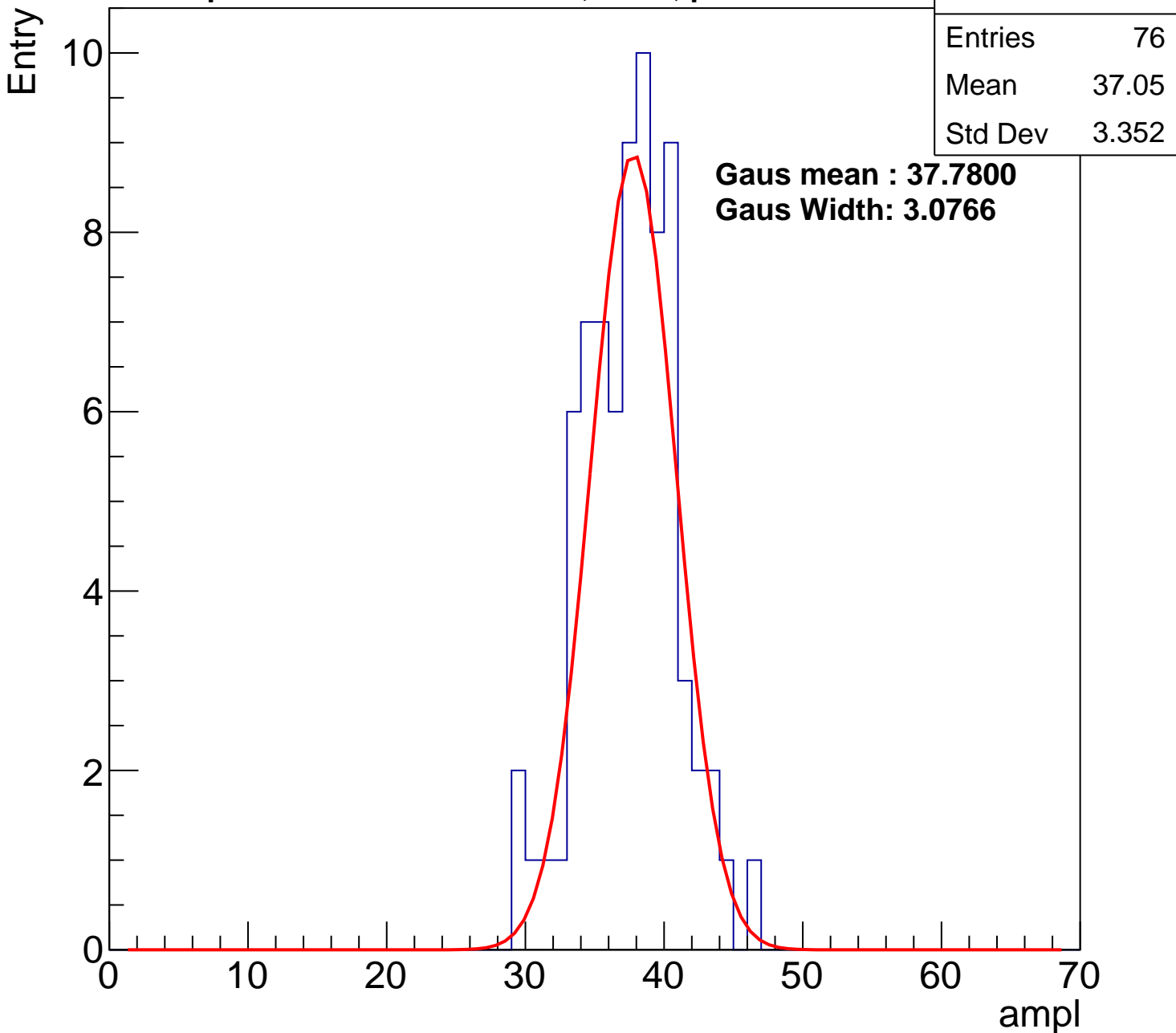
Gaus Width: 3.0766

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch24, adc2

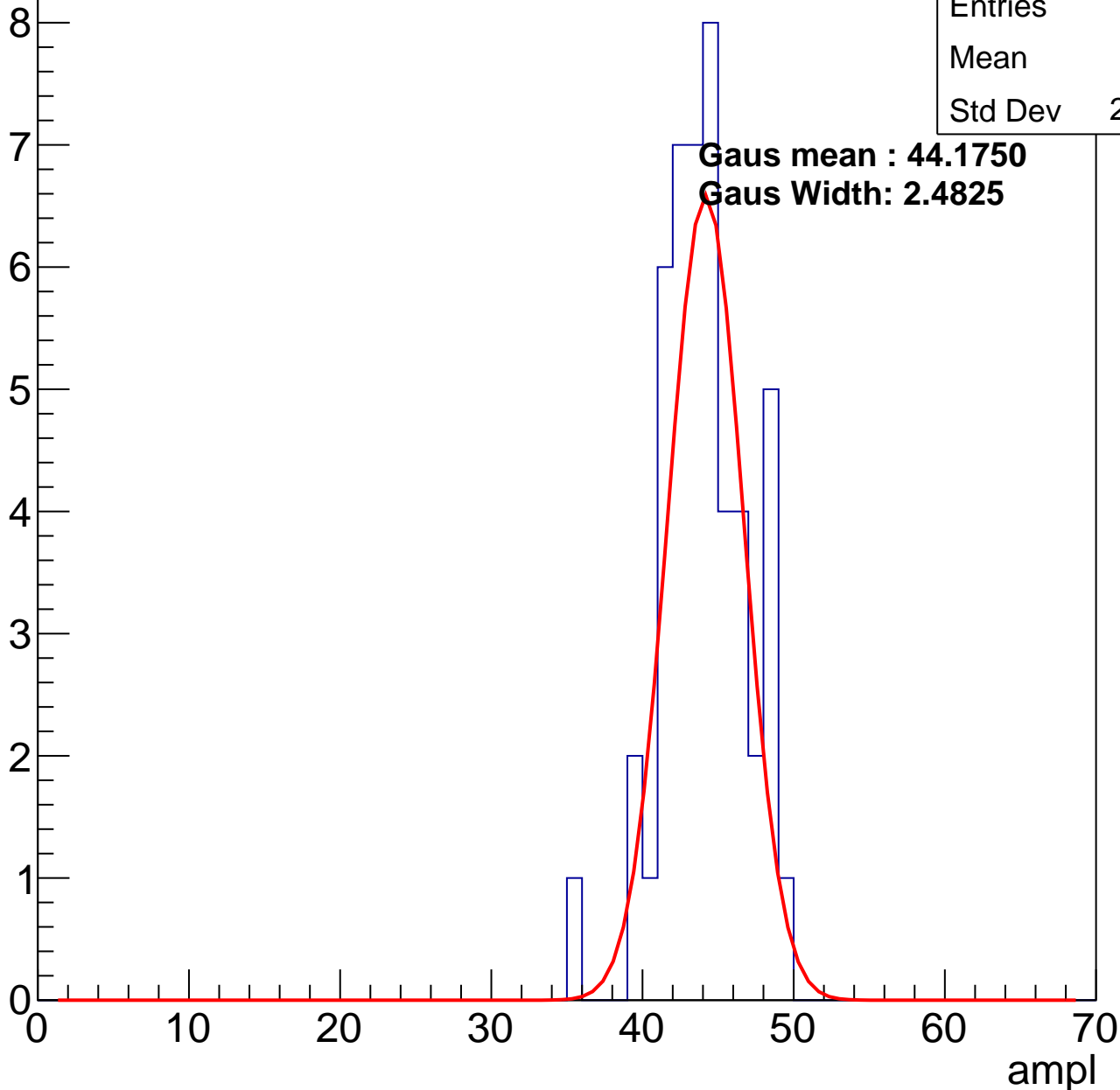
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	43.6
Std Dev	2.782

Gaus mean : 44.1750

Gaus Width: 2.4825

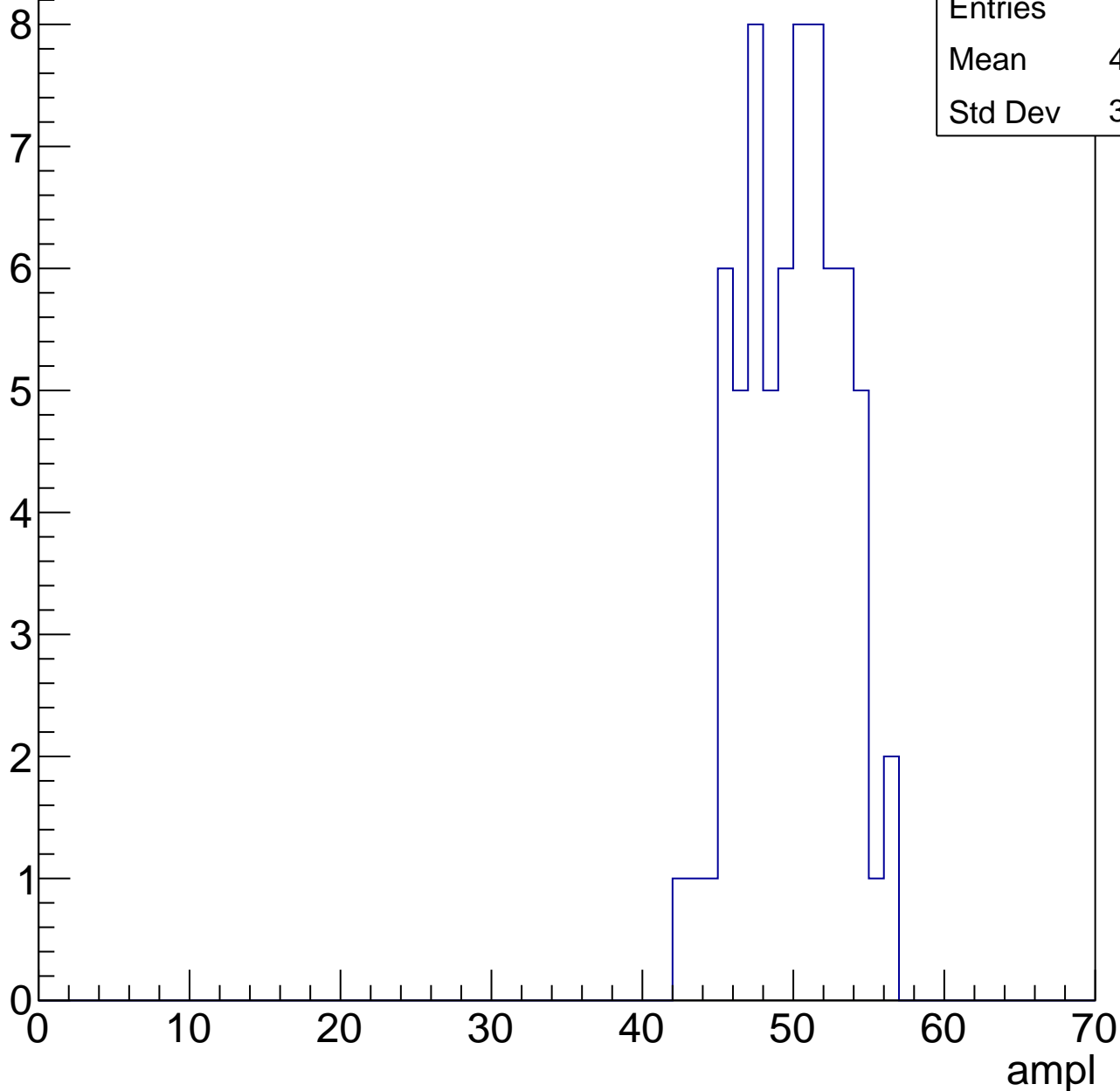


B1L103S, U12-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.48
Std Dev	3.233

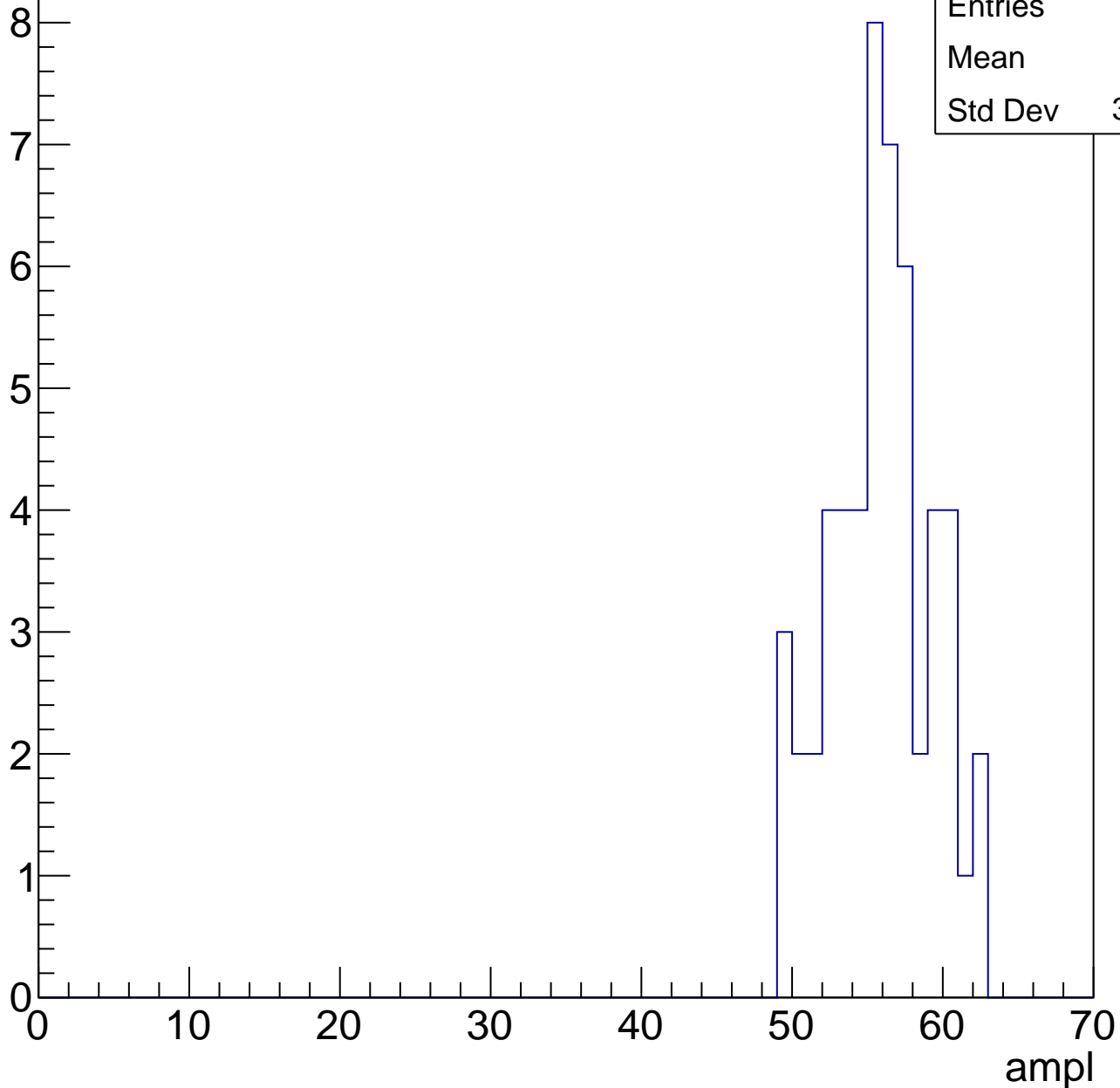


B1L103S, U12-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

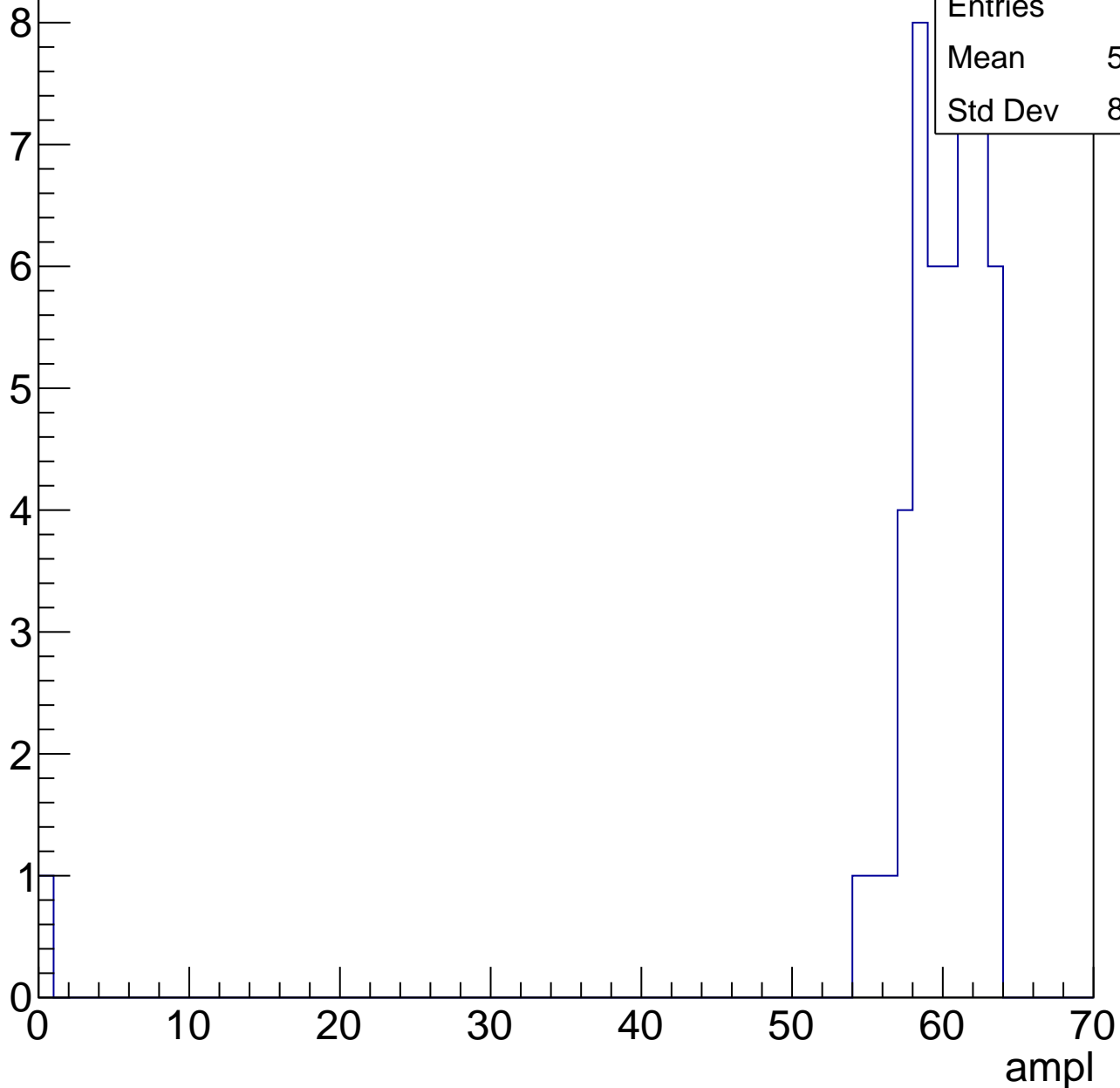
Entries	53
Mean	55.4
Std Dev	3.321



B1L103S, U12-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

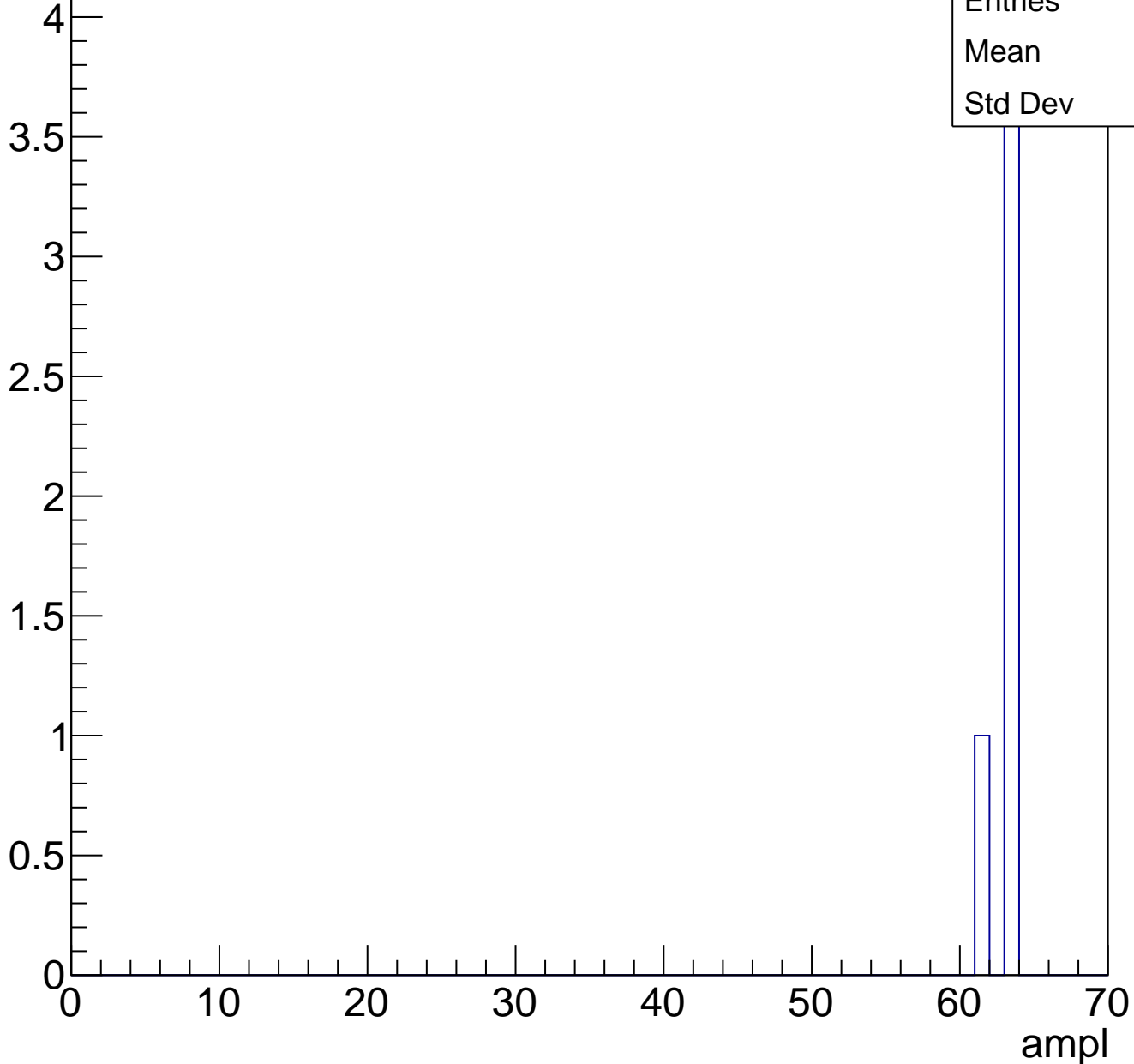
Entry



B1L103S, U12-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch25, adc0

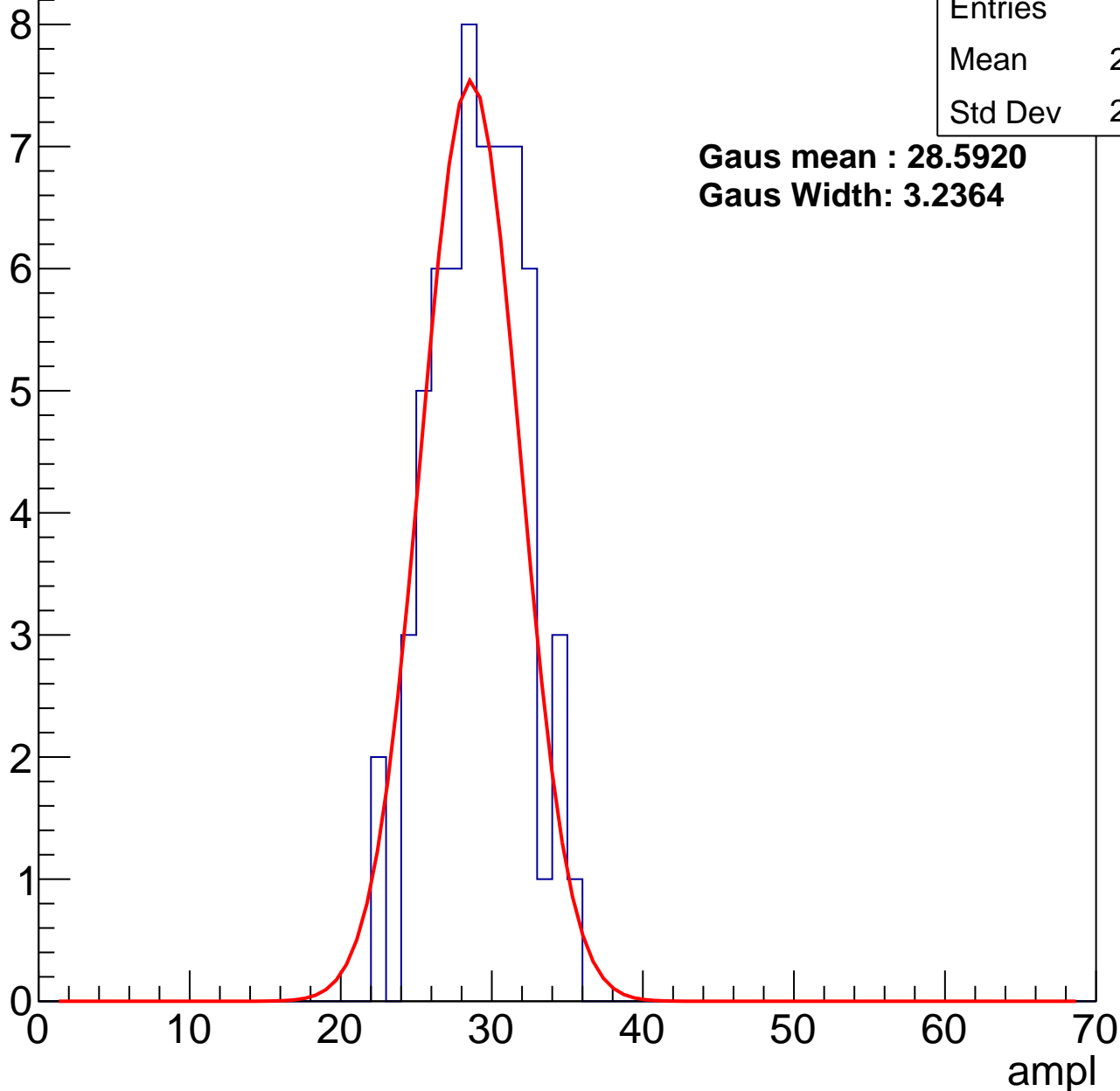
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.63
Std Dev	2.969

Gaus mean : 28.5920

Gaus Width: 3.2364



B1L103S, U12-ch25, adc1

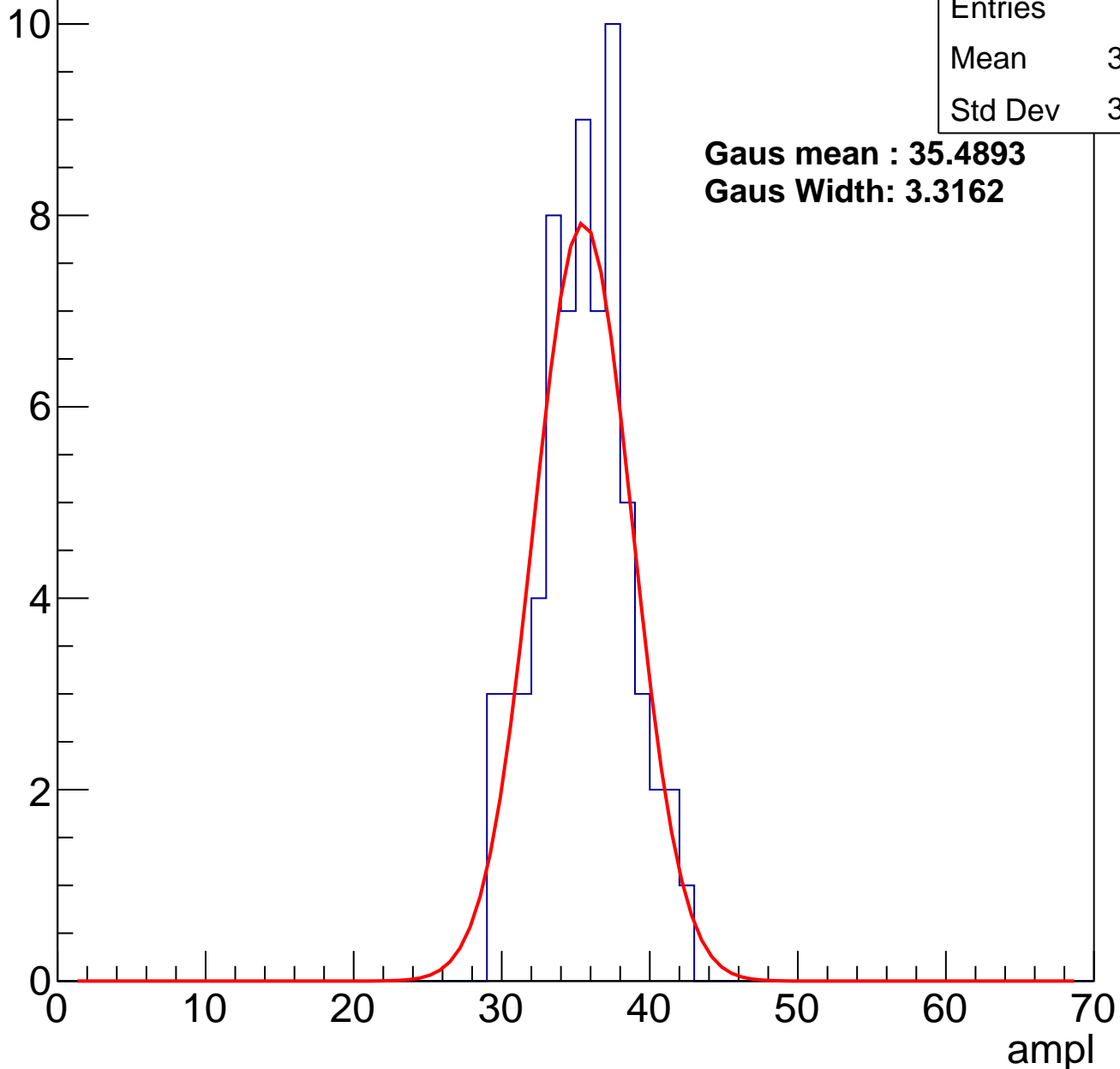
calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	35.04
Std Dev	3.034

Gaus mean : 35.4893

Gaus Width: 3.3162

Entry



B1L103S, U12-ch25, adc2

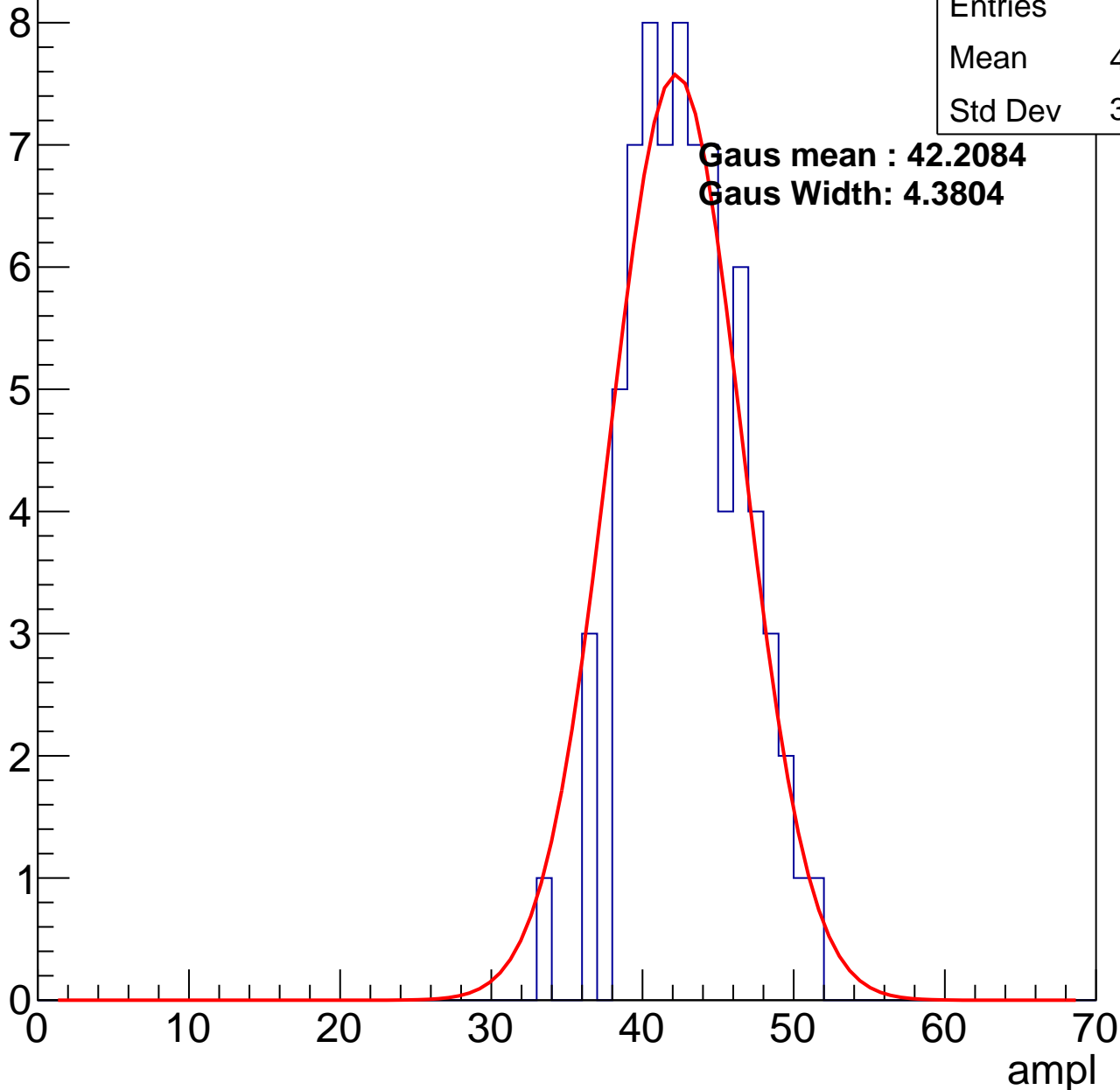
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.47
Std Dev	3.625

Gaus mean : 42.2084

Gaus Width: 4.3804

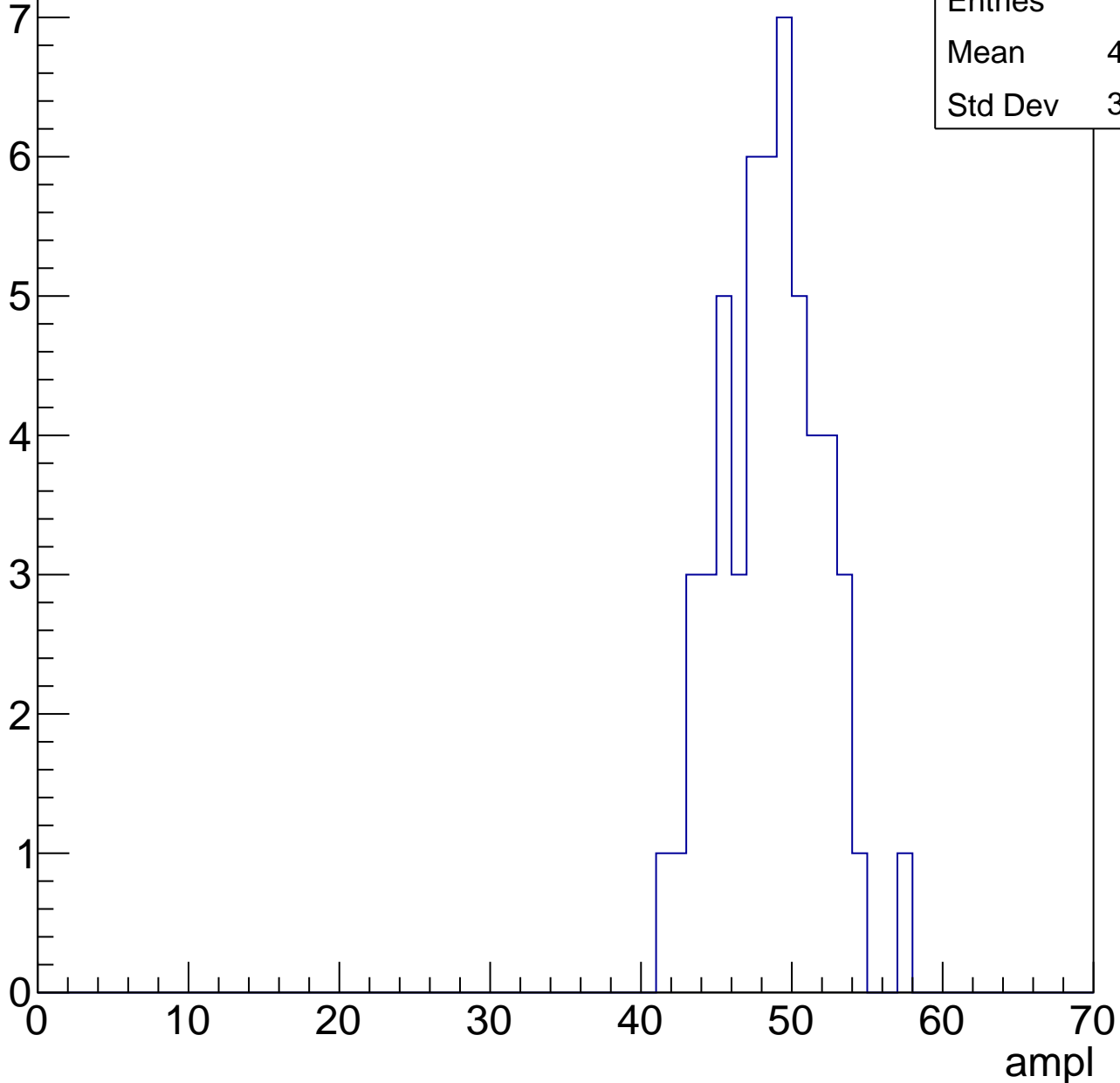


B1L103S, U12-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.15
Std Dev	3.333

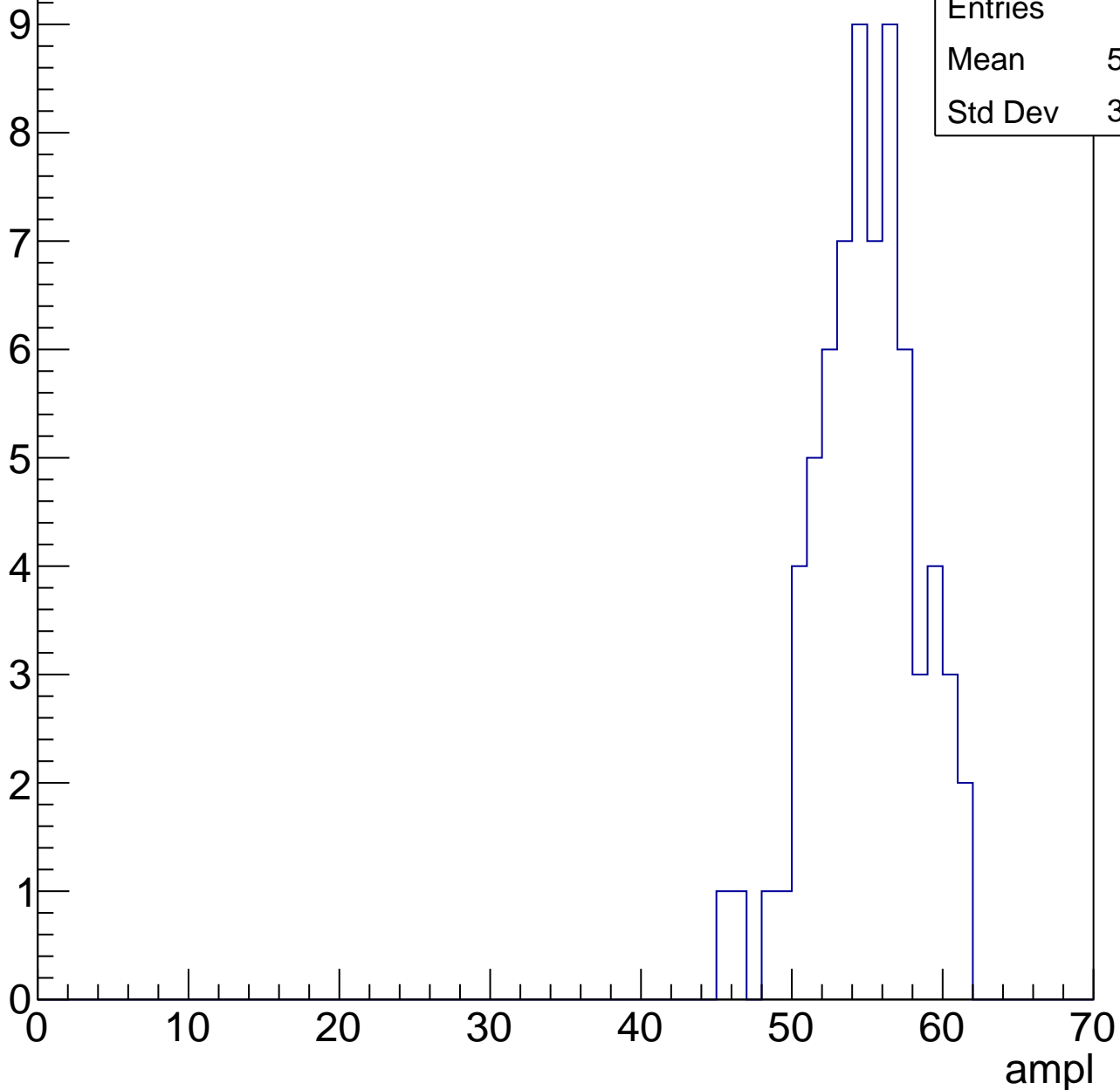


B1L103S, U12-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	54.42
Std Dev	3.373



B1L103S, U12-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

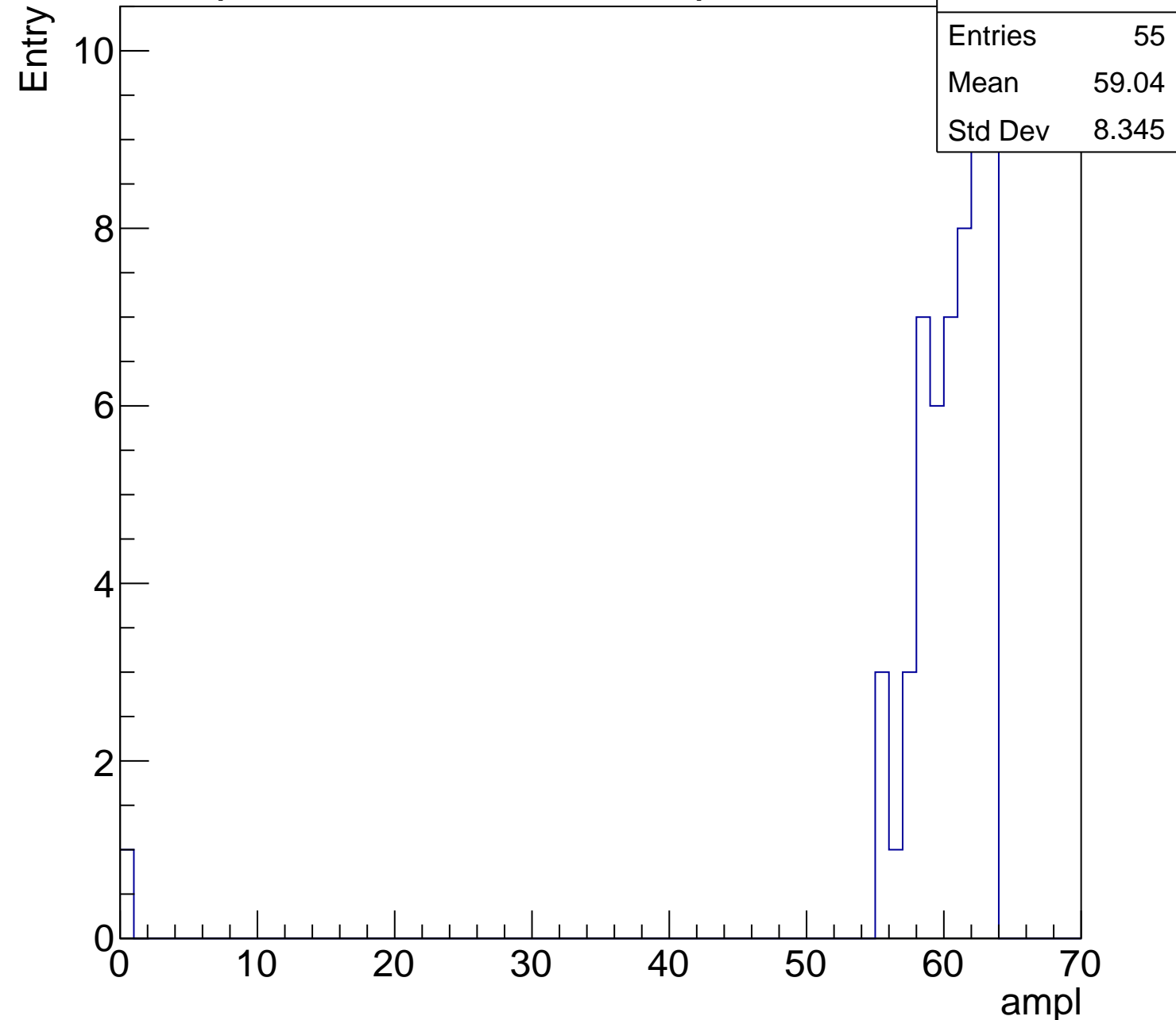
40

50

60

ampl

Entries	55
Mean	59.04
Std Dev	8.345



B1L103S, U12-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U12-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch26, adc0

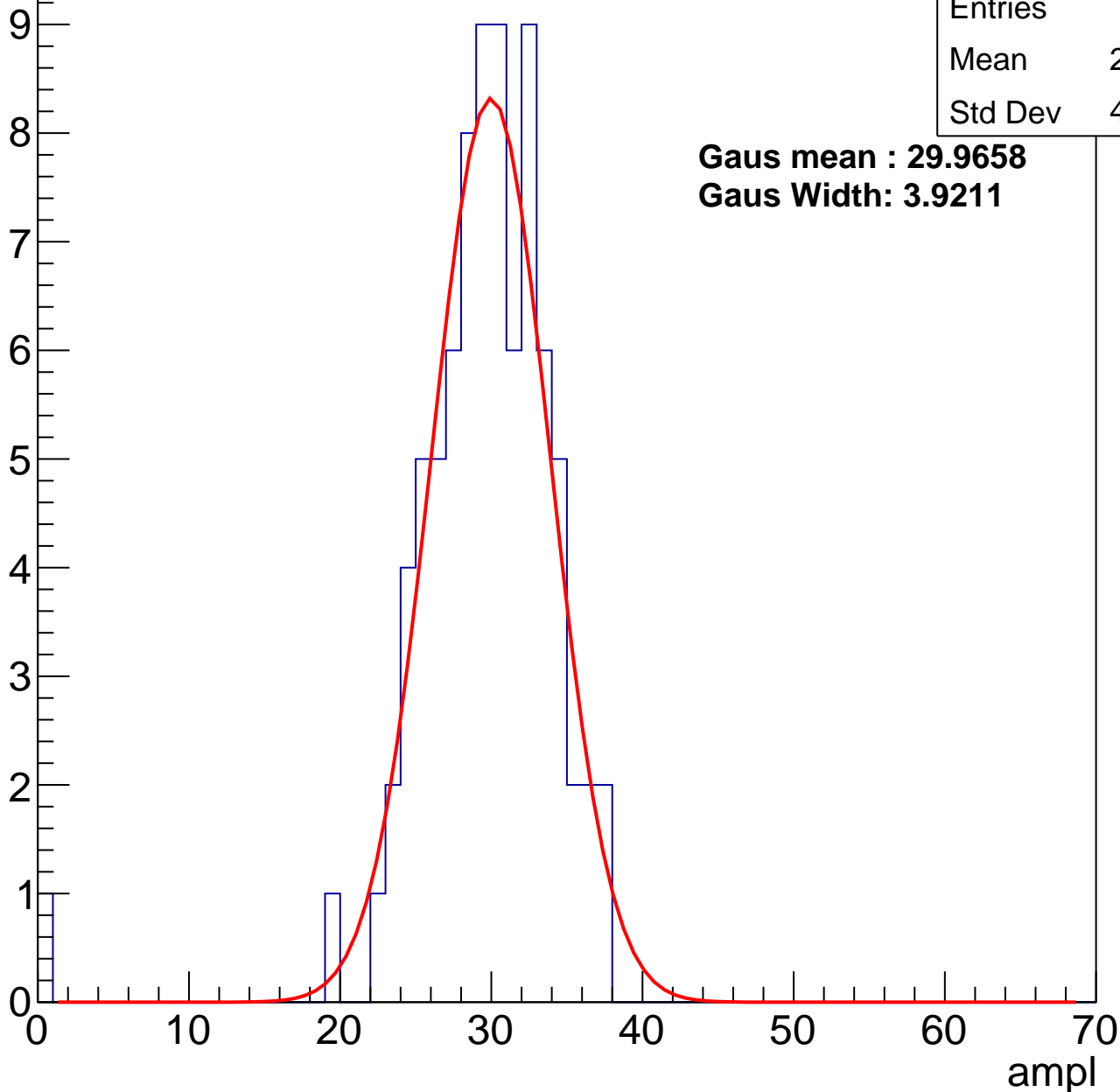
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	29.07
Std Dev	4.849

Gaus mean : 29.9658

Gaus Width: 3.9211



B1L103S, U12-ch26, adc1

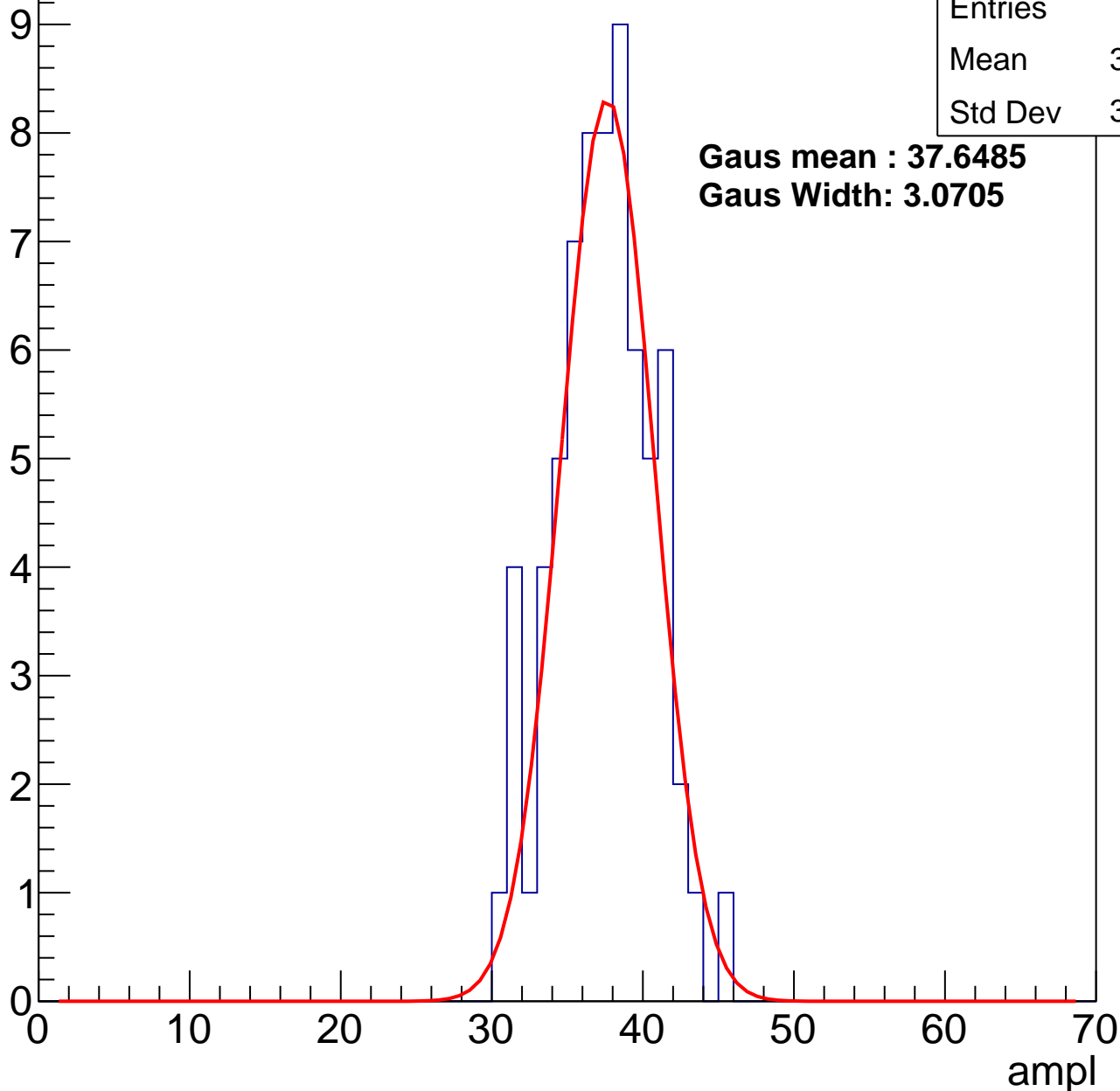
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.93
Std Dev	3.178

Gaus mean : 37.6485

Gaus Width: 3.0705



B1L103S, U12-ch26, adc2

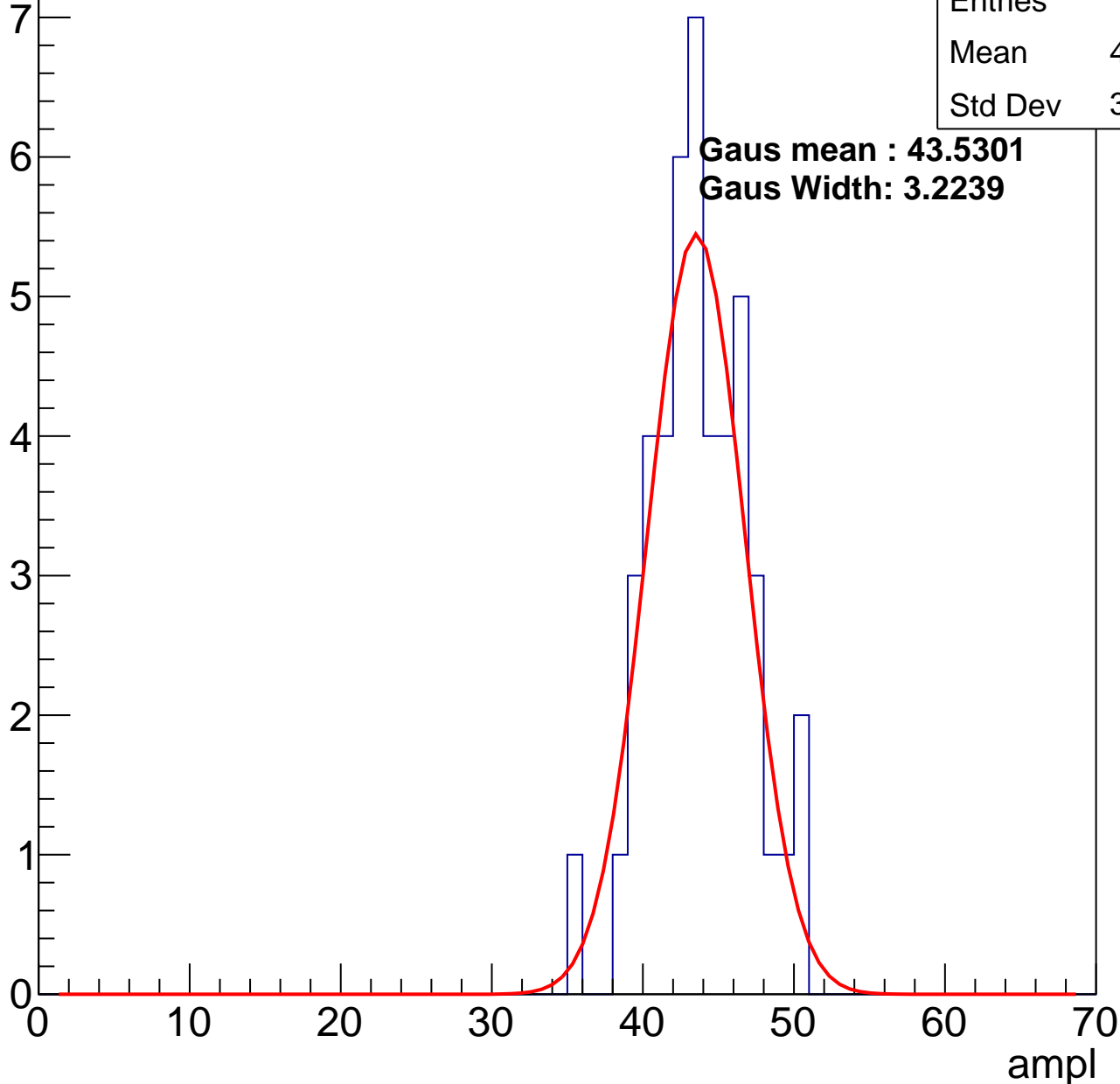
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	43.28
Std Dev	3.174

Gaus mean : 43.5301

Gaus Width: 3.2239

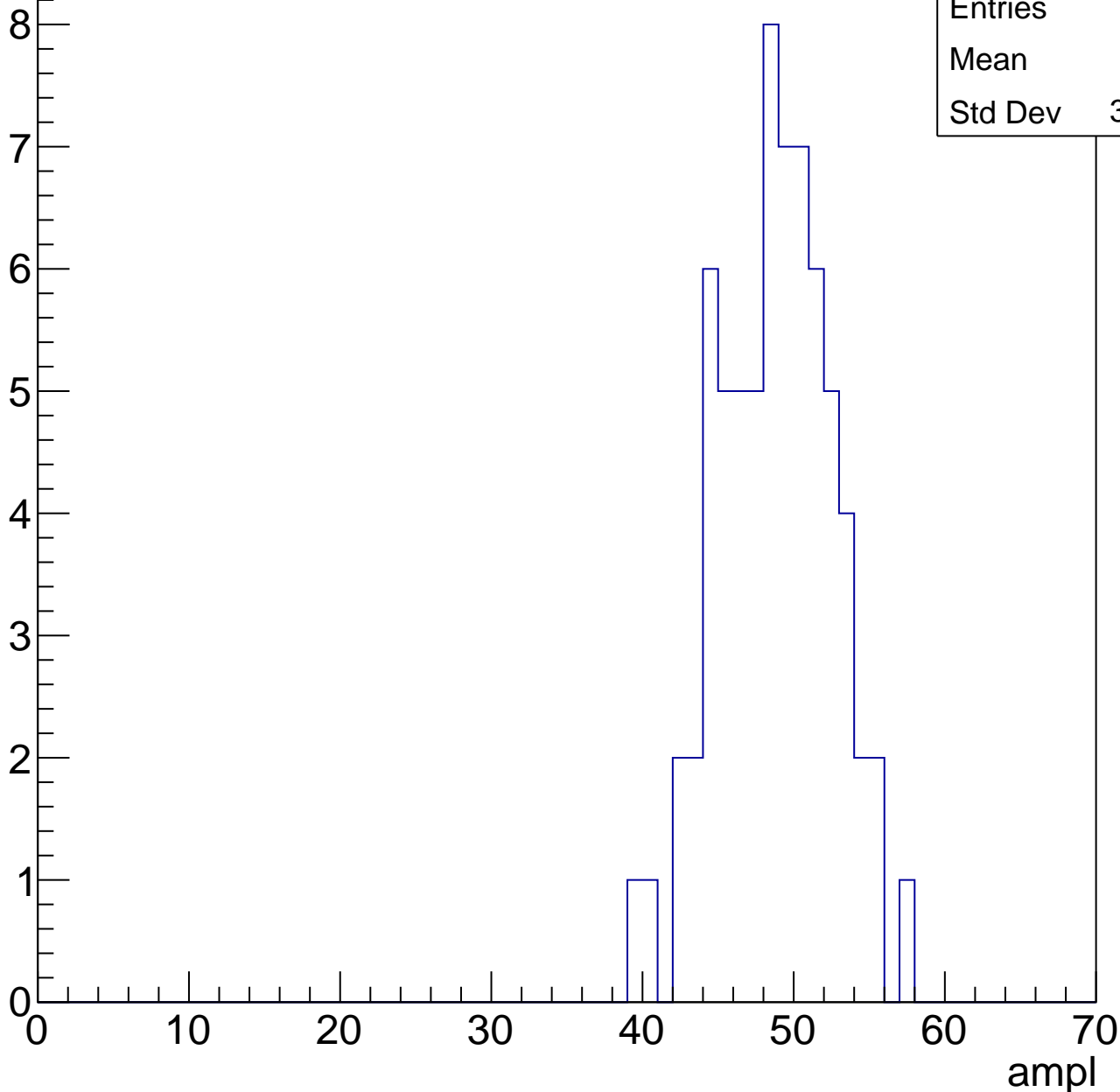


B1L103S, U12-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.3
Std Dev	3.704

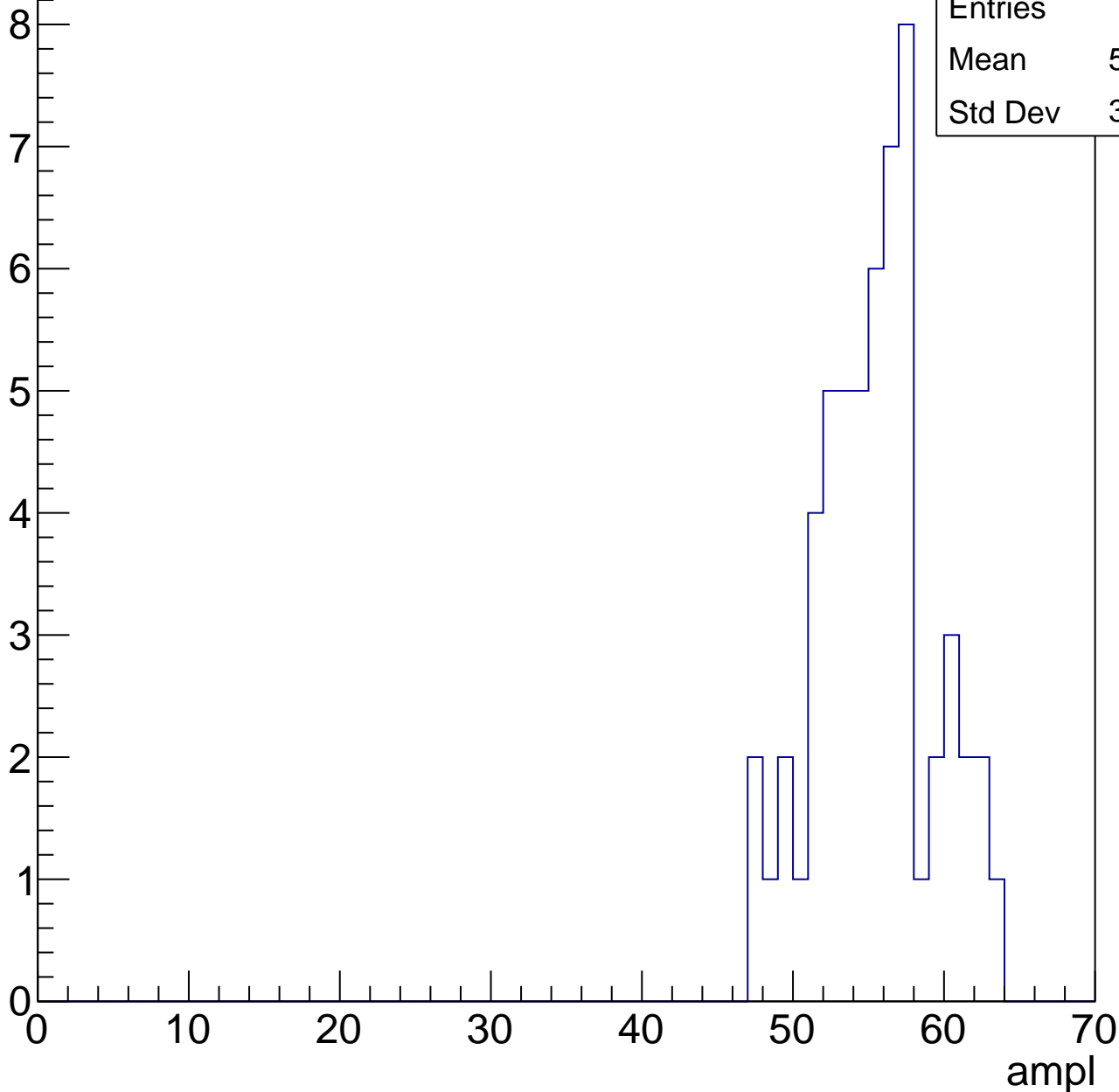


B1L103S, U12-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.95
Std Dev	3.739

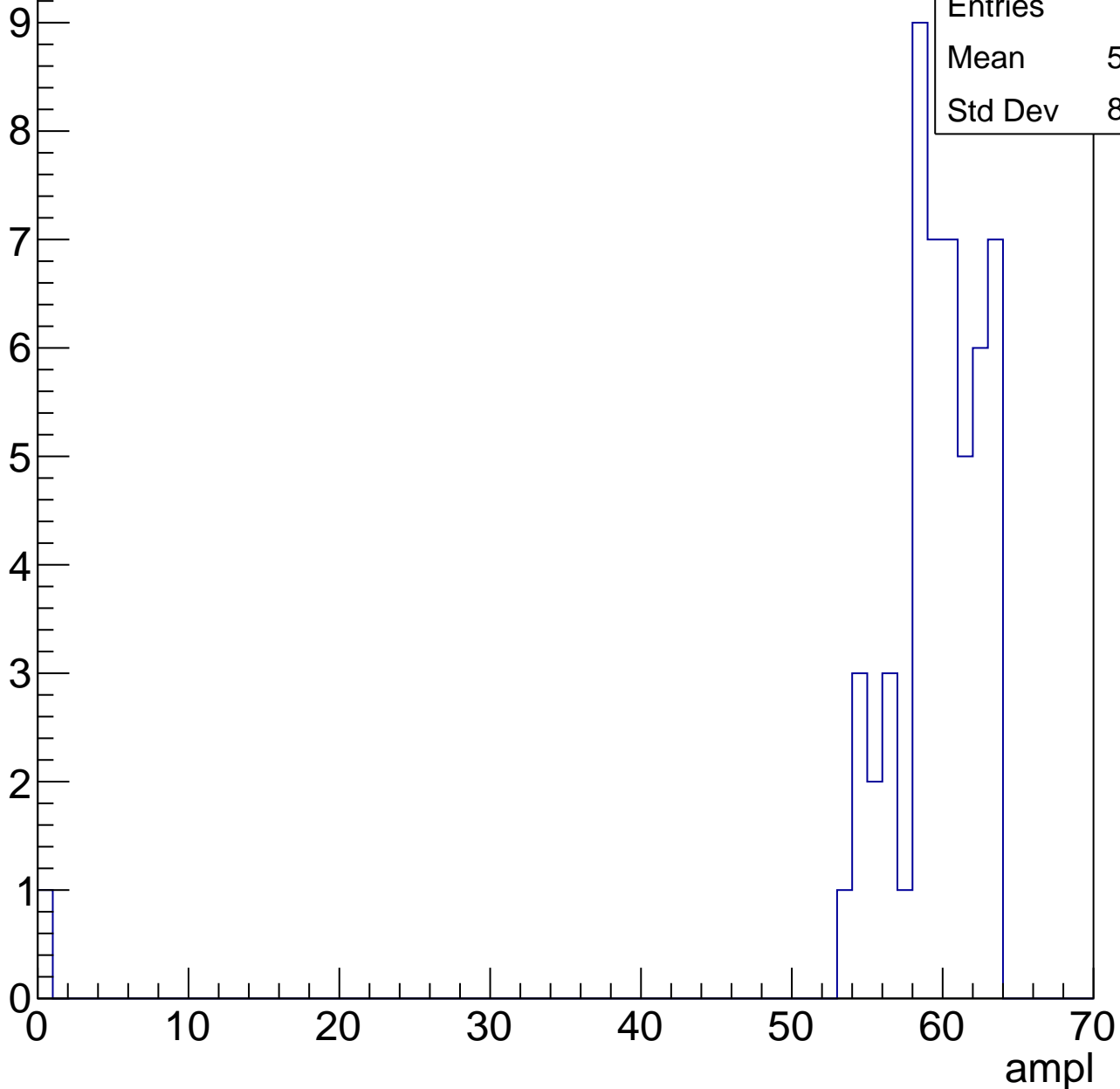


B1L103S, U12-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

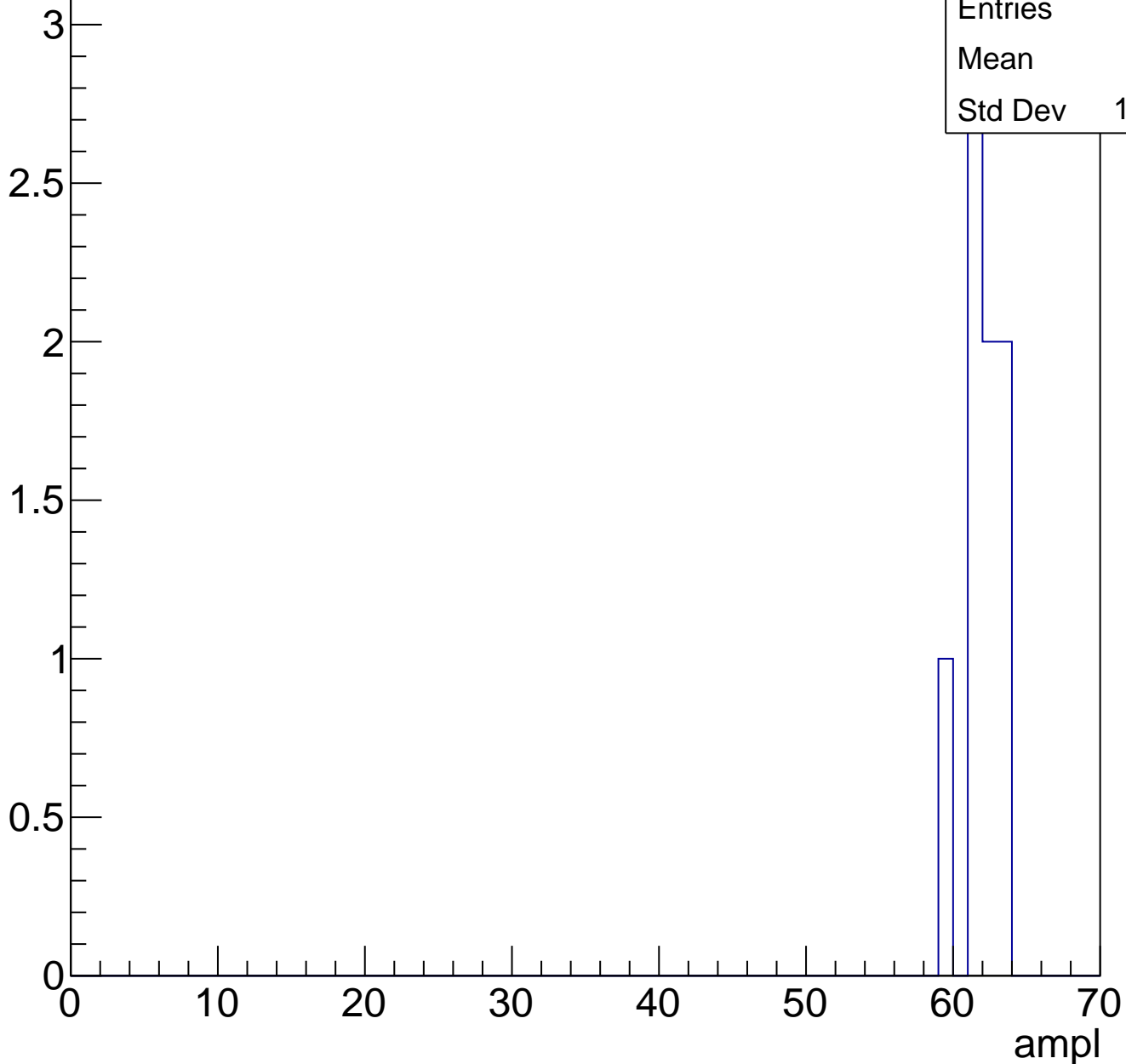
Entries	52
Mean	58.13
Std Dev	8.569



B1L103S, U12-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch27, adc0

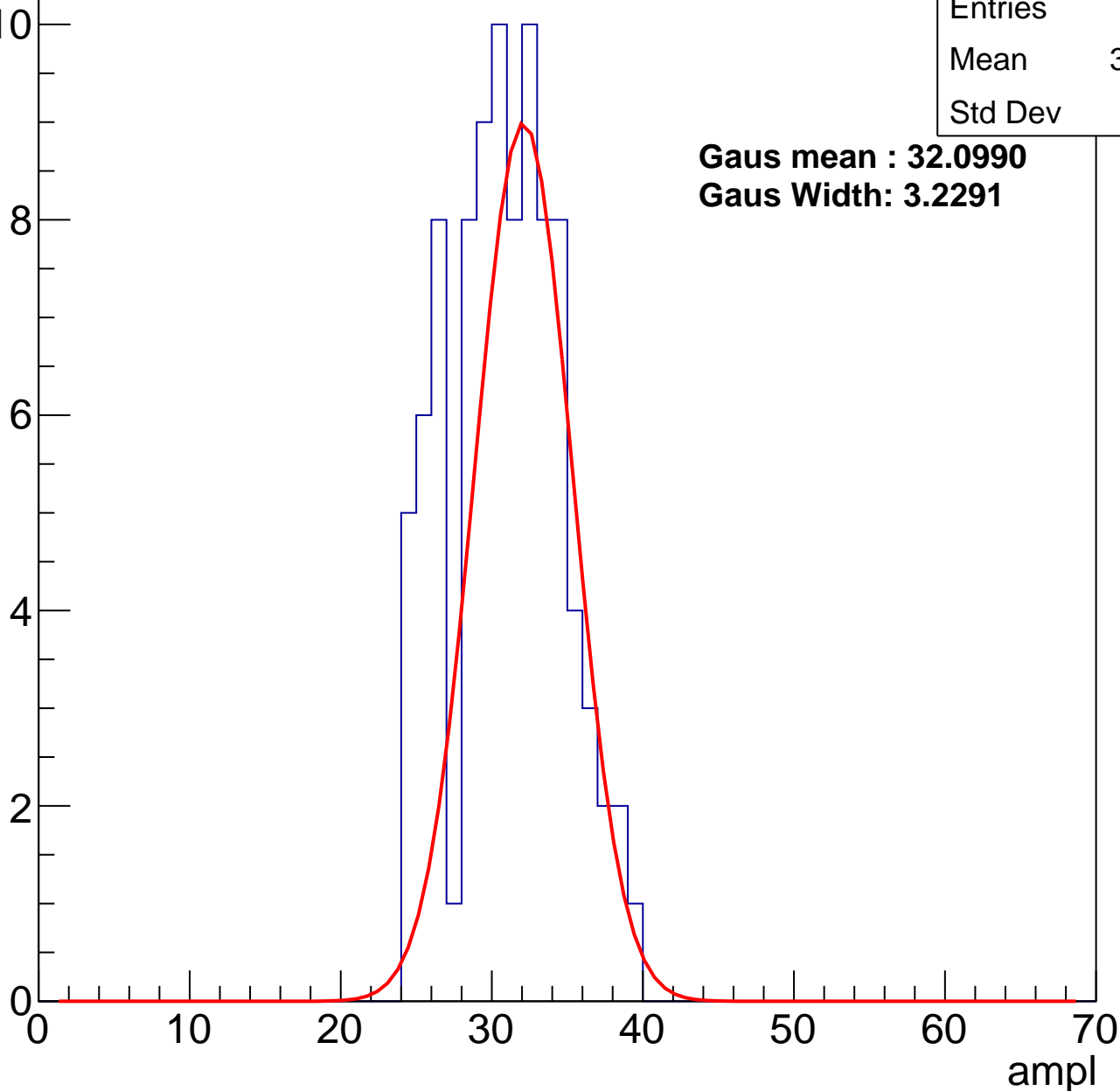
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	93
Mean	30.44
Std Dev	3.67

Gaus mean : 32.0990

Gaus Width: 3.2291



B1L103S, U12-ch27, adc1

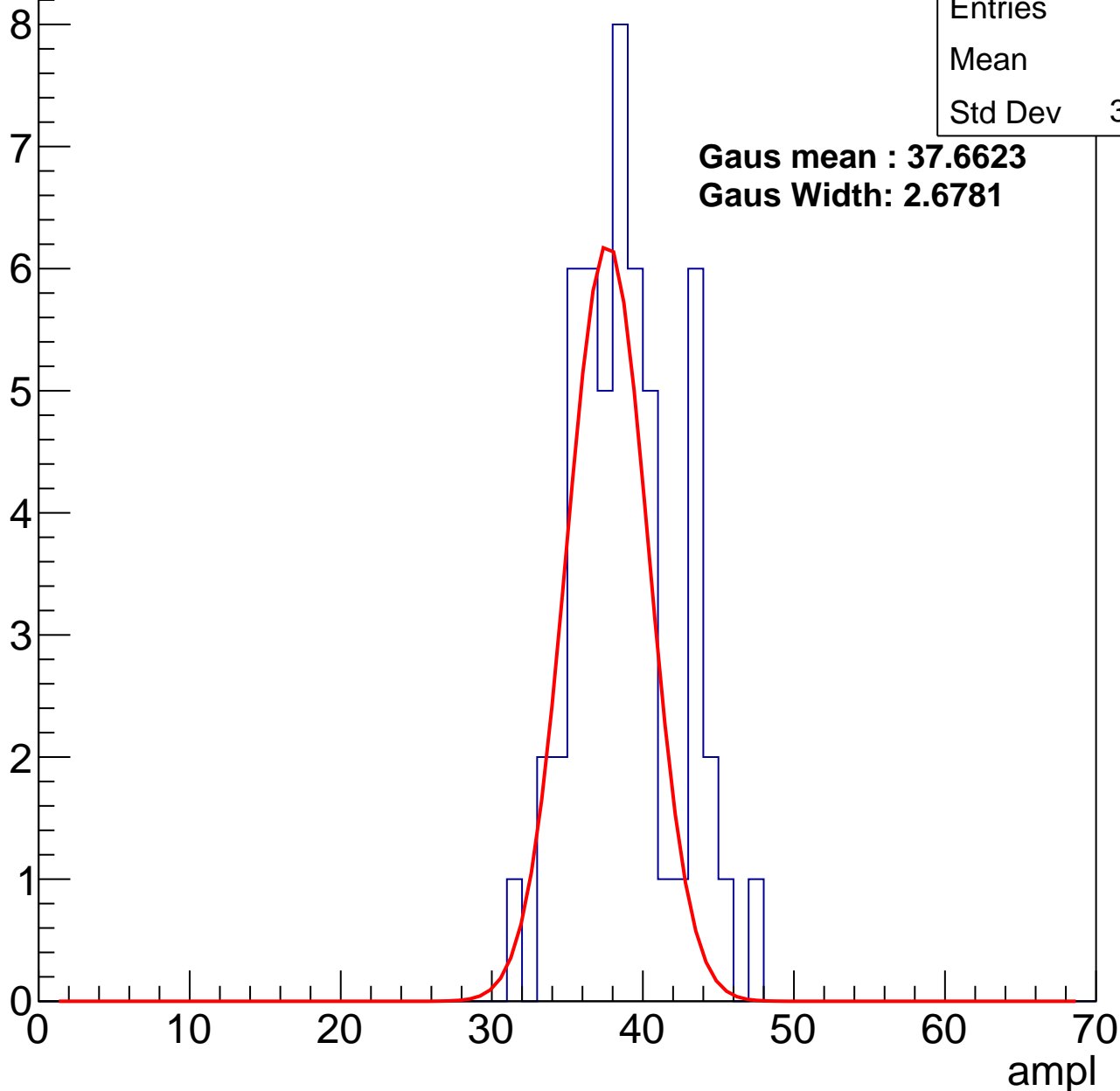
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	38.4
Std Dev	3.389

Gaus mean : 37.6623

Gaus Width: 2.6781



B1L103S, U12-ch27, adc2

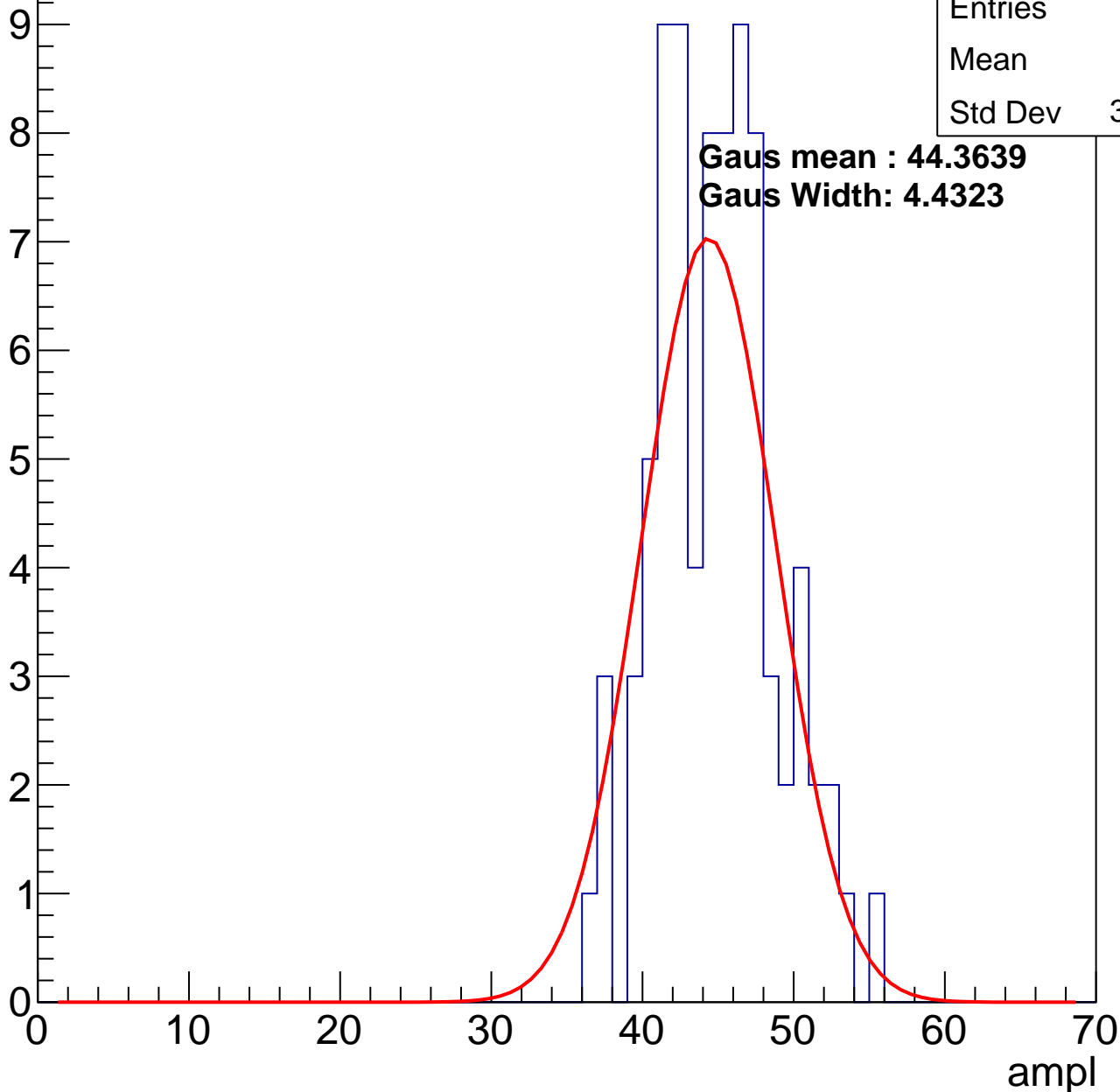
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	44.4
Std Dev	3.919

Gaus mean : 44.3639

Gaus Width: 4.4323

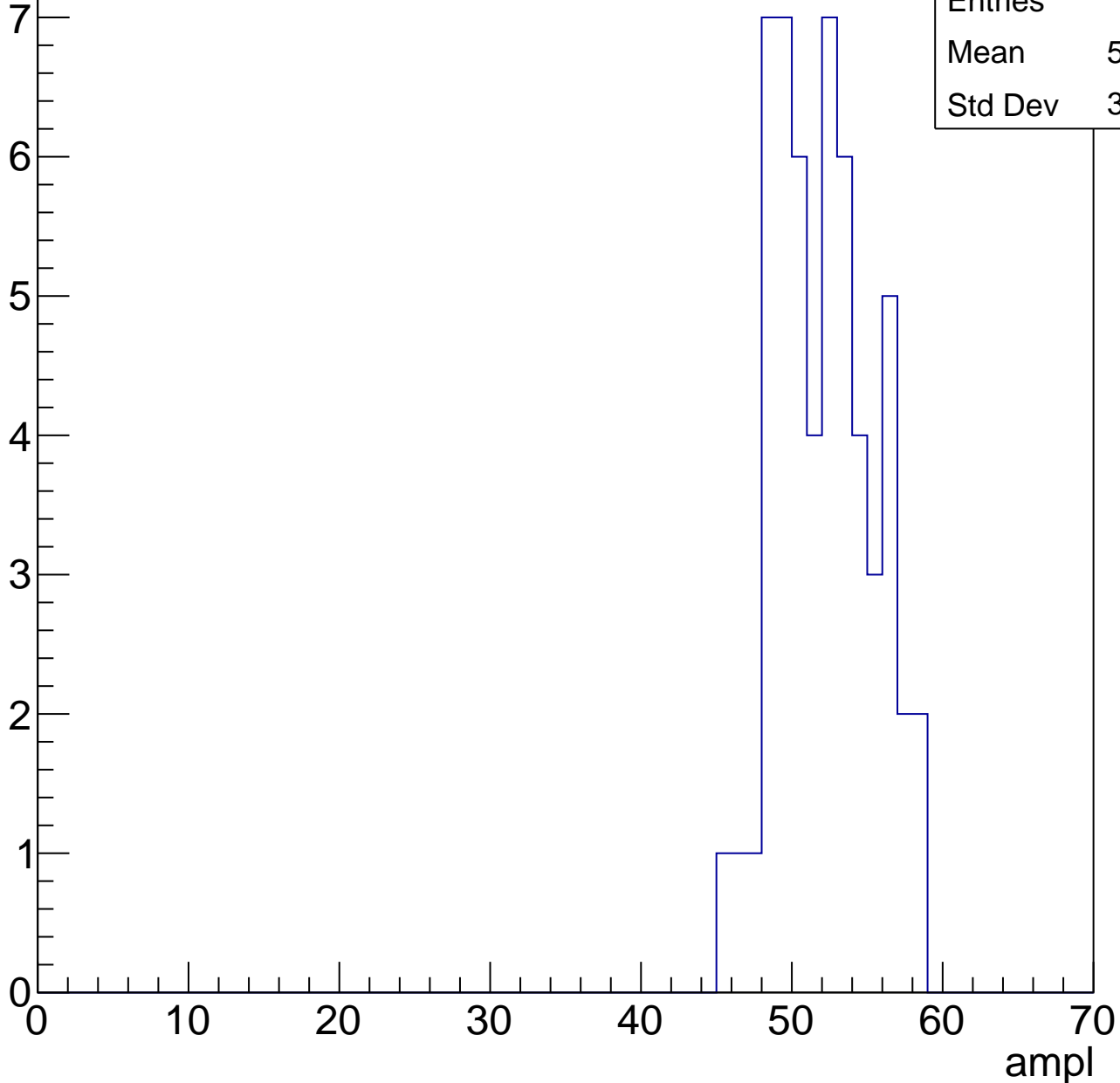


B1L103S, U12-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	51.68
Std Dev	3.157

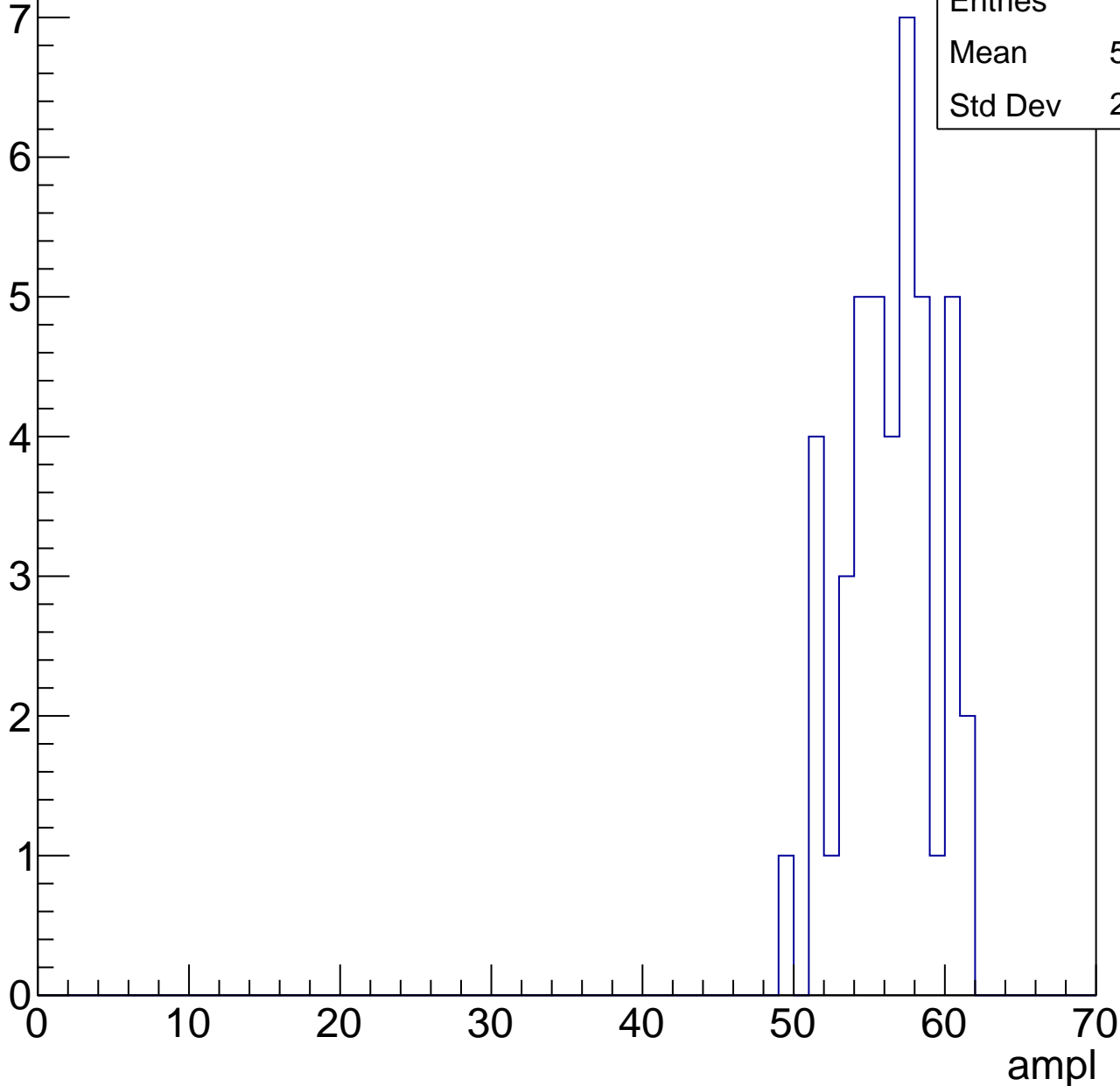


B1L103S, U12-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	55.88
Std Dev	2.982



B1L103S, U12-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

Entries 48

Mean 59.15

Std Dev 8.888

8

6

4

2

0

0

10

20

30

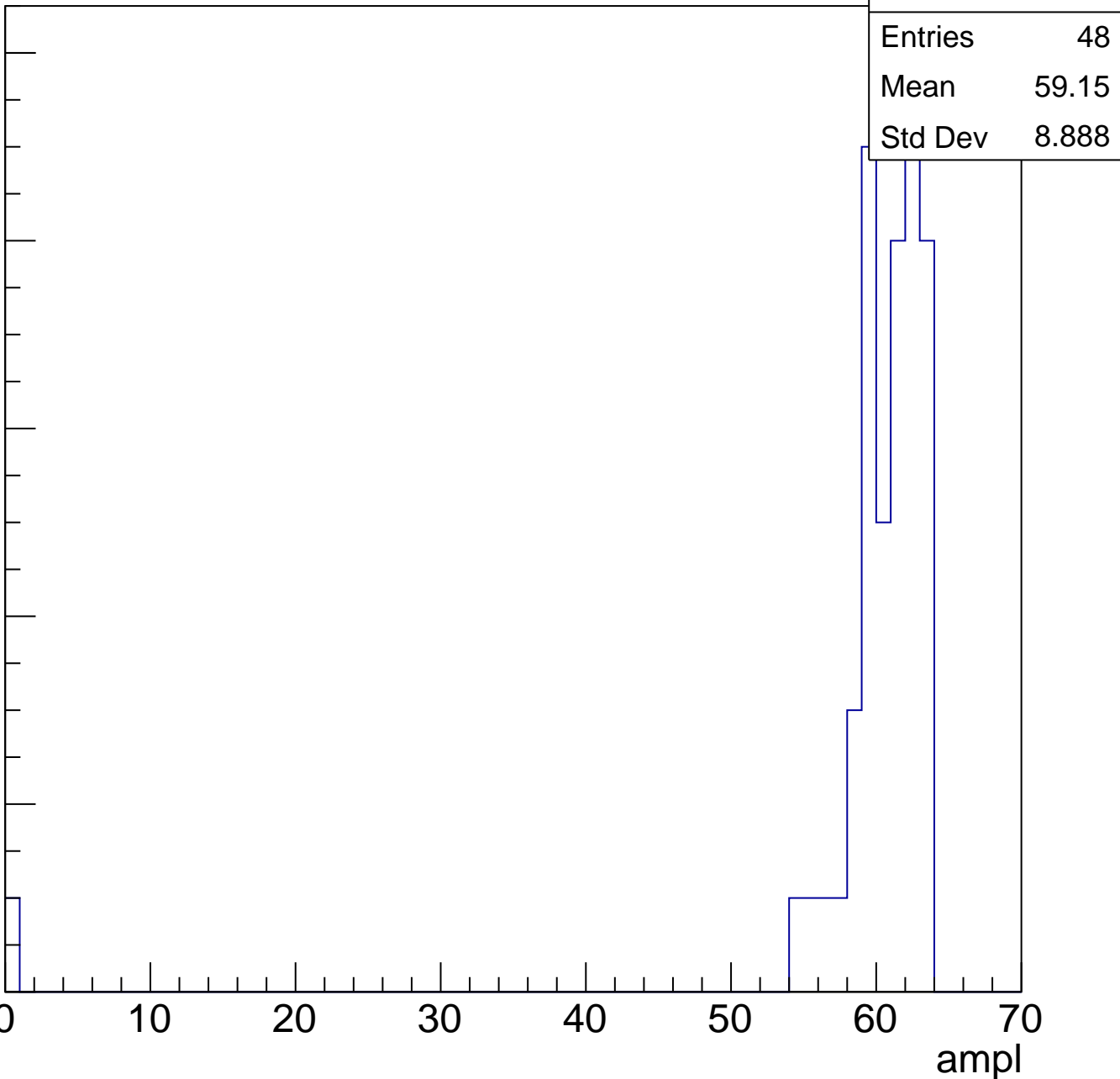
40

50

60

70

ampl



B1L103S, U12-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L103S, U12-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch28, adc0

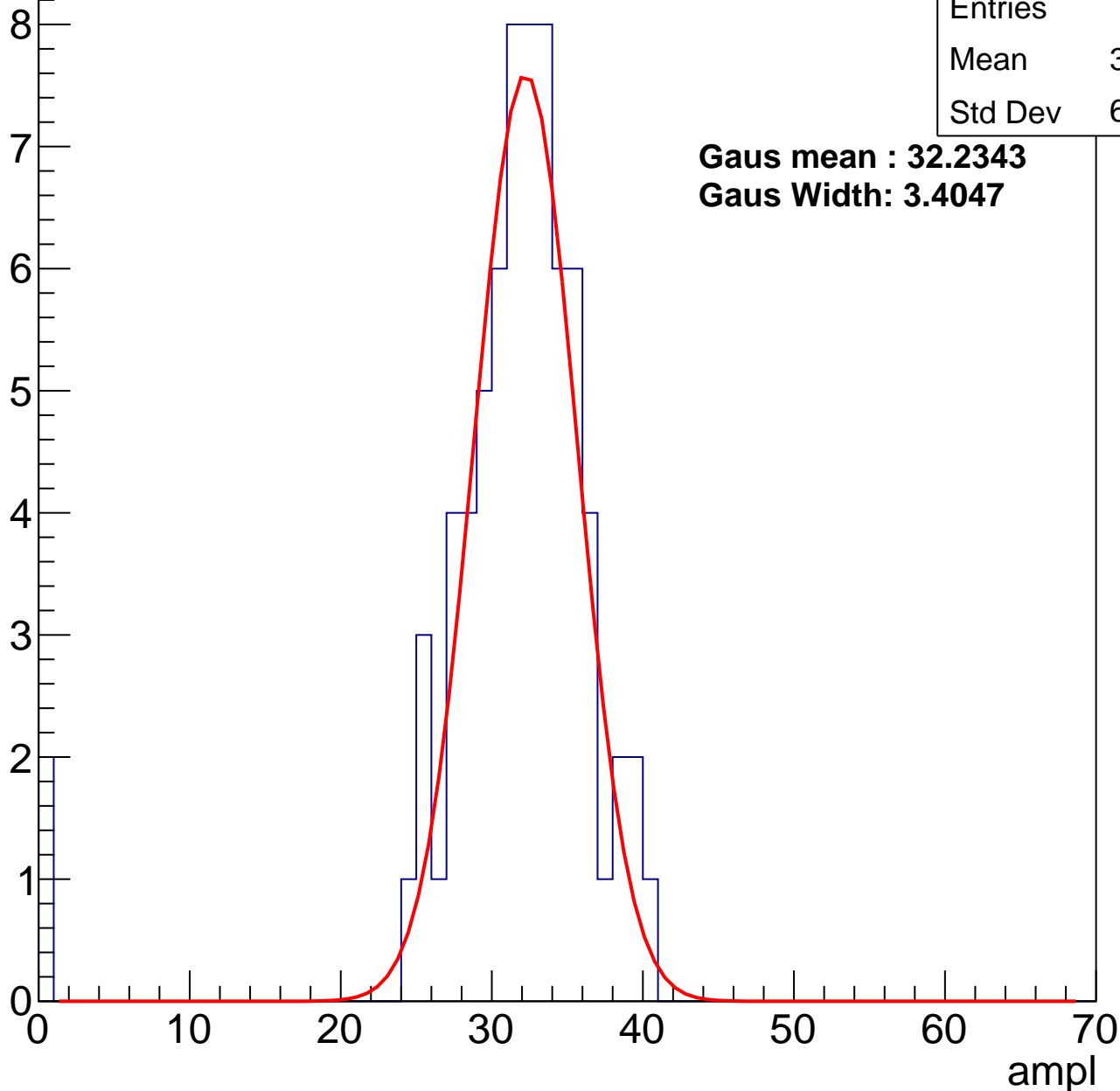
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.93
Std Dev	6.305

Gaus mean : 32.2343

Gaus Width: 3.4047



B1L103S, U12-ch28, adc1

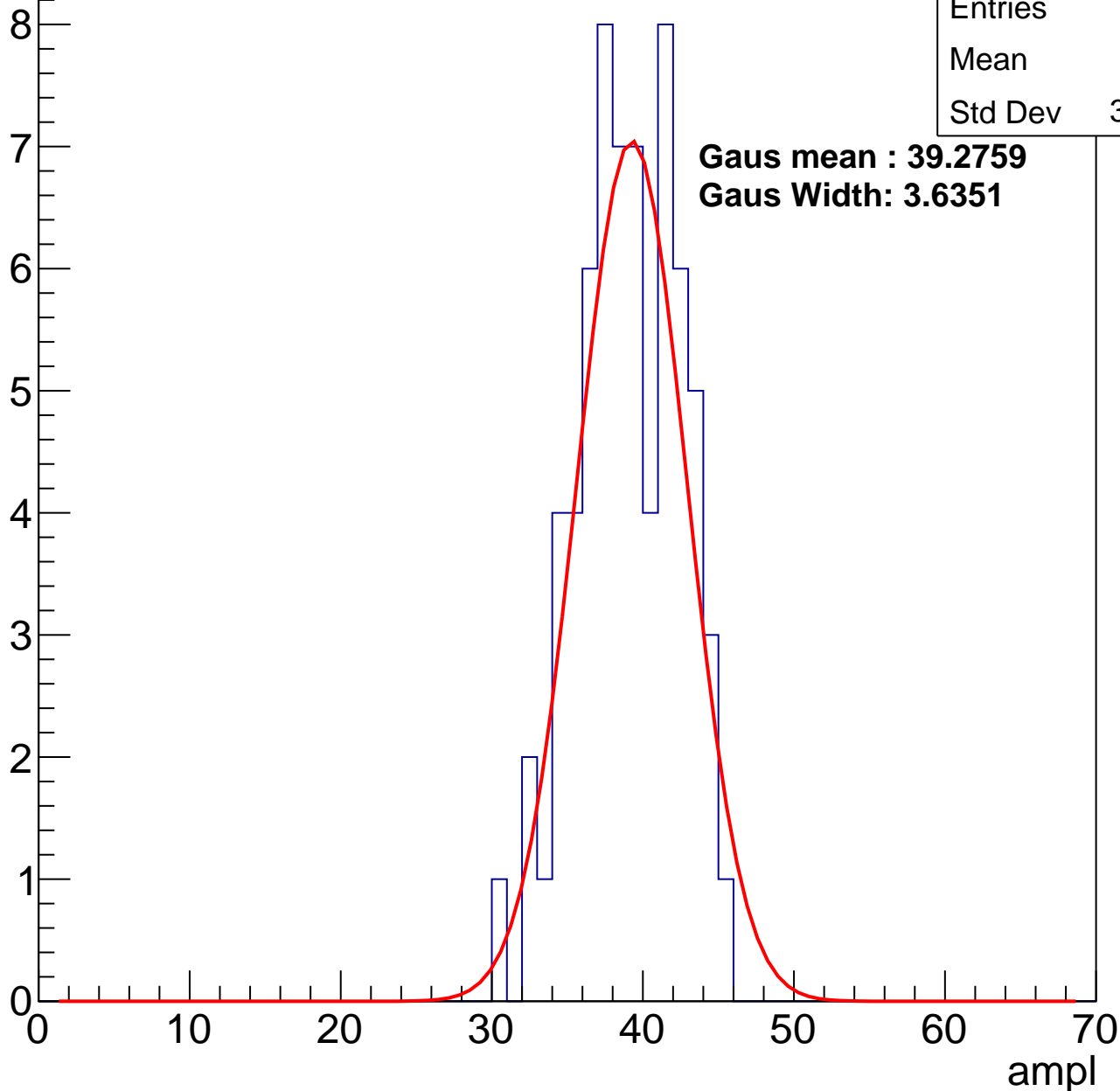
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	38.6
Std Dev	3.328

Gaus mean : 39.2759

Gaus Width: 3.6351



B1L103S, U12-ch28, adc2

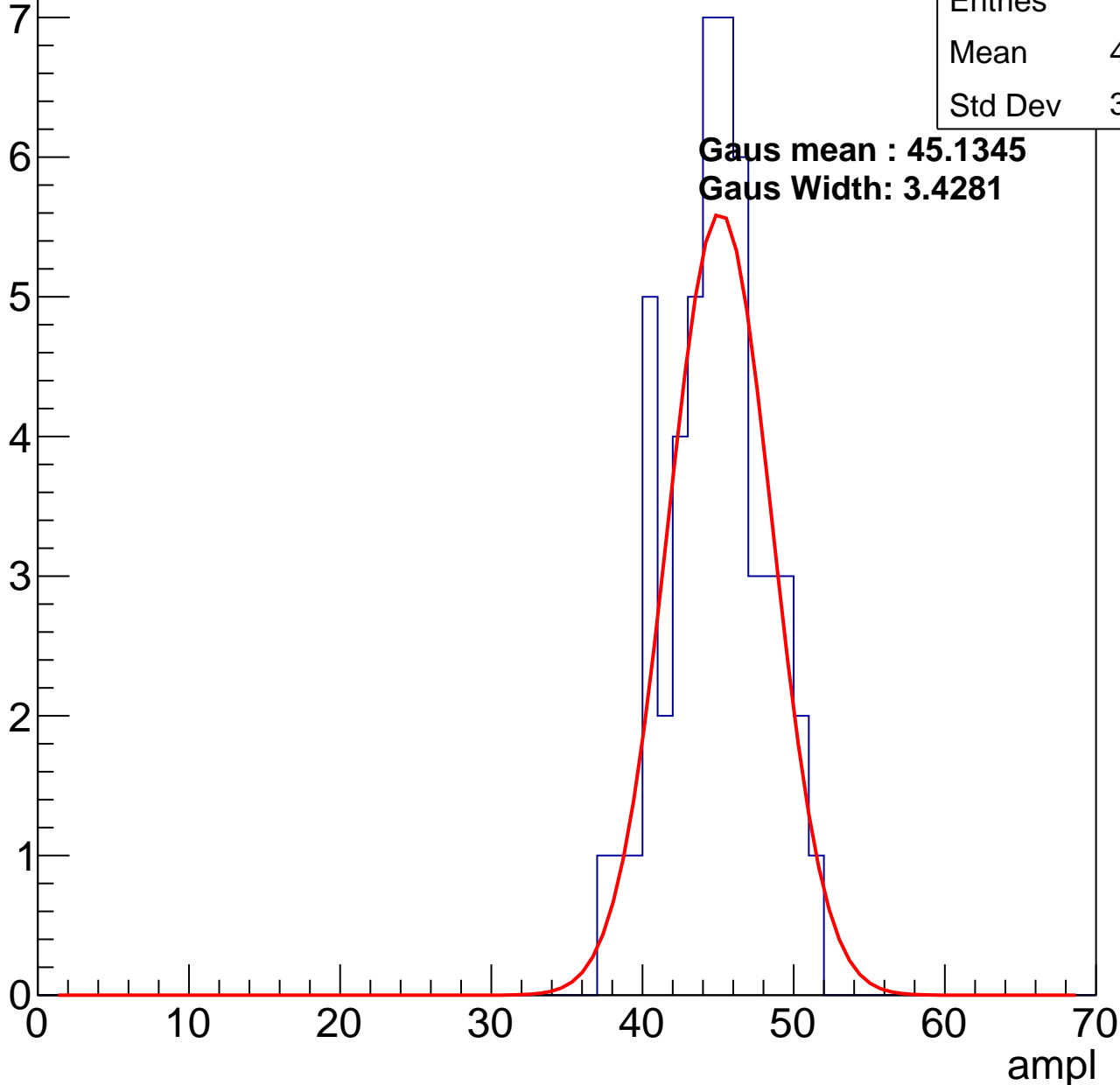
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	44.33
Std Dev	3.209

Gaus mean : 45.1345

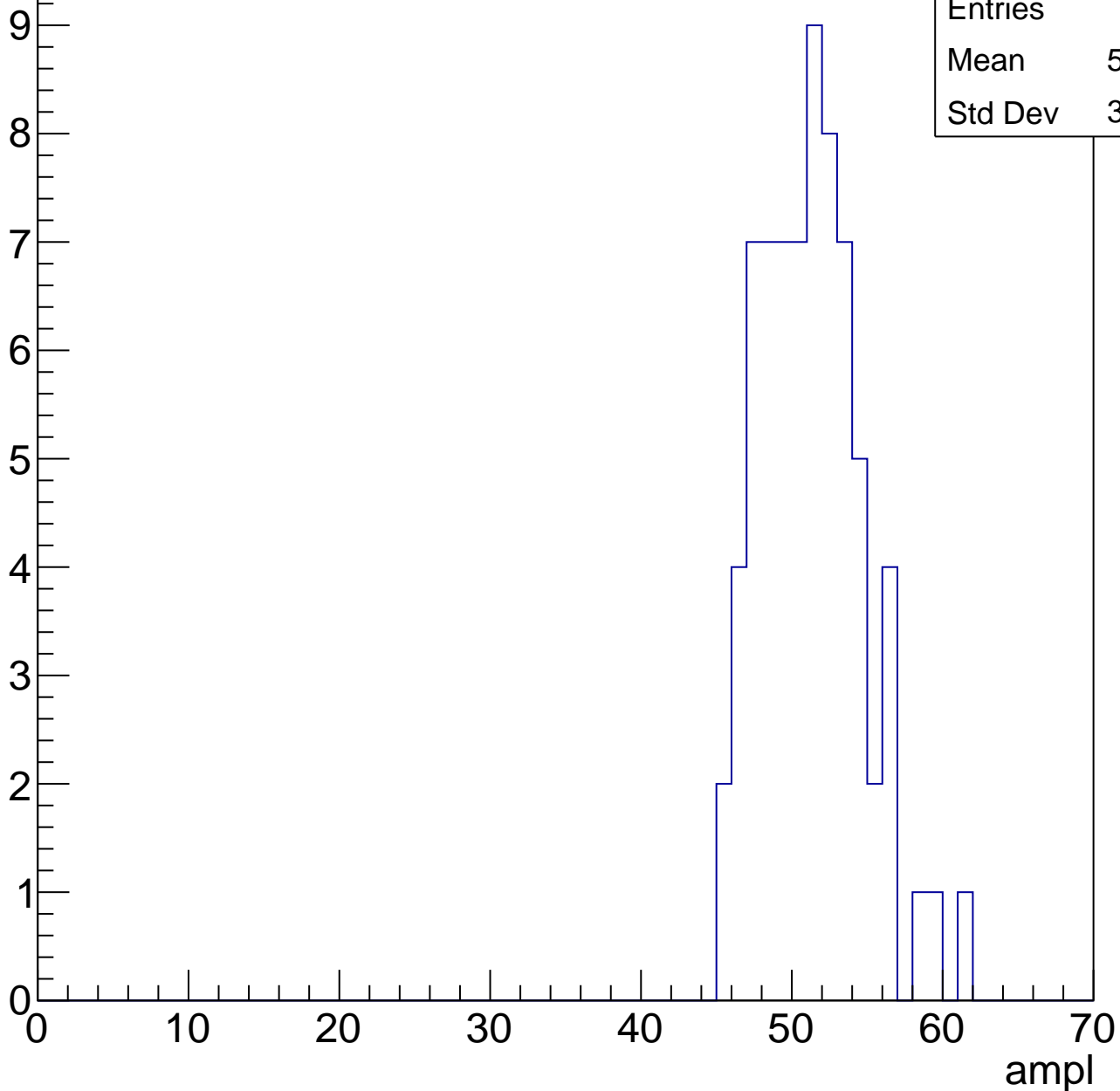
Gaus Width: 3.4281



B1L103S, U12-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



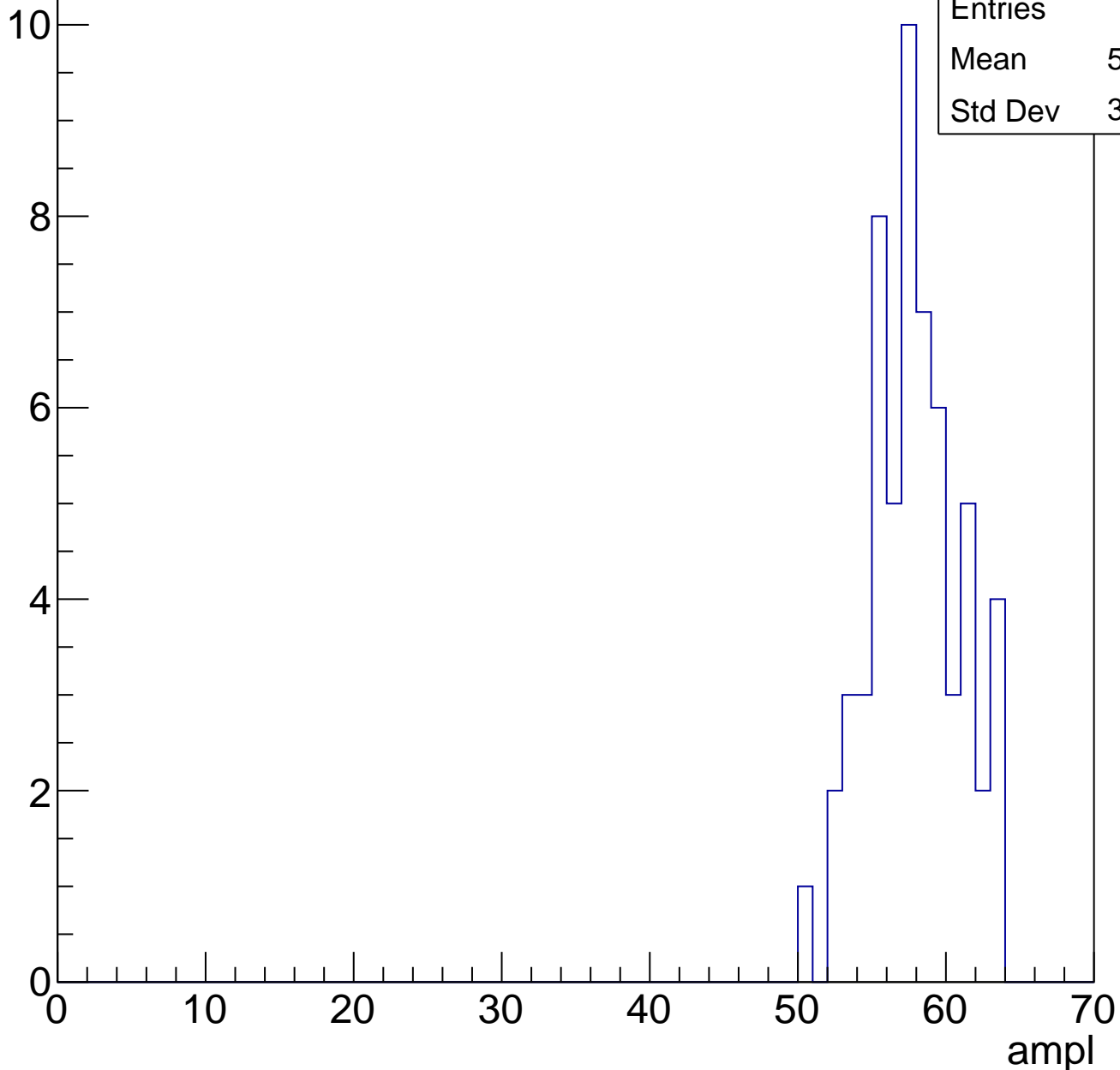
Entries	72
Mean	50.83
Std Dev	3.346

B1L103S, U12-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	57.39
Std Dev	3.009

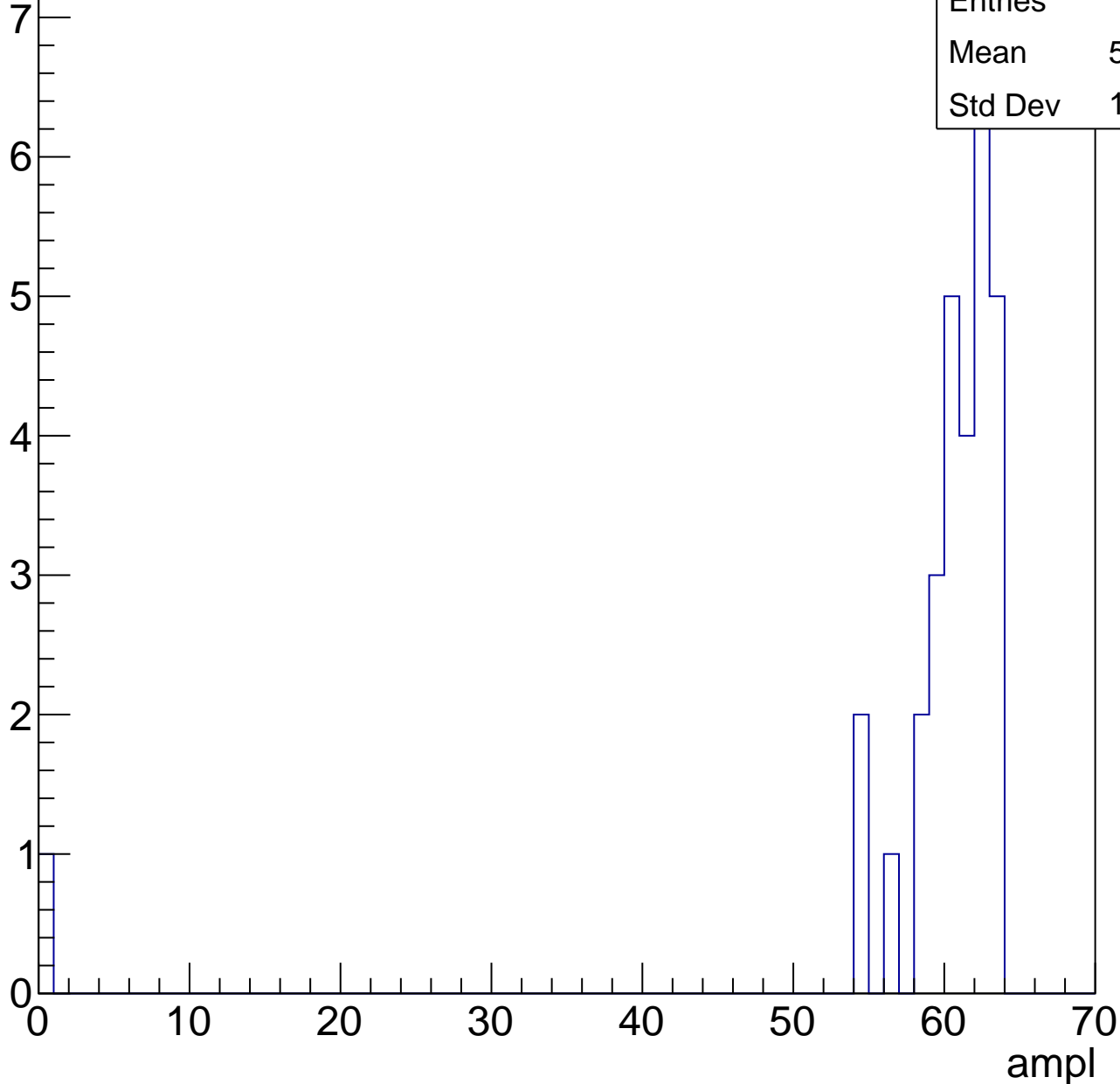


B1L103S, U12-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

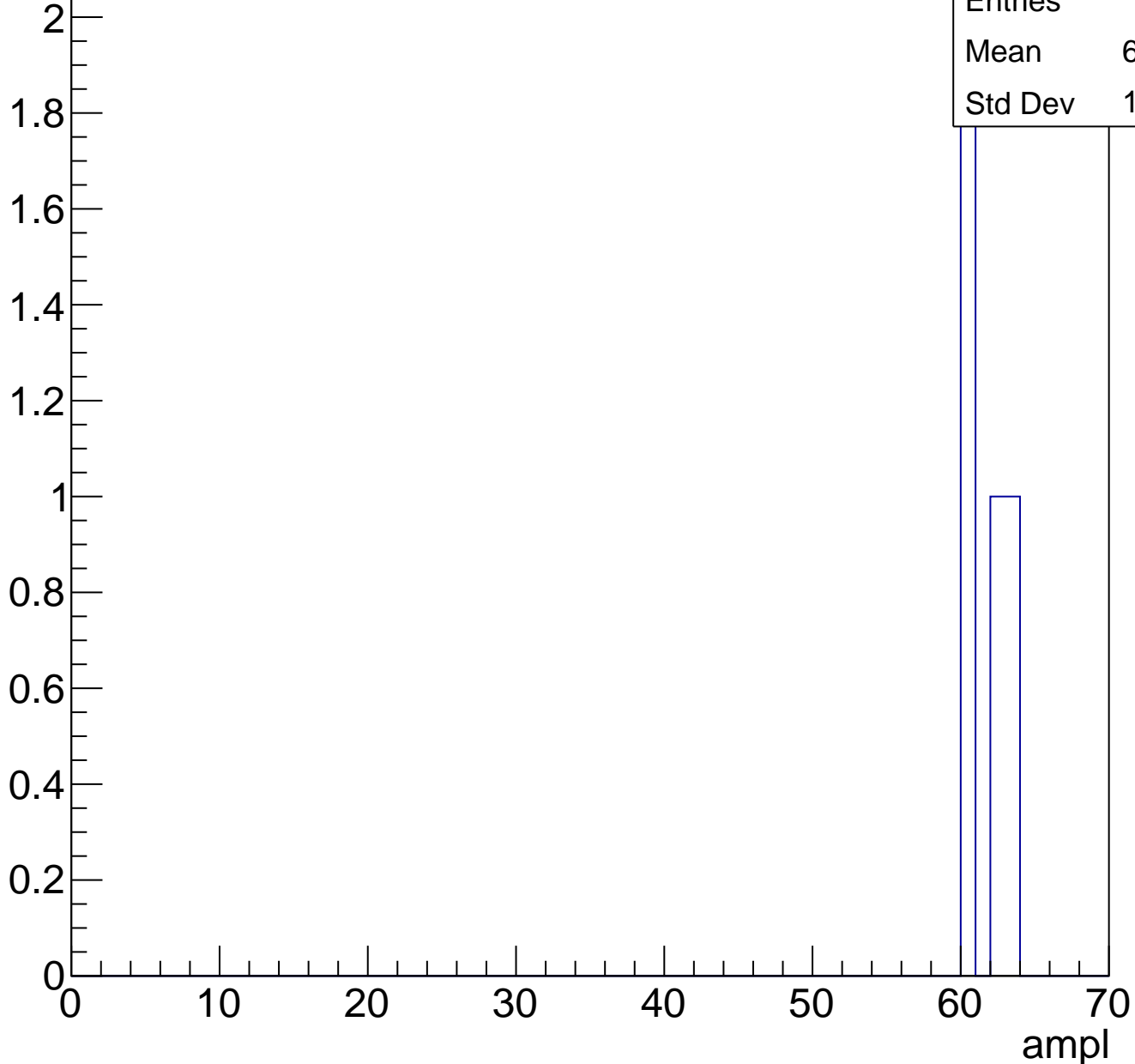
Entries	30
Mean	58.33
Std Dev	11.09



B1L103S, U12-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch29, adc0

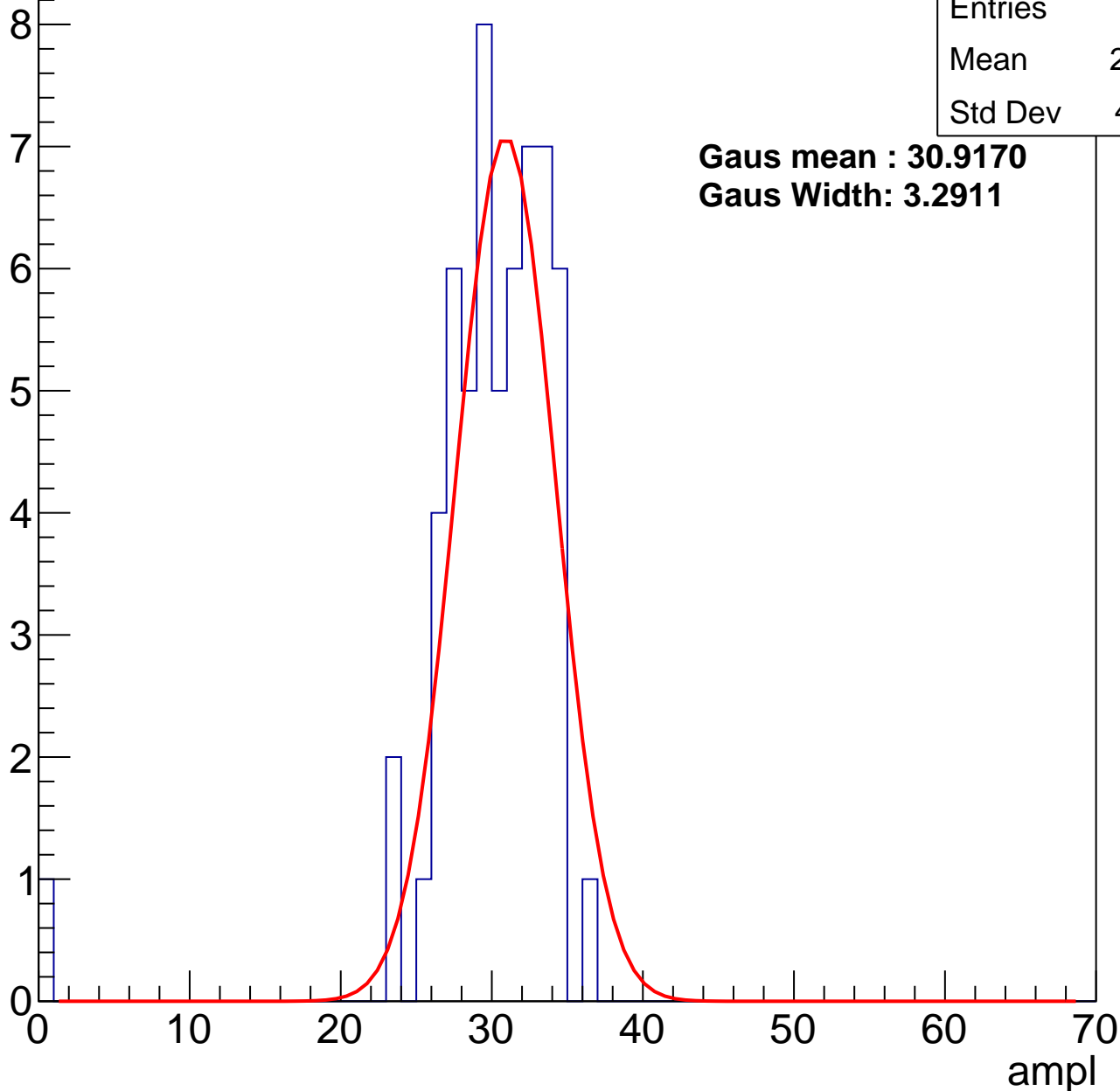
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	29.49
Std Dev	4.841

Gaus mean : 30.9170

Gaus Width: 3.2911



B1L103S, U12-ch29, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	36.52
Std Dev	3.392

Gaus mean : 37.4949

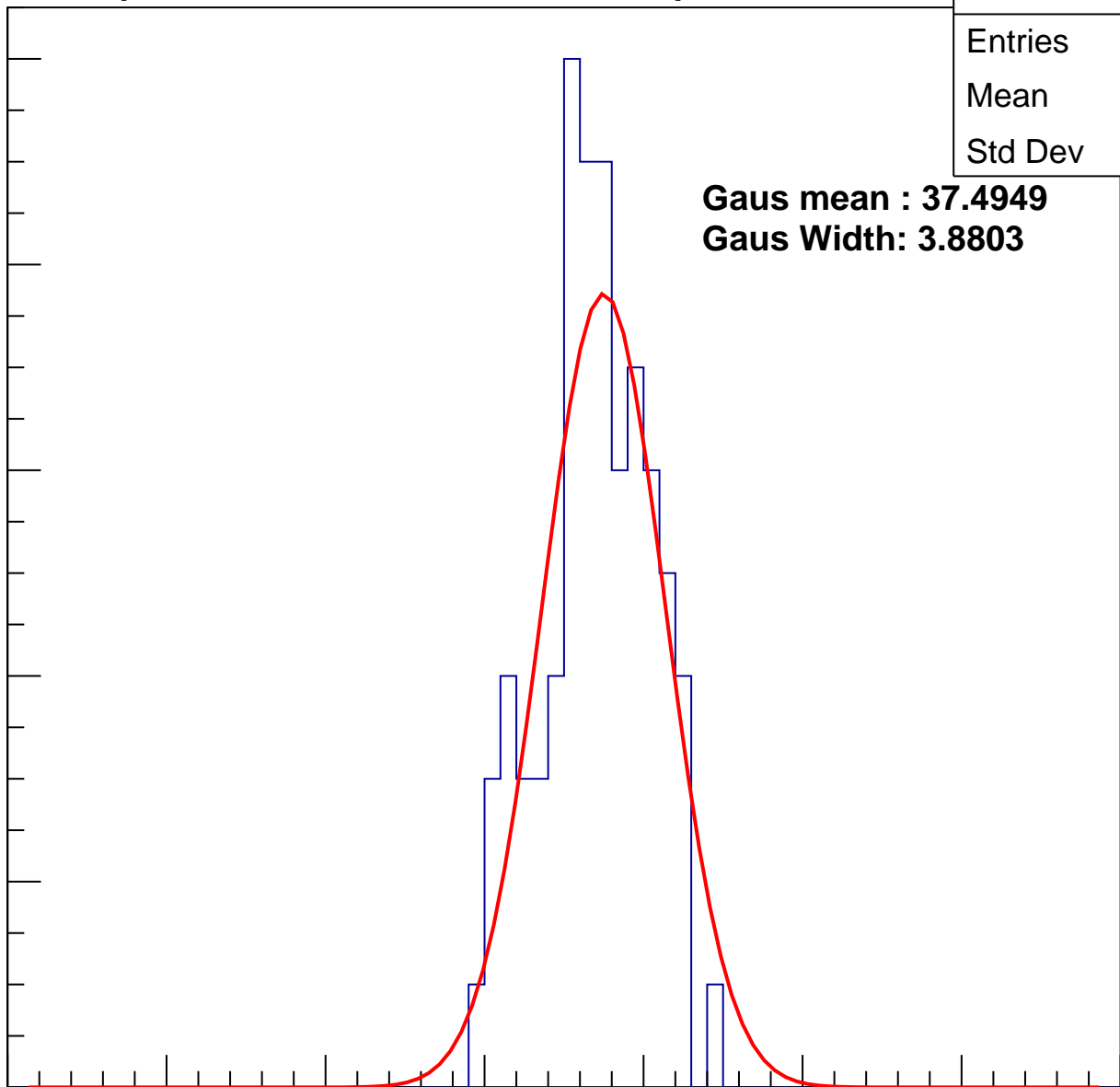
Gaus Width: 3.8803

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch29, adc2

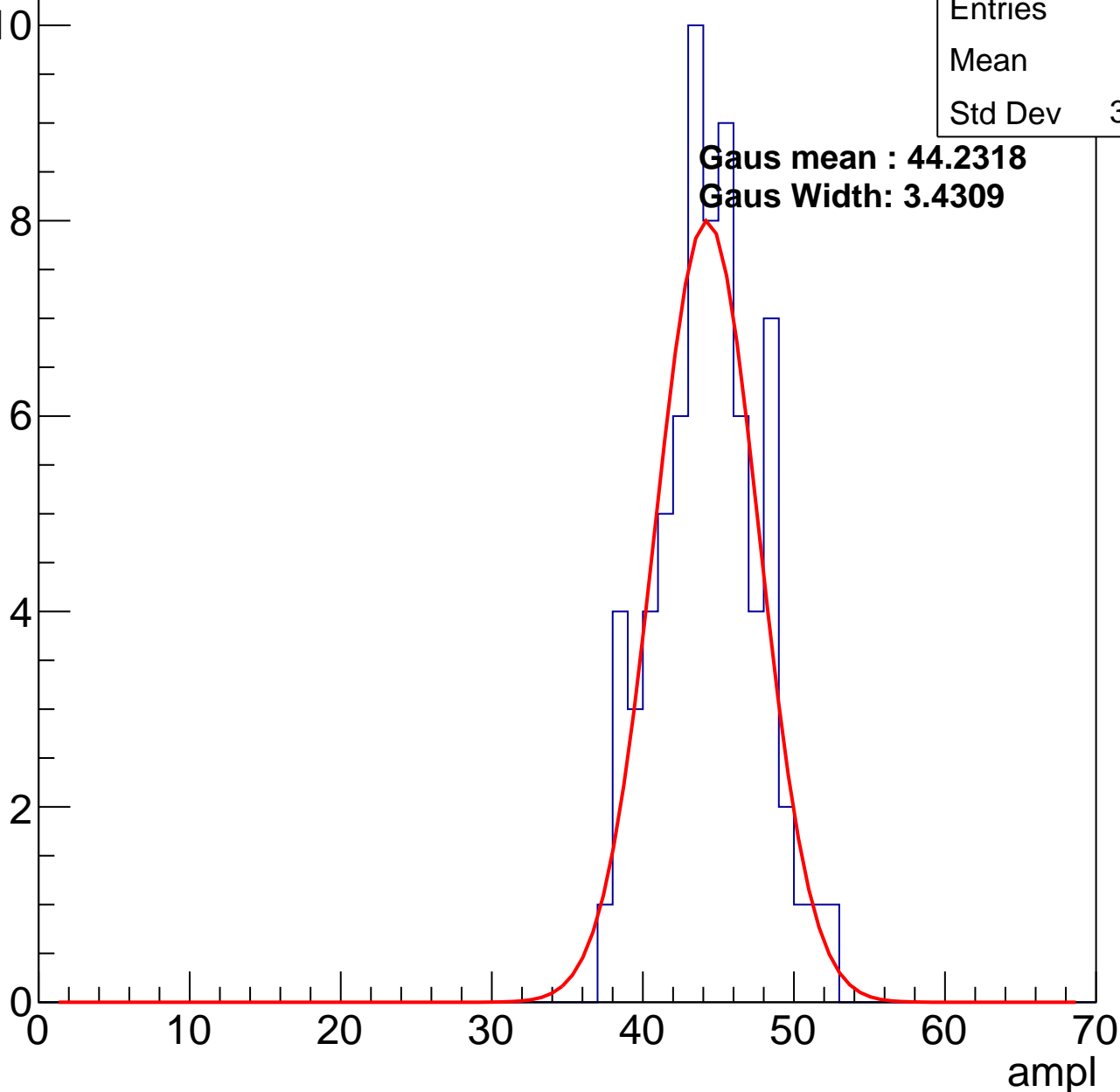
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.9
Std Dev	3.313

Gaus mean : 44.2318

Gaus Width: 3.4309

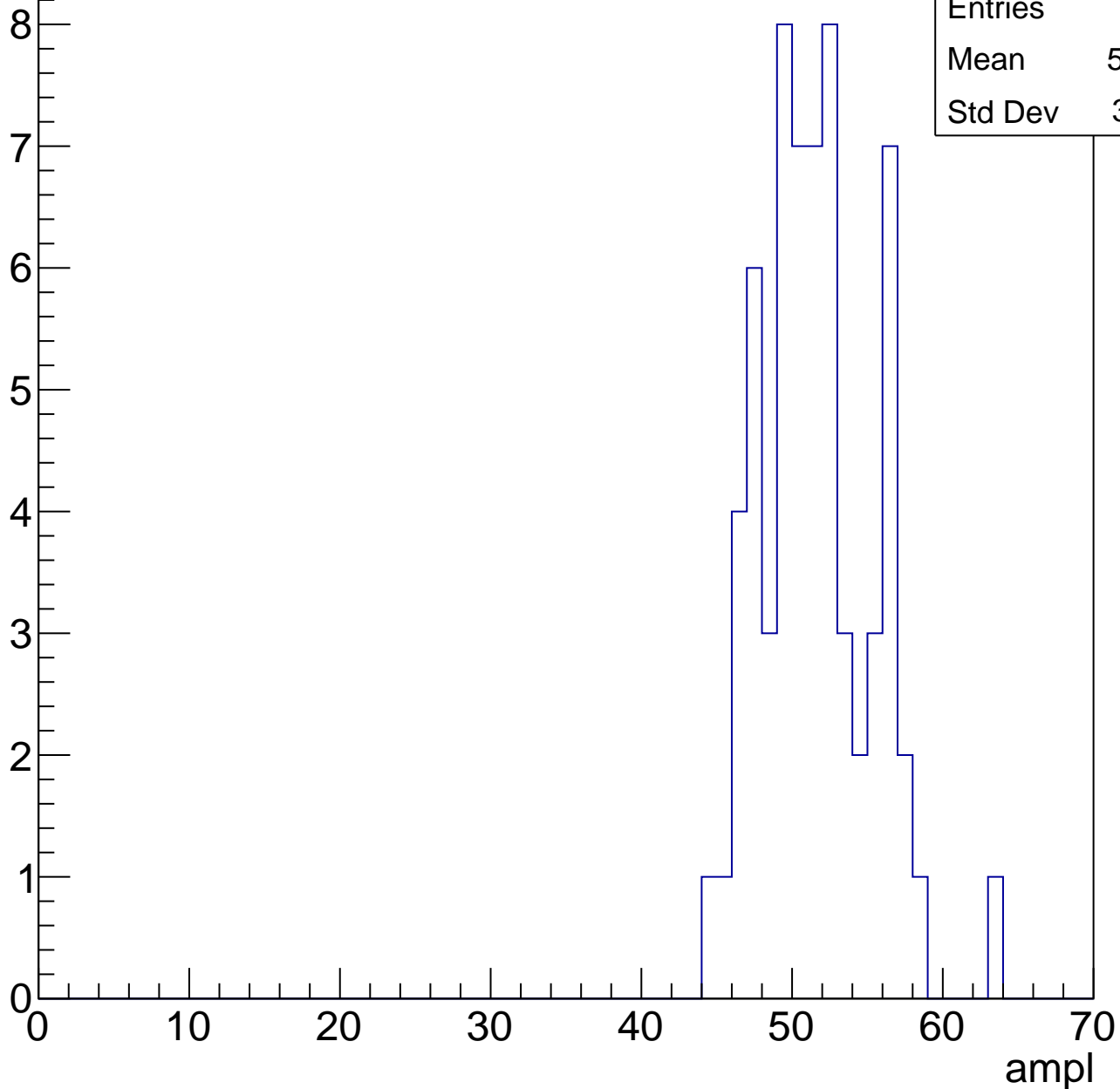


B1L103S, U12-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

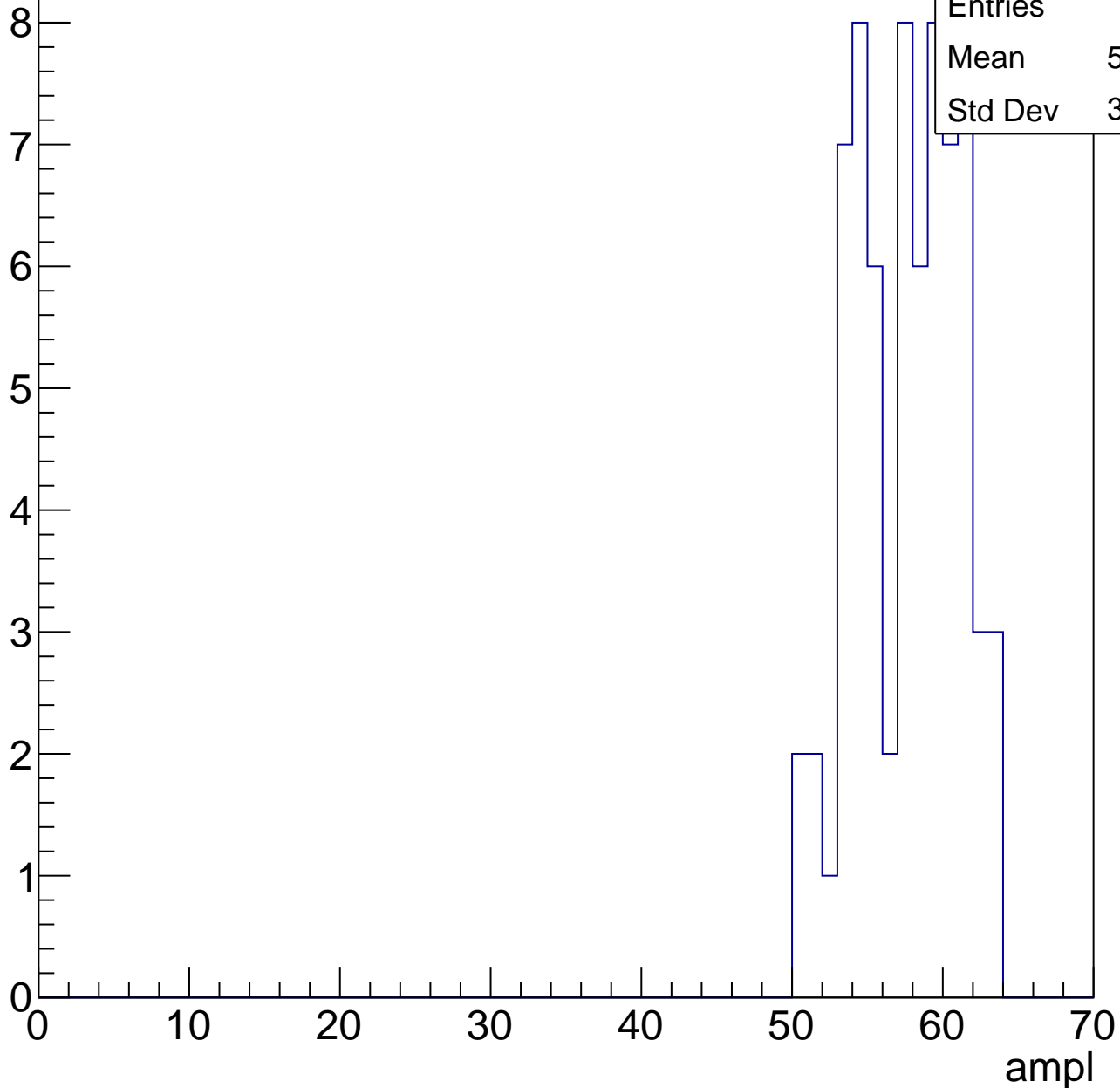
Entries	64
Mean	51.14
Std Dev	3.691



B1L103S, U12-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

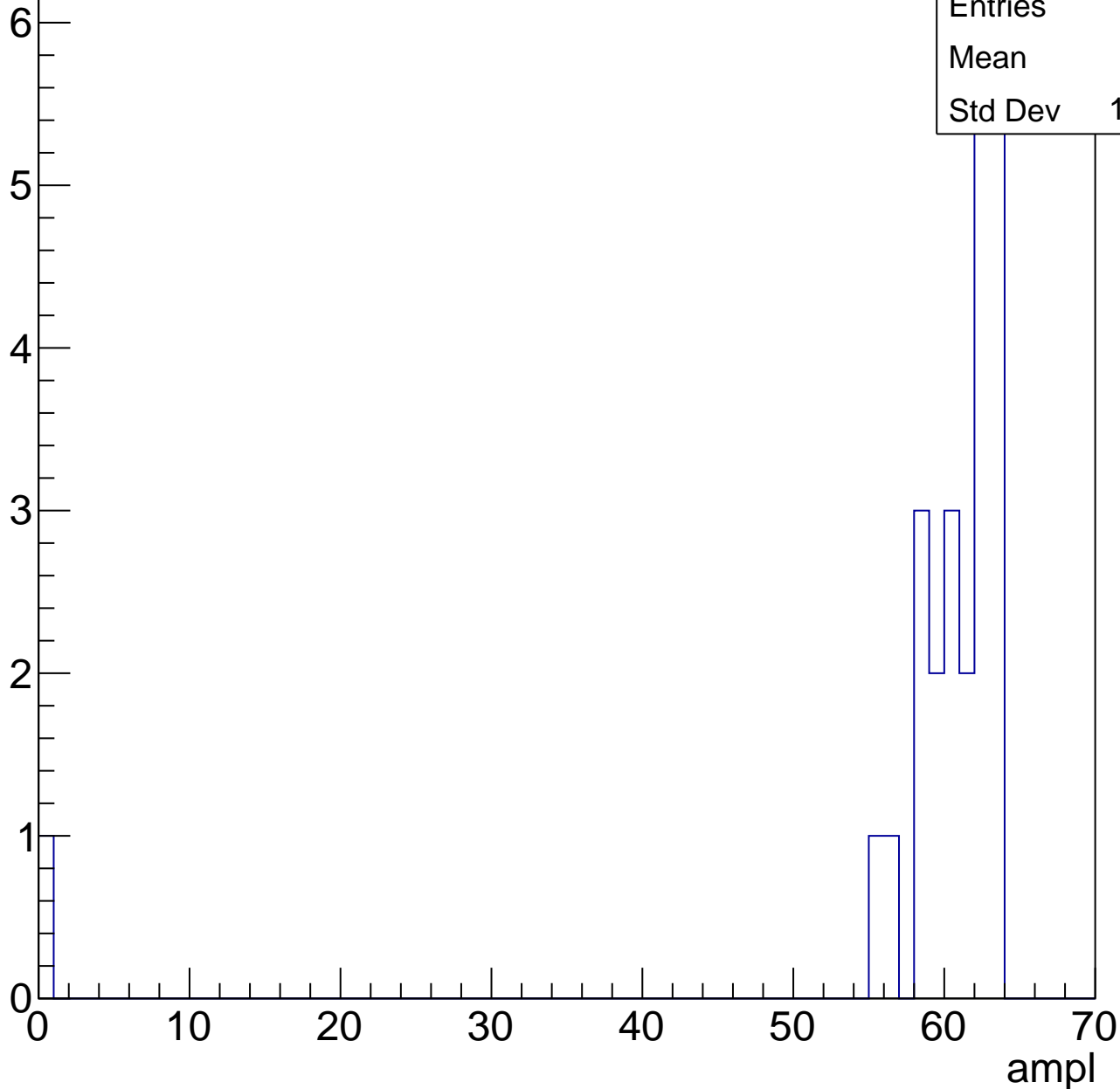


B1L103S, U12-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	58.2
Std Dev	12.09



B1L103S, U12-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U12-ch30, adc0

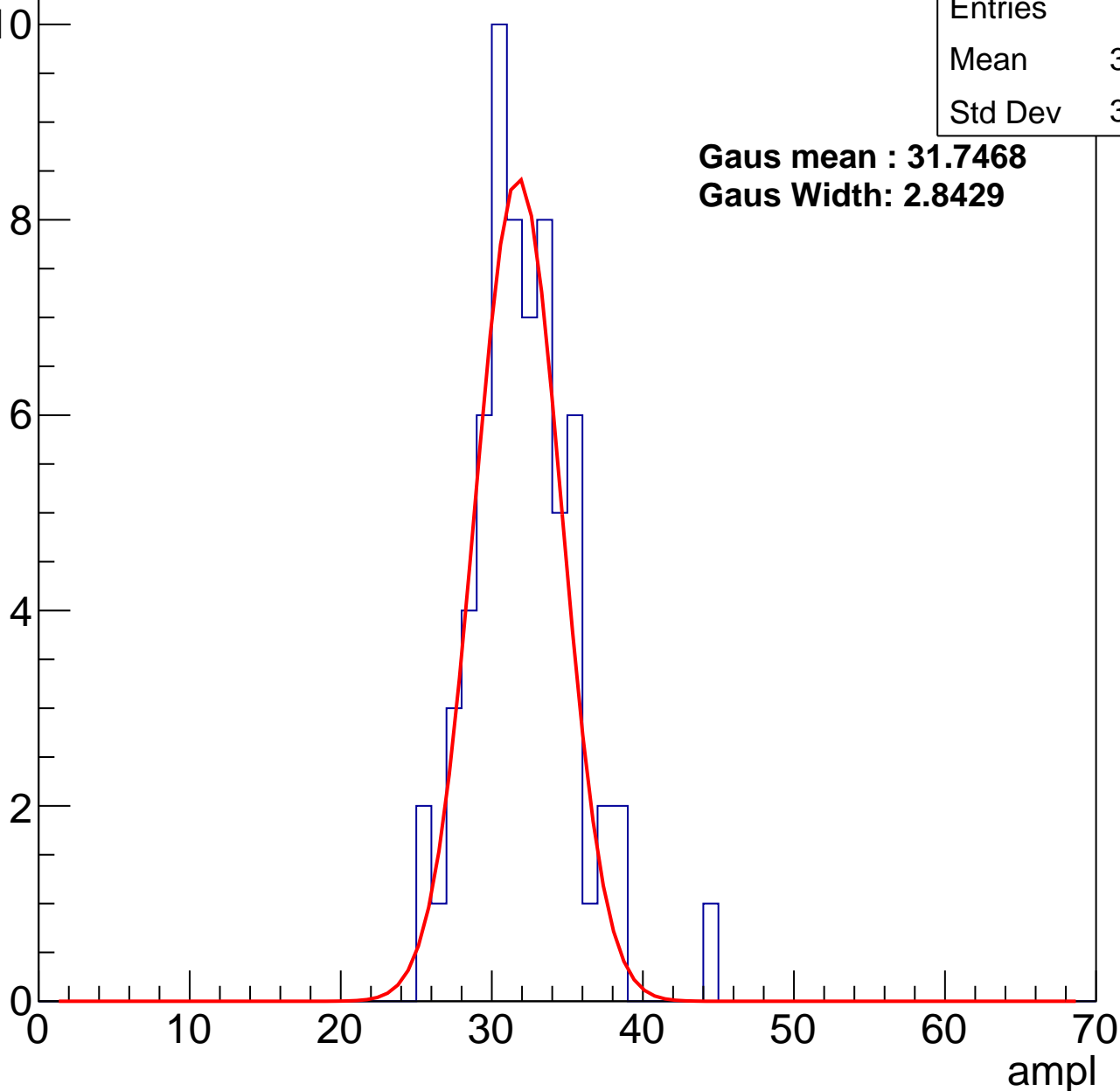
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	31.65
Std Dev	3.328

Gaus mean : 31.7468

Gaus Width: 2.8429



B1L103S, U12-ch30, adc1

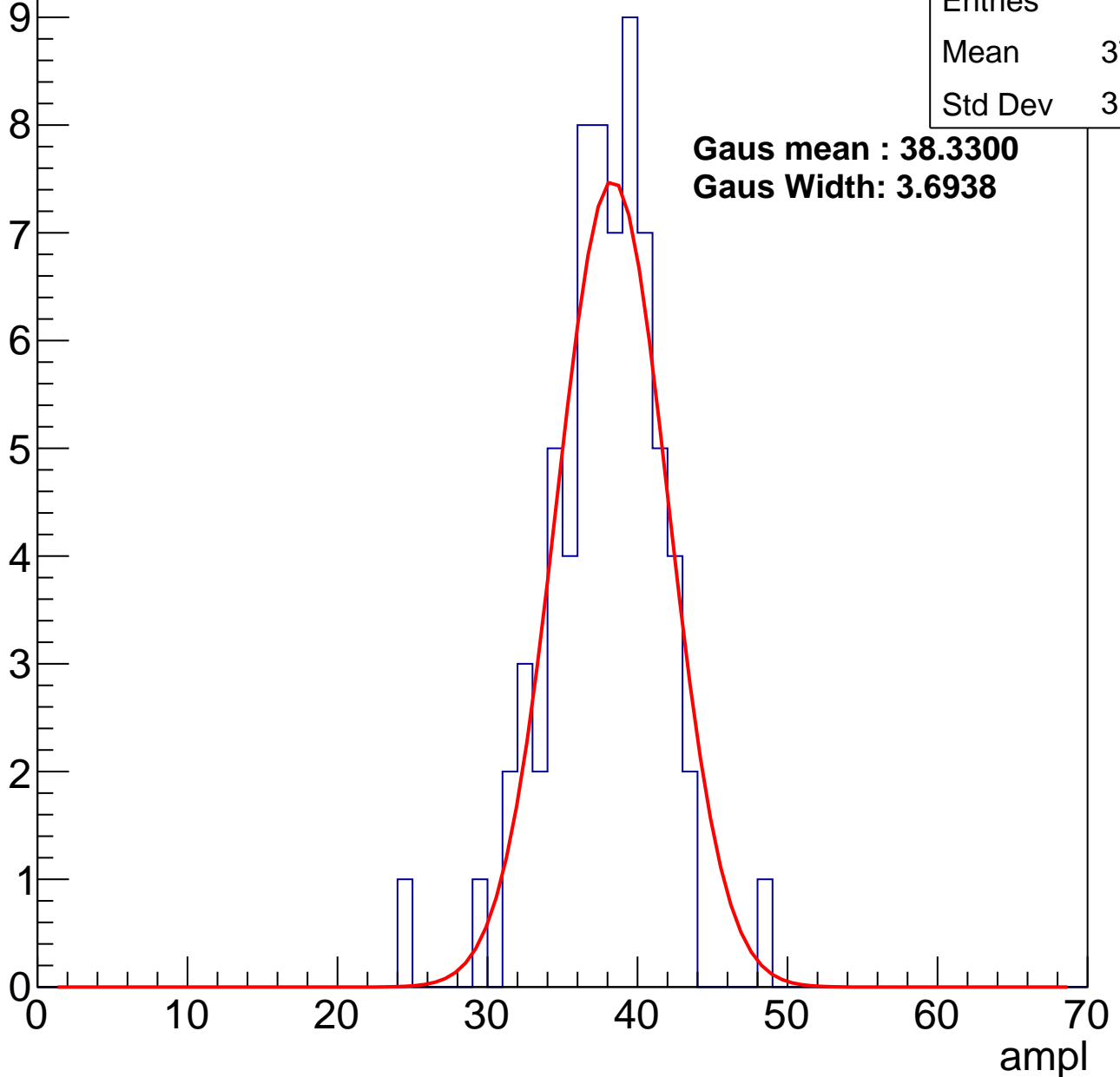
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.32
Std Dev	3.716

Gaus mean : 38.3300

Gaus Width: 3.6938



B1L103S, U12-ch30, adc2

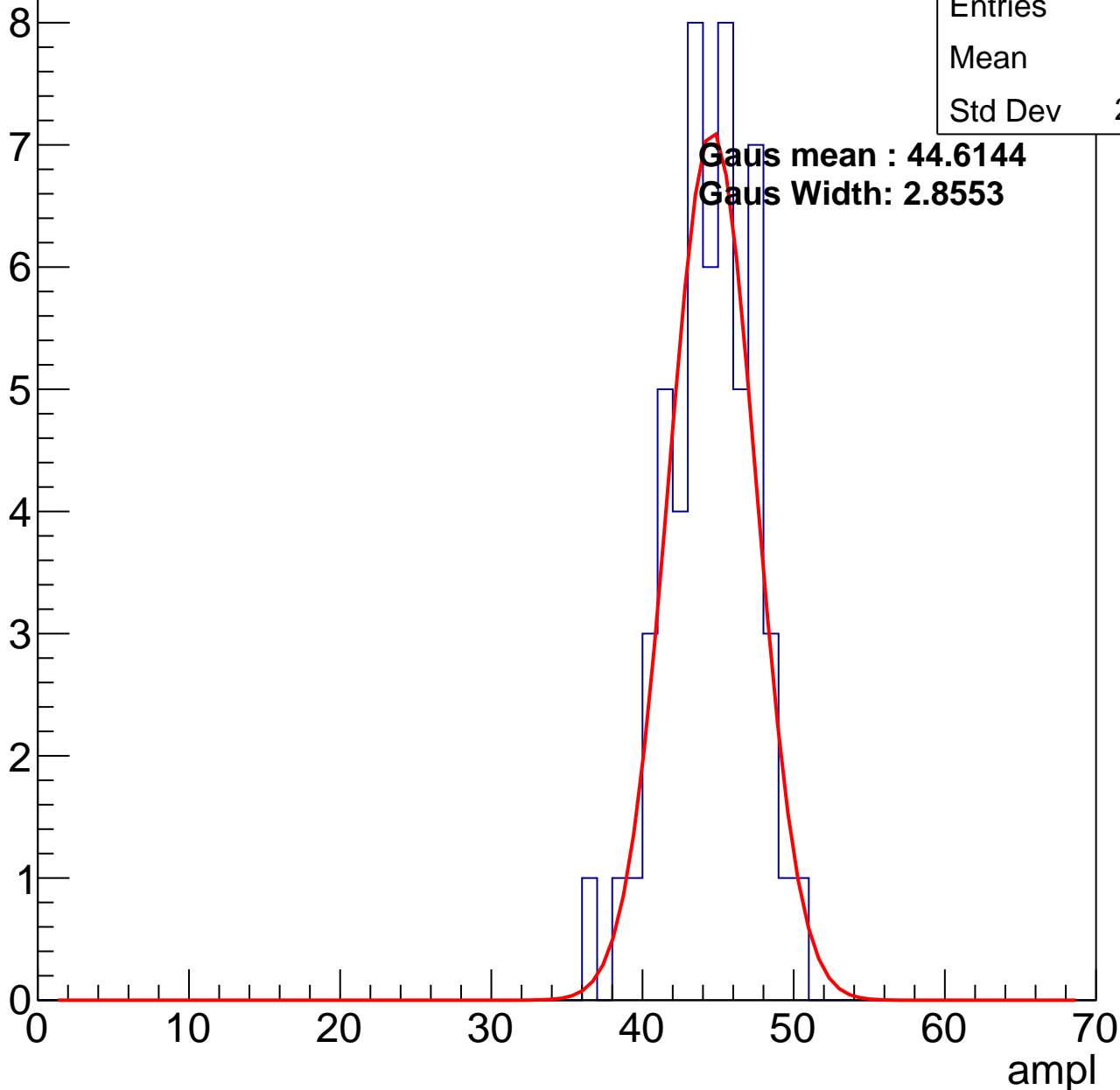
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	44
Std Dev	2.861

Gaus mean : 44.6144

Gaus Width: 2.8553

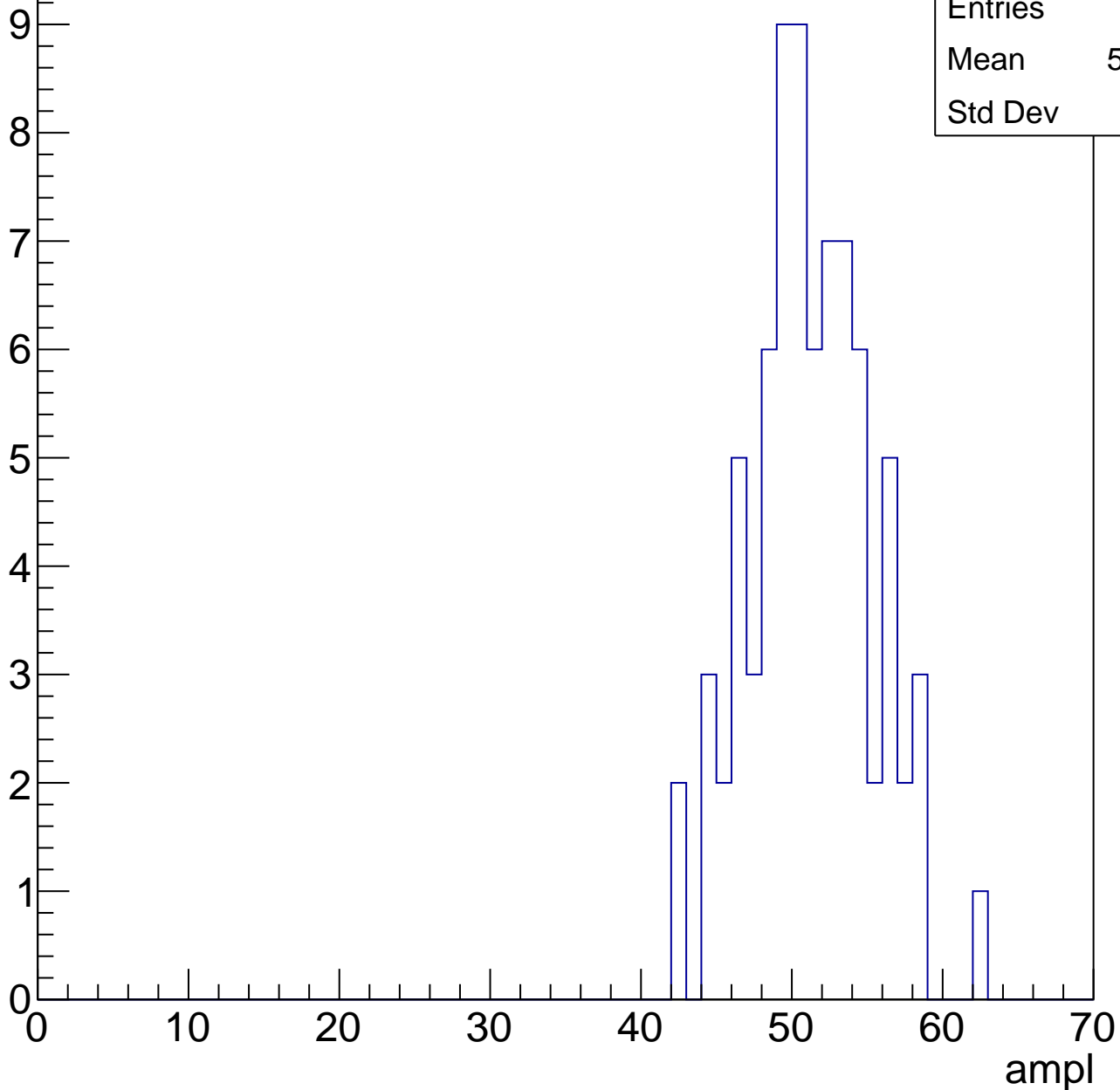


B1L103S, U12-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

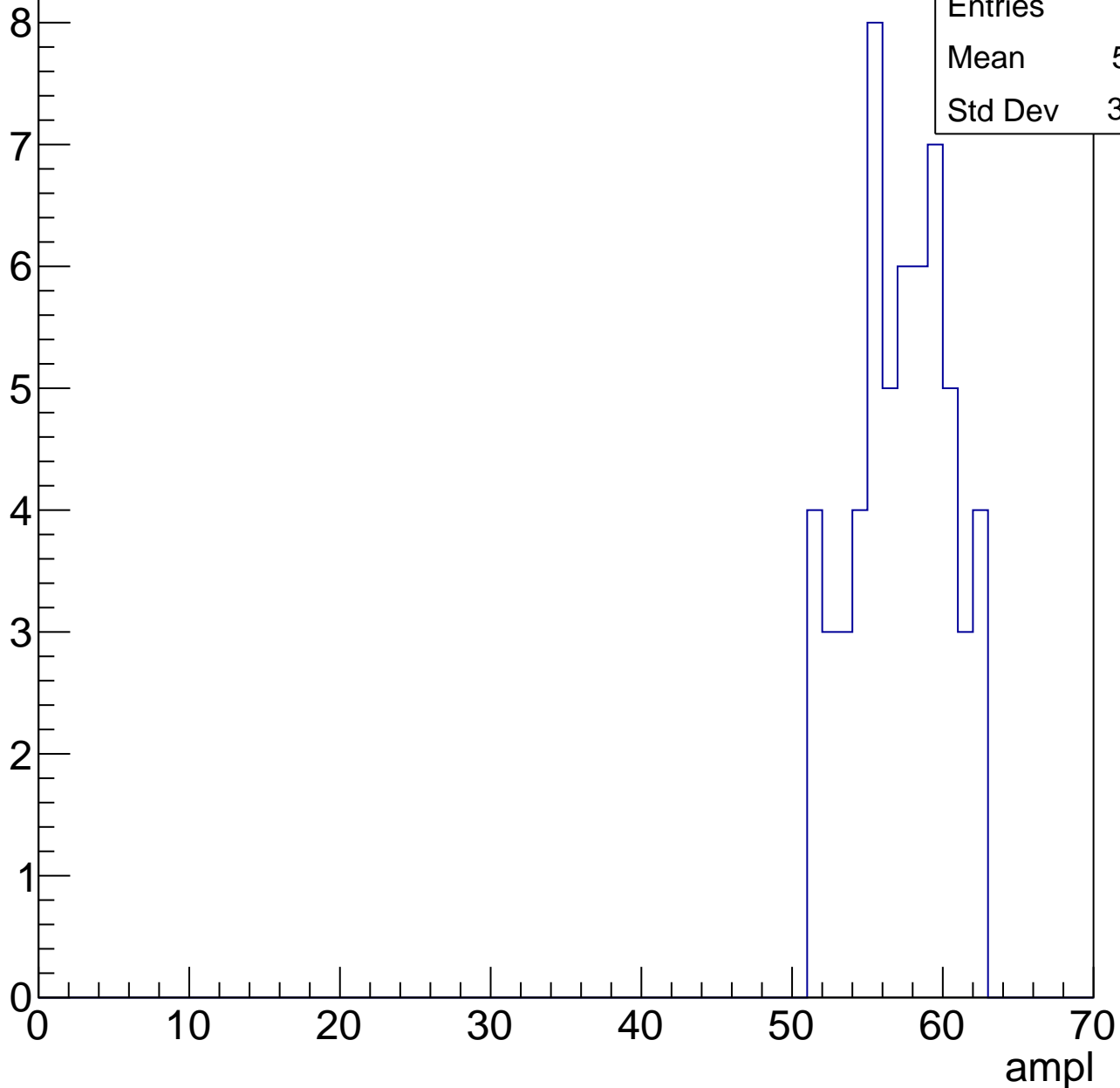
Entries	78
Mean	50.78
Std Dev	3.98



B1L103S, U12-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



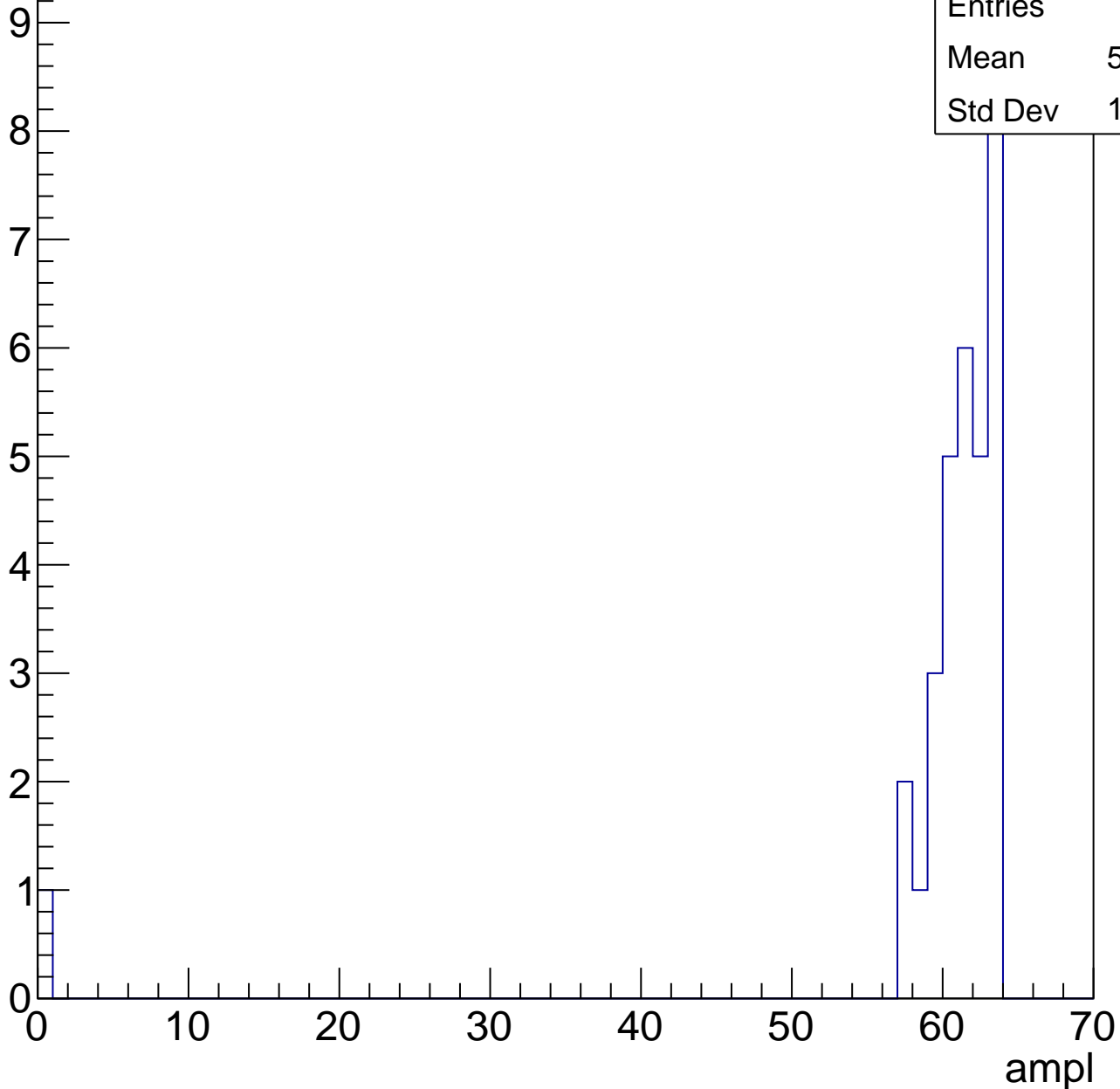
Entries	58
Mean	56.71
Std Dev	3.113

B1L103S, U12-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	59.12
Std Dev	10.76



B1L103S, U12-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch31, adc0

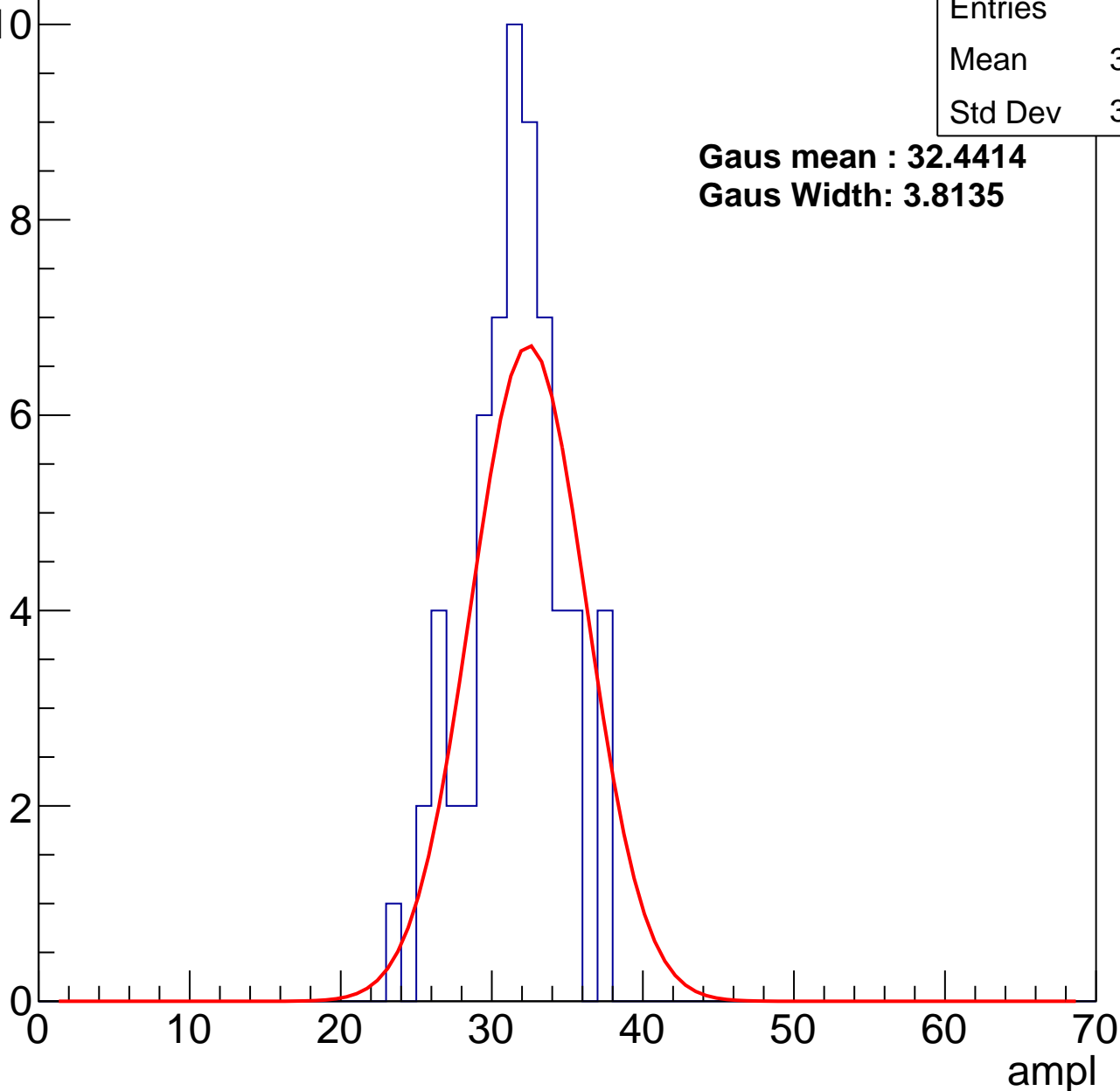
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	31.03
Std Dev	3.105

Gaus mean : 32.4414

Gaus Width: 3.8135



B1L103S, U12-ch31, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	38.39
Std Dev	3.085

Gaus mean : 39.2074

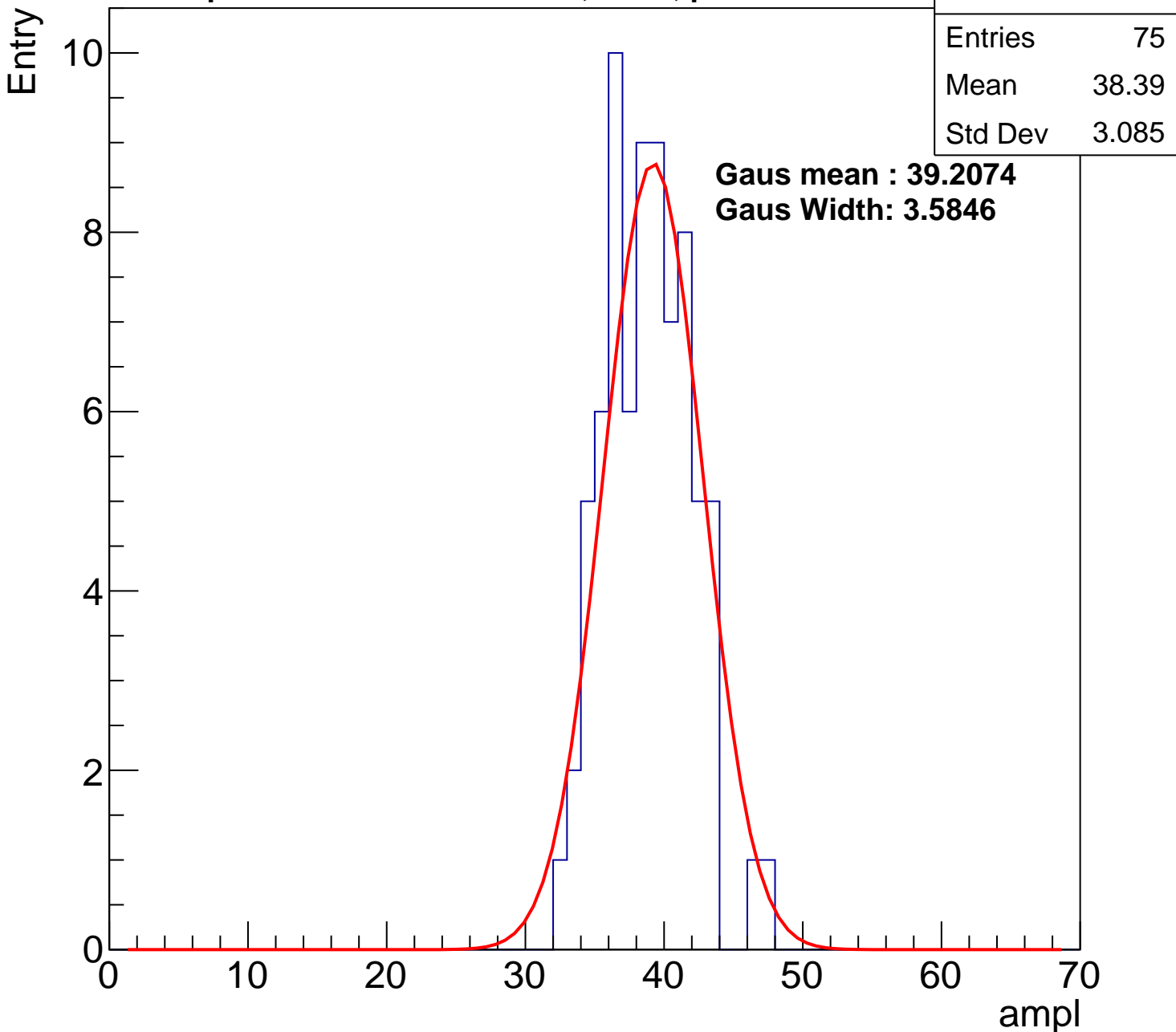
Gaus Width: 3.5846

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch31, adc2

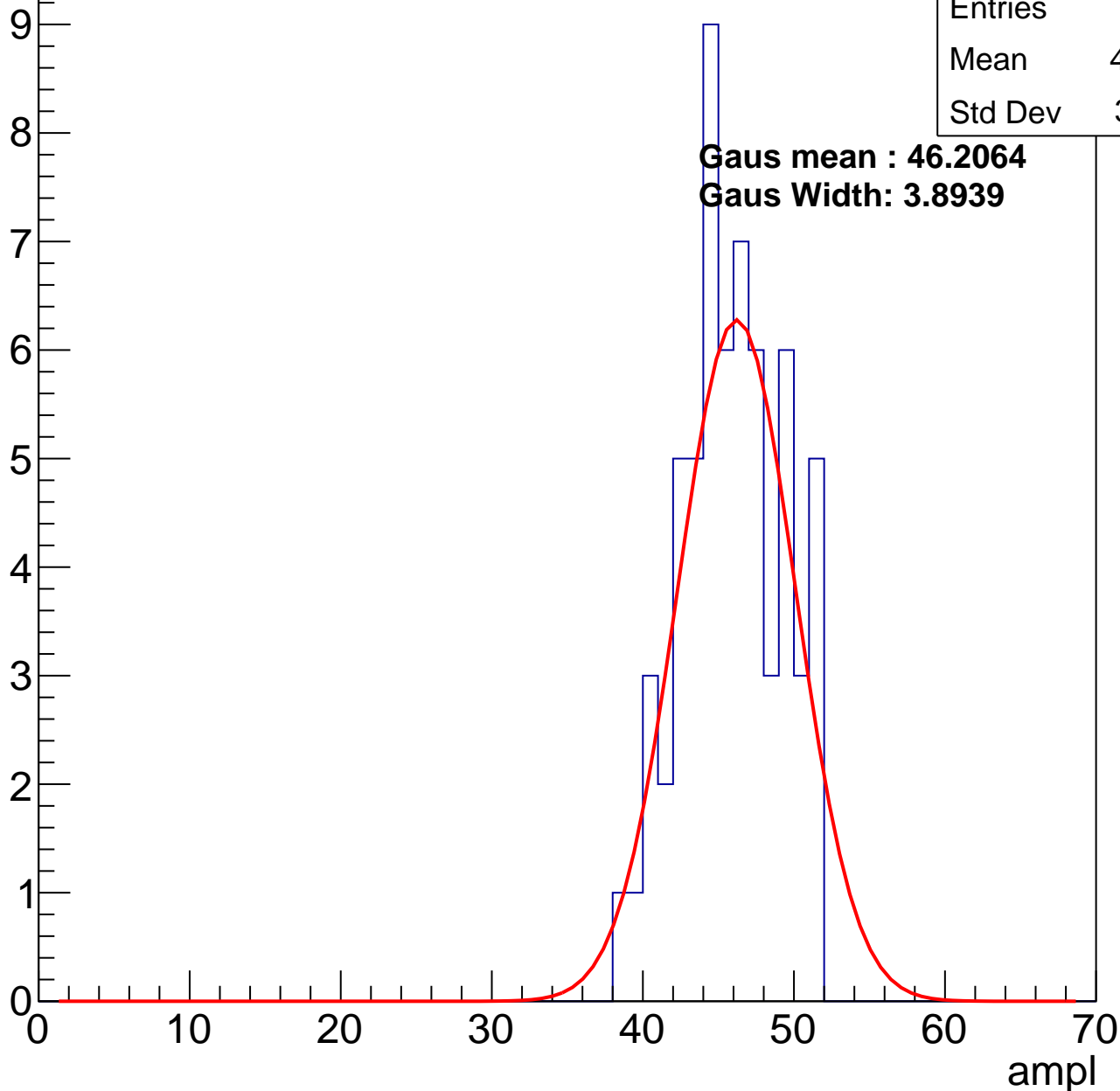
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	45.44
Std Dev	3.271

Gaus mean : 46.2064

Gaus Width: 3.8939

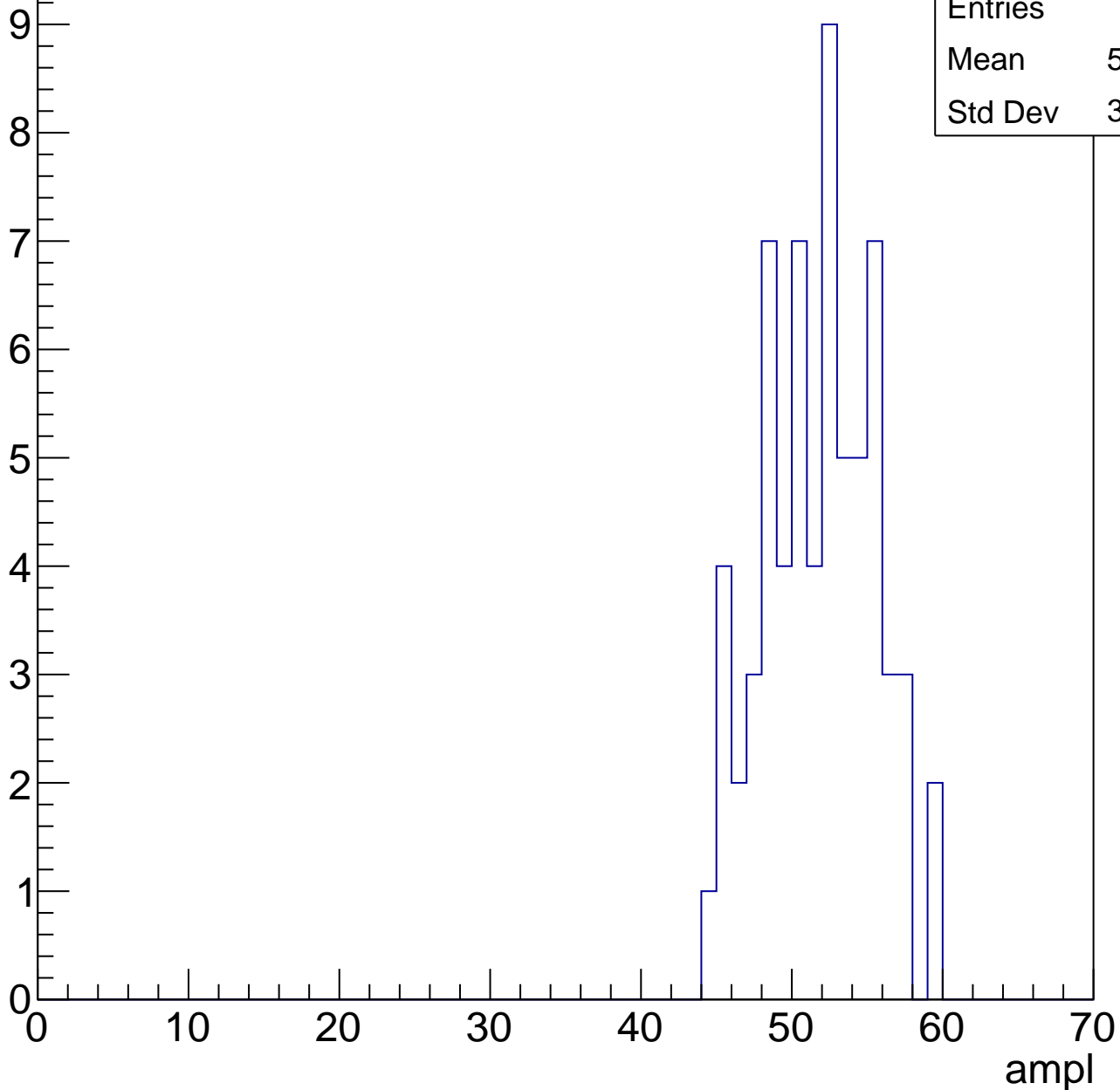


B1L103S, U12-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	51.33
Std Dev	3.624

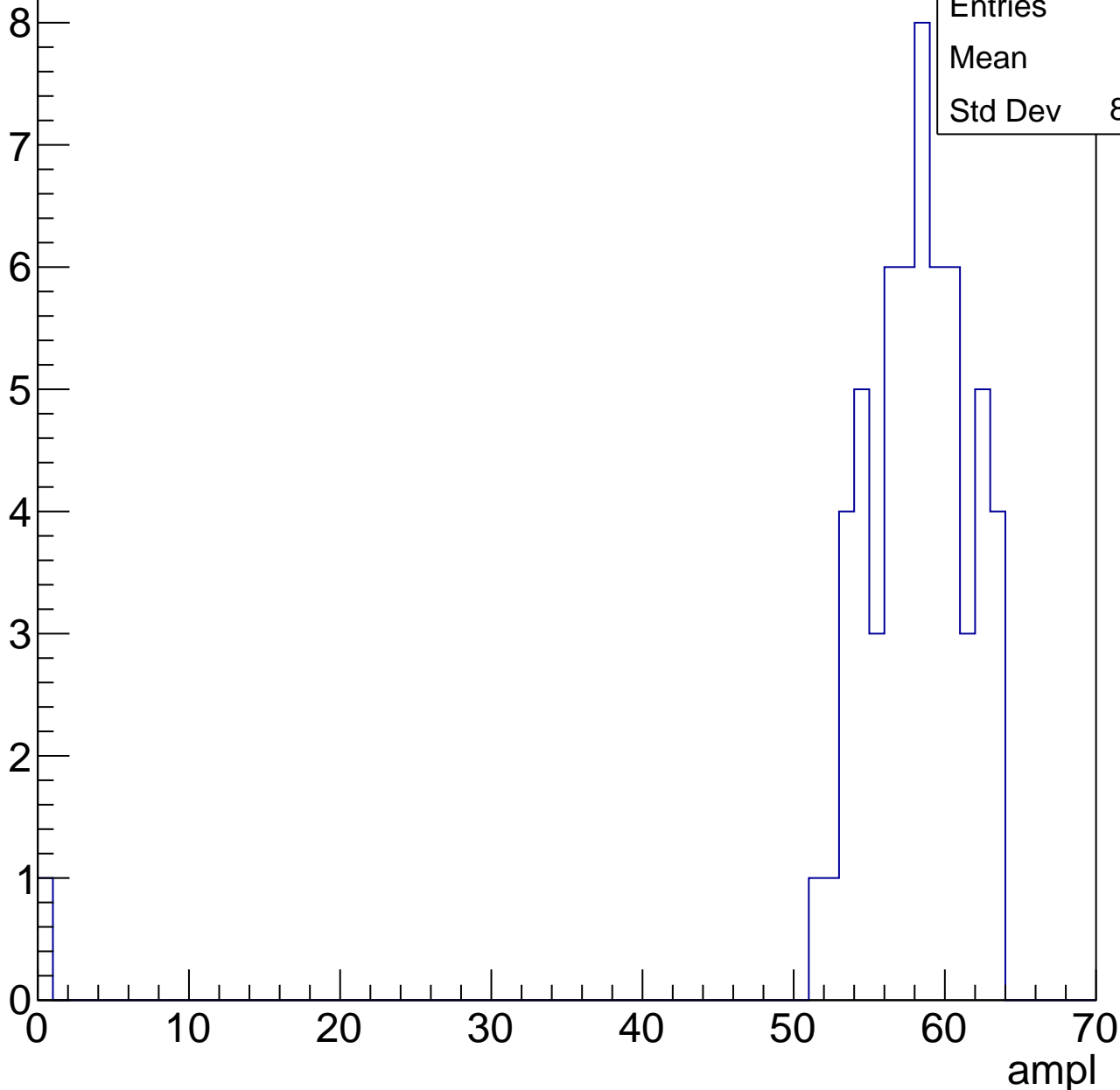


B1L103S, U12-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

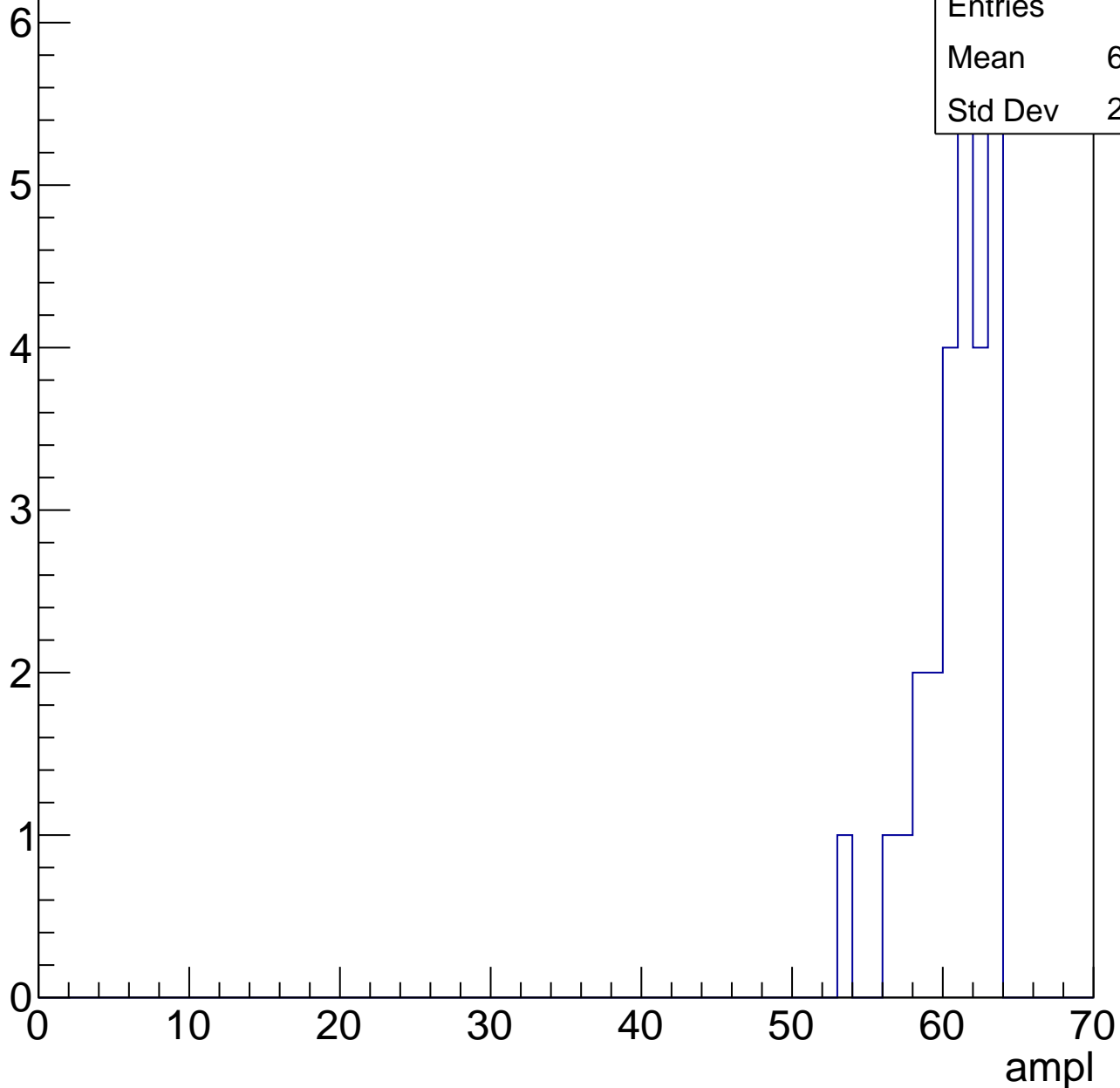
Entries	59
Mean	56.8
Std Dev	8.065



B1L103S, U12-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	27
Mean	60.44
Std Dev	2.393

B1L103S, U12-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



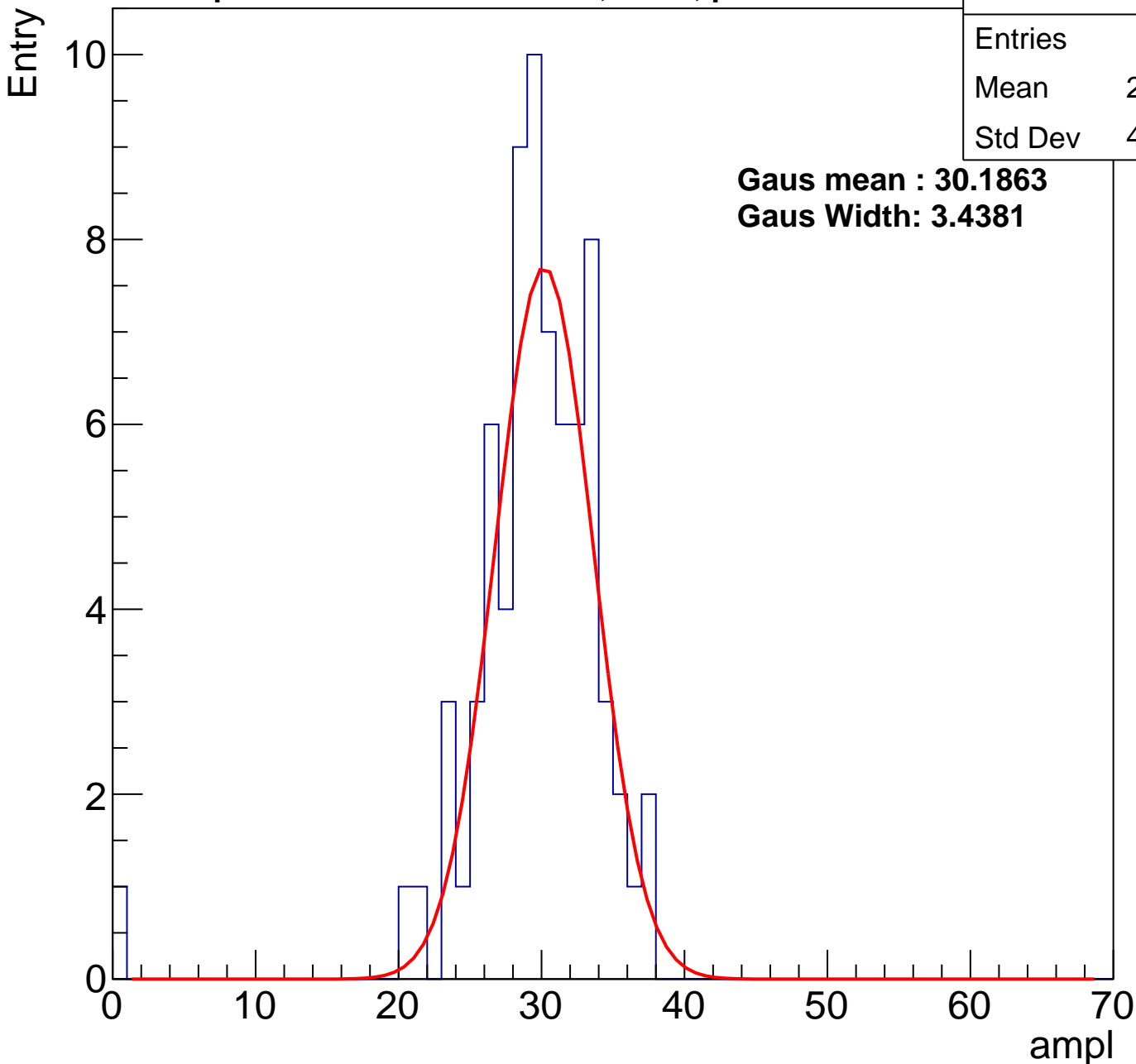
Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch32, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	29.04
Std Dev	4.908

Gaus mean : 30.1863
Gaus Width: 3.4381



B1L103S, U12-ch32, adc1

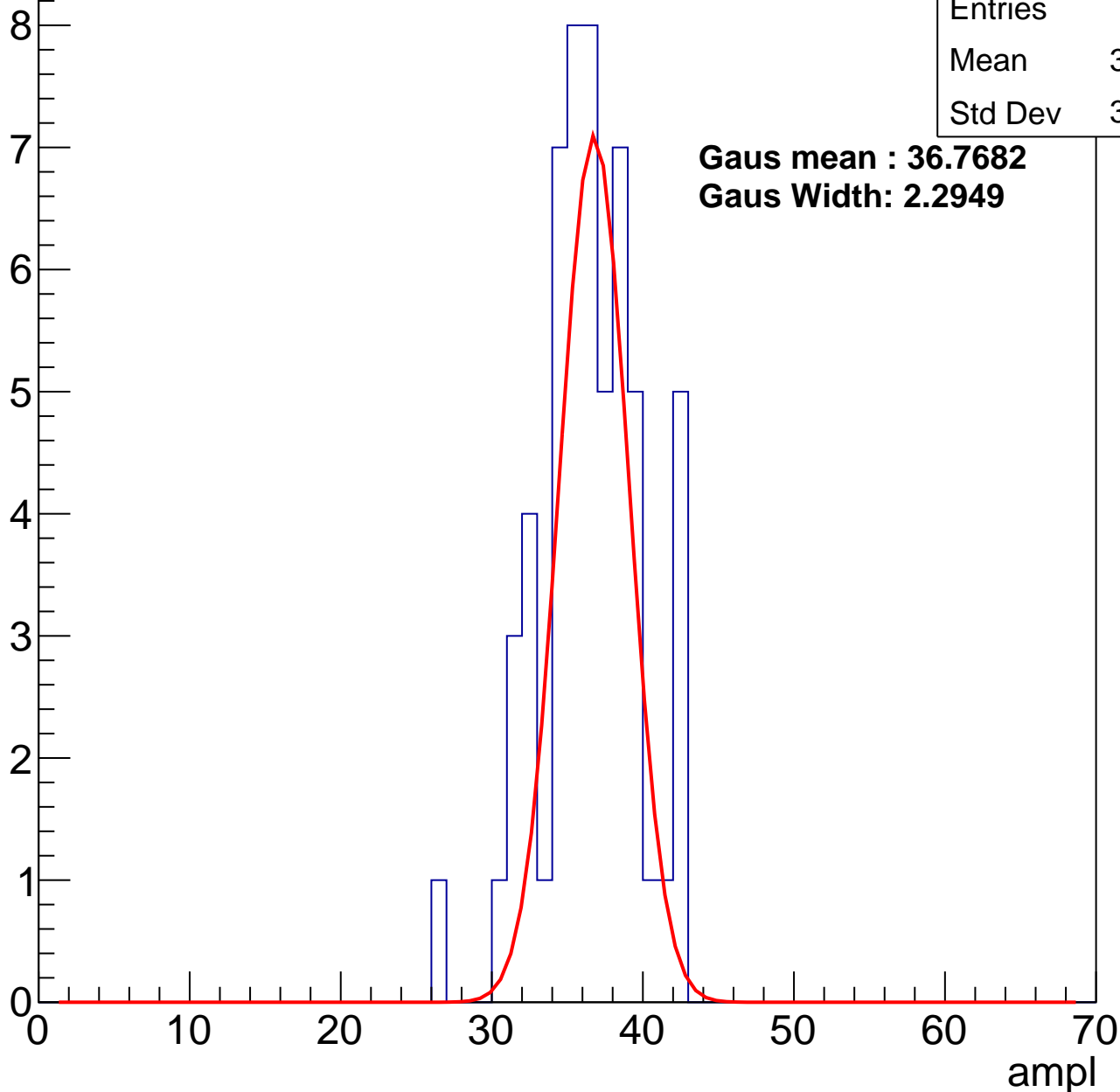
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.02
Std Dev	3.295

Gaus mean : 36.7682

Gaus Width: 2.2949



B1L103S, U12-ch32, adc2

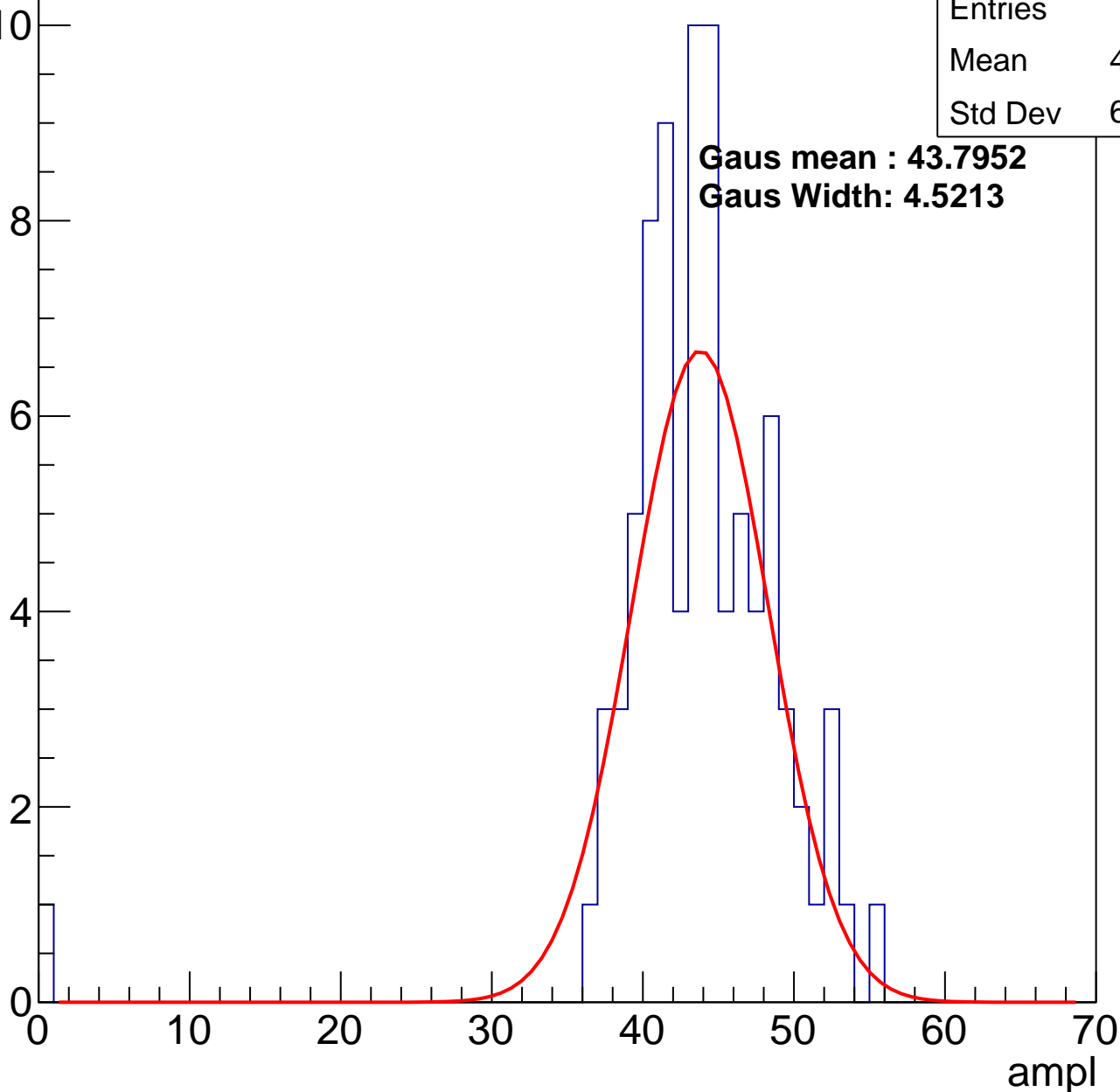
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	43.23
Std Dev	6.278

Gaus mean : 43.7952

Gaus Width: 4.5213

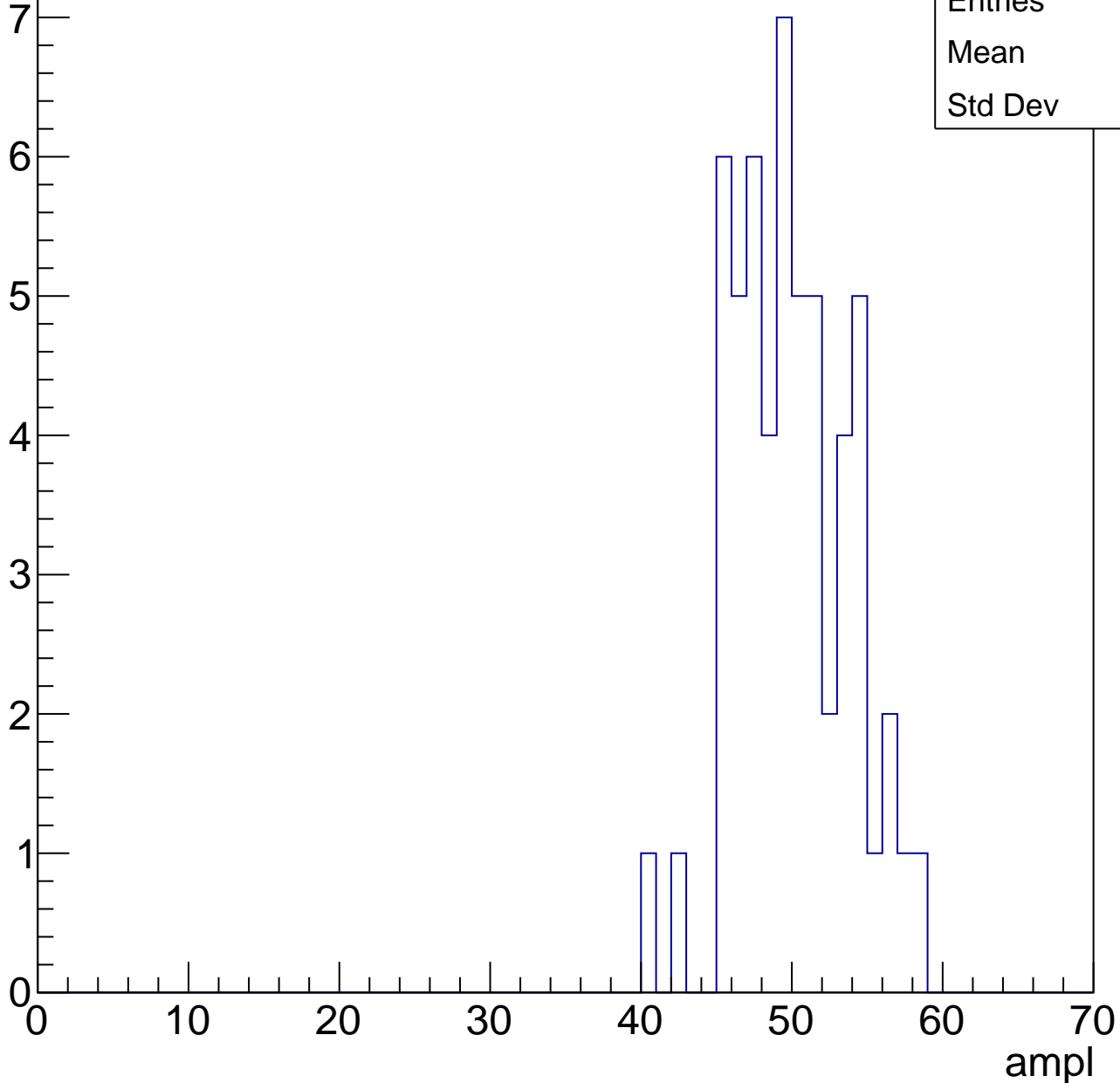


B1L103S, U12-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

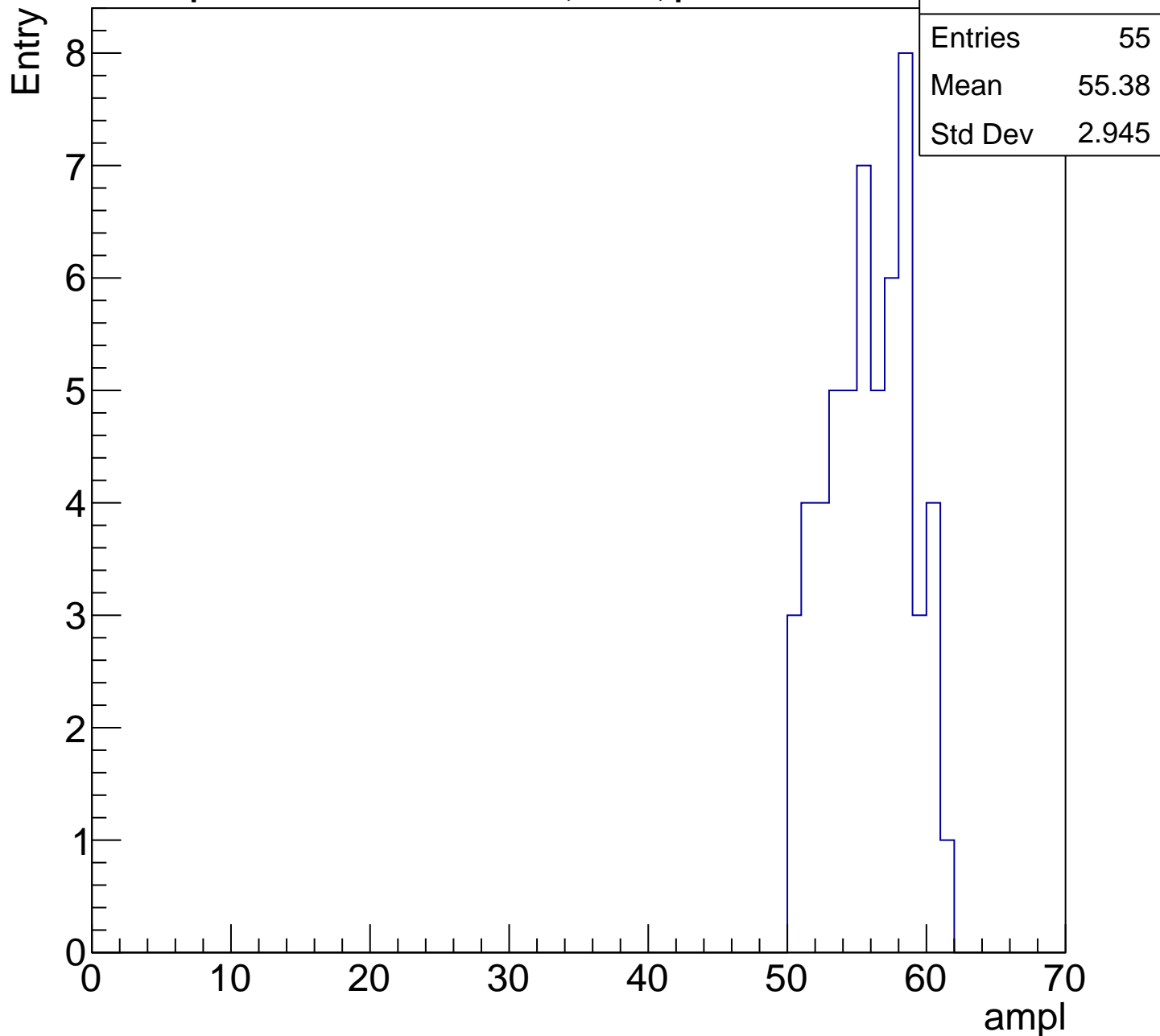
Entry

Entries	56
Mean	49.5
Std Dev	3.77



B1L103S, U12-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

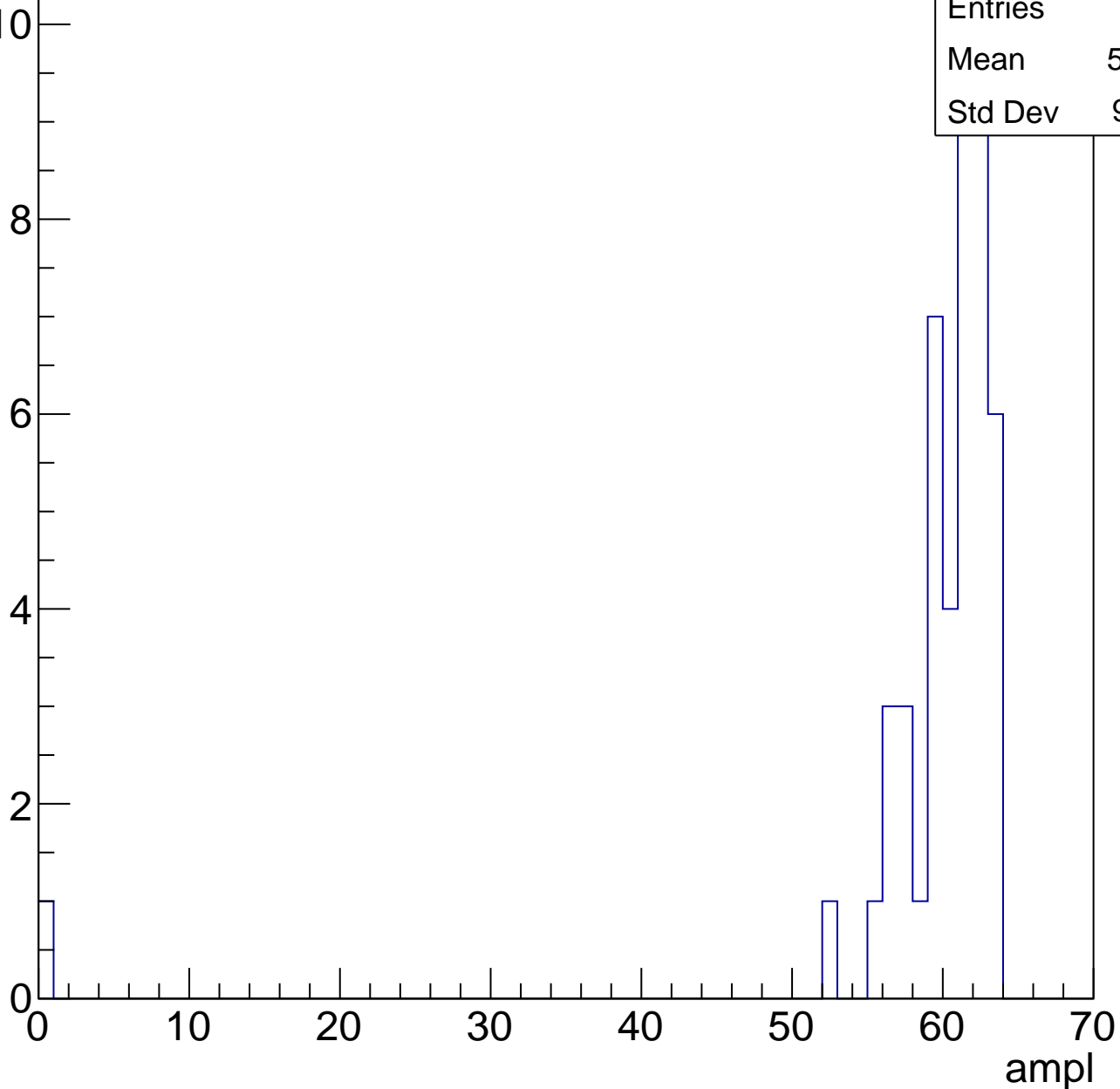


B1L103S, U12-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

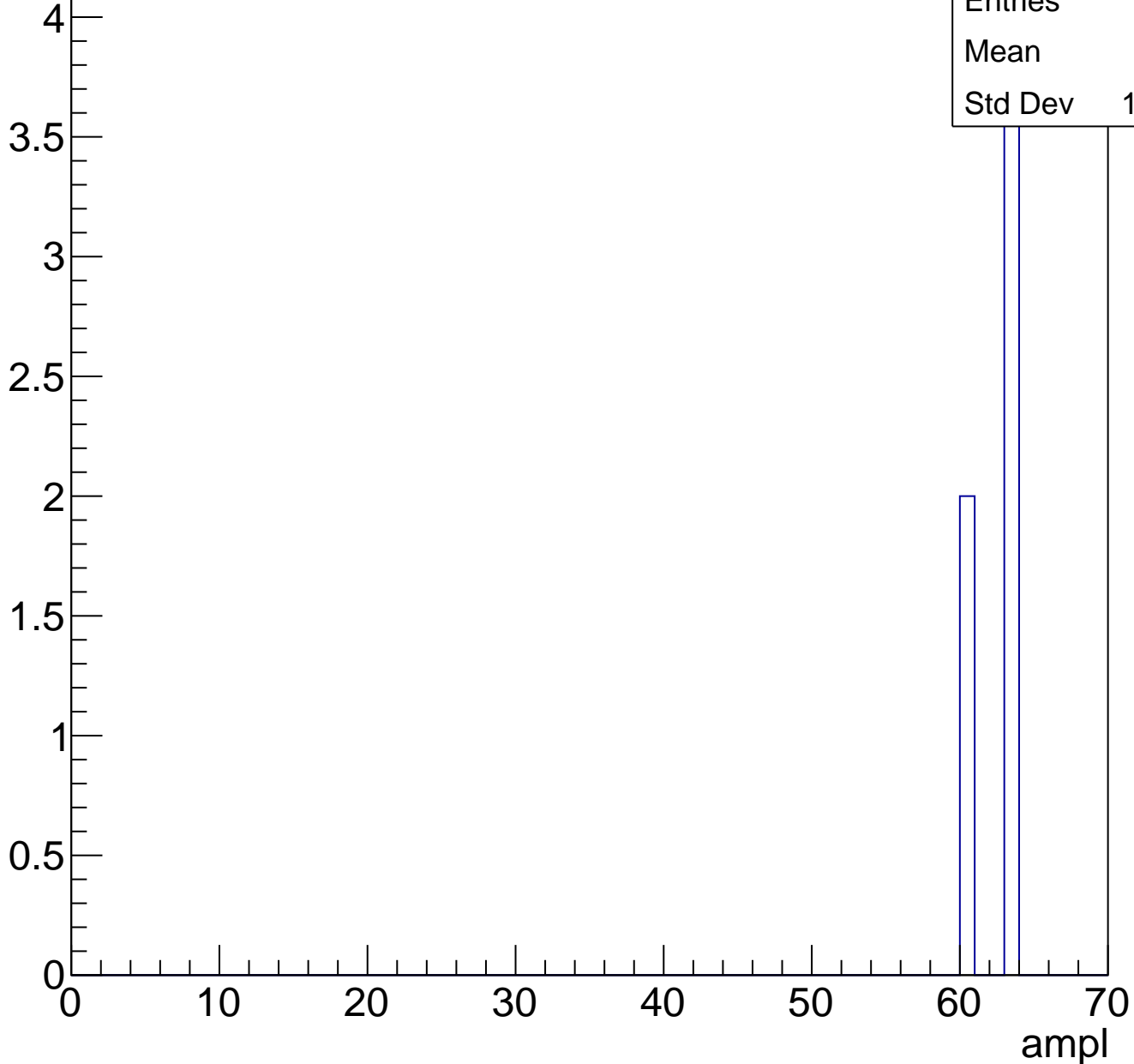
Entries	46
Mean	58.78
Std Dev	9.101



B1L103S, U12-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch33, adc0

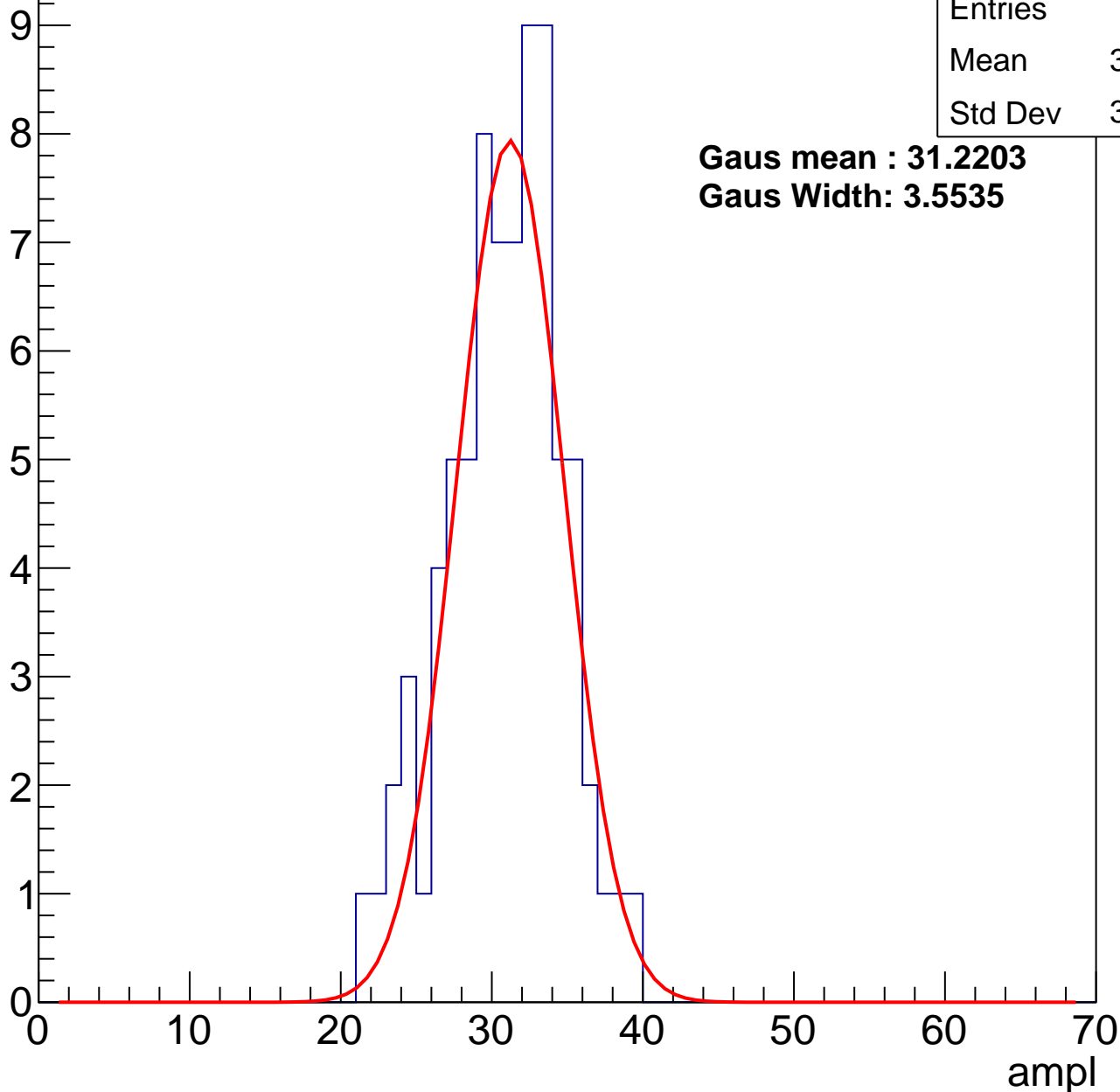
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	30.39
Std Dev	3.773

Gaus mean : 31.2203

Gaus Width: 3.5535



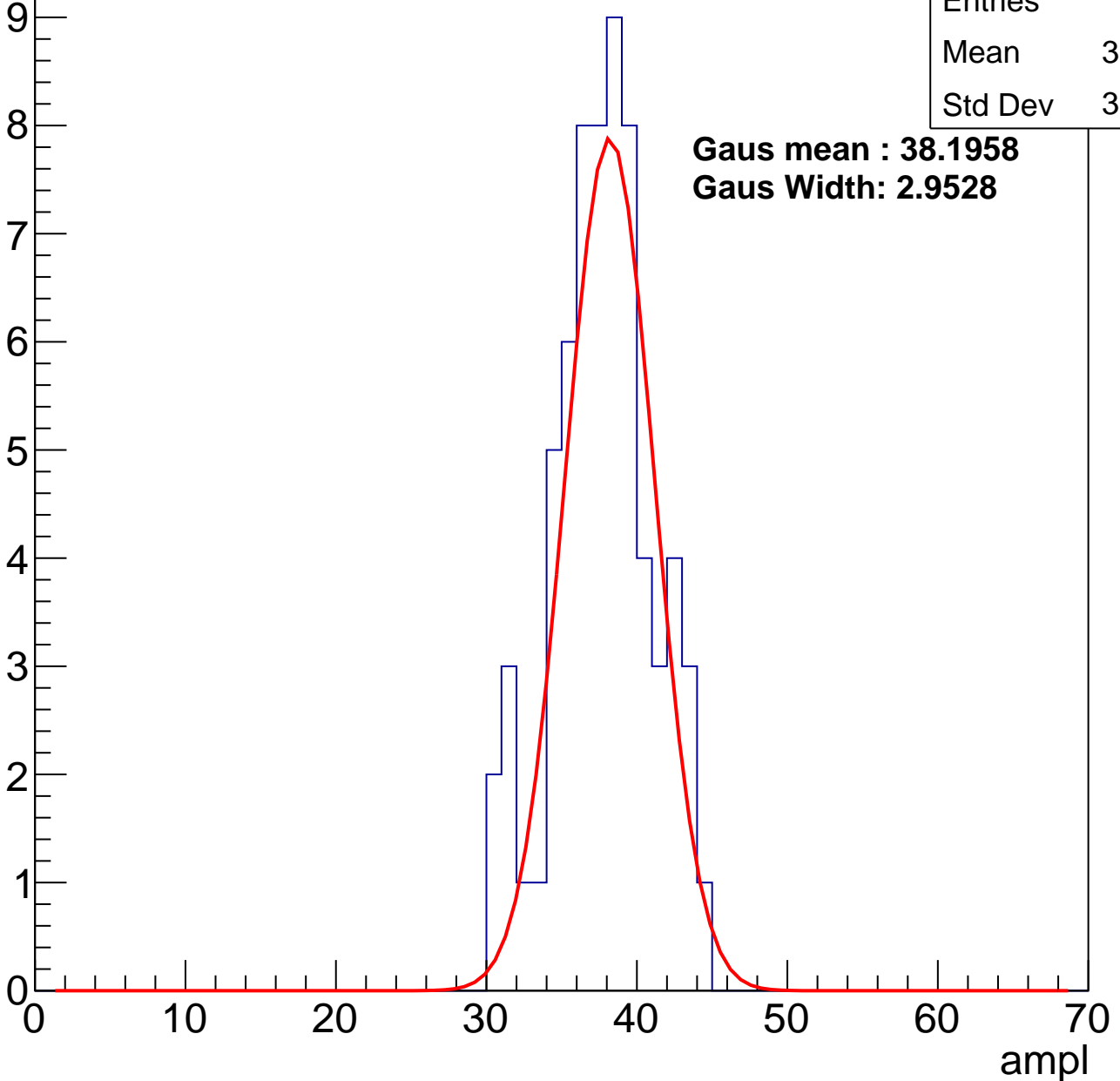
B1L103S, U12-ch33, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	37.27
Std Dev	3.259

Gaus mean : 38.1958
Gaus Width: 2.9528



B1L103S, U12-ch33, adc2

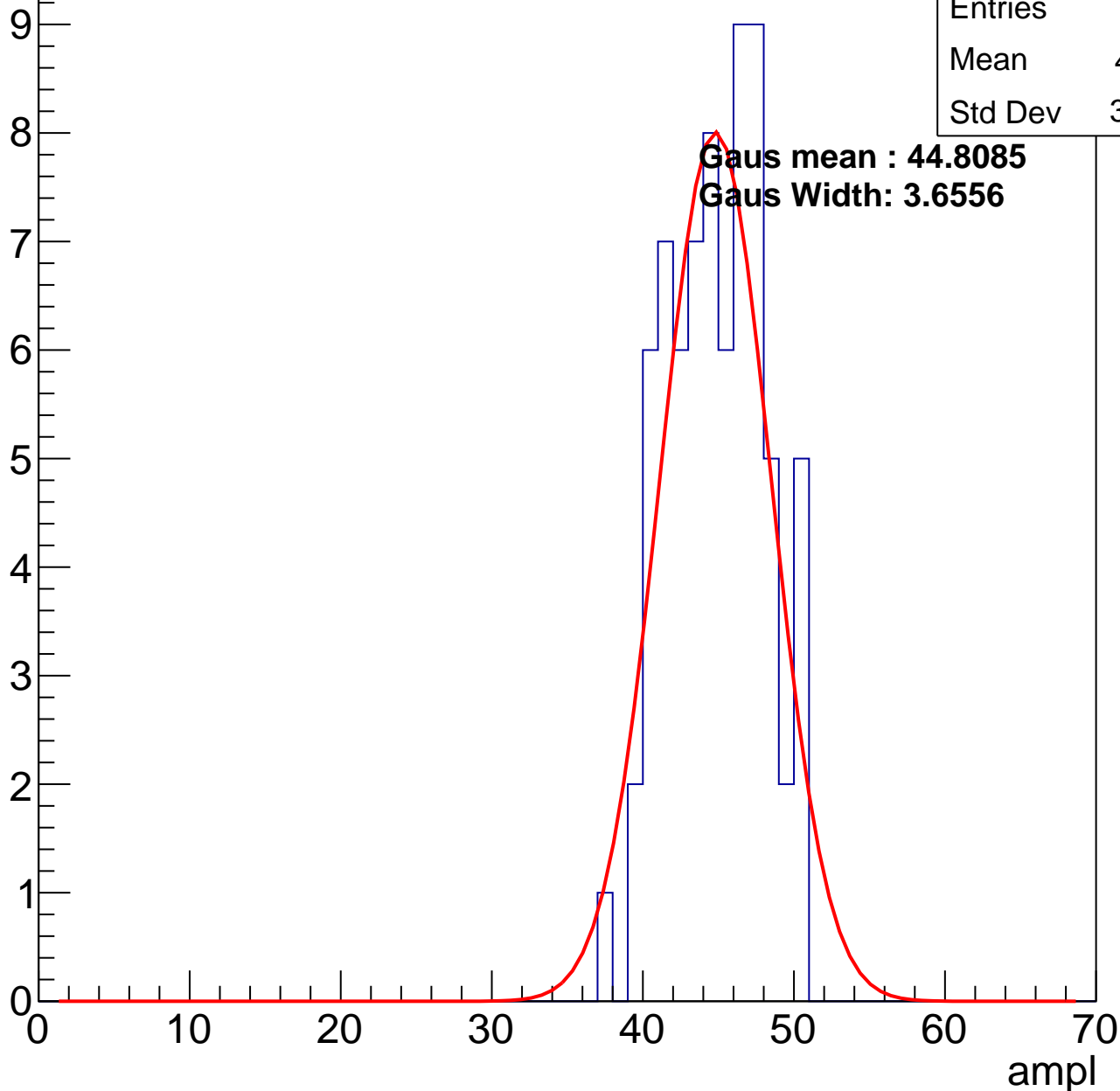
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	44.41
Std Dev	3.118

Gaus mean : 44.8085

Gaus Width: 3.6556

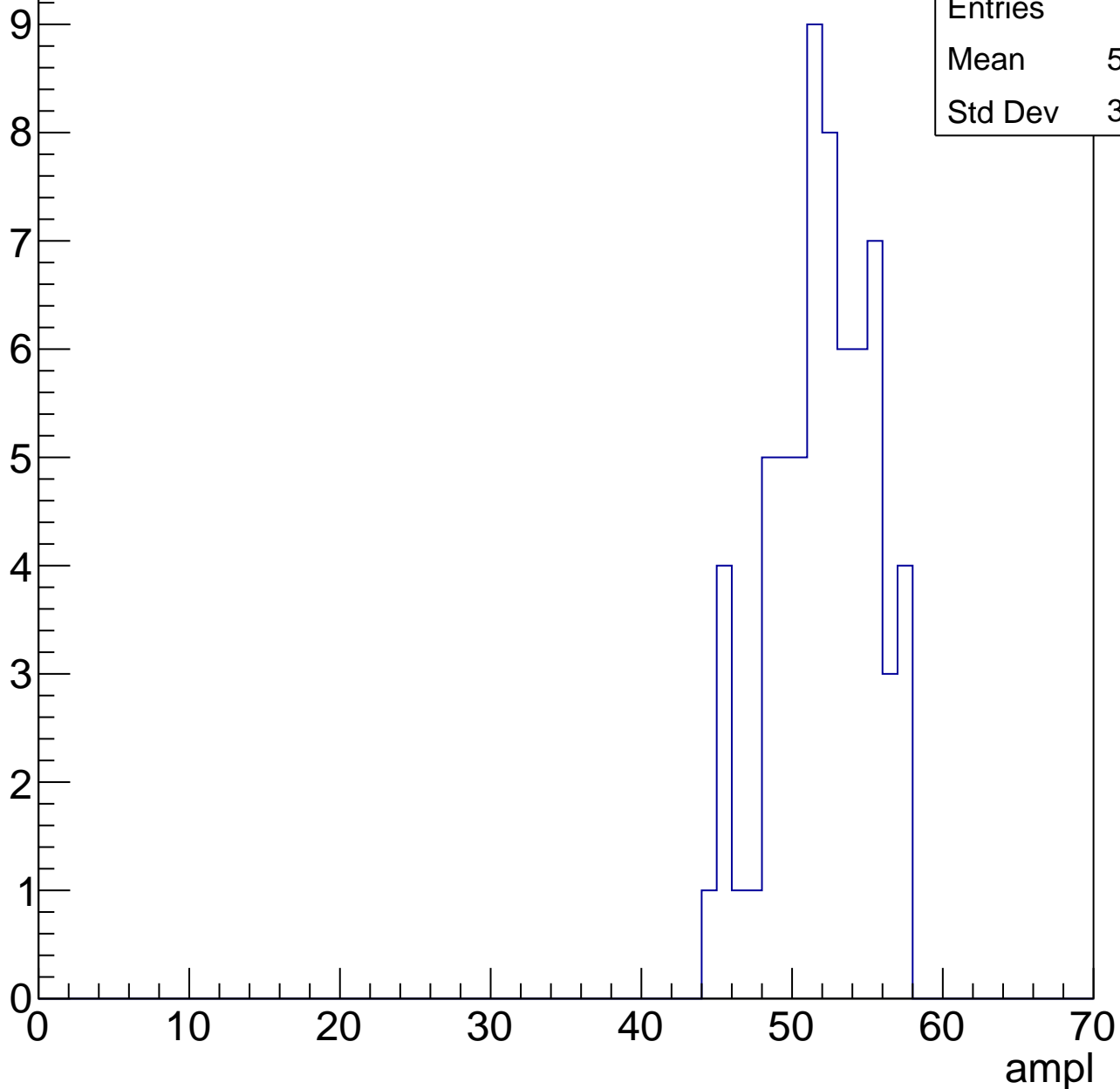


B1L103S, U12-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

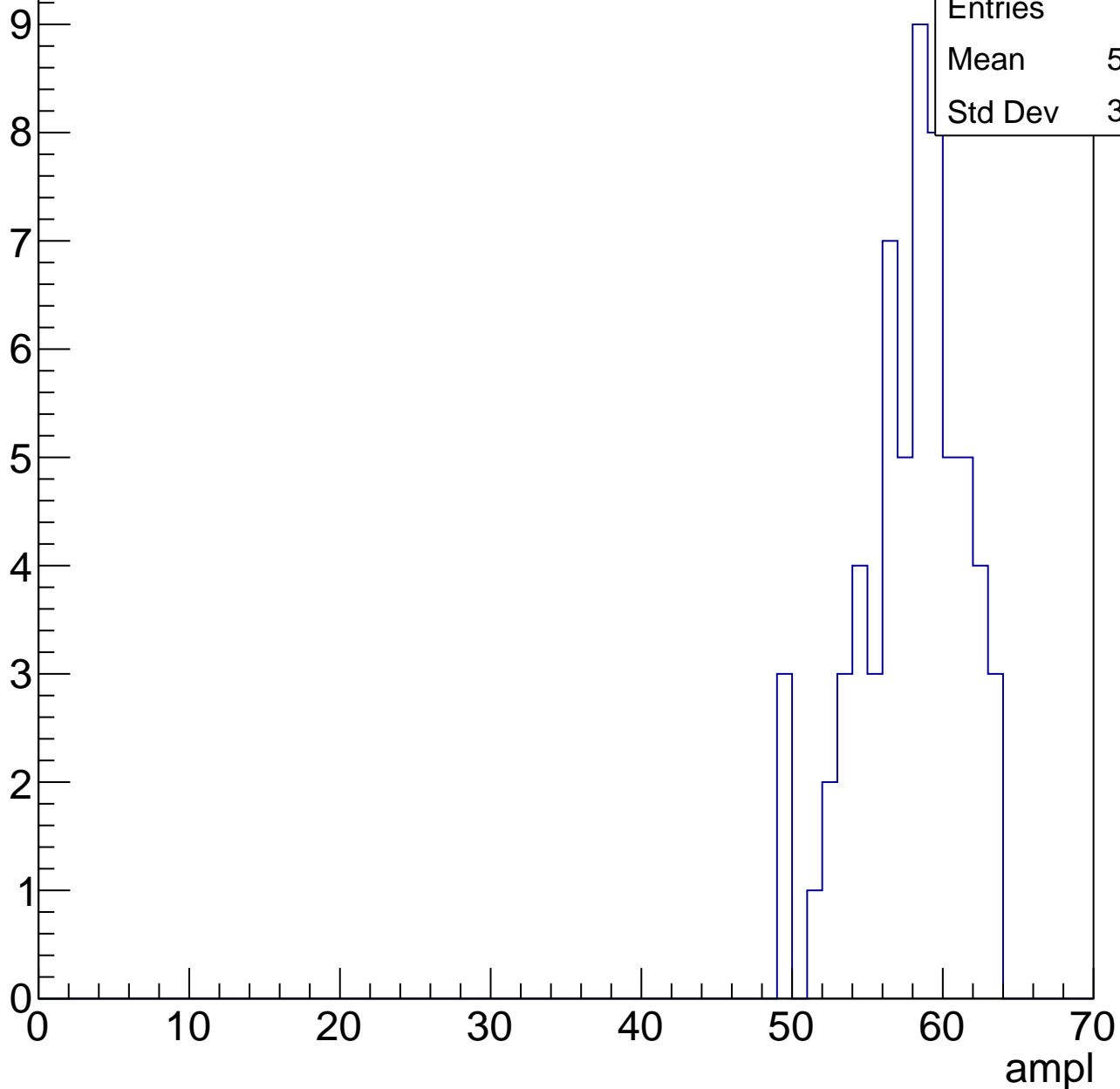
Entries	65
Mean	51.54
Std Dev	3.287



B1L103S, U12-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

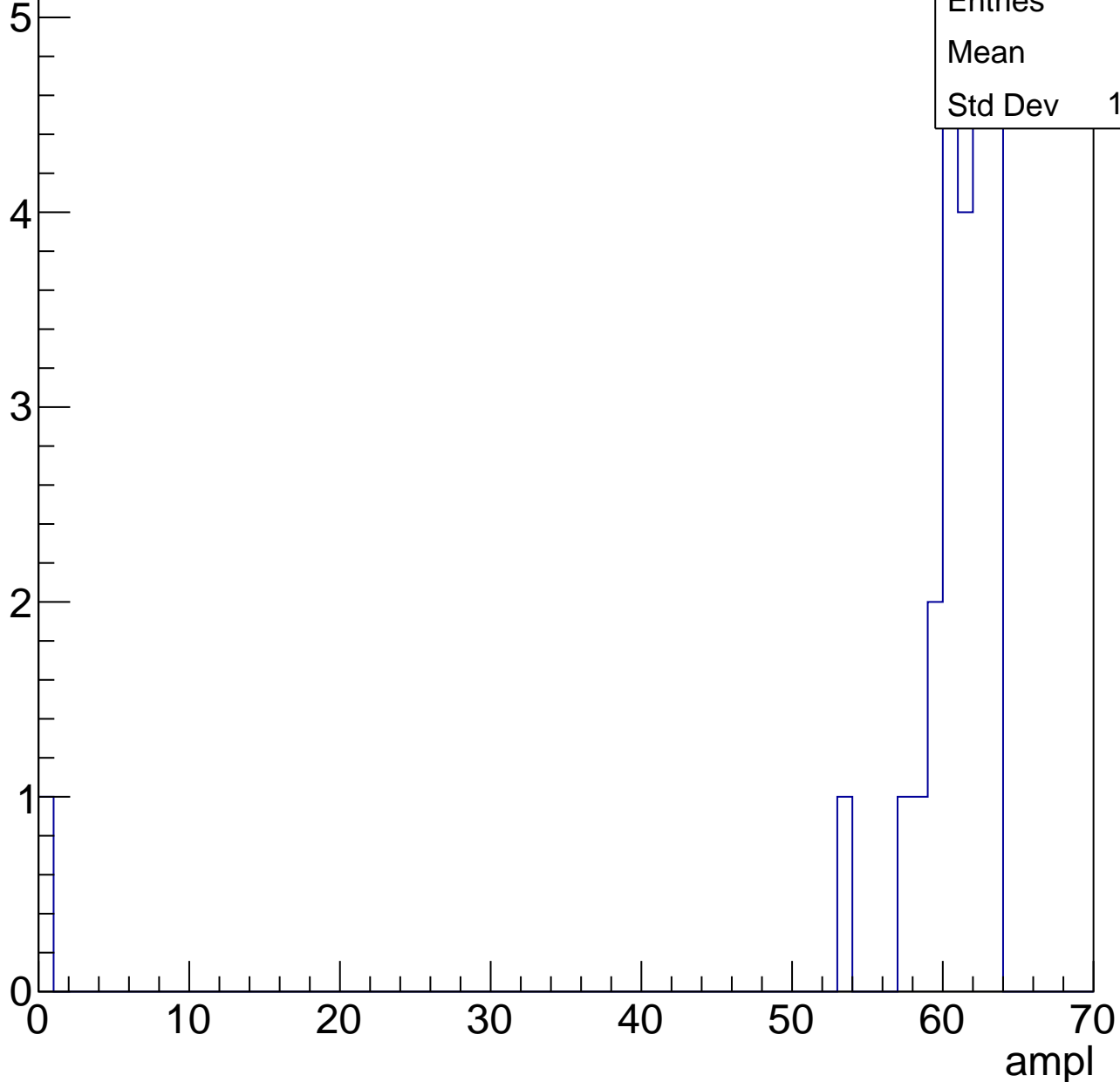
Entry



B1L103S, U12-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U12-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch34, adc0

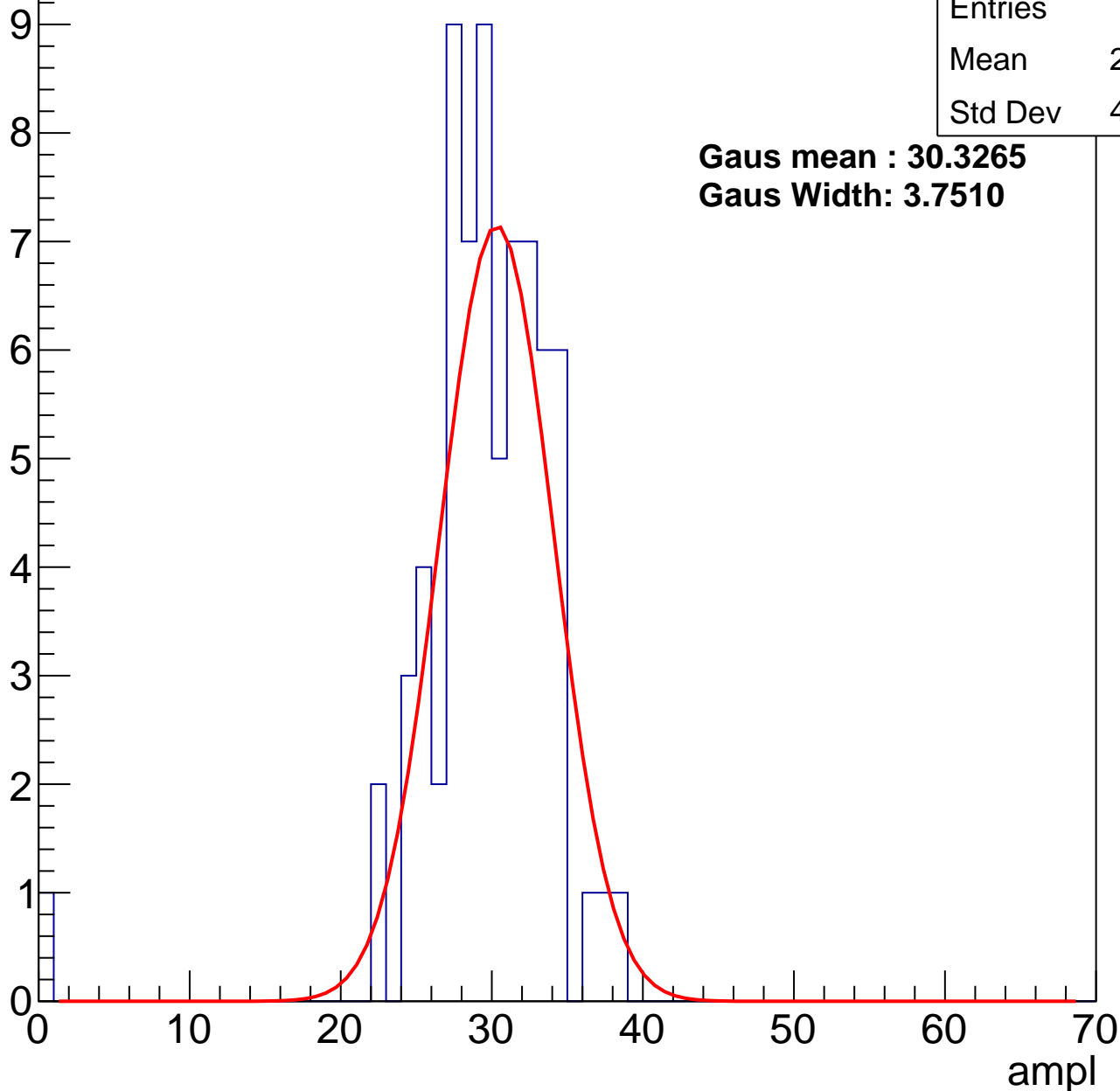
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	29.18
Std Dev	4.854

Gaus mean : 30.3265

Gaus Width: 3.7510



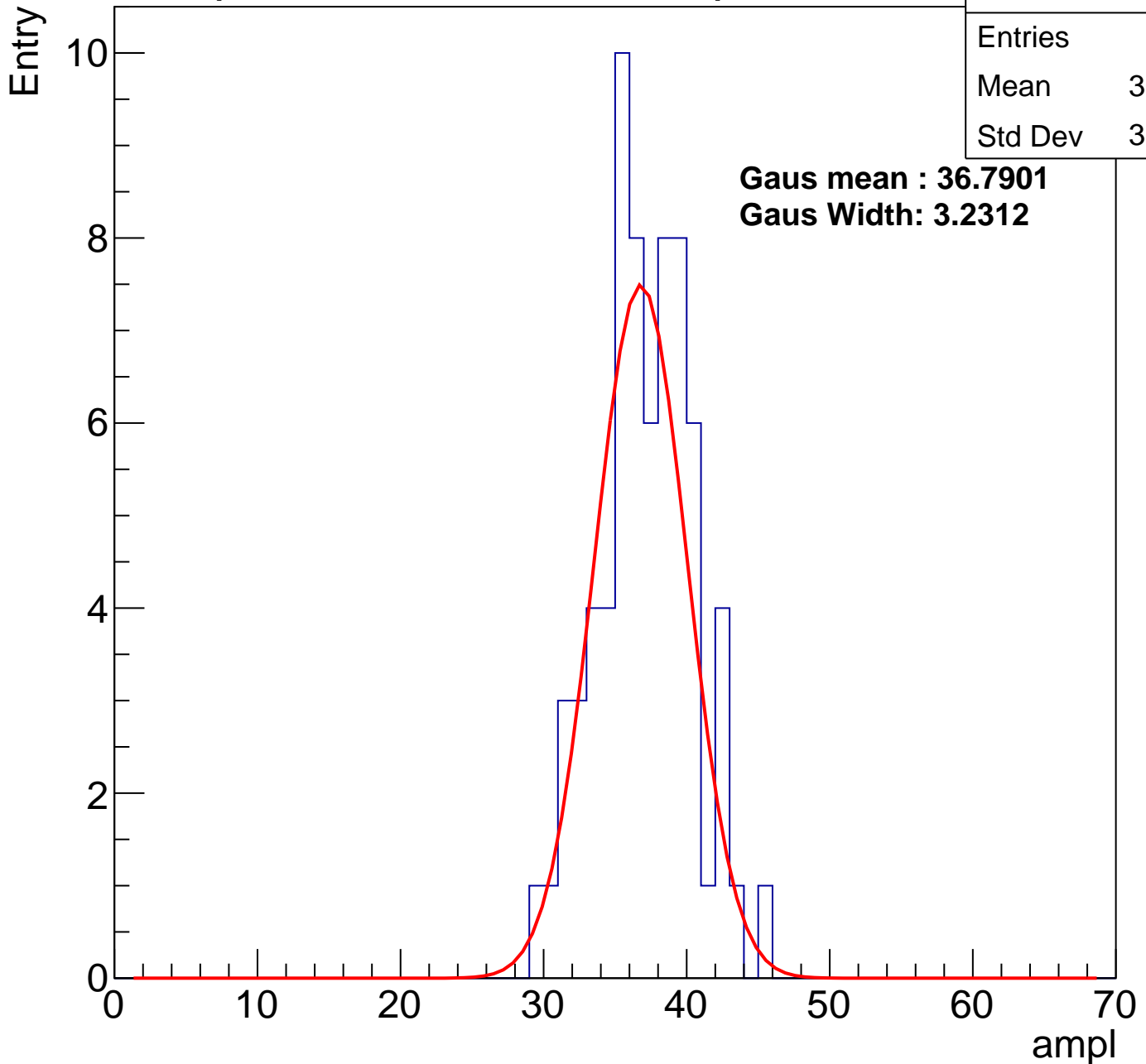
B1L103S, U12-ch34, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	36.65
Std Dev	3.287

Gaus mean : 36.7901

Gaus Width: 3.2312



B1L103S, U12-ch34, adc2

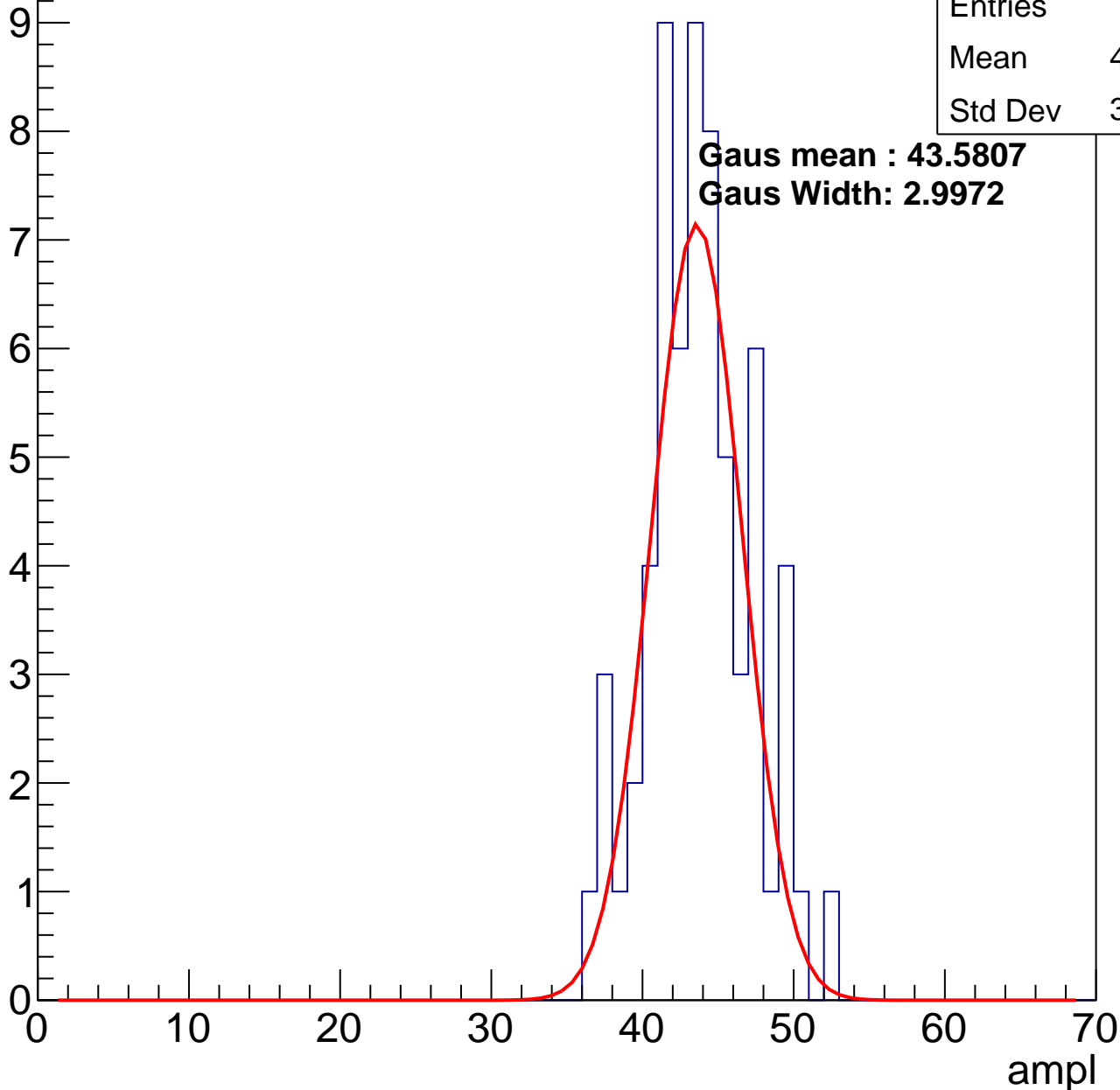
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.34
Std Dev	3.388

Gaus mean : 43.5807

Gaus Width: 2.9972

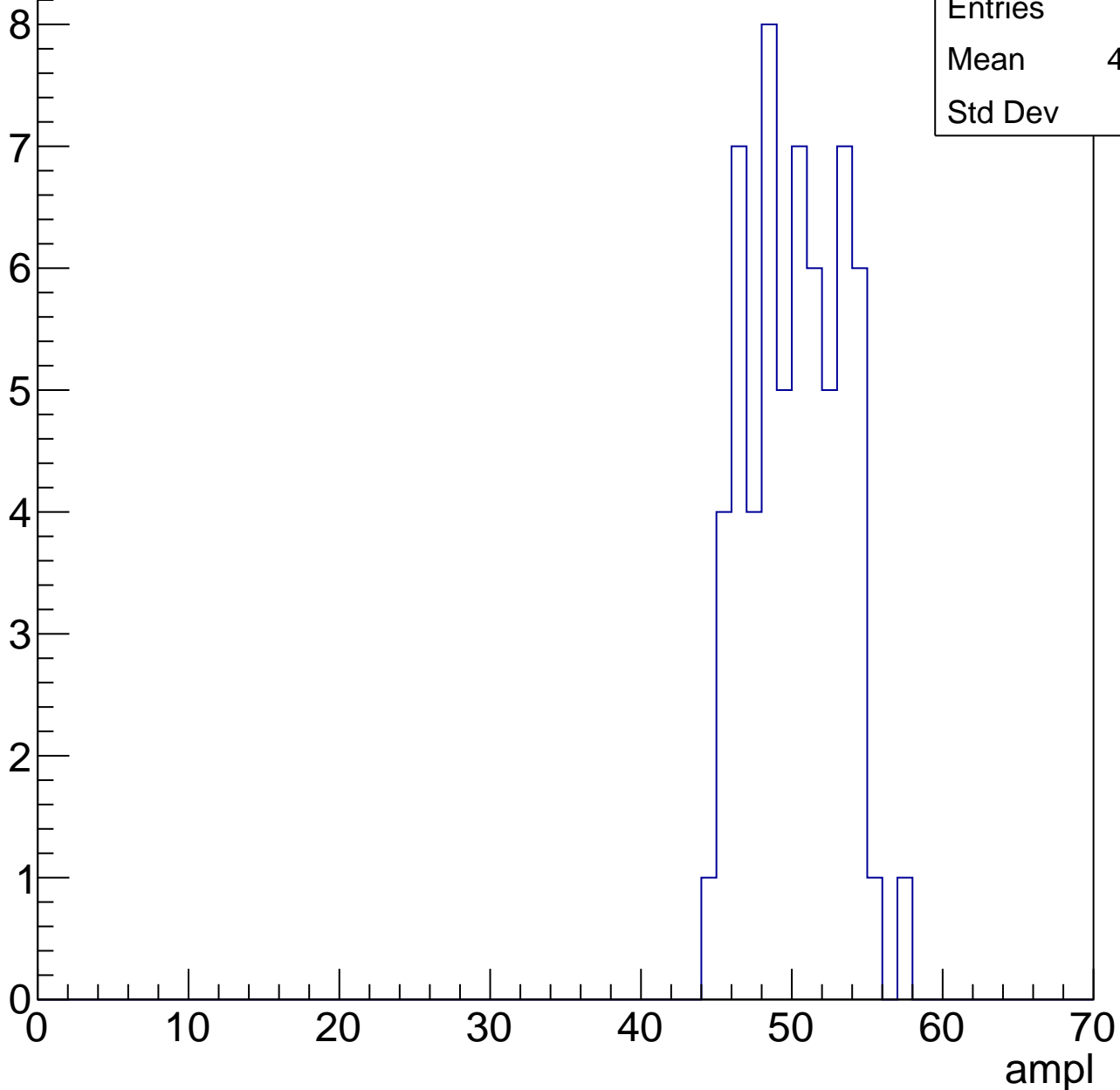


B1L103S, U12-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	49.77
Std Dev	3.05

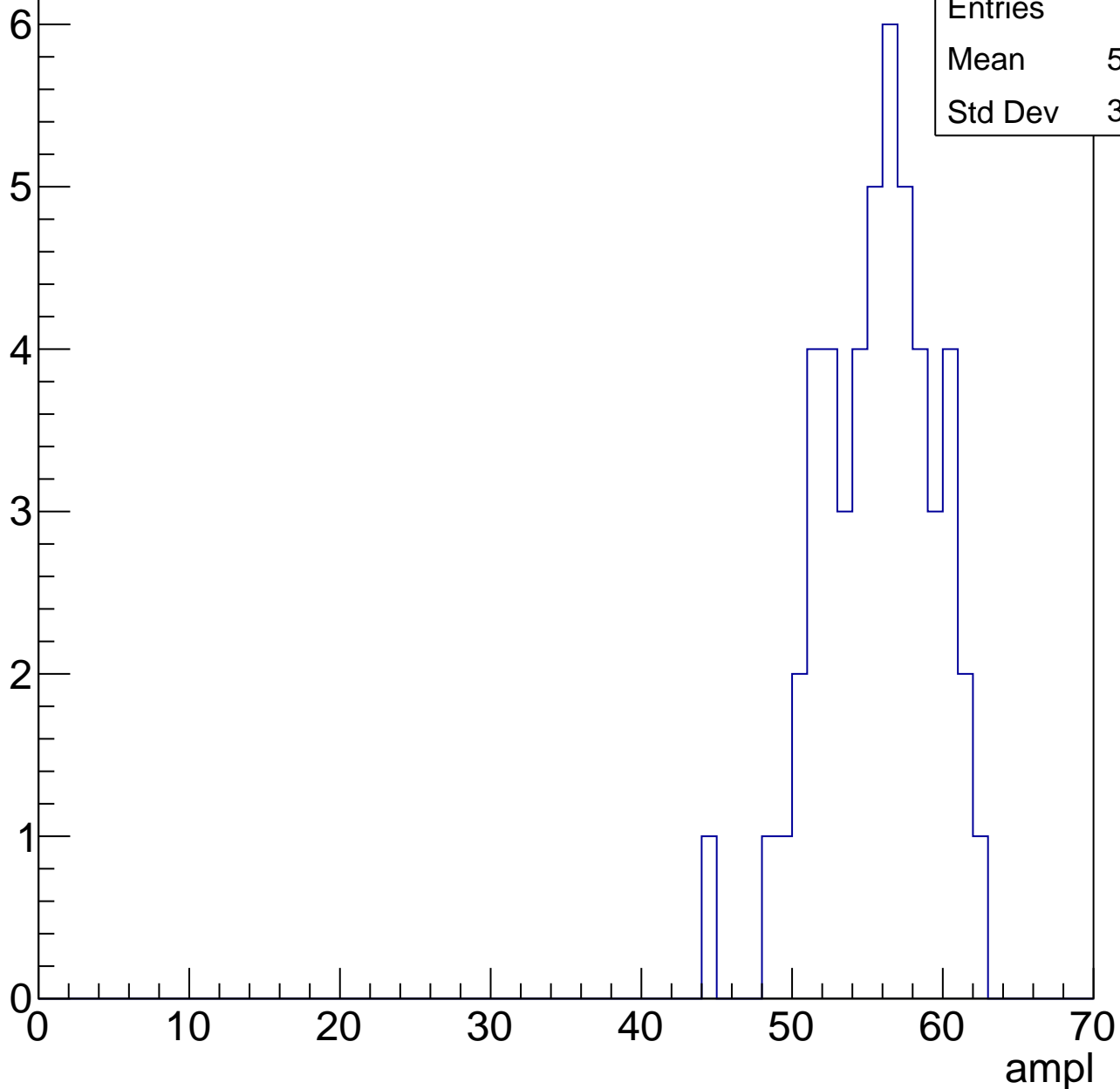


B1L103S, U12-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

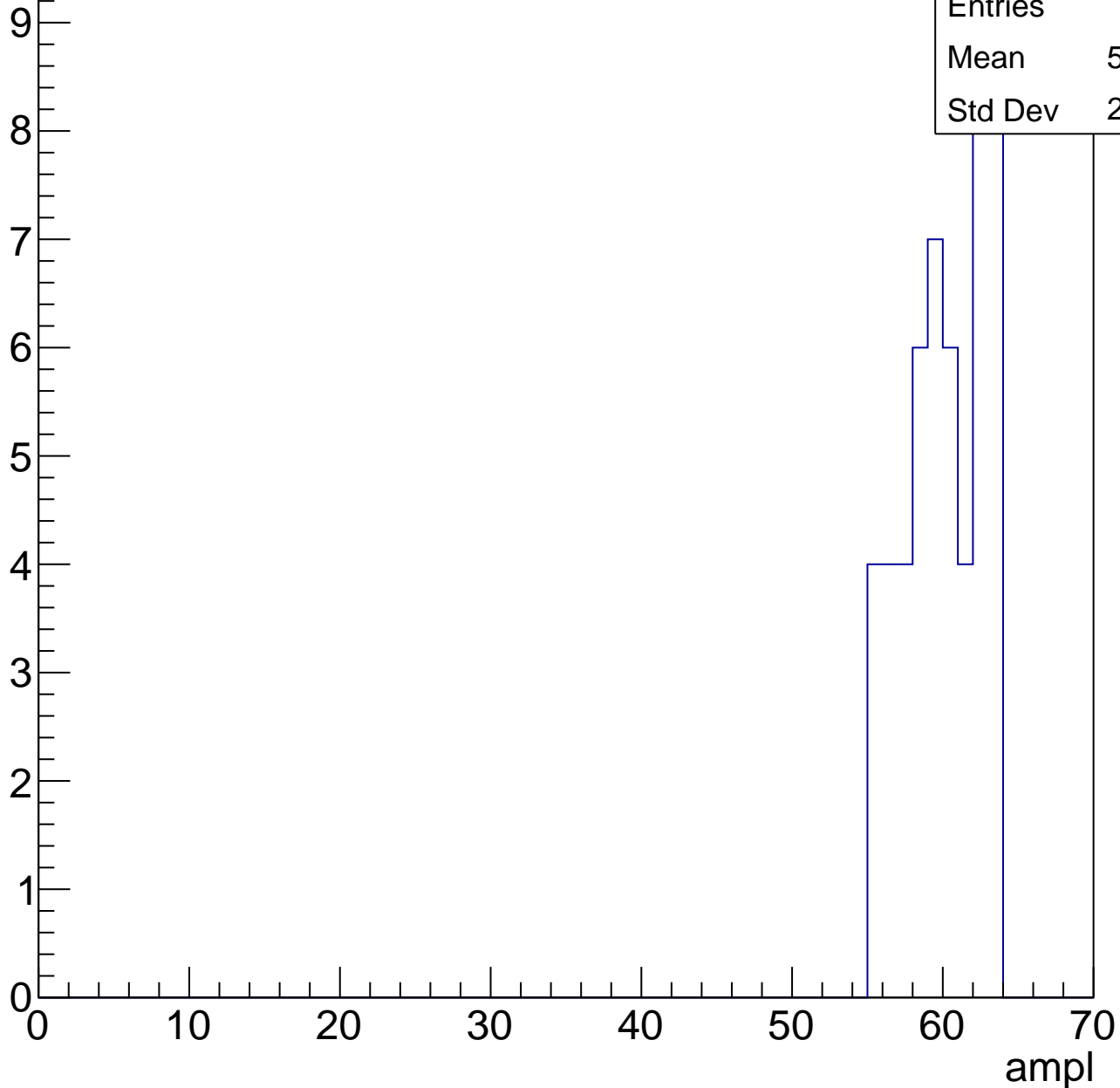
Entries	50
Mean	55.14
Std Dev	3.747



B1L103S, U12-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

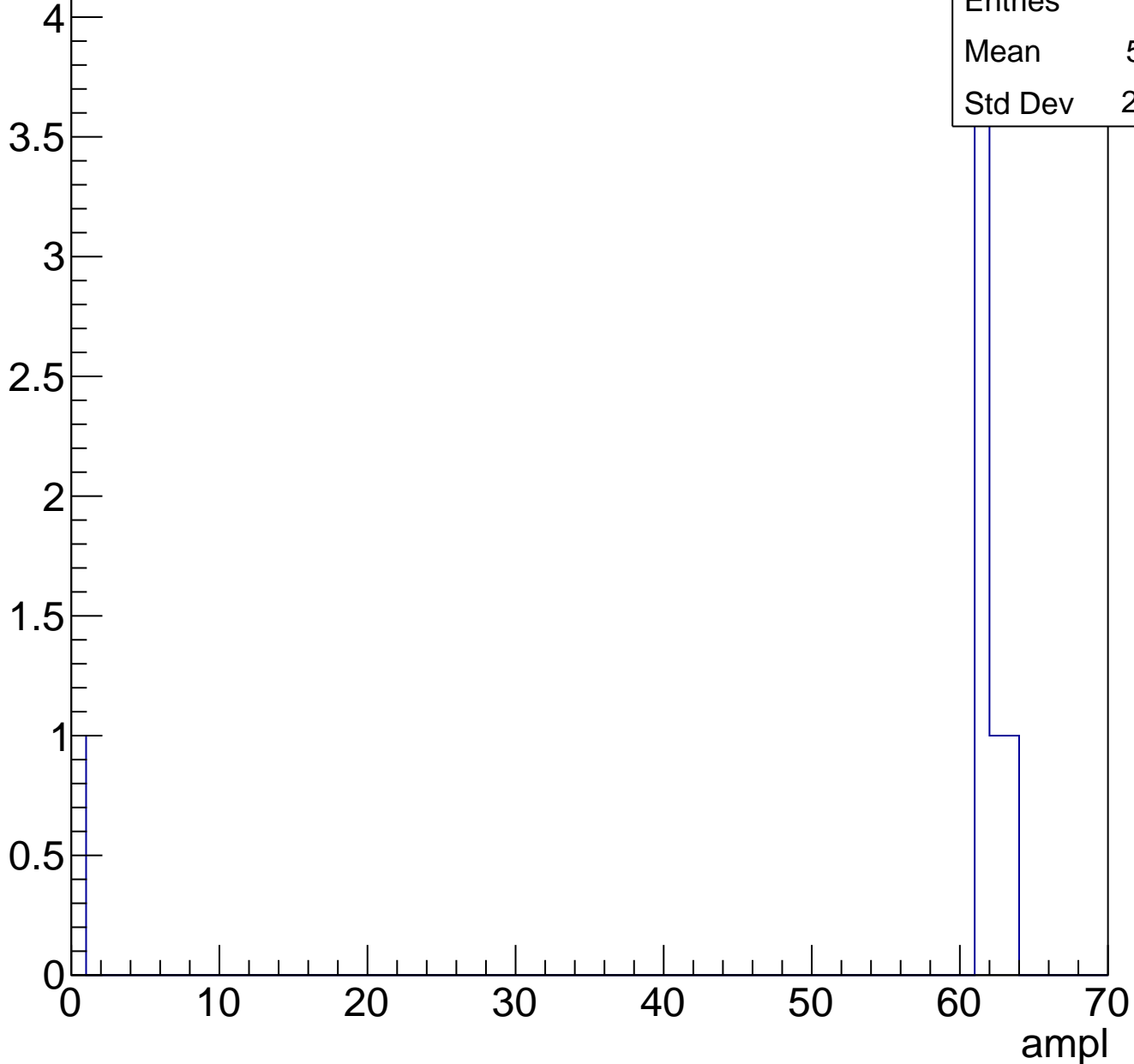
Entry



B1L103S, U12-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch35, adc0

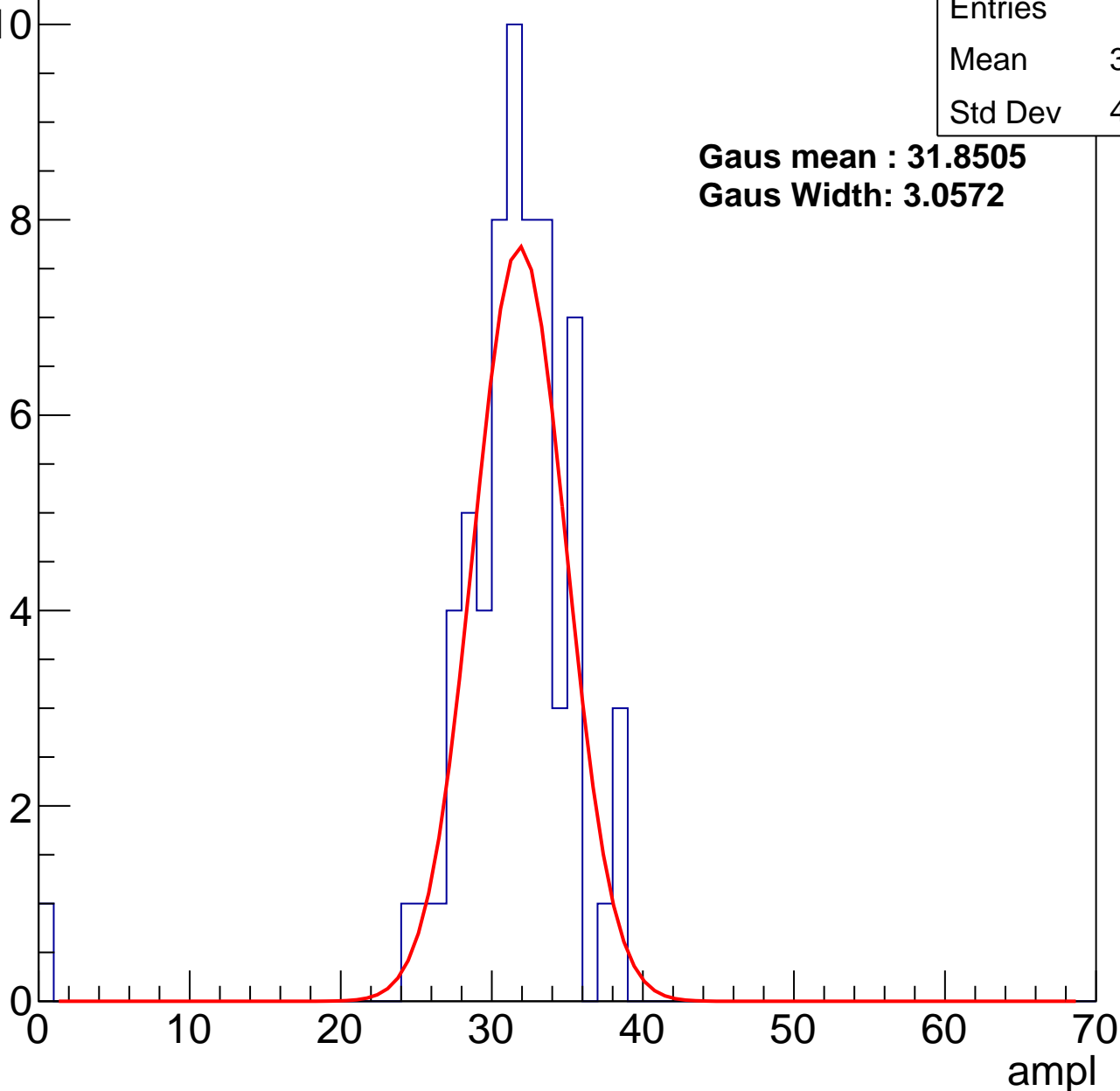
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.88
Std Dev	4.907

Gaus mean : 31.8505

Gaus Width: 3.0572



B1L103S, U12-ch35, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	38.18
Std Dev	3.249

Gaus mean : 38.7229

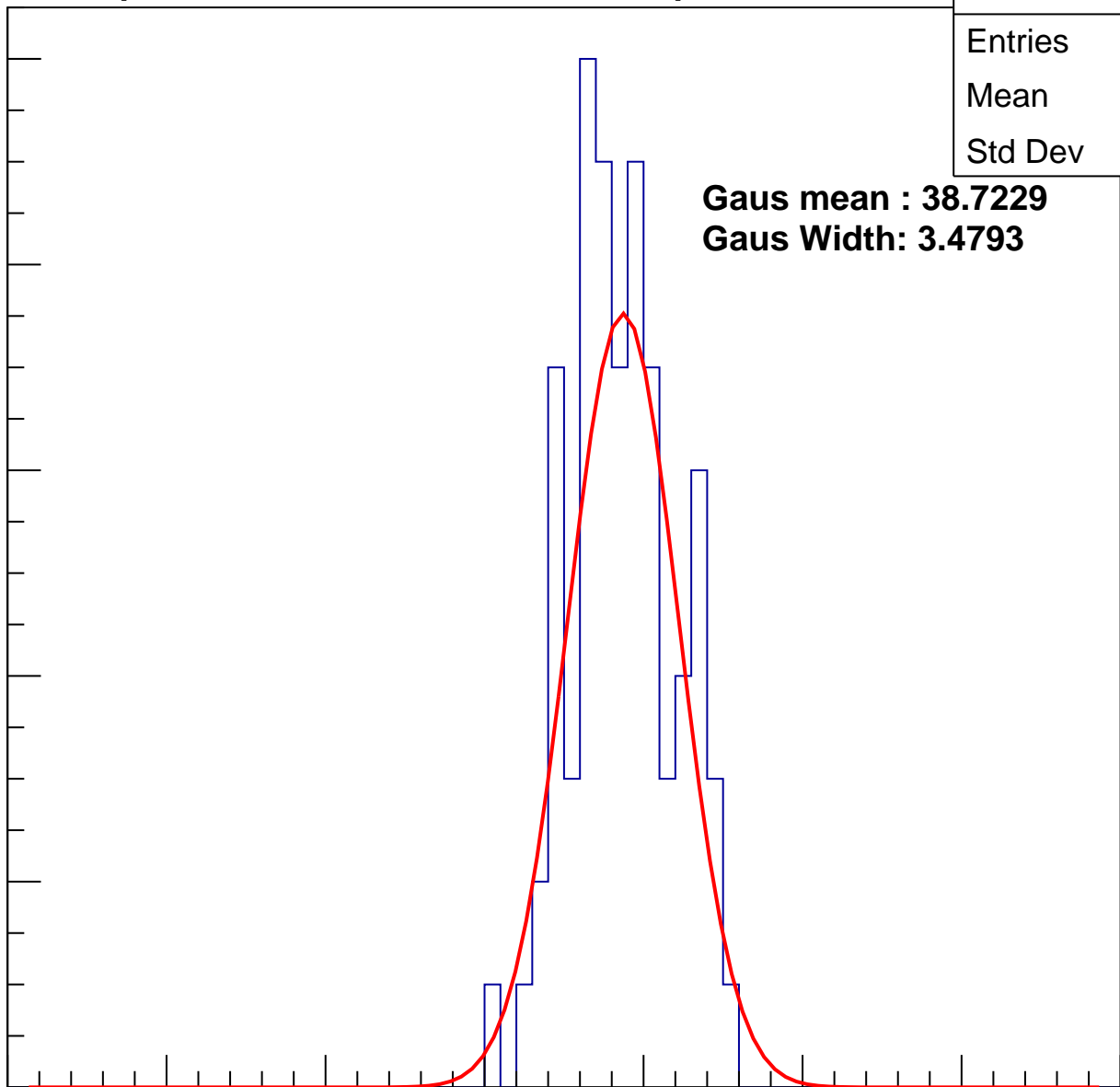
Gaus Width: 3.4793

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch35, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	44.6
Std Dev	2.967

7

6

5

4

3

2

1

0

Gaus mean : 45.2498

Gaus Width: 3.2765

ampl

0

10

20

30

40

50

60

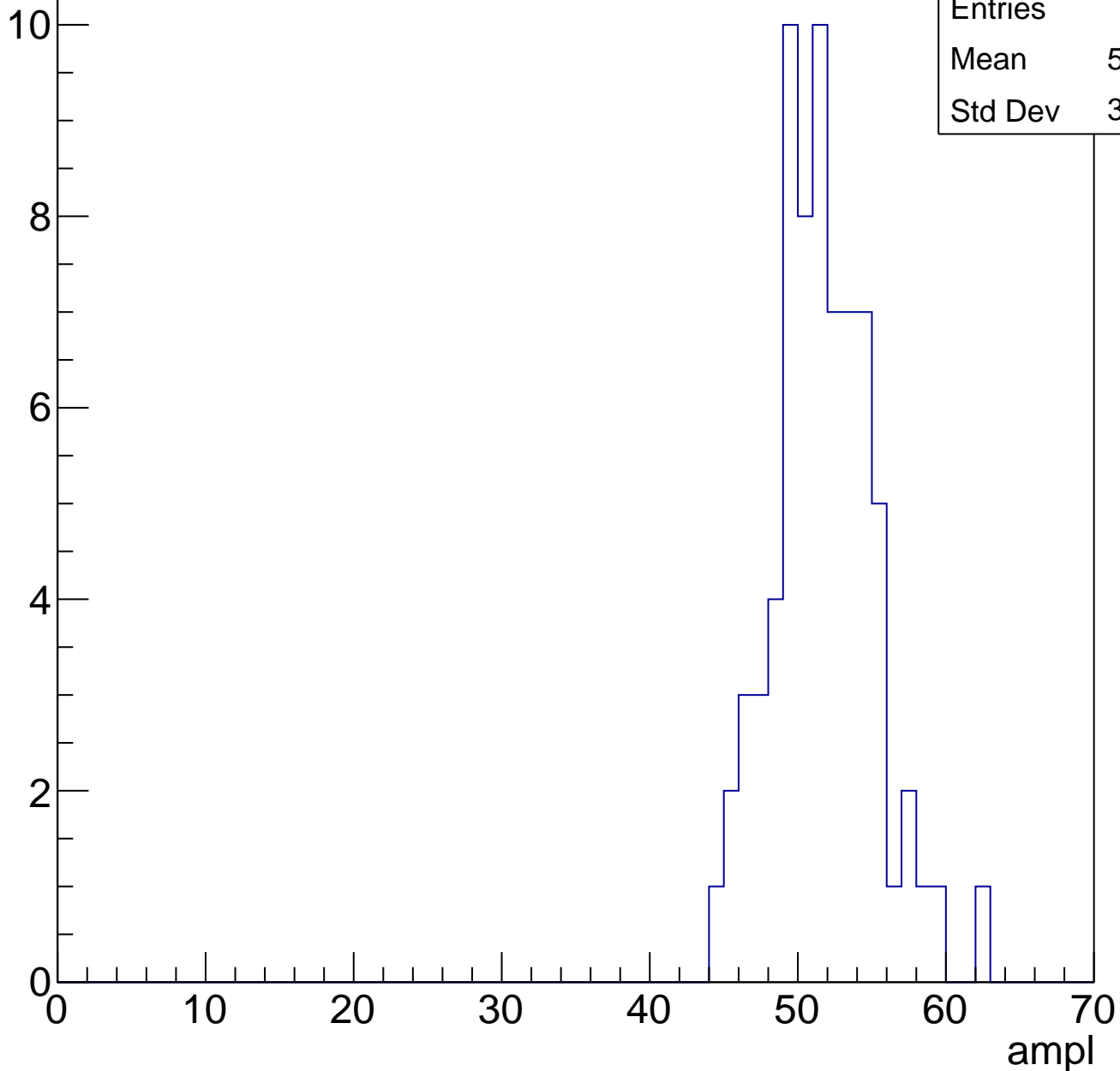
70

B1L103S, U12-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	51.26
Std Dev	3.376

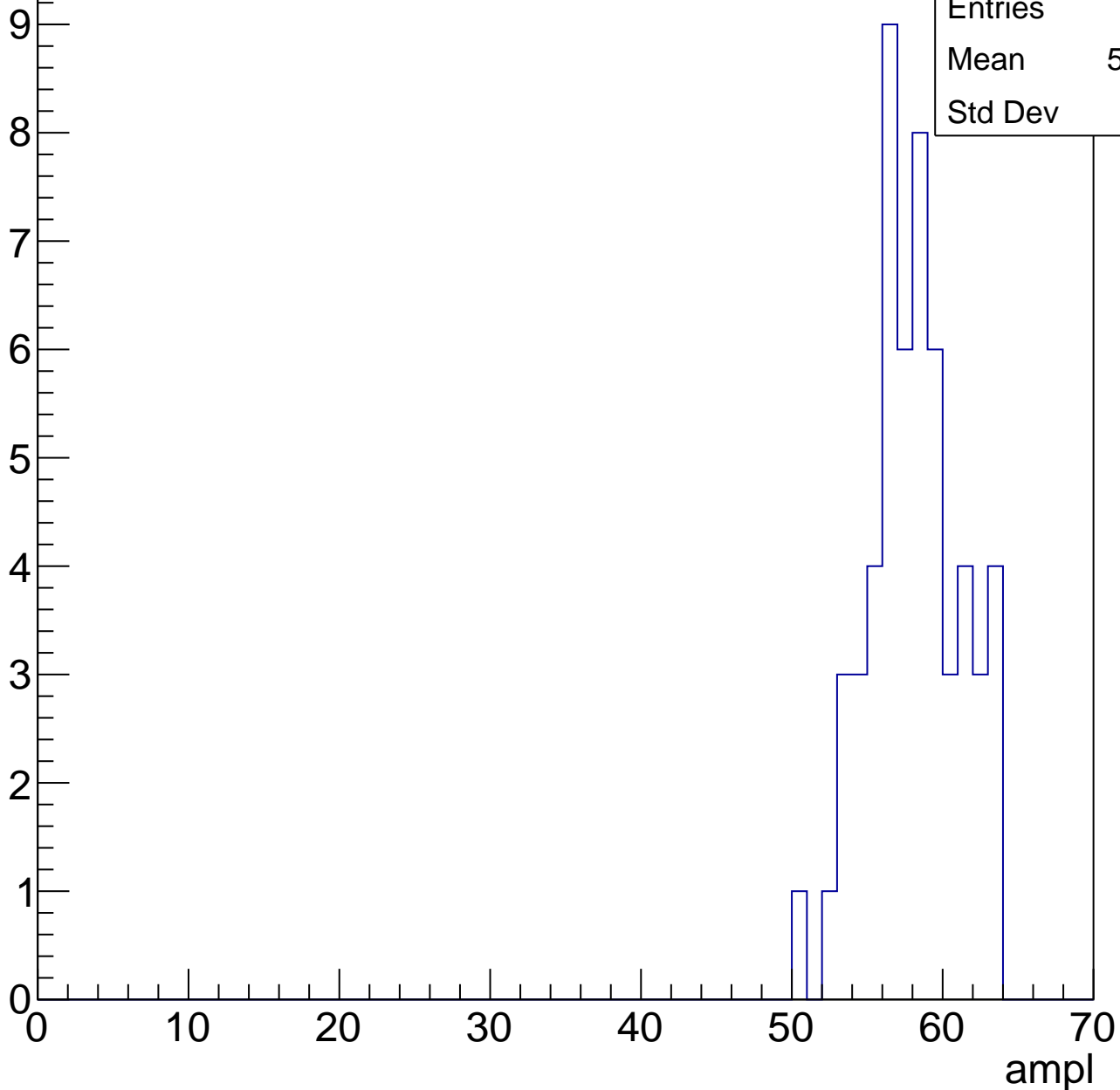


B1L103S, U12-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.62
Std Dev	3



B1L103S, U12-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

32

Mean

58.72

Std Dev

10.72

ampl

0

10

20

30

40

50

60

70

B1L103S, U12-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	23
Std Dev	0

B1L103S, U12-ch36, adc0

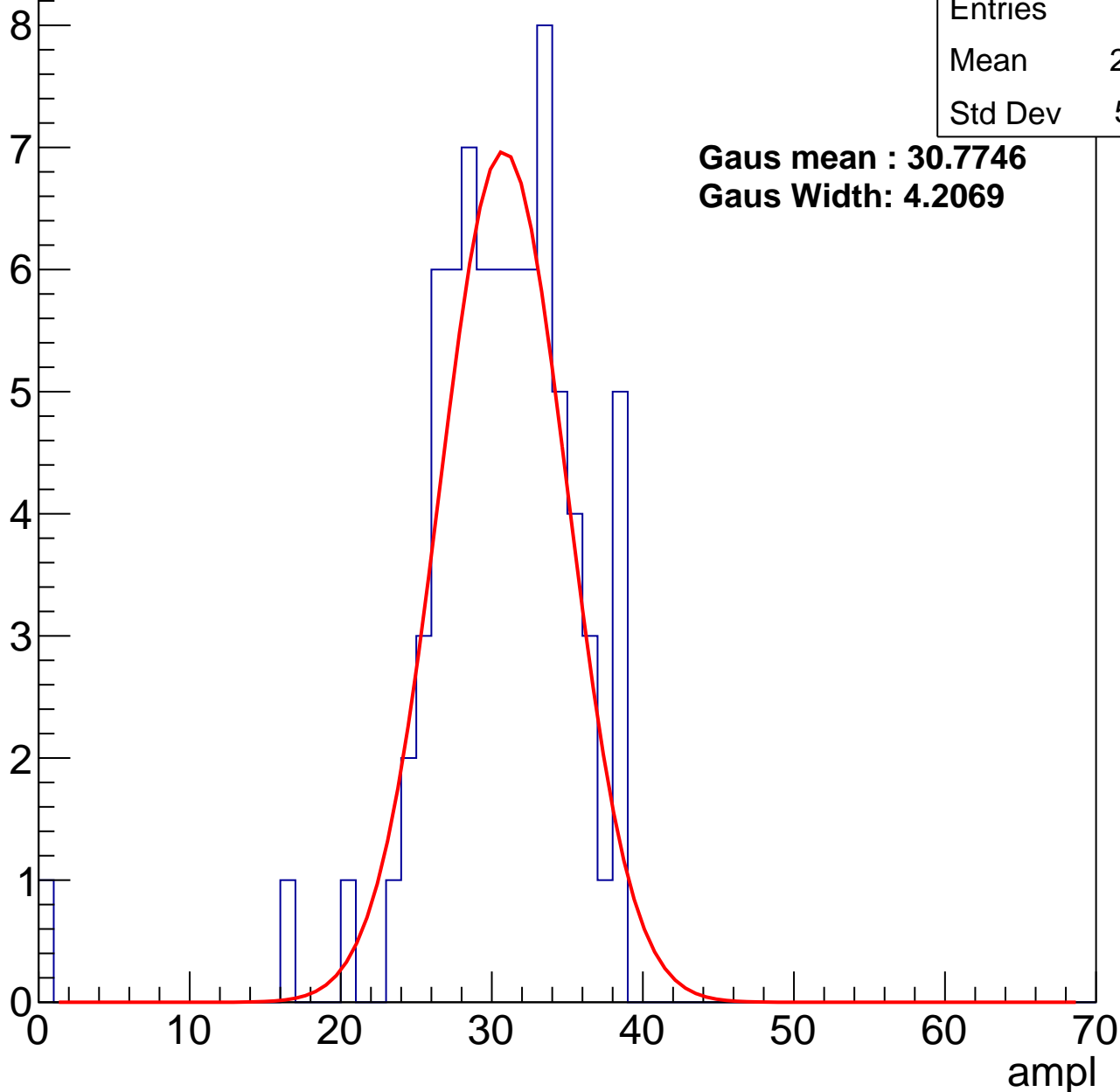
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.96
Std Dev	5.471

Gaus mean : 30.7746

Gaus Width: 4.2069



B1L103S, U12-ch36, adc1

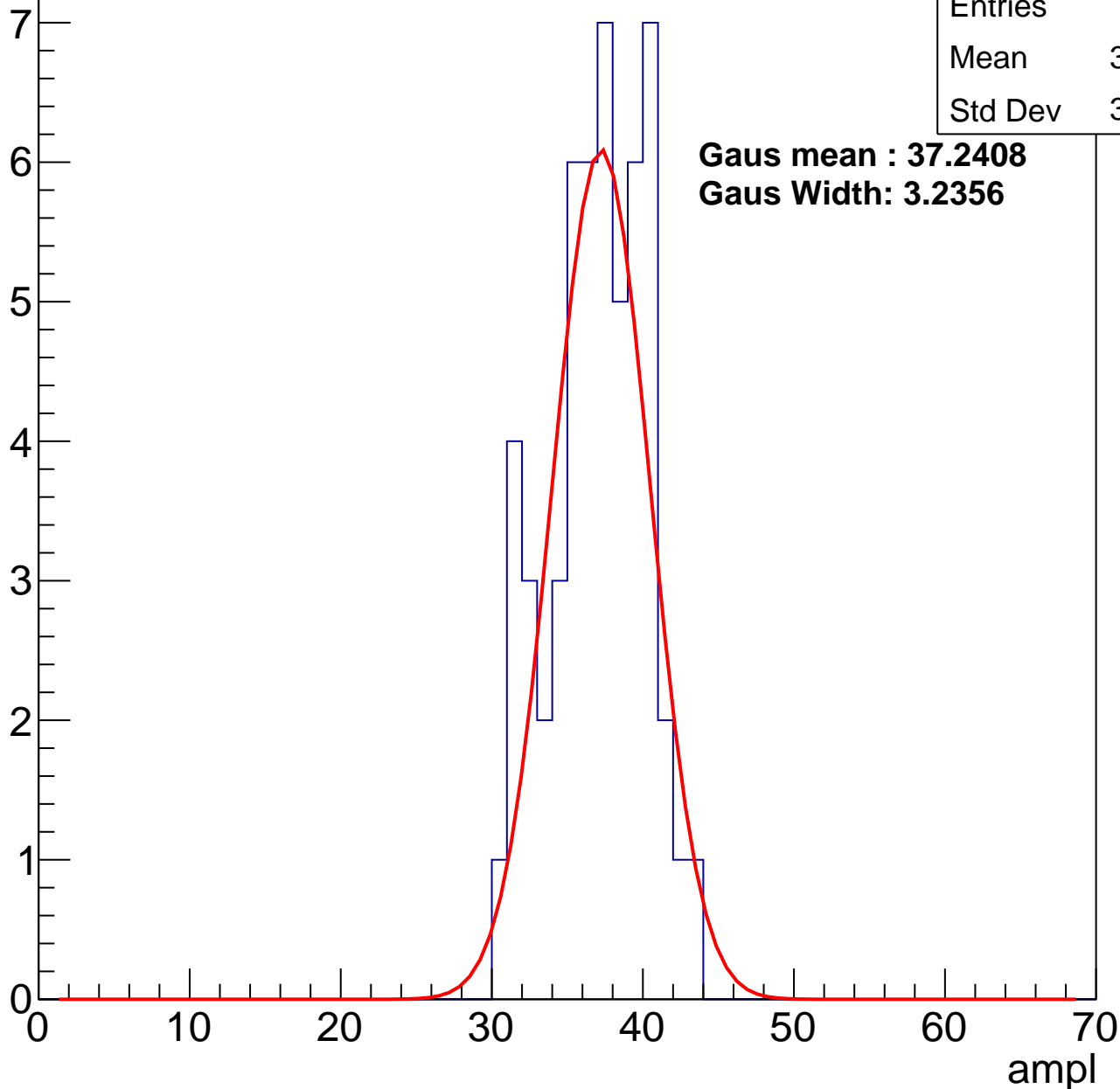
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.56
Std Dev	3.137

Gaus mean : 37.2408

Gaus Width: 3.2356



B1L103S, U12-ch36, adc2

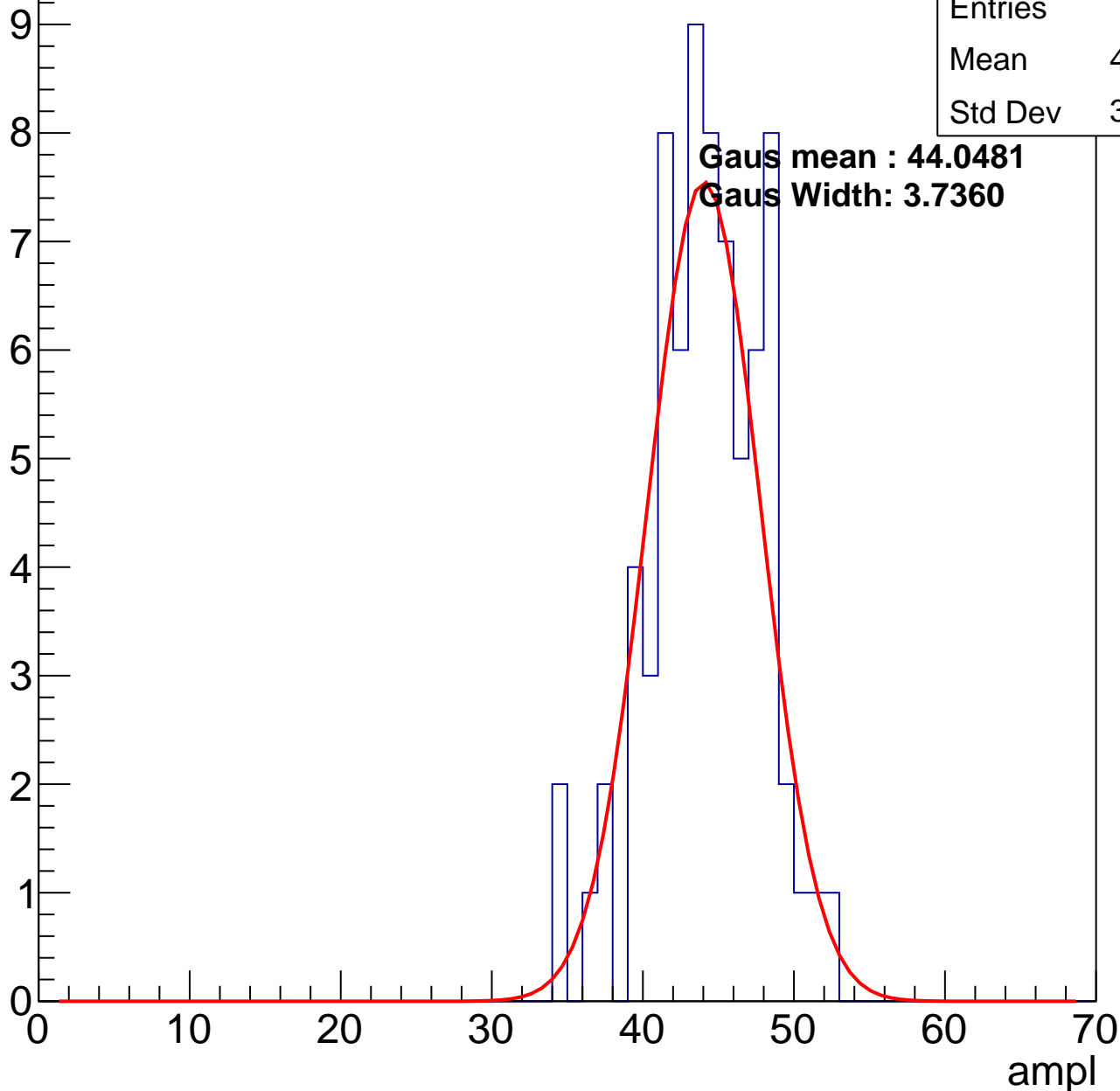
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	43.72
Std Dev	3.707

Gaus mean : 44.0481

Gaus Width: 3.7360

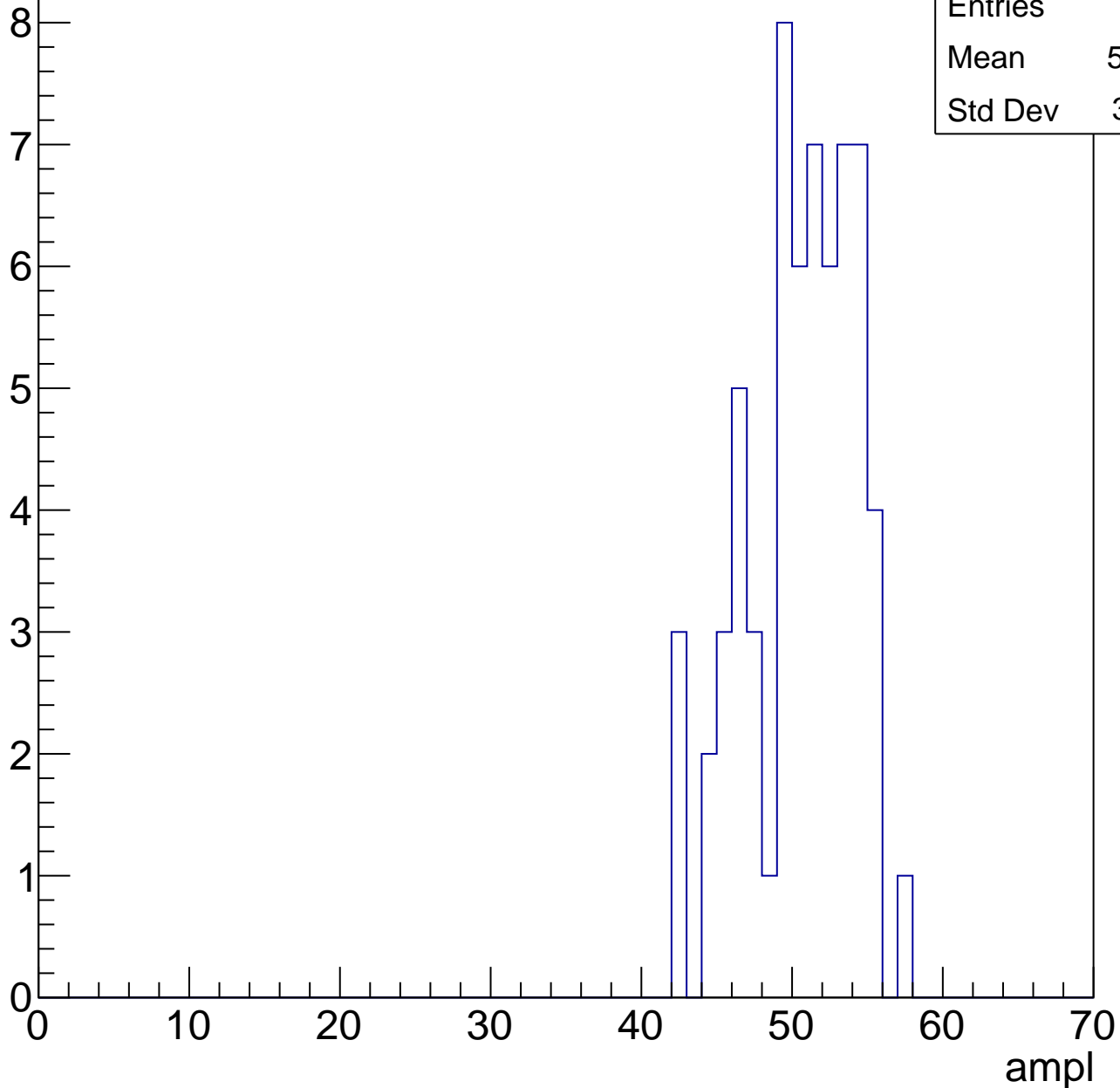


B1L103S, U12-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	50.08
Std Dev	3.591

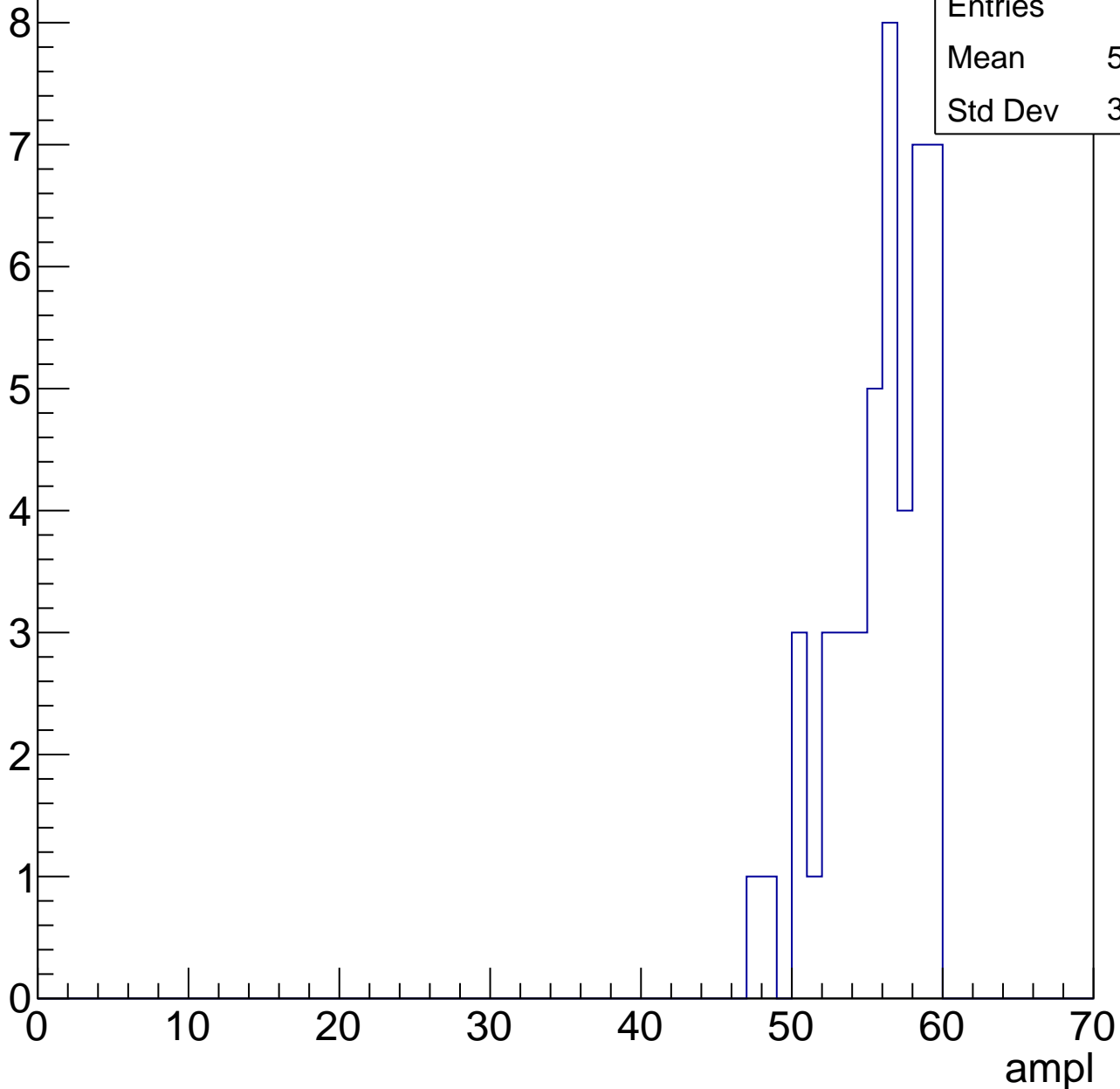


B1L103S, U12-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	55.28
Std Dev	3.097

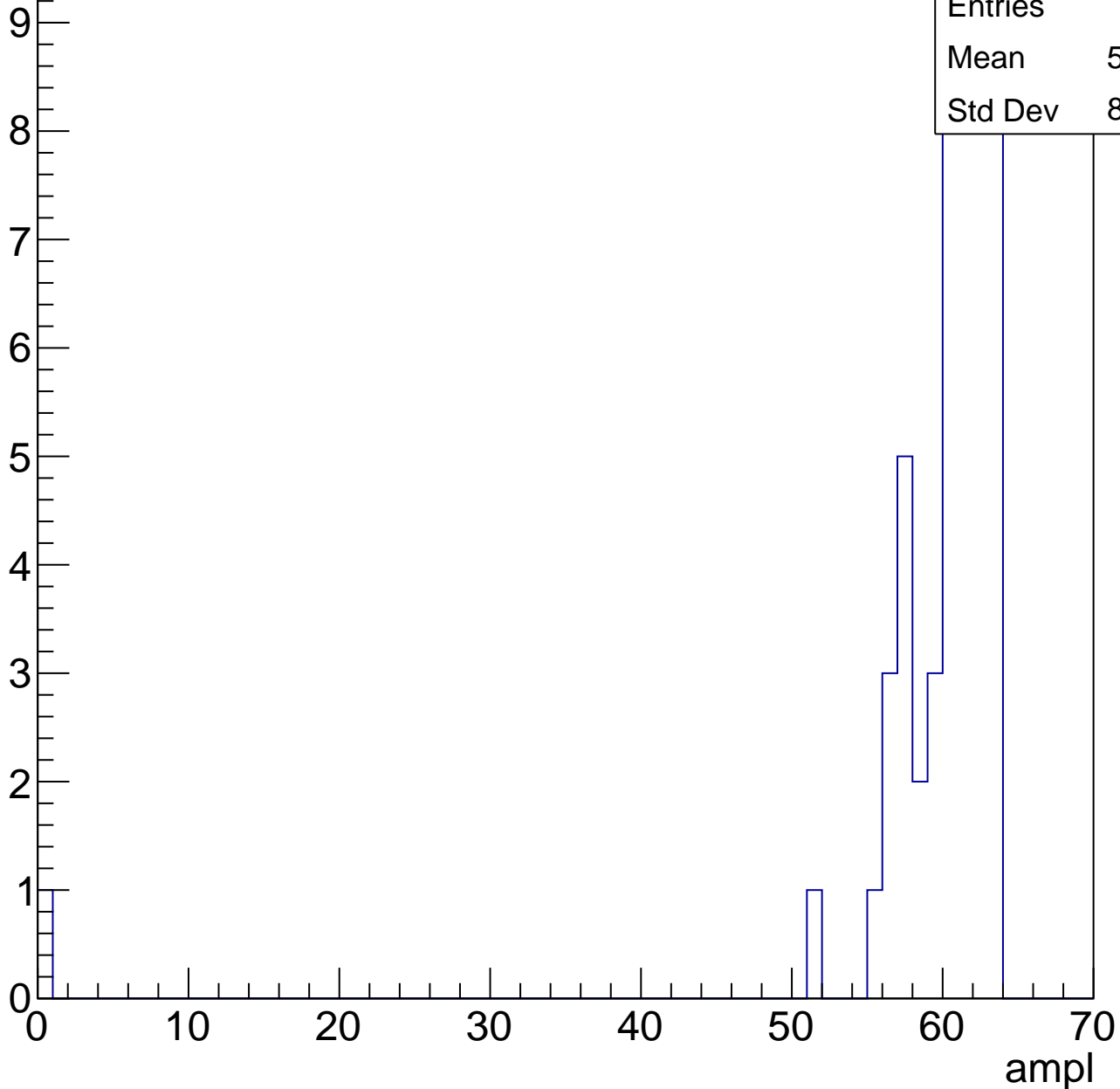


B1L103S, U12-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.82
Std Dev	8.779



B1L103S, U12-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

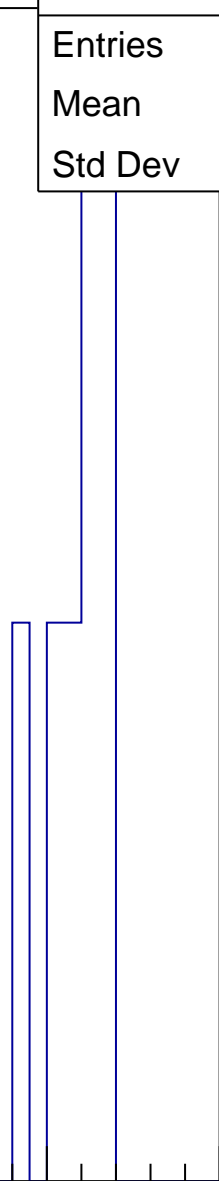
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.29
Std Dev	1.666

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch37, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	29.81
Std Dev	5.157

Gaus mean : 30.9302

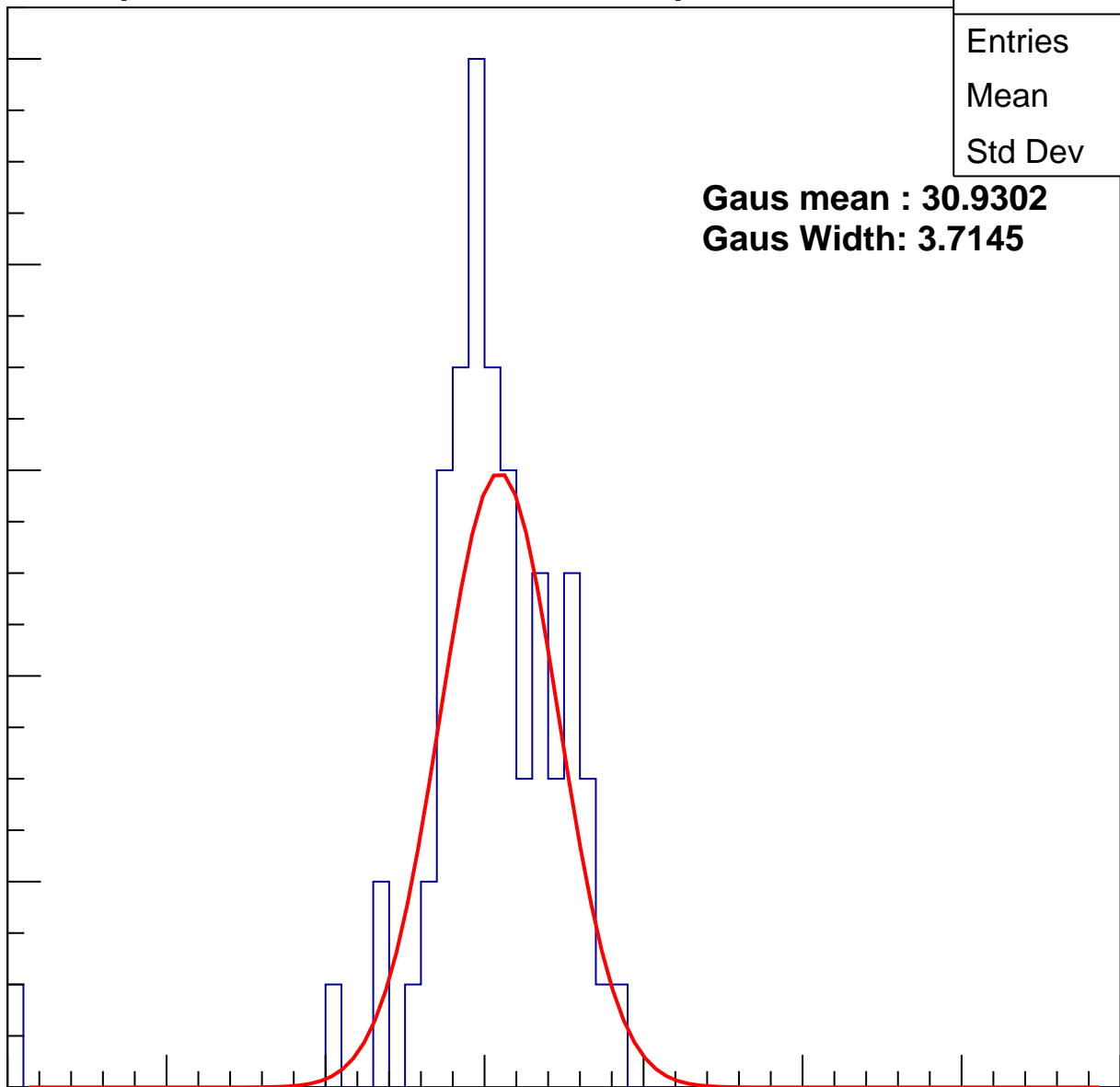
Gaus Width: 3.7145

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch37, adc1

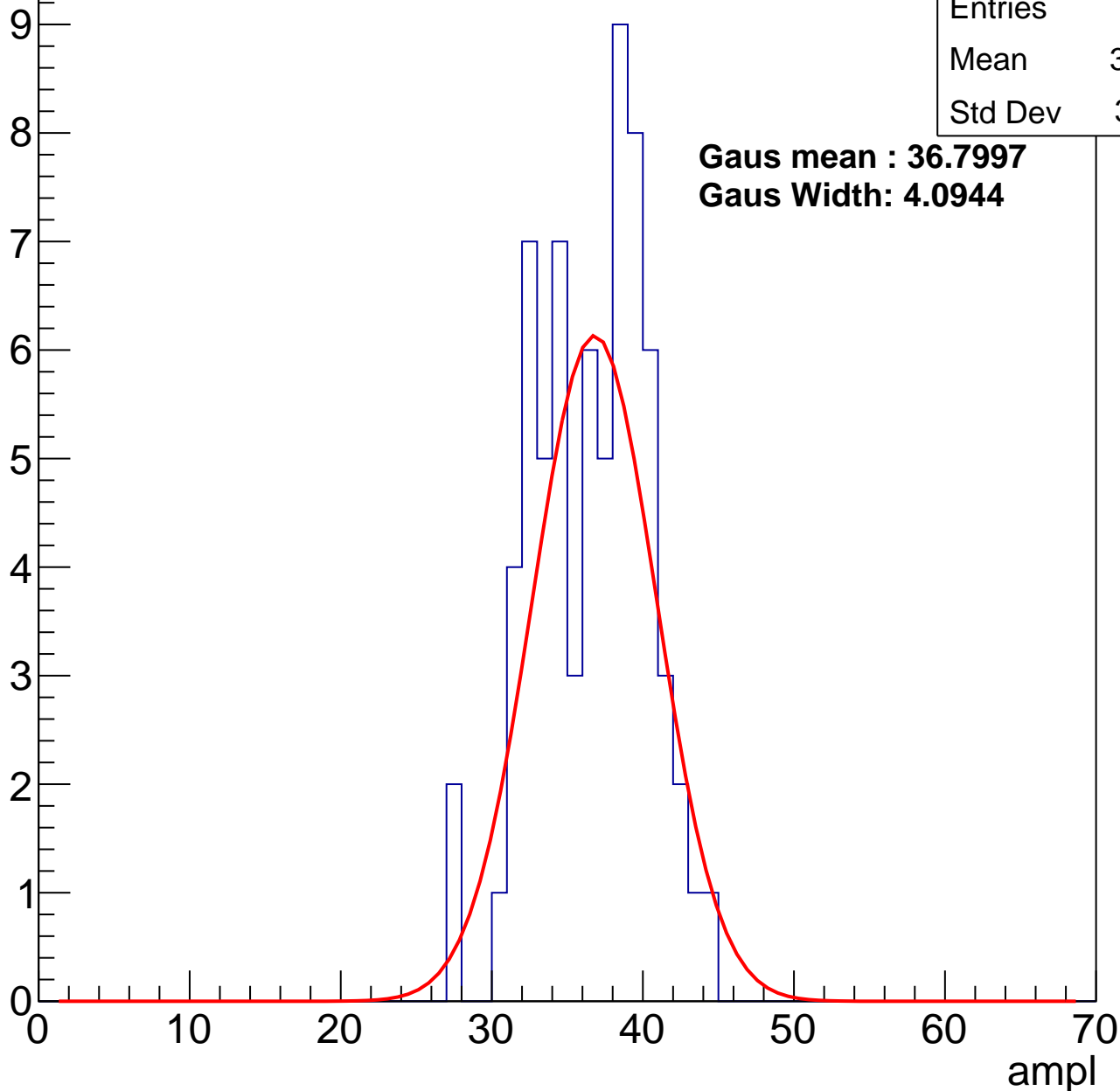
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.13
Std Dev	3.691

Gaus mean : 36.7997

Gaus Width: 4.0944



B1L103S, U12-ch37, adc2

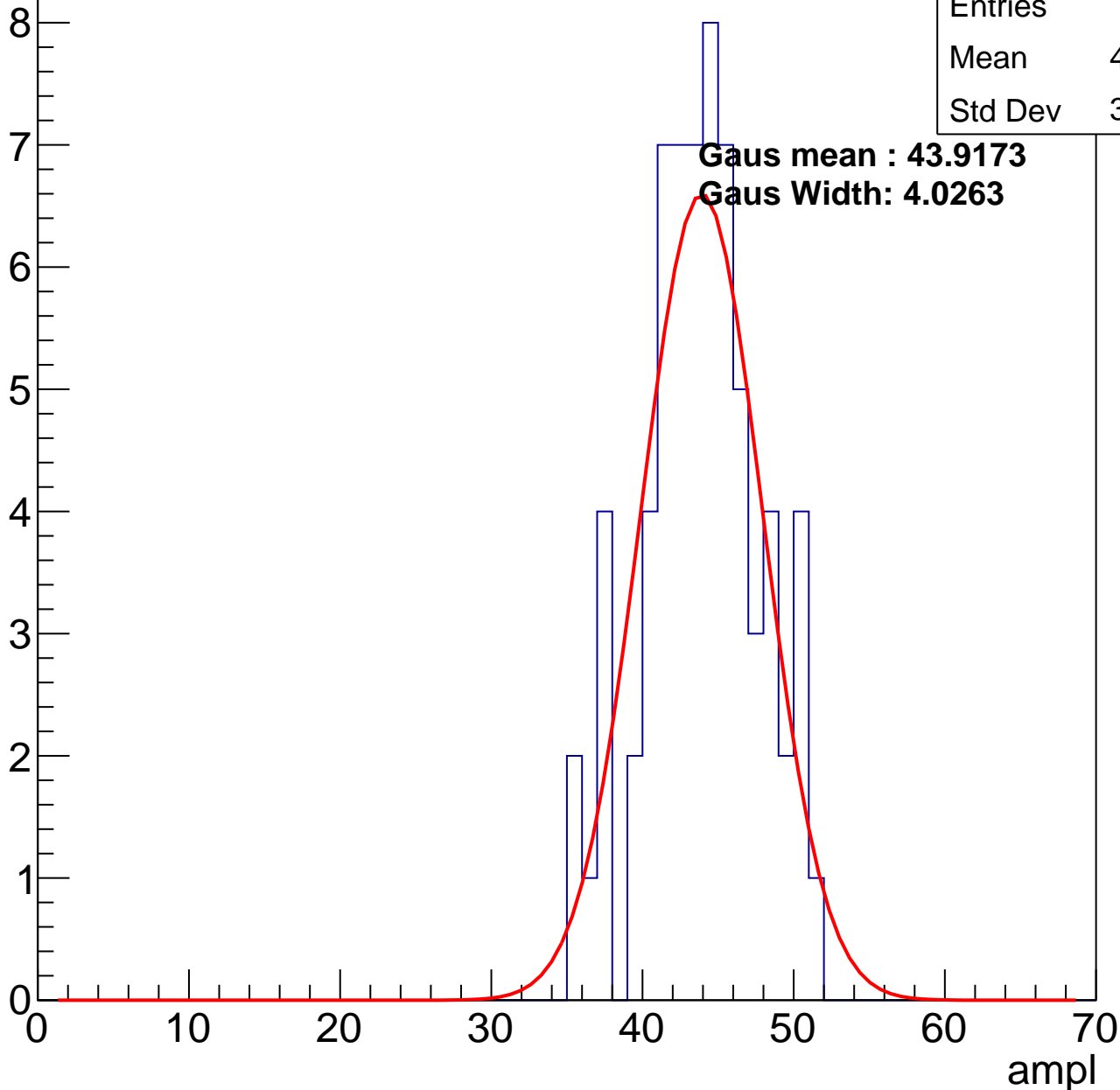
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.43
Std Dev	3.782

Gaus mean : 43.9173

Gaus Width: 4.0263

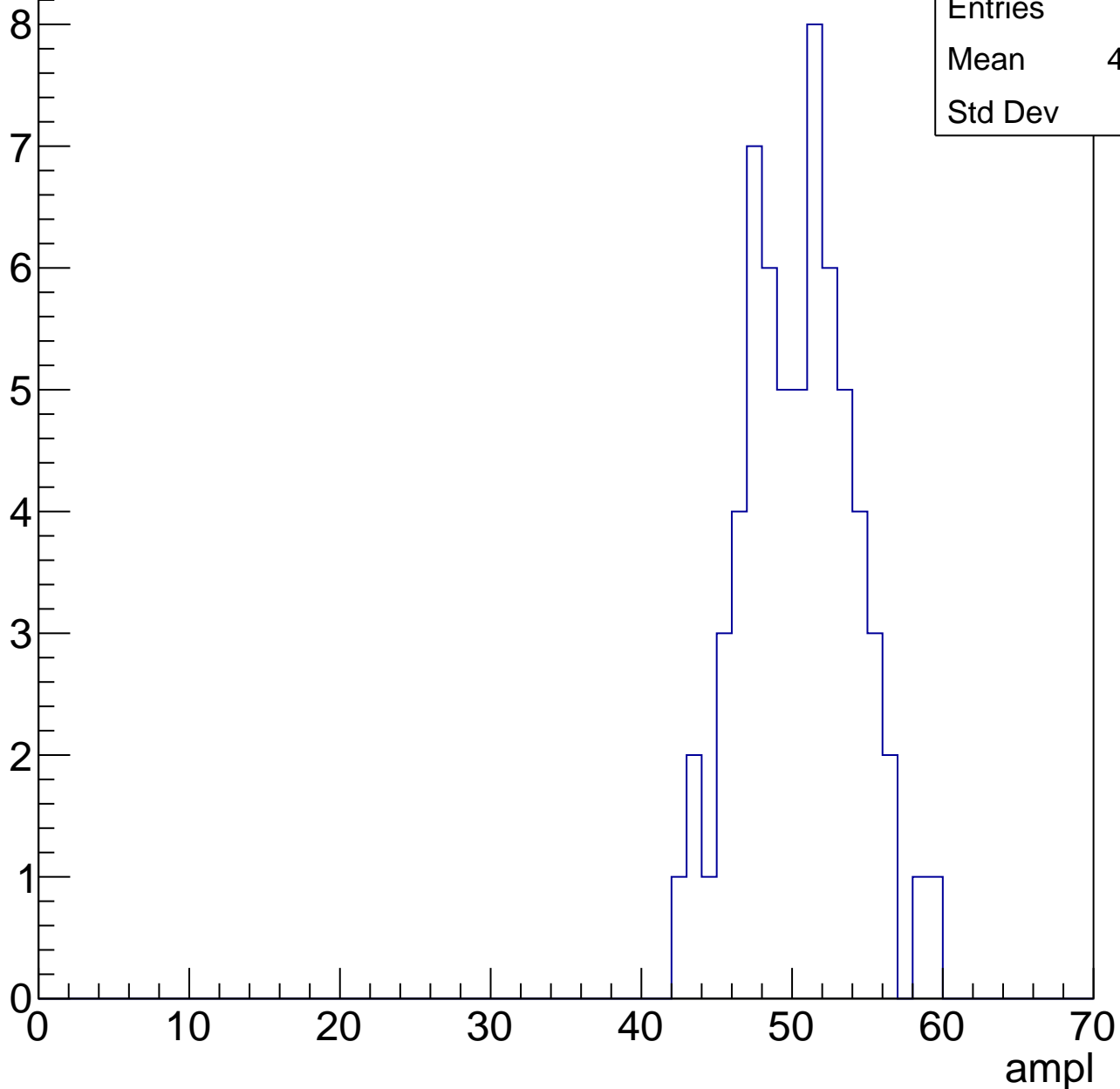


B1L103S, U12-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

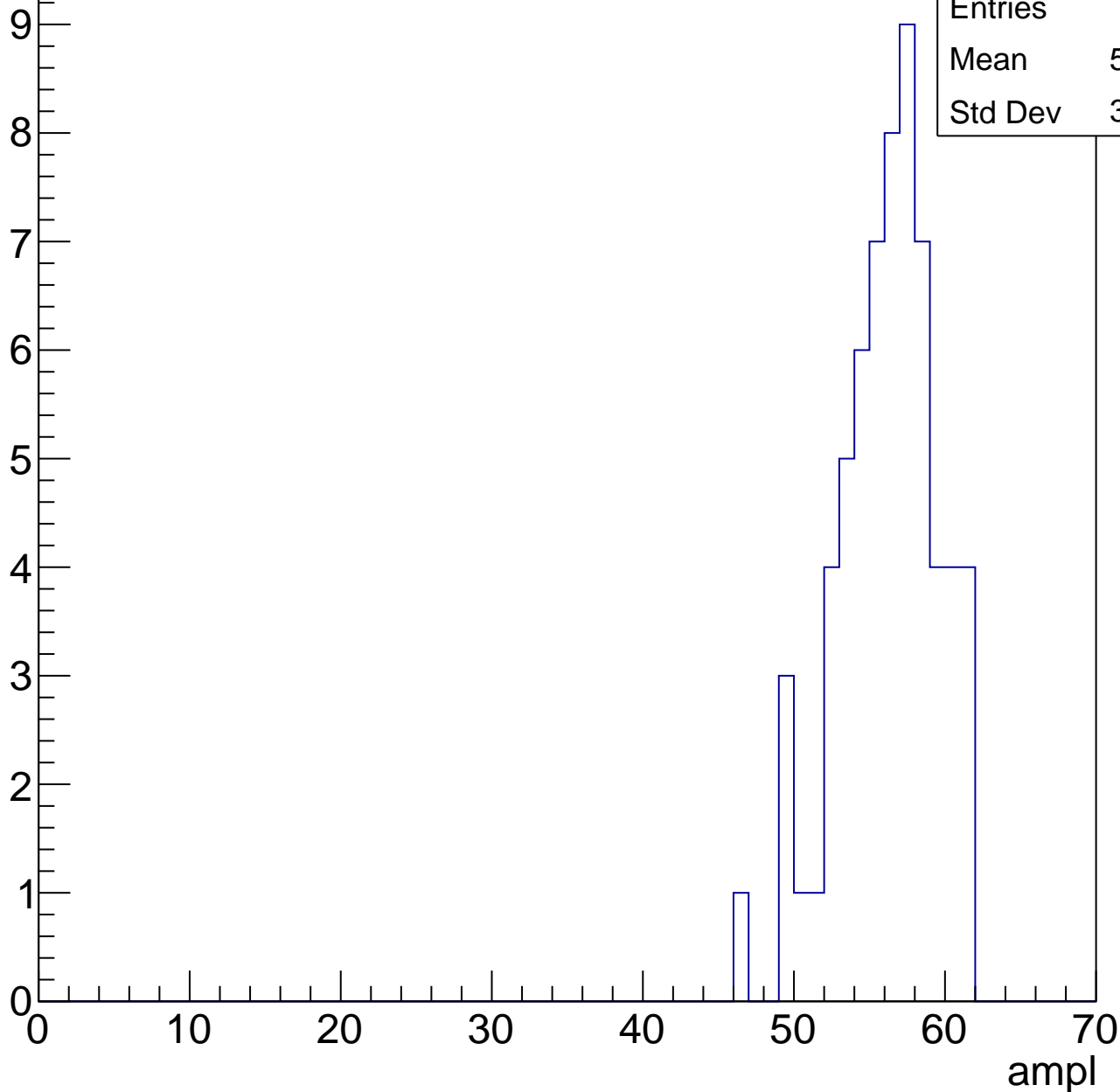
Entries	64
Mean	49.97
Std Dev	3.67



B1L103S, U12-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



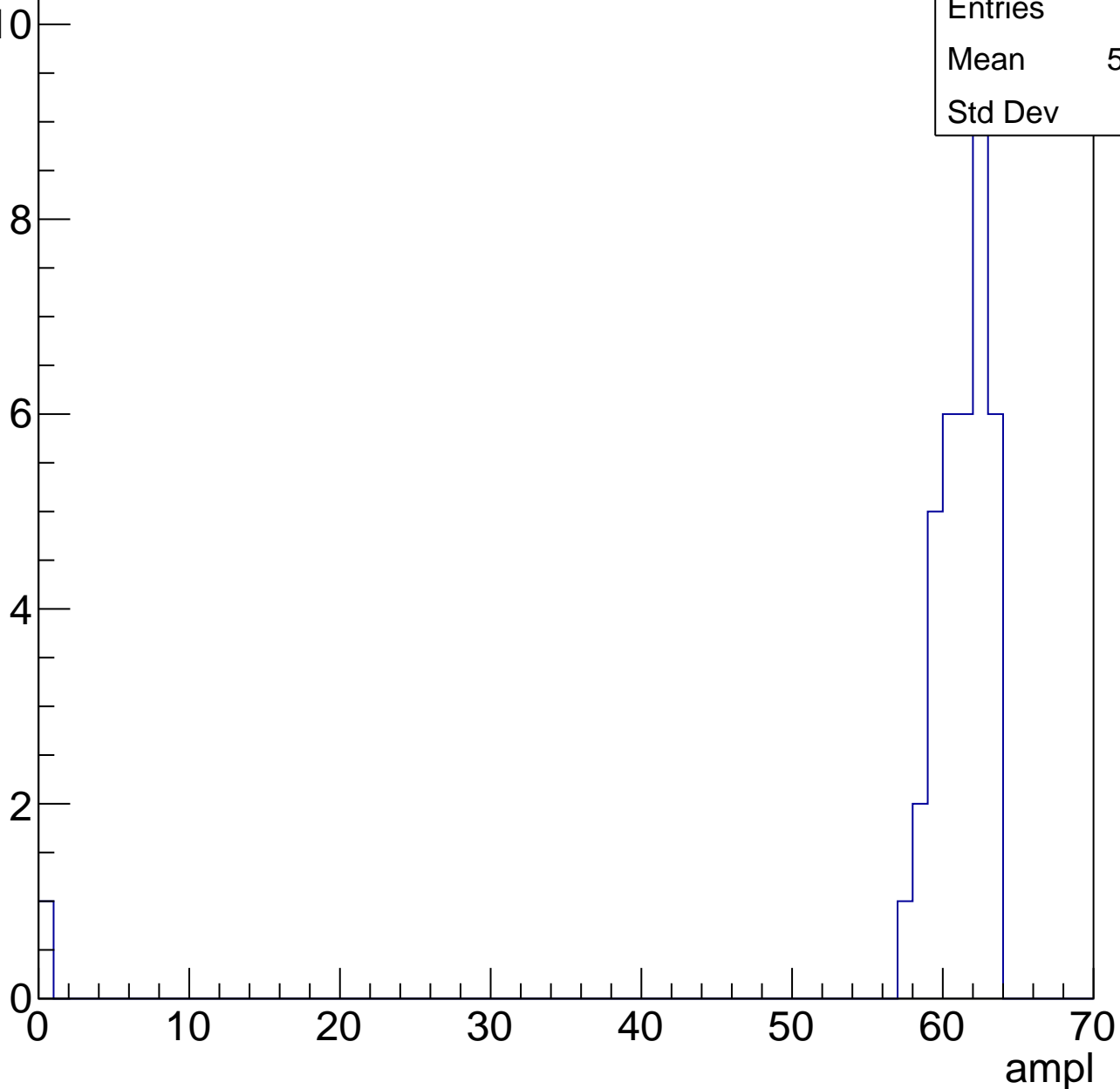
Entries	64
Mean	55.67
Std Dev	3.255

B1L103S, U12-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

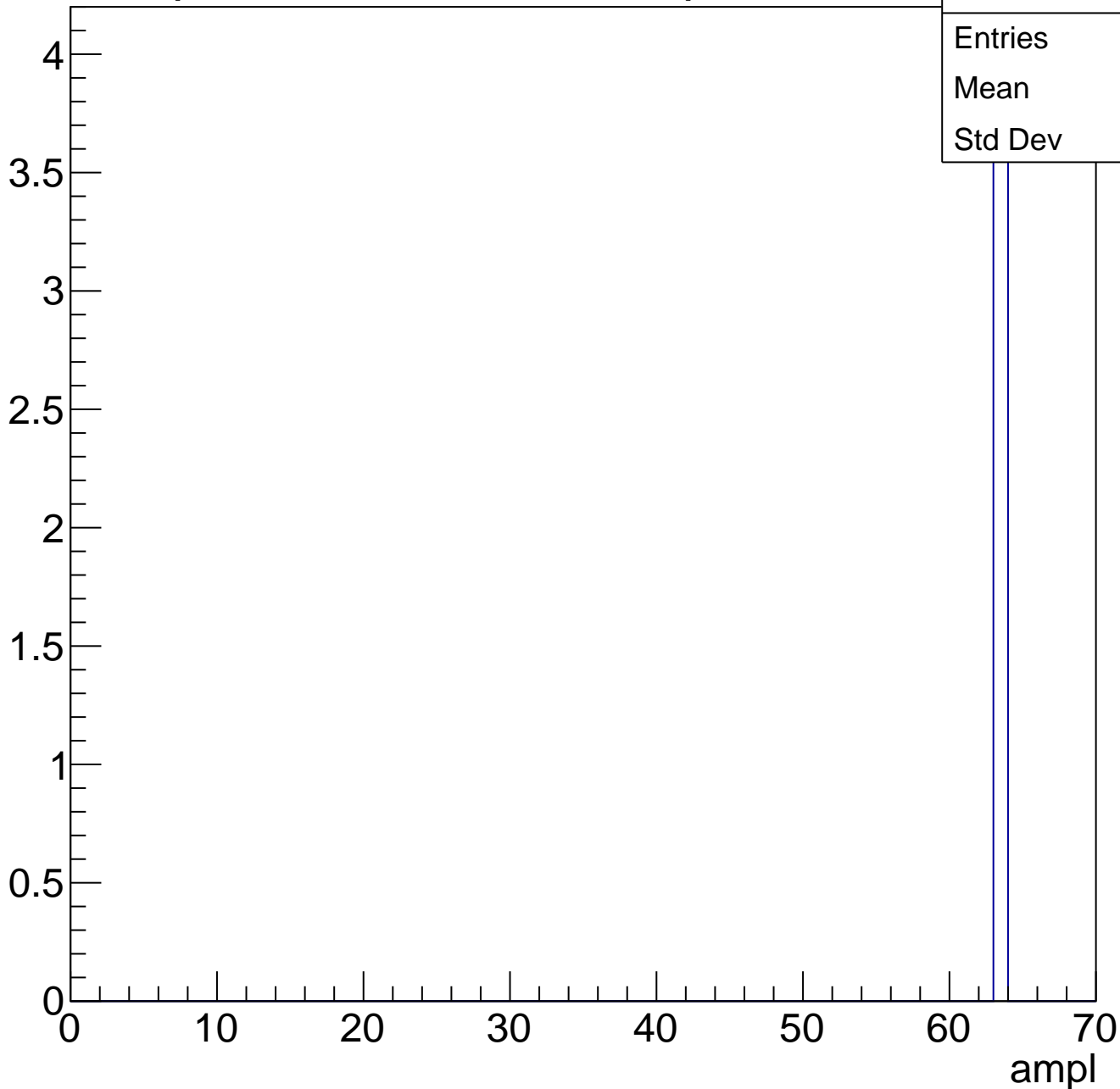
Entries	37
Mean	59.24
Std Dev	10



B1L103S, U12-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch38, adc0

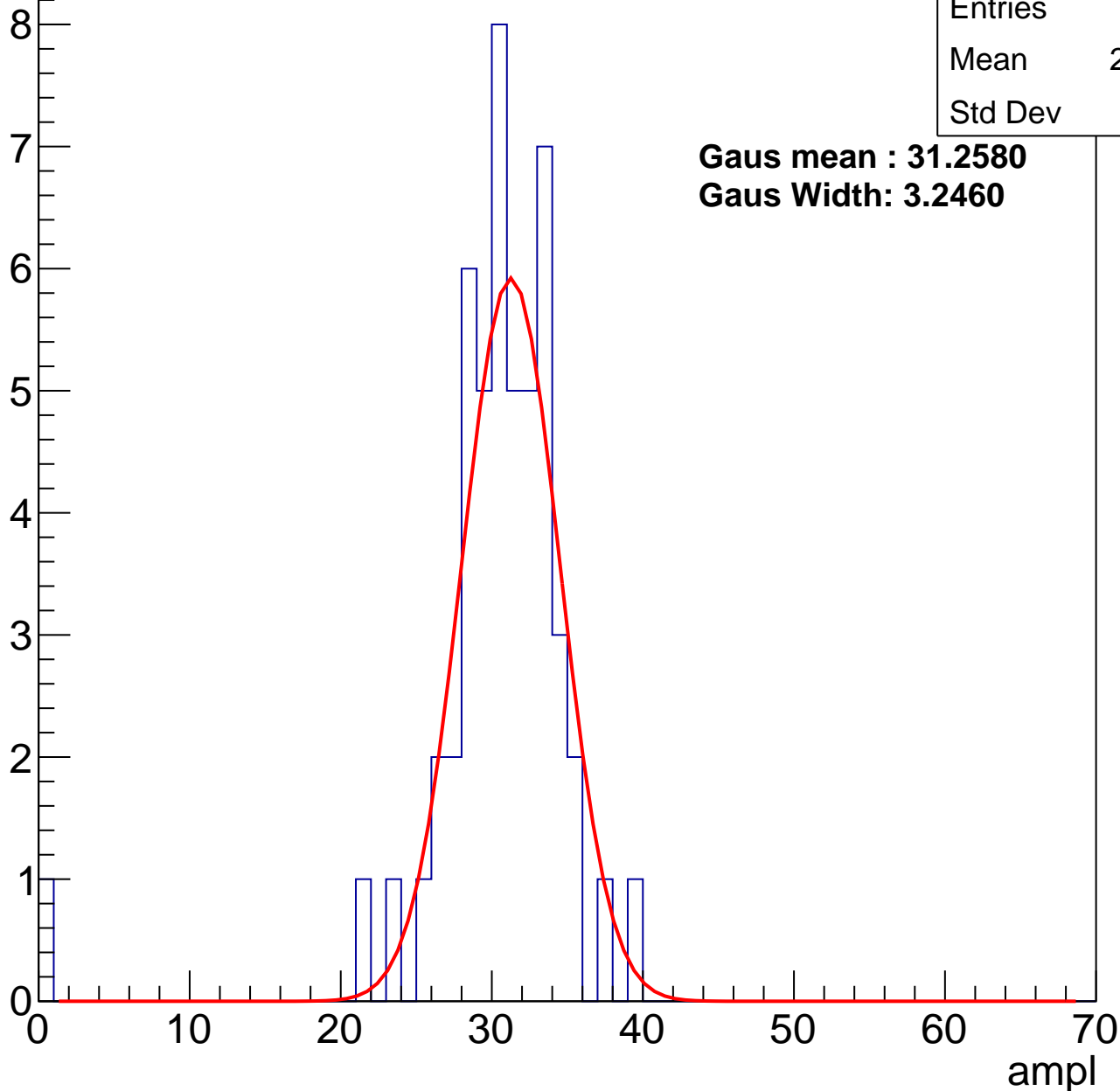
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	29.84
Std Dev	5.33

Gaus mean : 31.2580

Gaus Width: 3.2460



B1L103S, U12-ch38, adc1

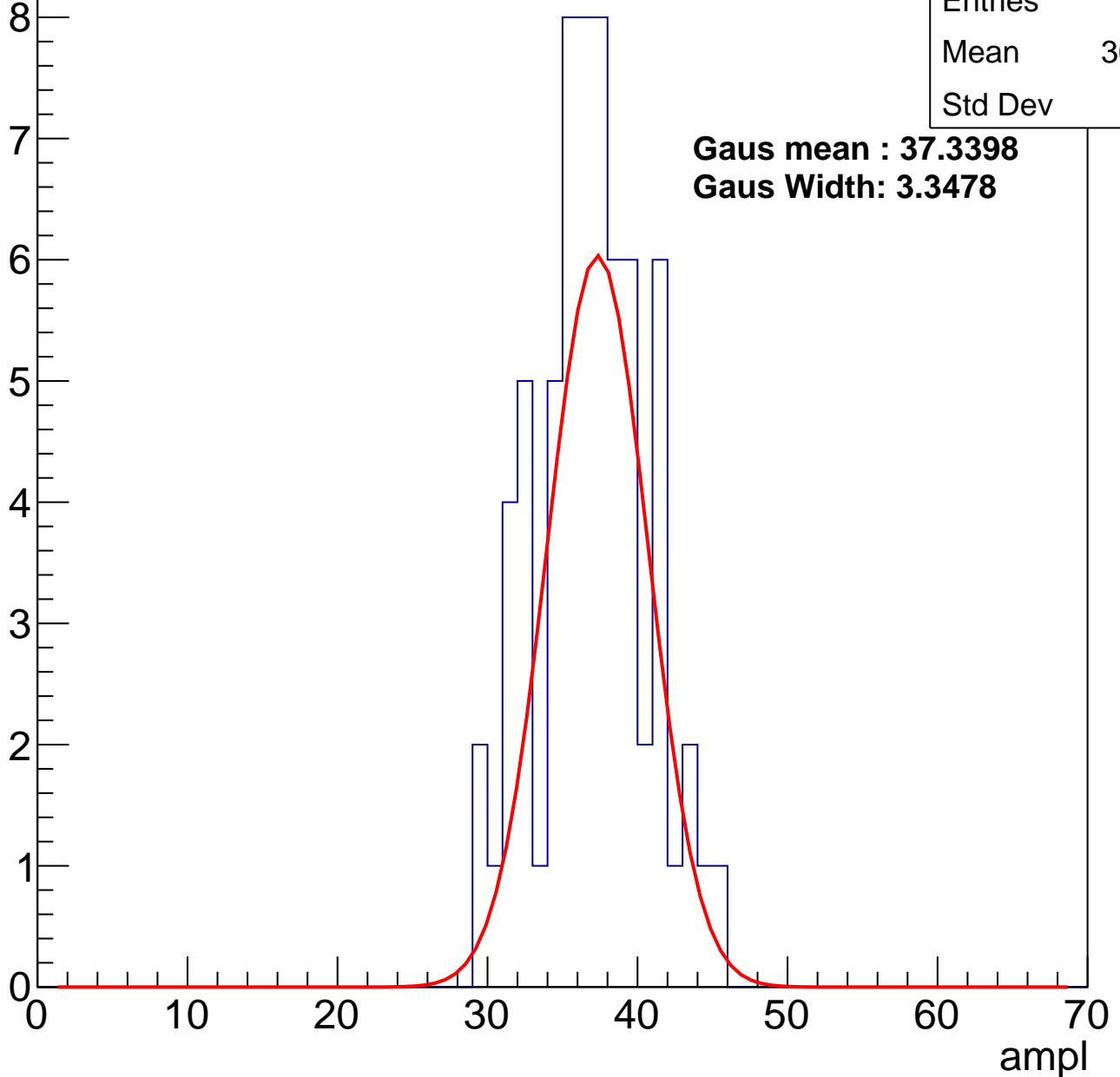
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36.48
Std Dev	3.63

Gaus mean : 37.3398

Gaus Width: 3.3478



B1L103S, U12-ch38, adc2

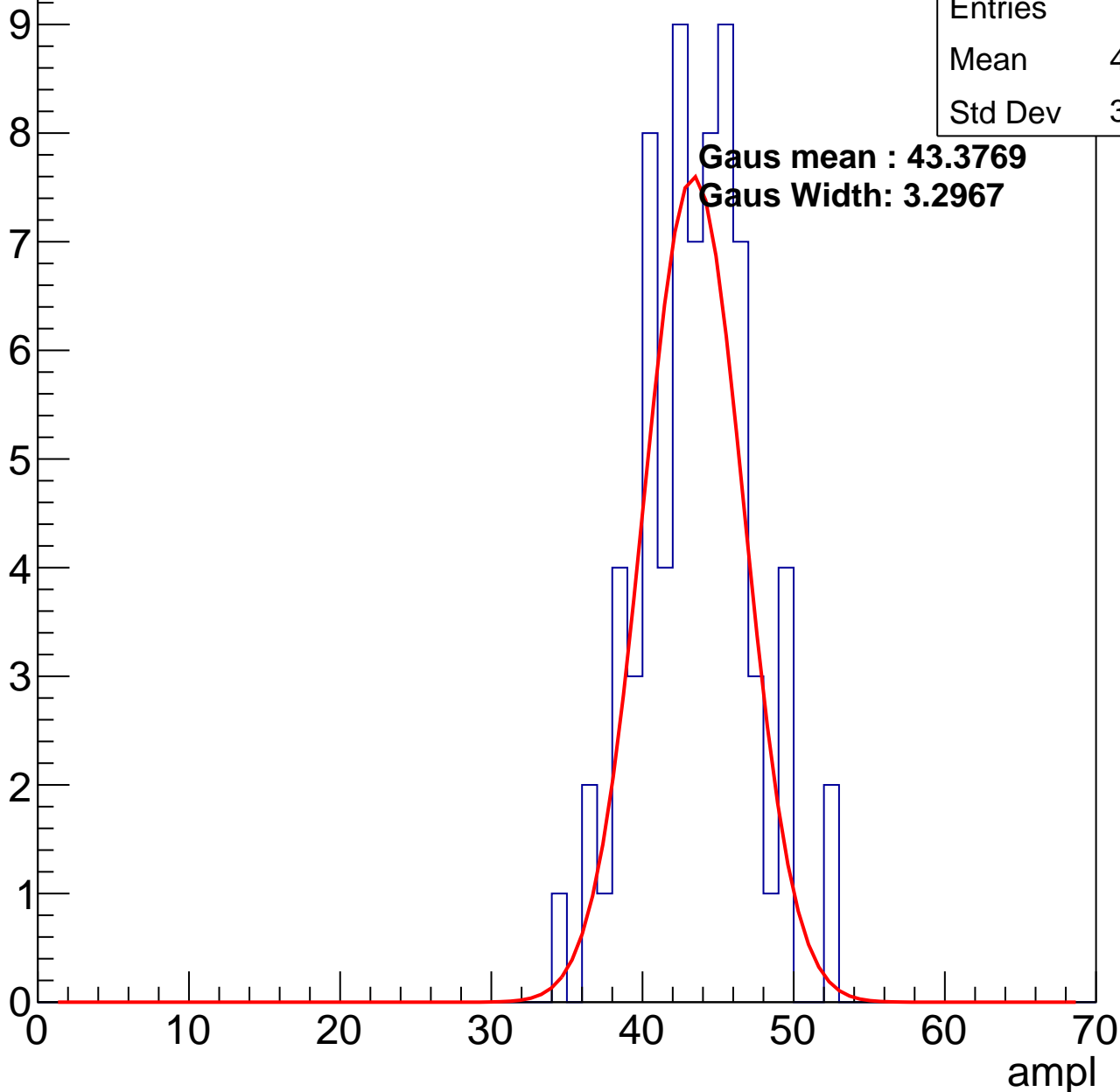
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.05
Std Dev	3.599

Gaus mean : 43.3769

Gaus Width: 3.2967

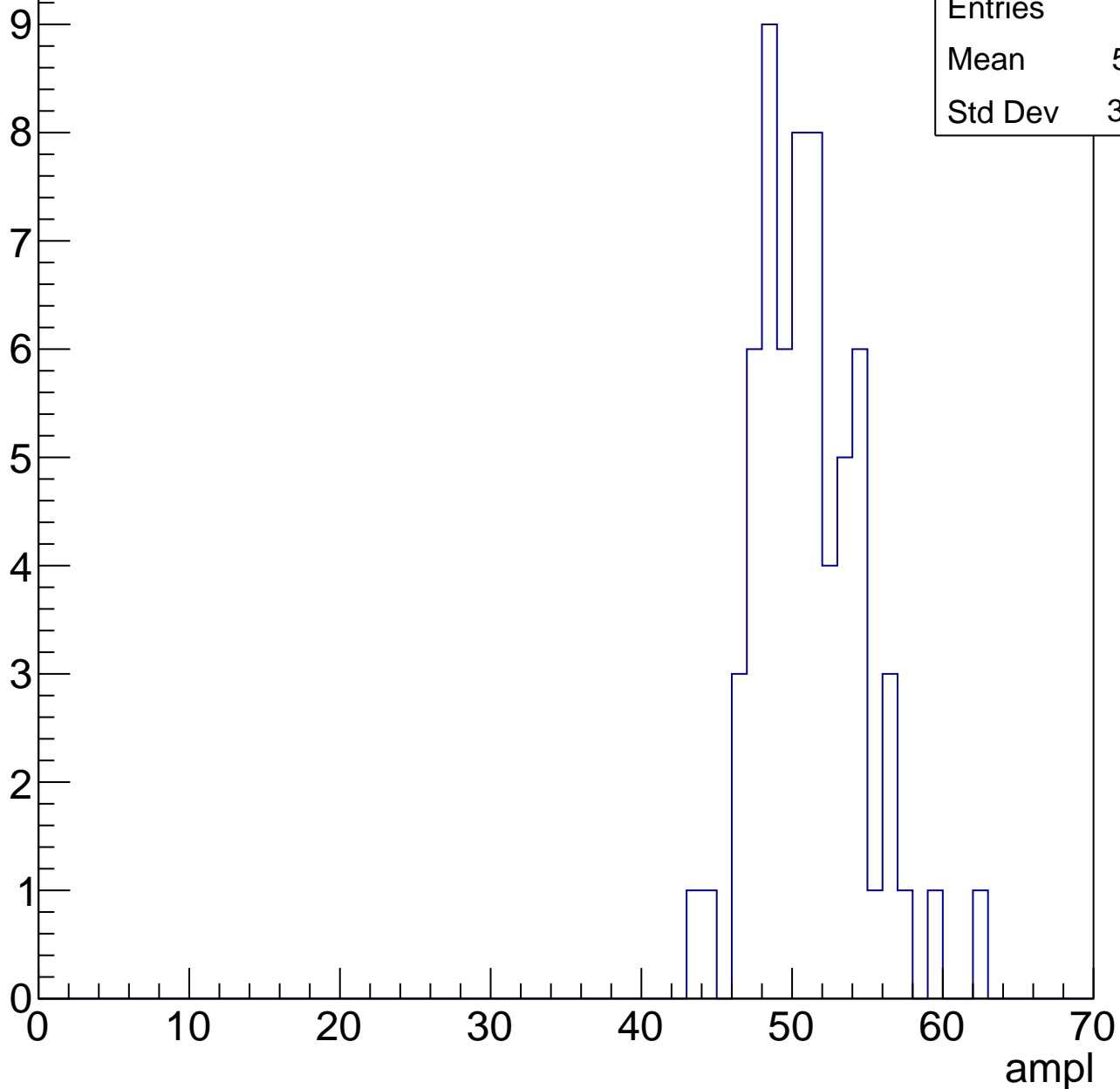


B1L103S, U12-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.61
Std Dev	3.485

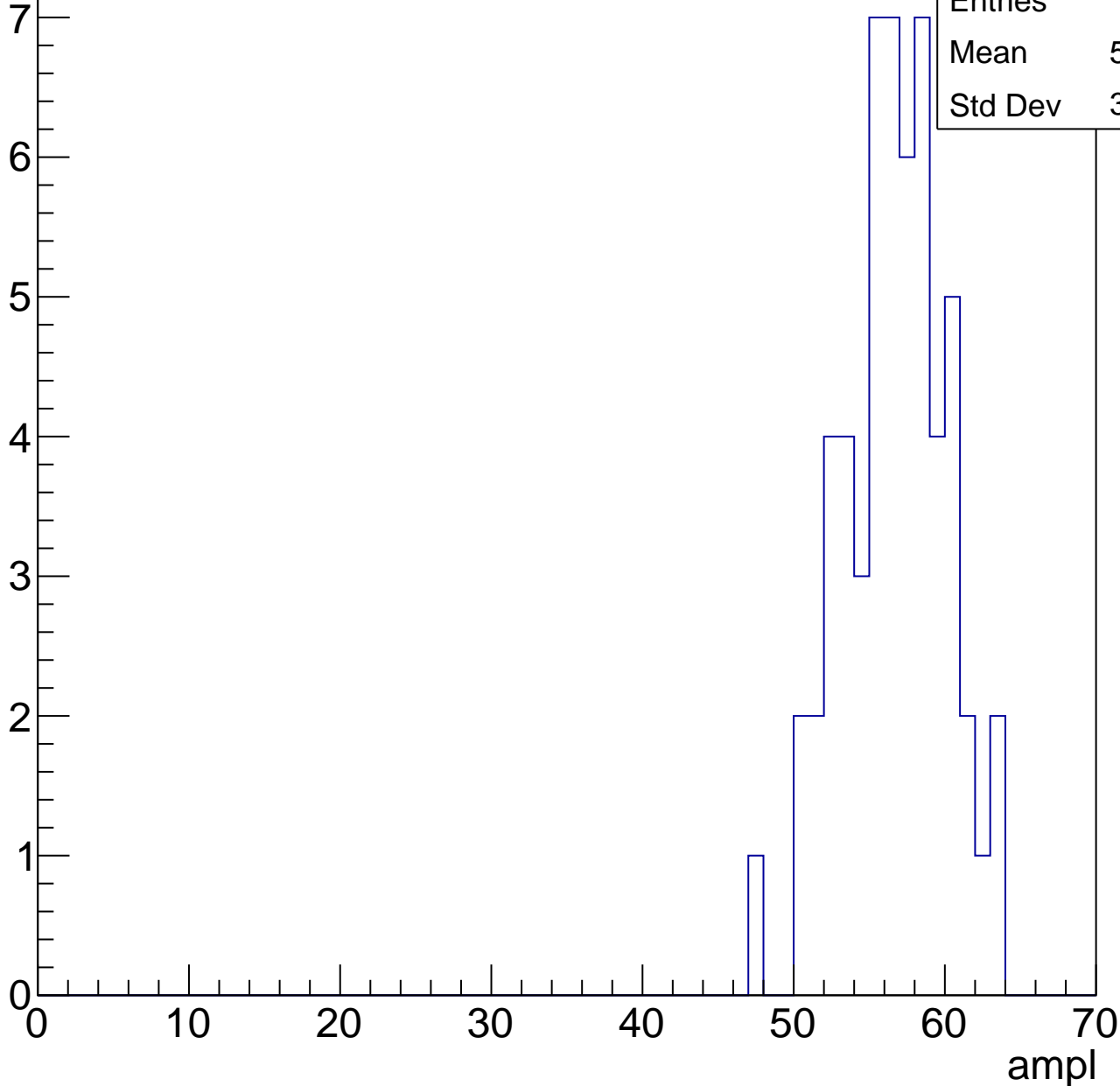


B1L103S, U12-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	56.18
Std Dev	3.388

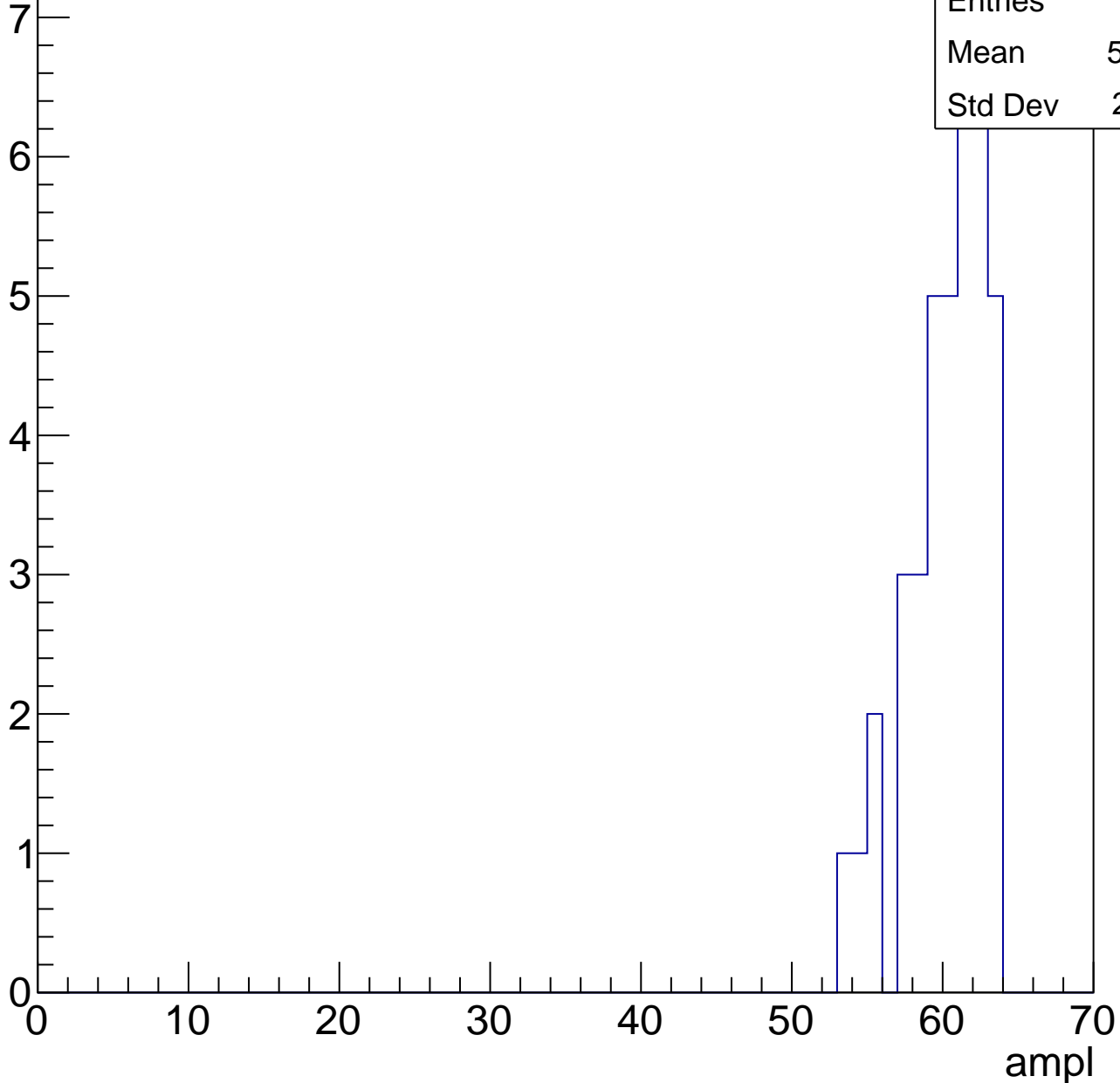


B1L103S, U12-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	59.82
Std Dev	2.571



B1L103S, U12-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	52
Std Dev	23.27

B1L103S, U12-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

B1L103S, U12-ch39, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	28.86
Std Dev	4.701

Gaus mean : 30.0024

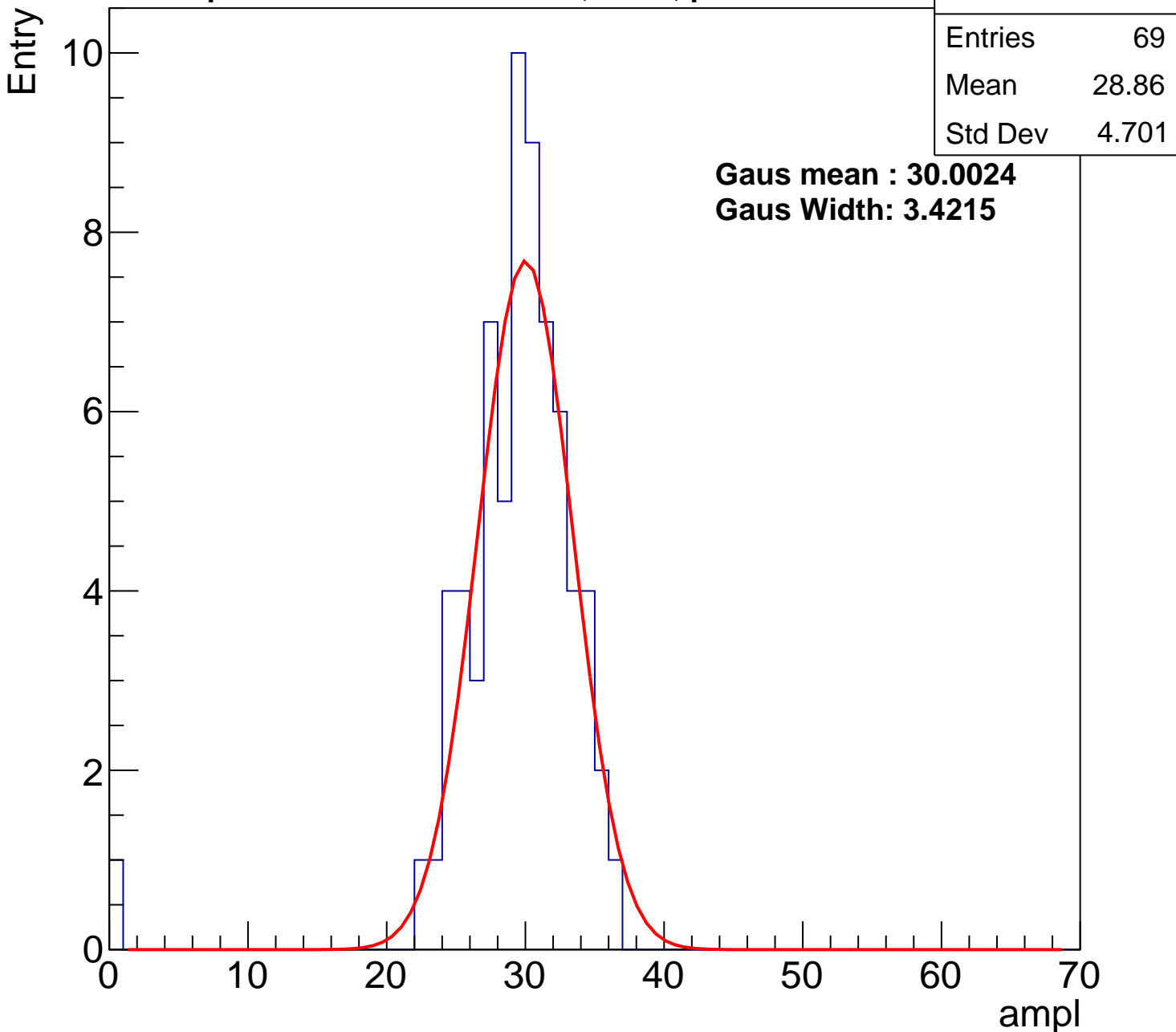
Gaus Width: 3.4215

Entry

10
8
6
4
2
0

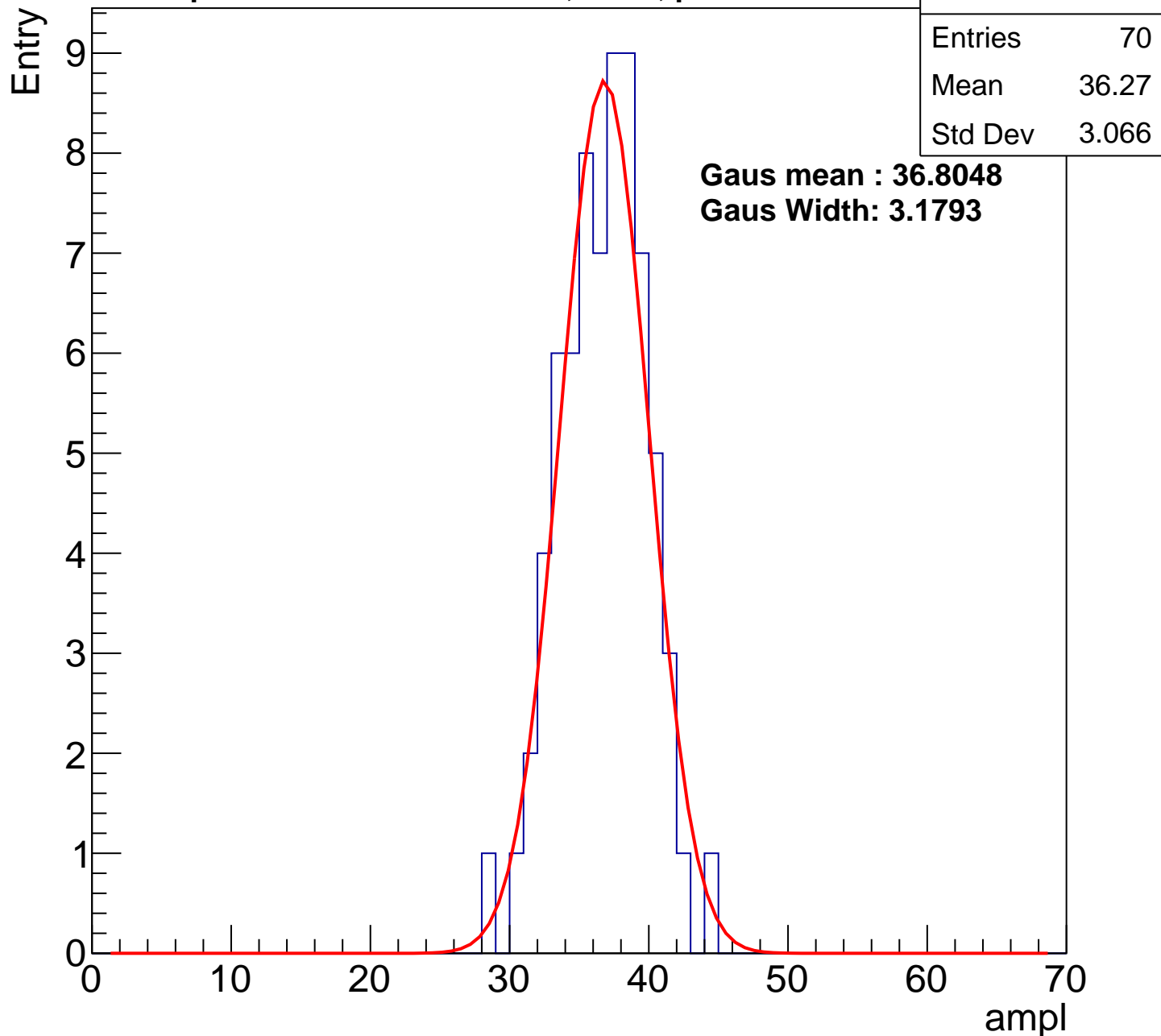
ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch39, adc1

calib_packv5_042523_0143.root, FC#7, port C2



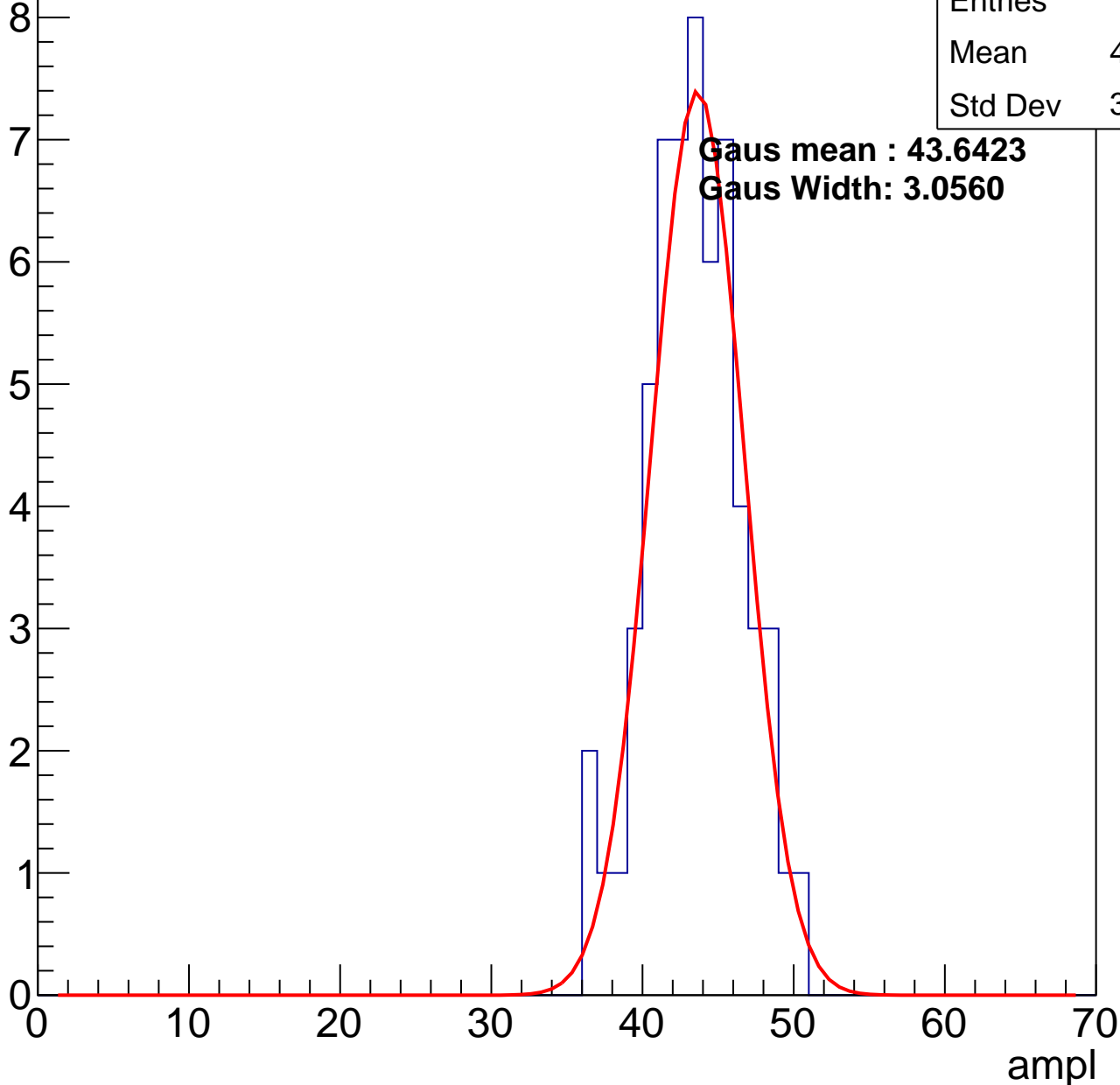
B1L103S, U12-ch39, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.98
Std Dev	3.095

Gaus mean : 43.6423
Gaus Width: 3.0560

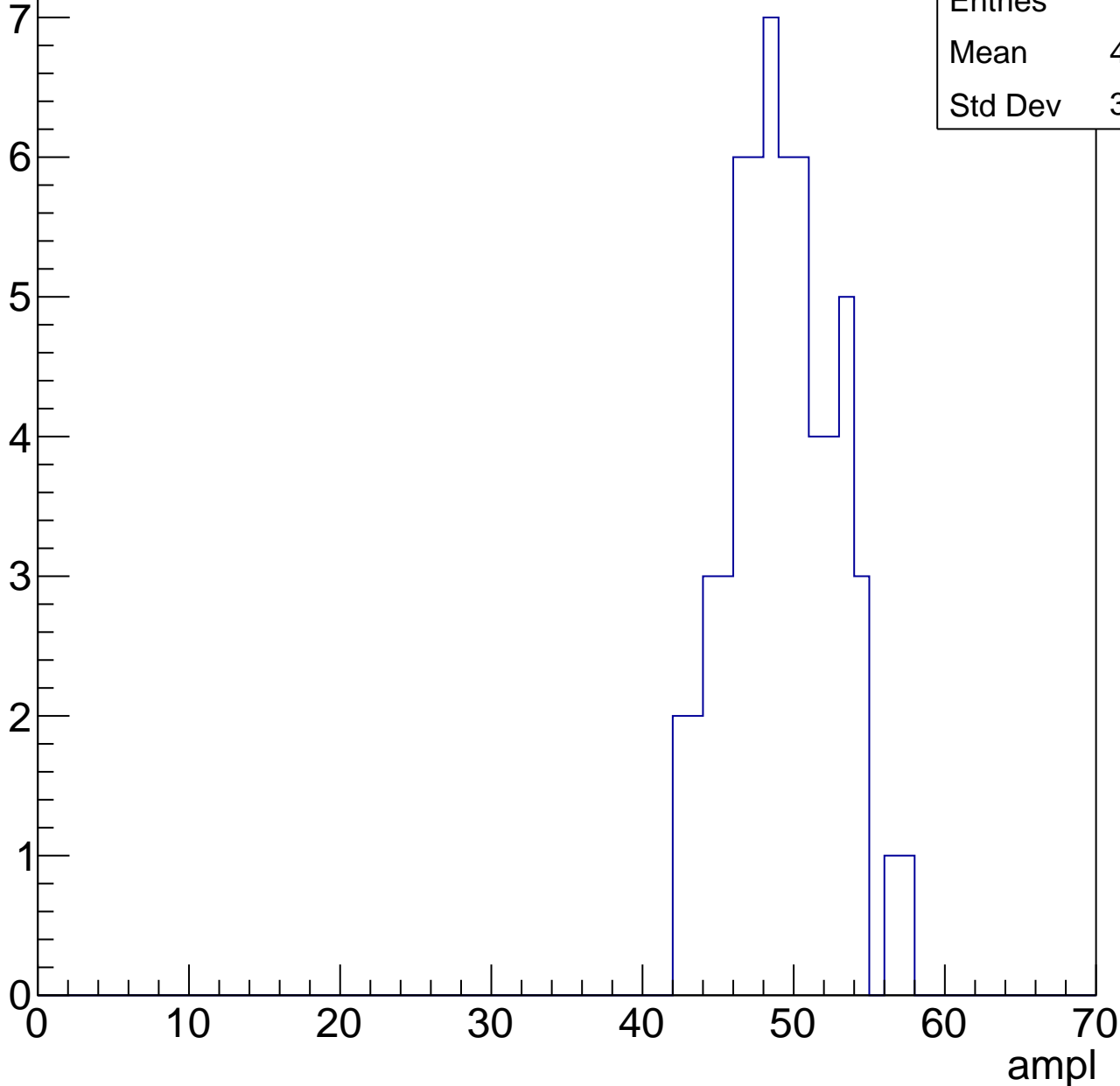


B1L103S, U12-ch39, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.76
Std Dev	3.446

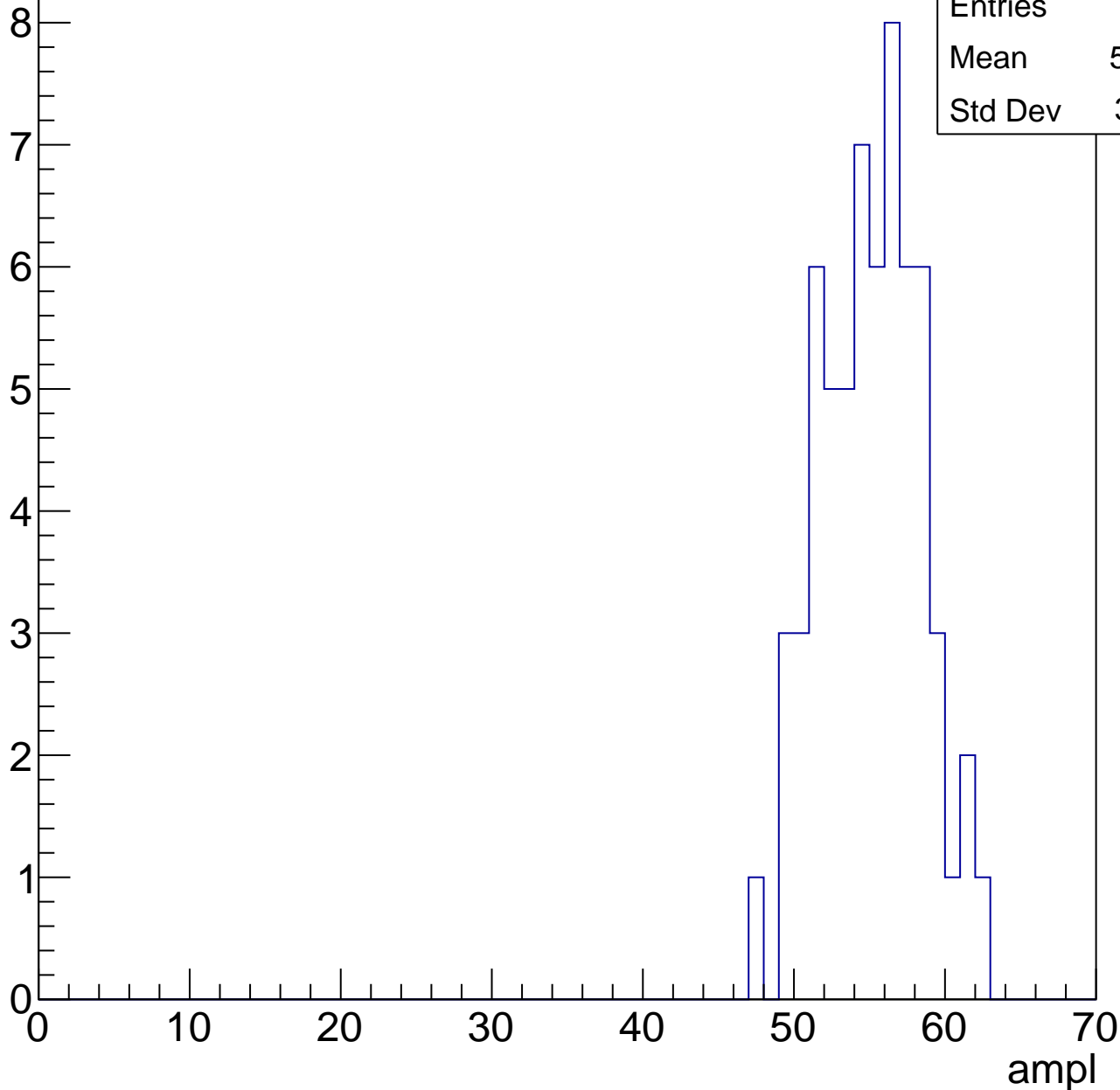


B1L103S, U12-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	54.63
Std Dev	3.301

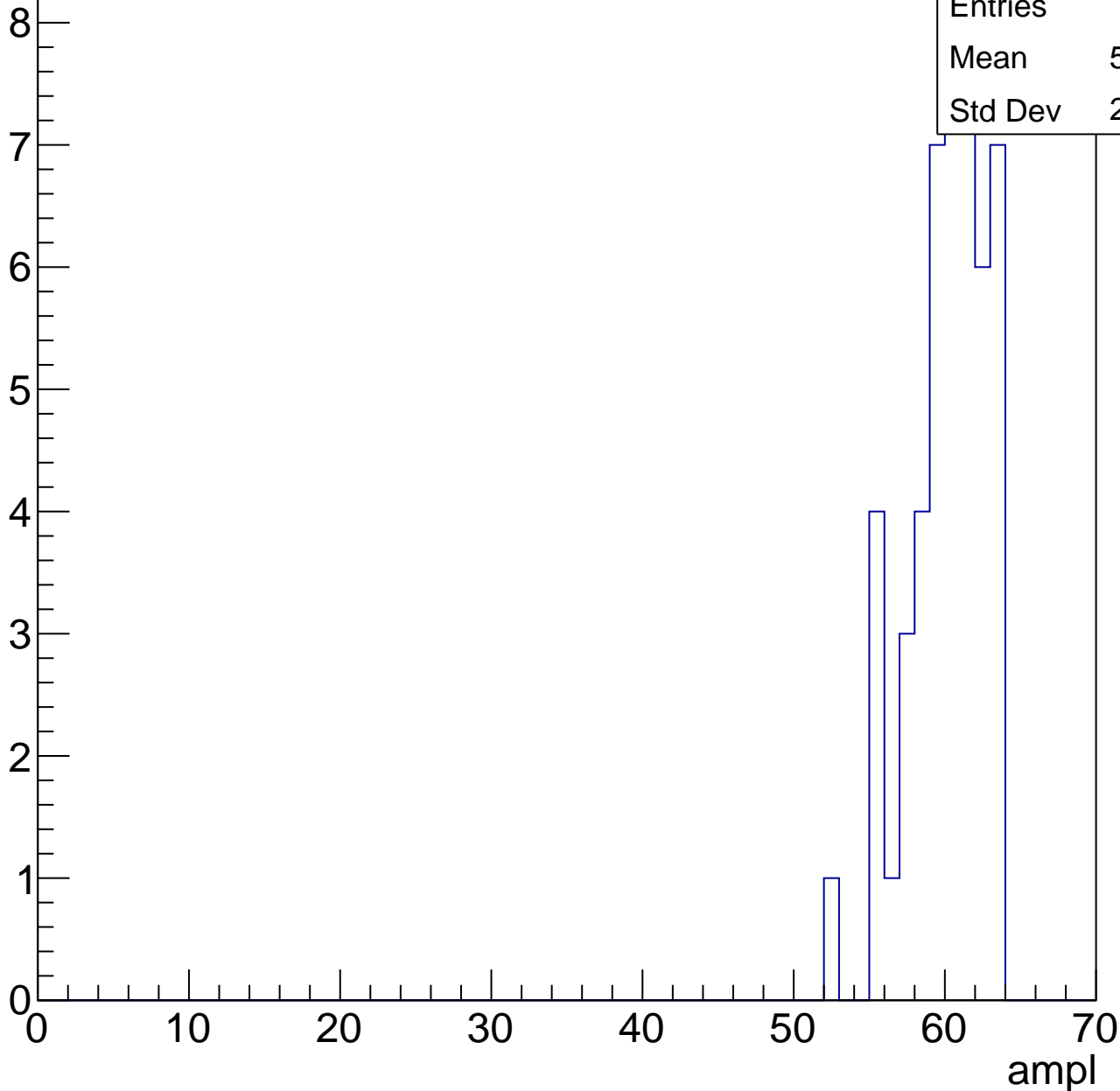


B1L103S, U12-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

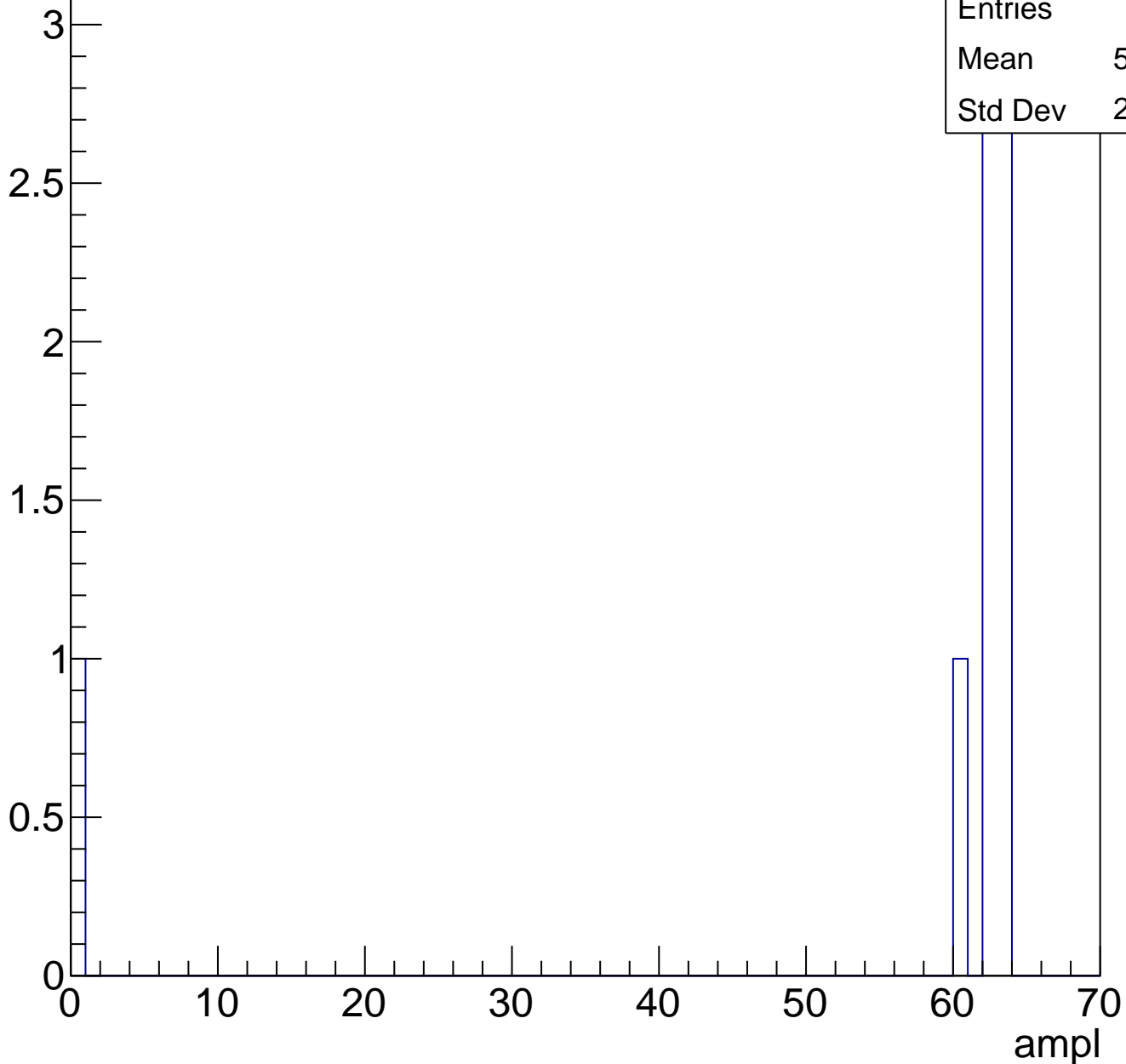
Entries	49
Mean	59.69
Std Dev	2.557



B1L103S, U12-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch40, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	28.73
Std Dev	5.108

Gaus mean : 29.8677

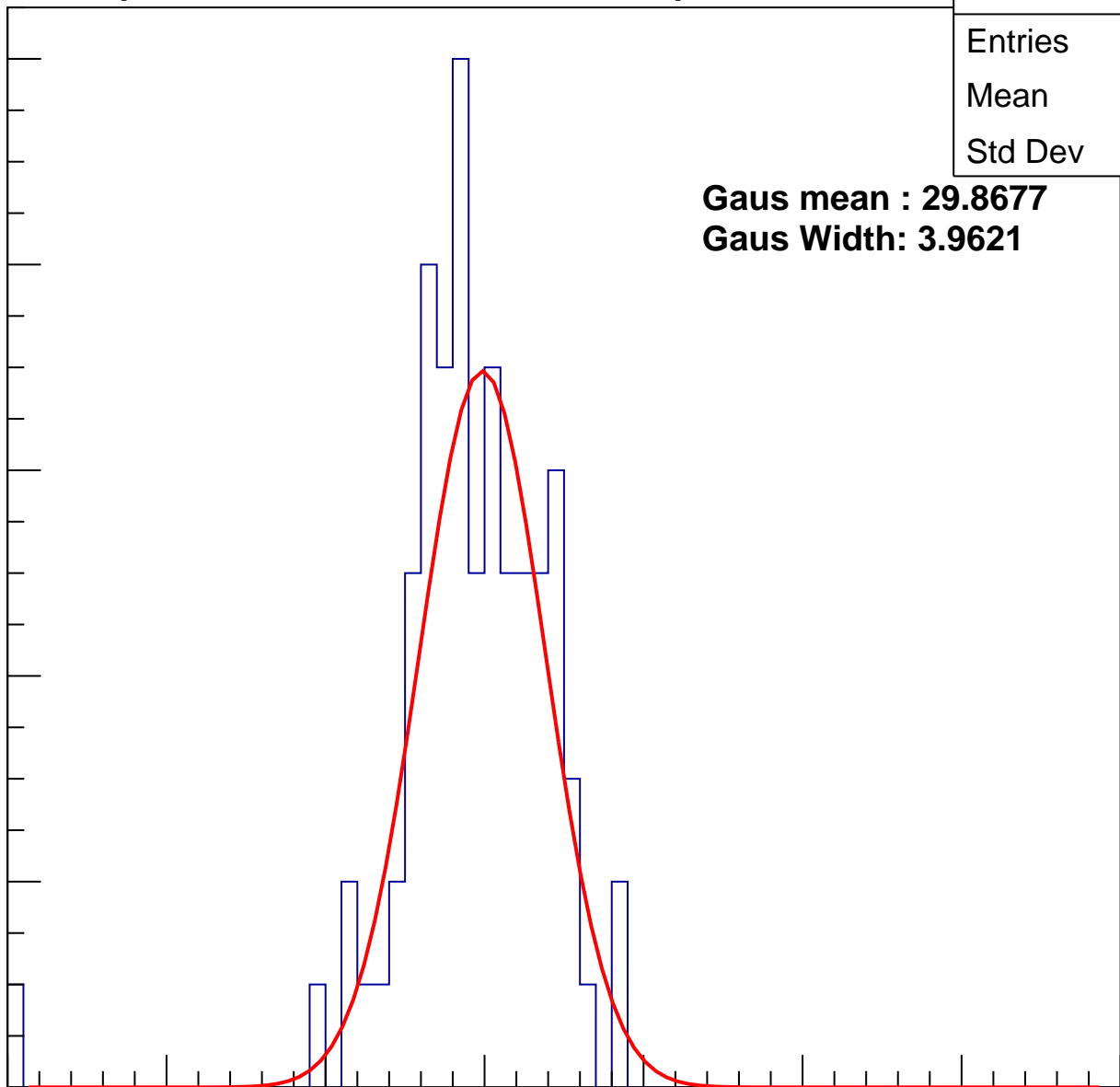
Gaus Width: 3.9621

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch40, adc1

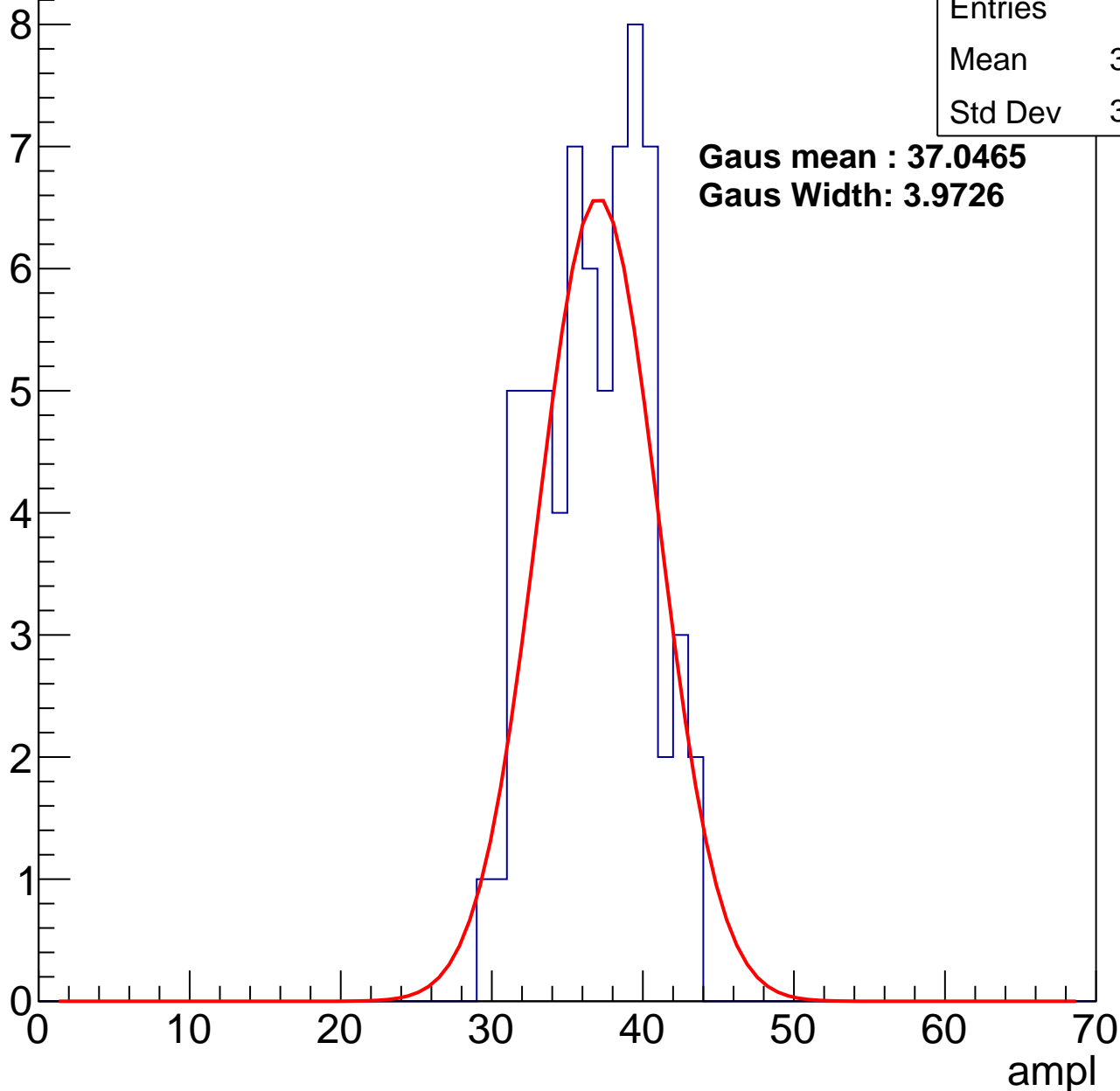
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	36.37
Std Dev	3.476

Gaus mean : 37.0465

Gaus Width: 3.9726



B1L103S, U12-ch40, adc2

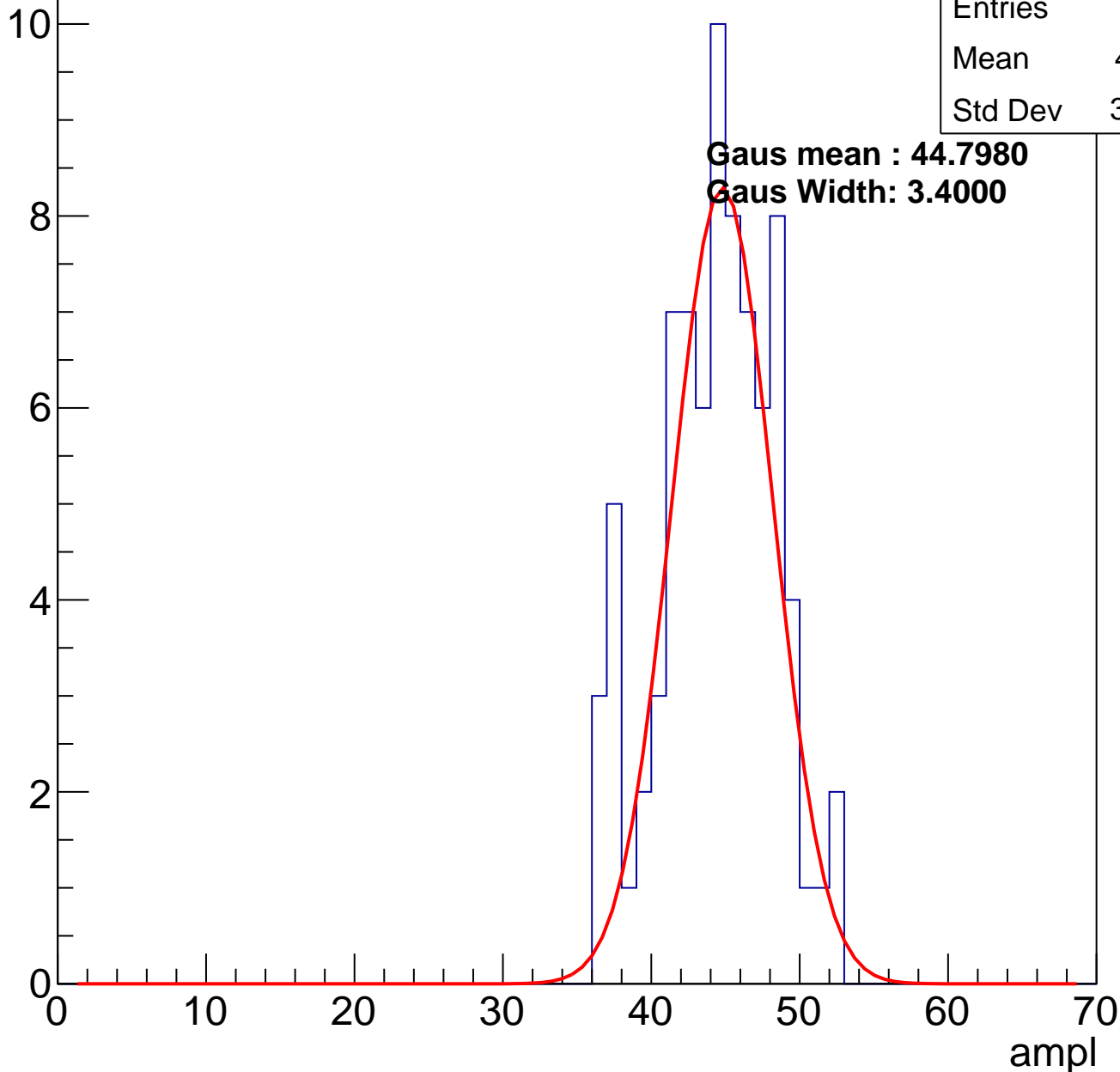
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	43.91
Std Dev	3.846

Gaus mean : 44.7980

Gaus Width: 3.4000

Entry

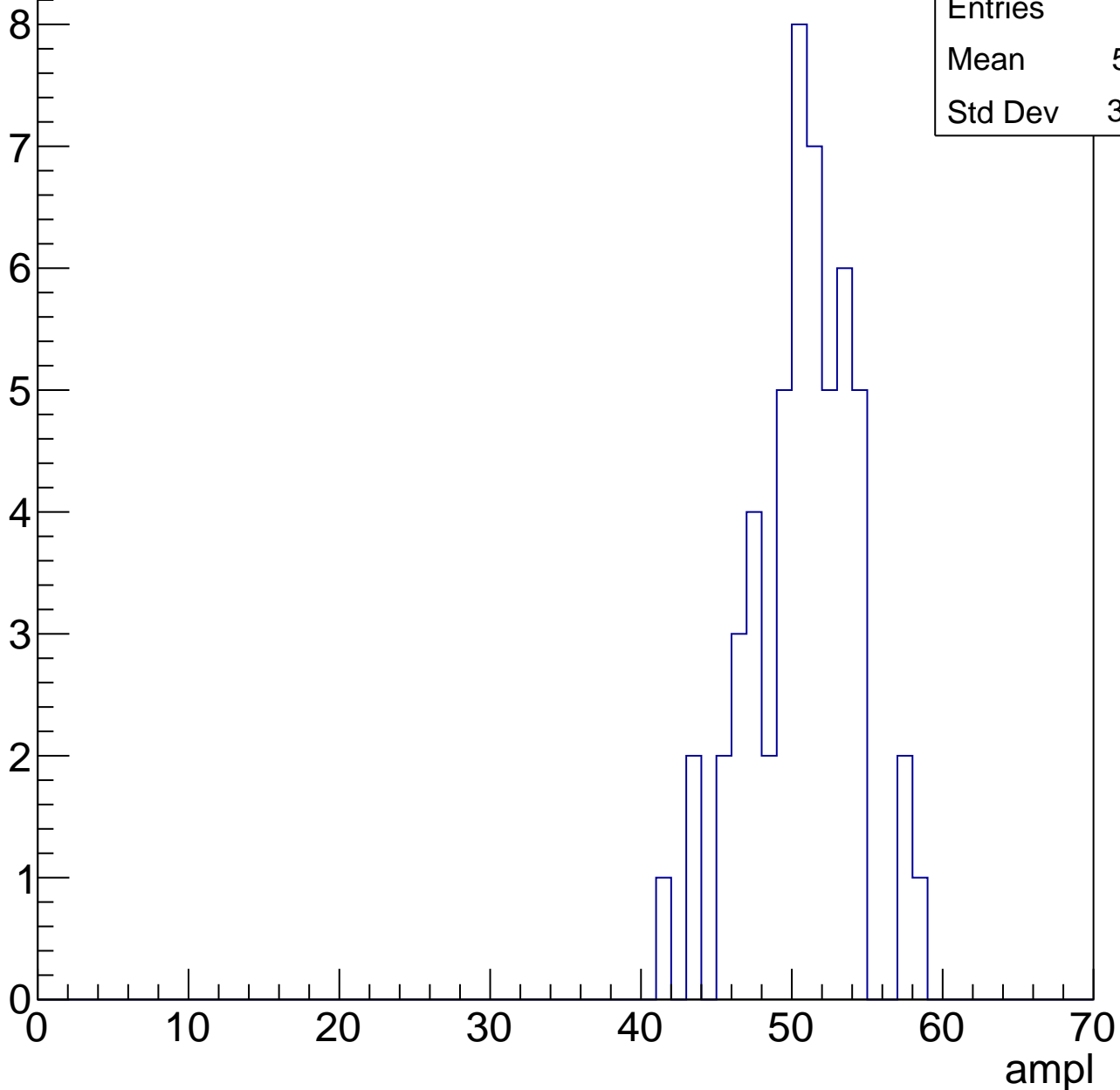


B1L103S, U12-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

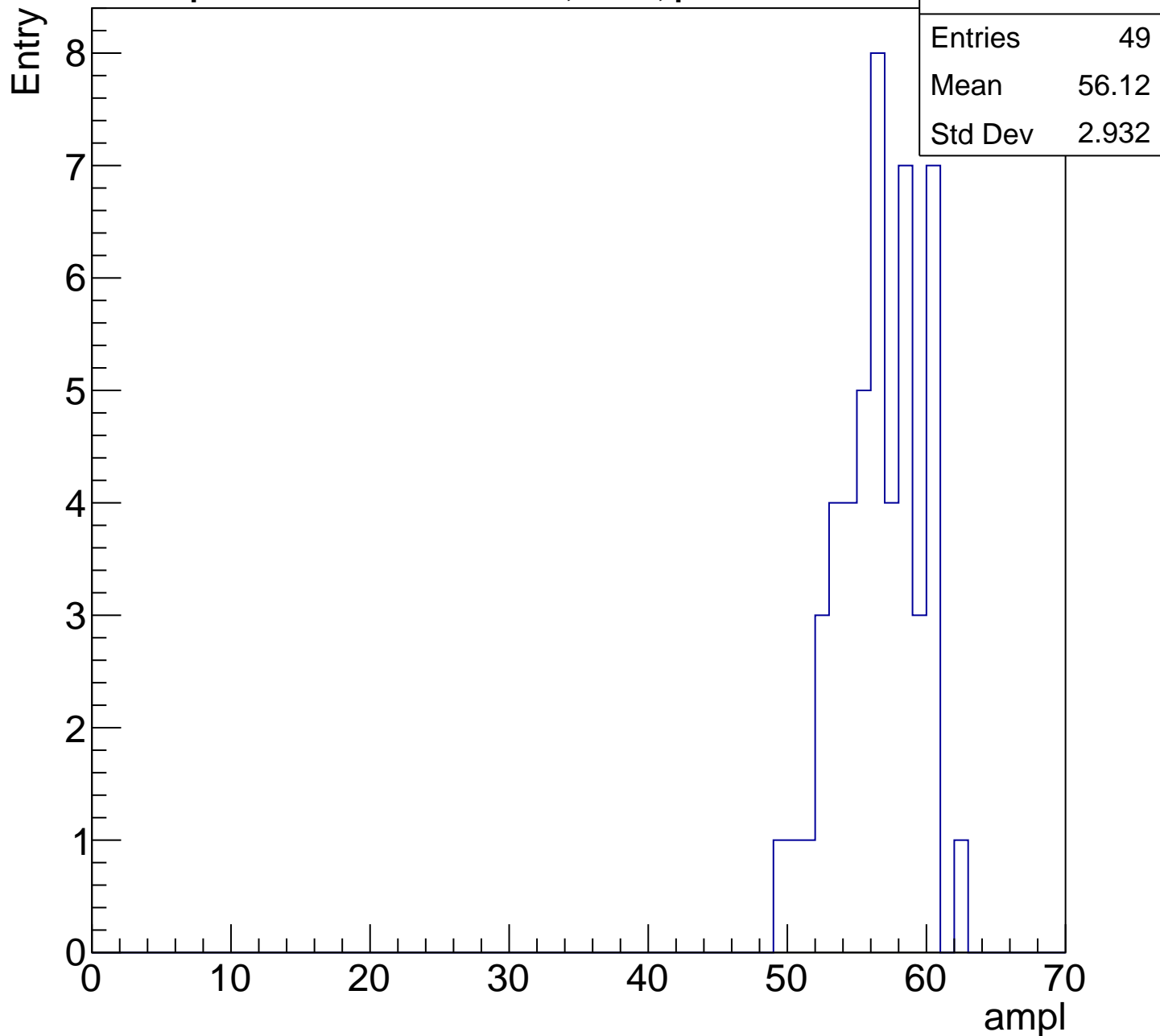
Entry

Entries	53
Mean	50.21
Std Dev	3.493



B1L103S, U12-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

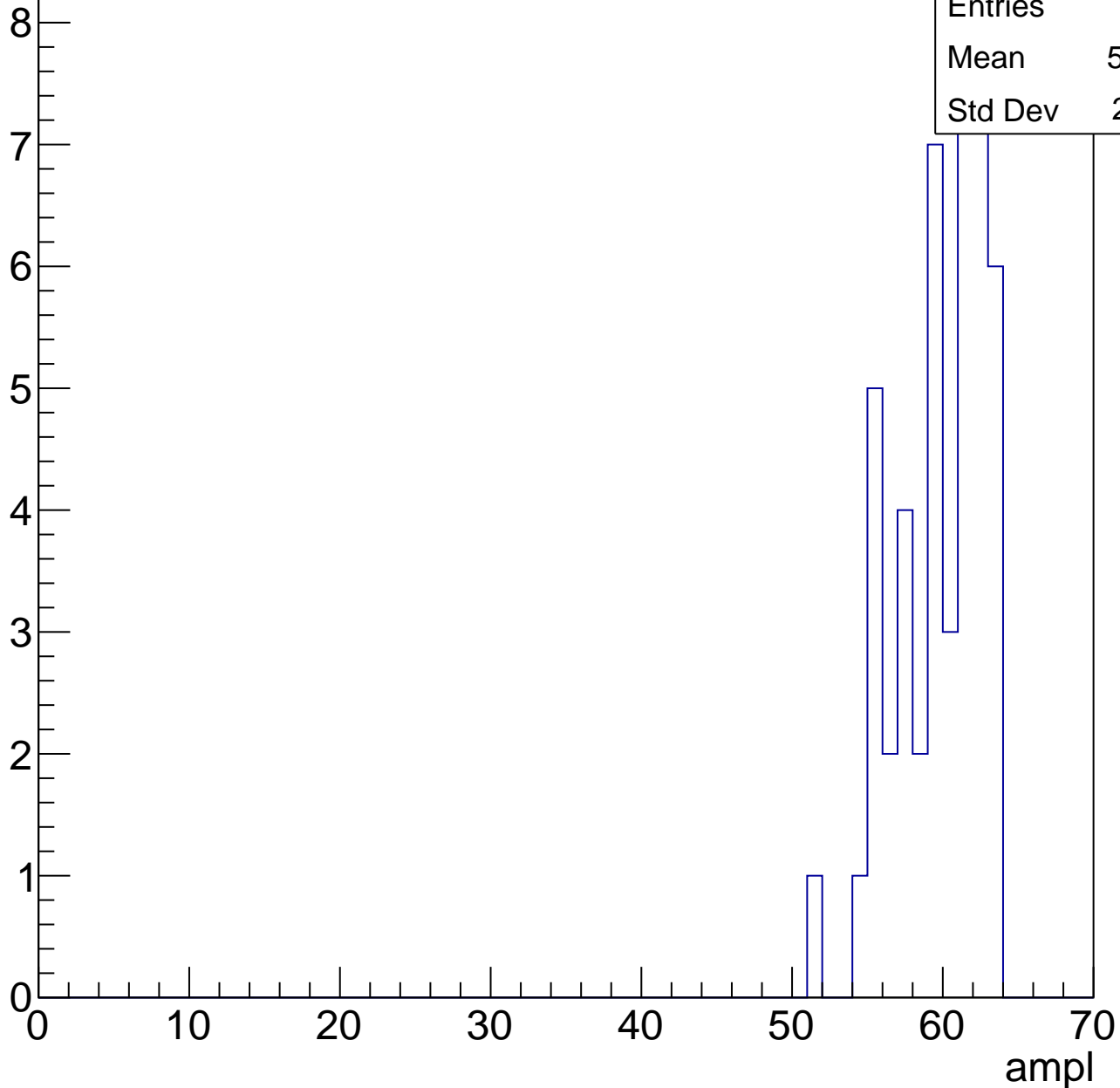


B1L103S, U12-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

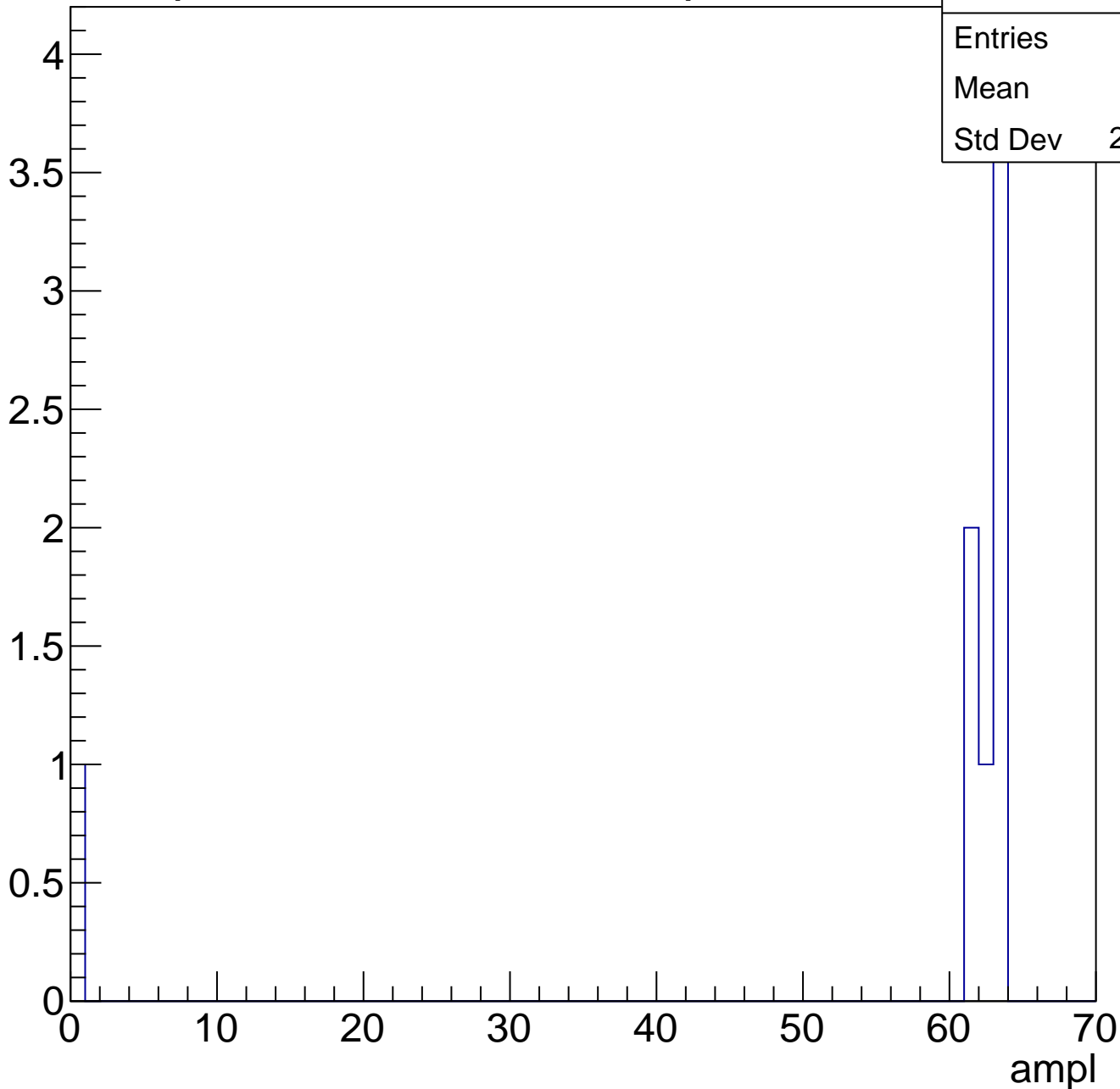
Entries	47
Mean	59.38
Std Dev	2.921



B1L103S, U12-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch41, adc0

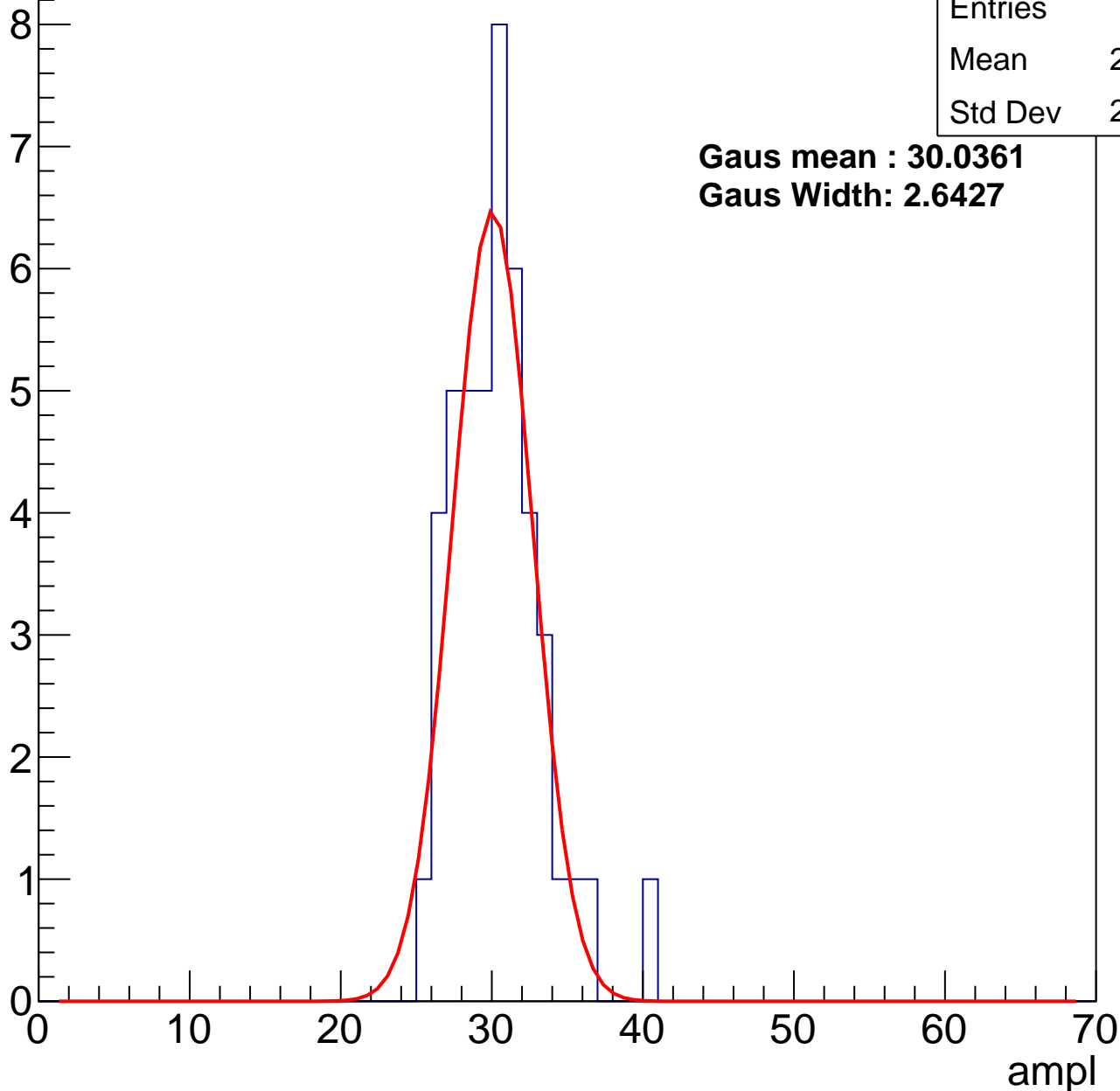
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	29.93
Std Dev	2.924

Gaus mean : 30.0361

Gaus Width: 2.6427



B1L103S, U12-ch41, adc1

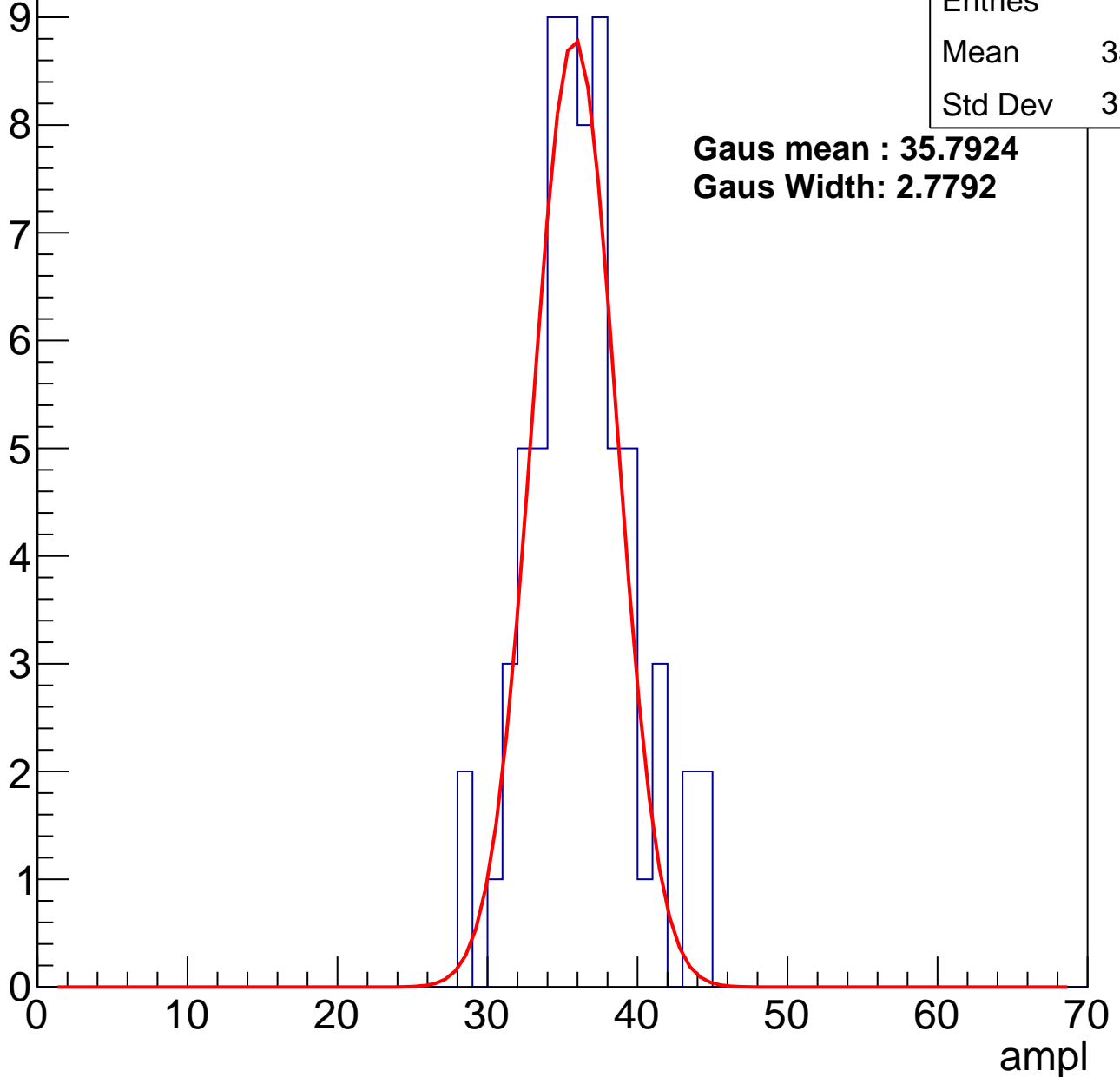
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.77
Std Dev	3.397

Gaus mean : 35.7924

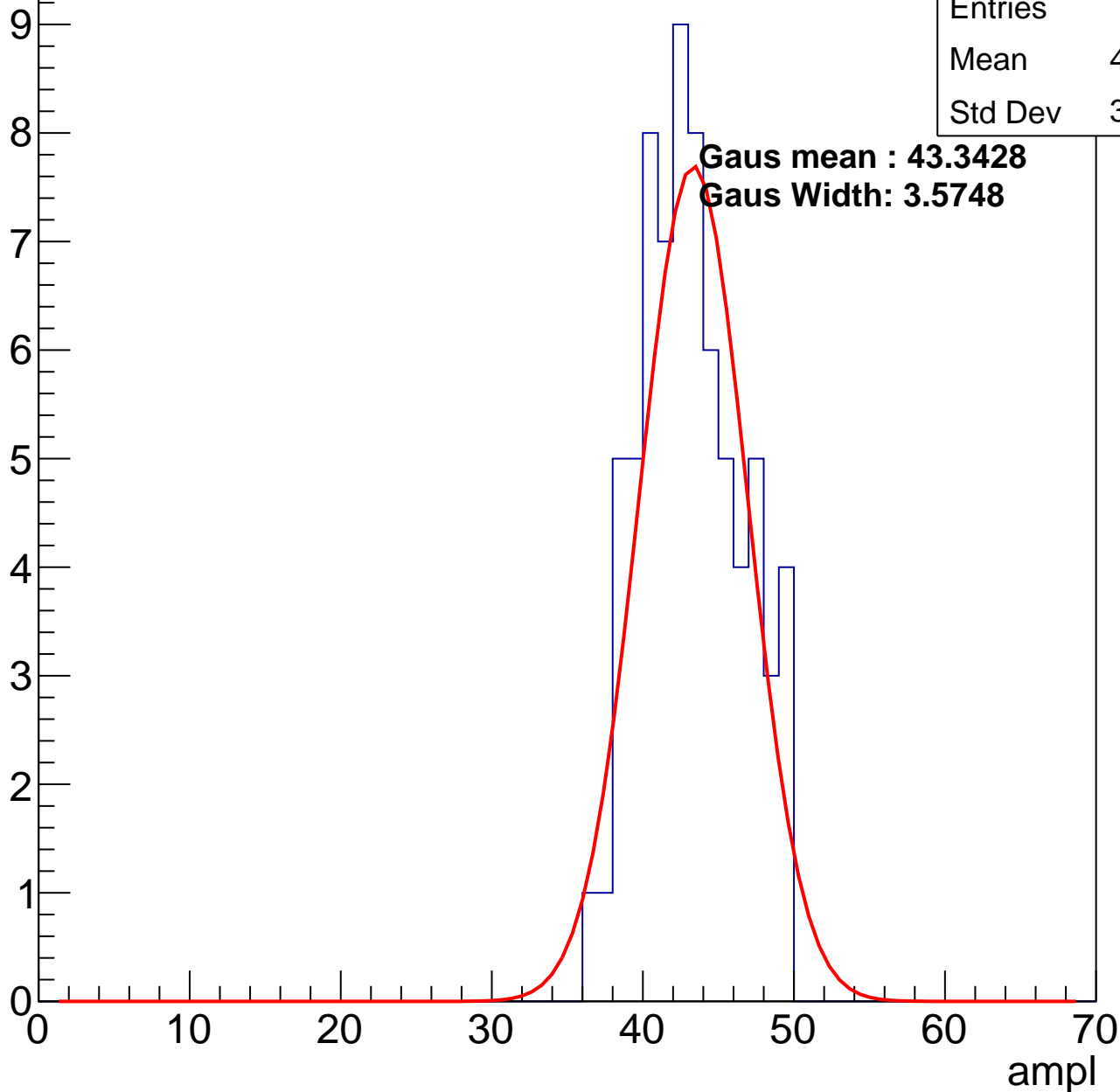
Gaus Width: 2.7792



B1L103S, U12-ch41, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

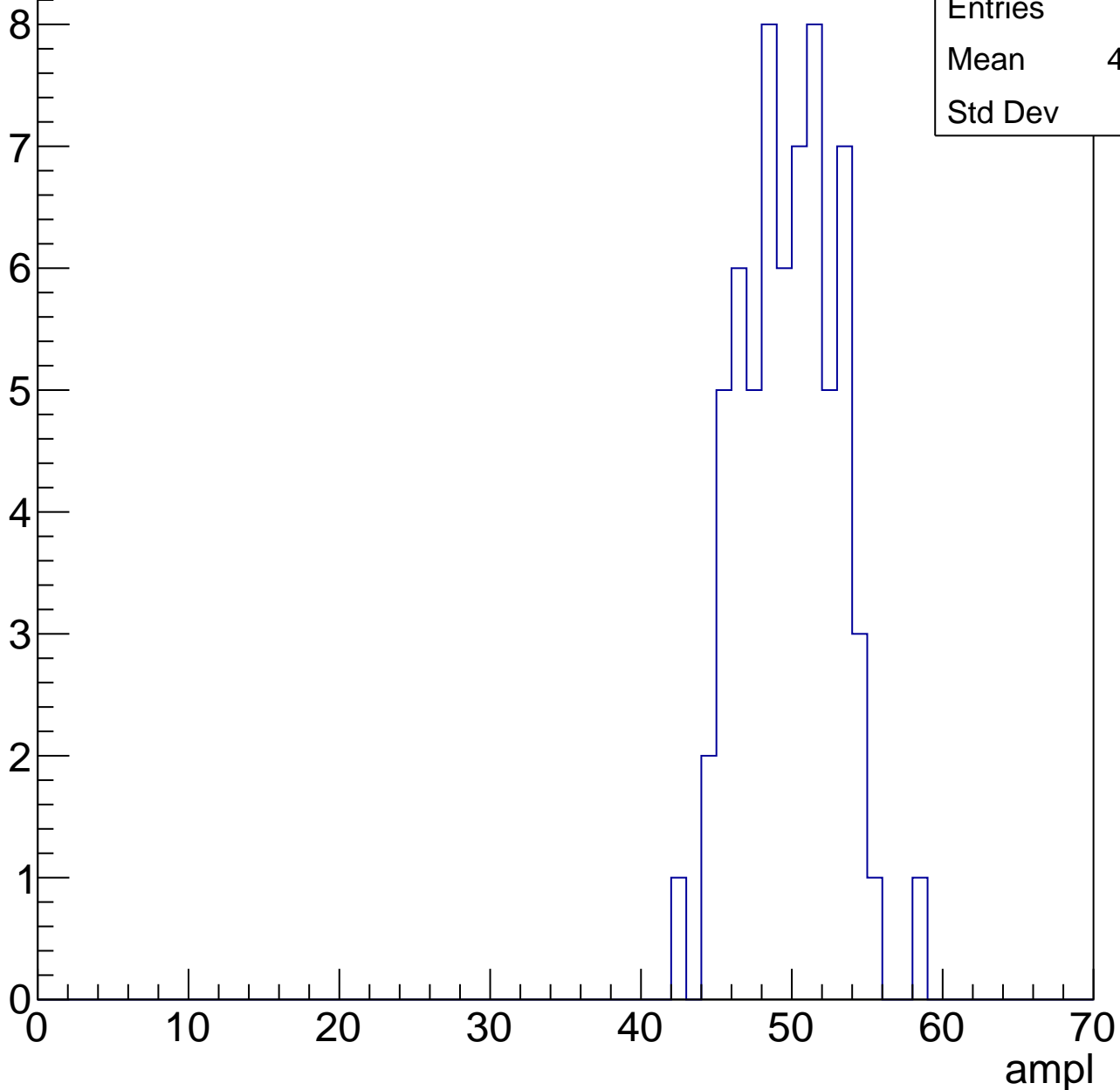


B1L103S, U12-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

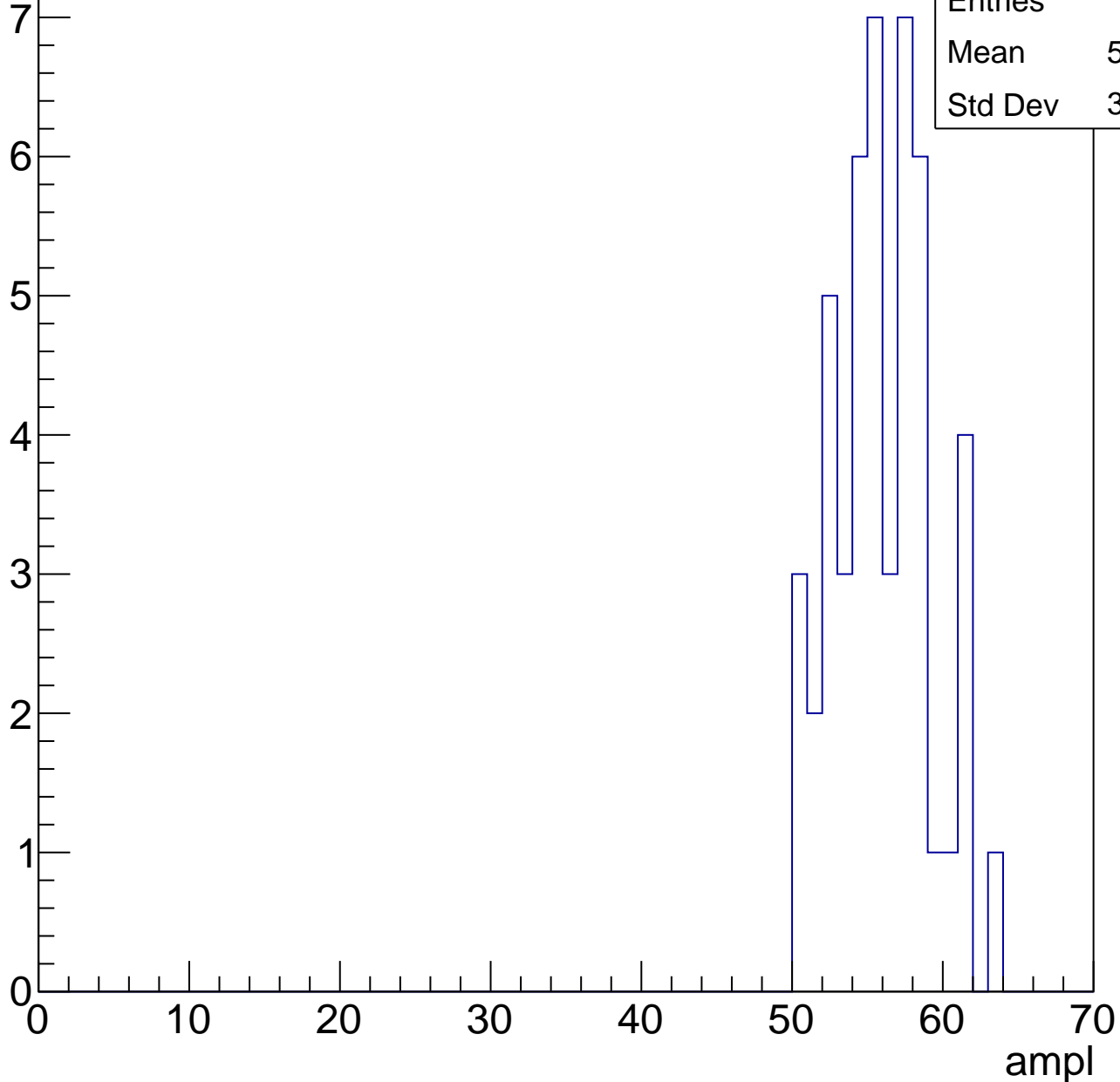
Entries	65
Mean	49.35
Std Dev	3.14



B1L103S, U12-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



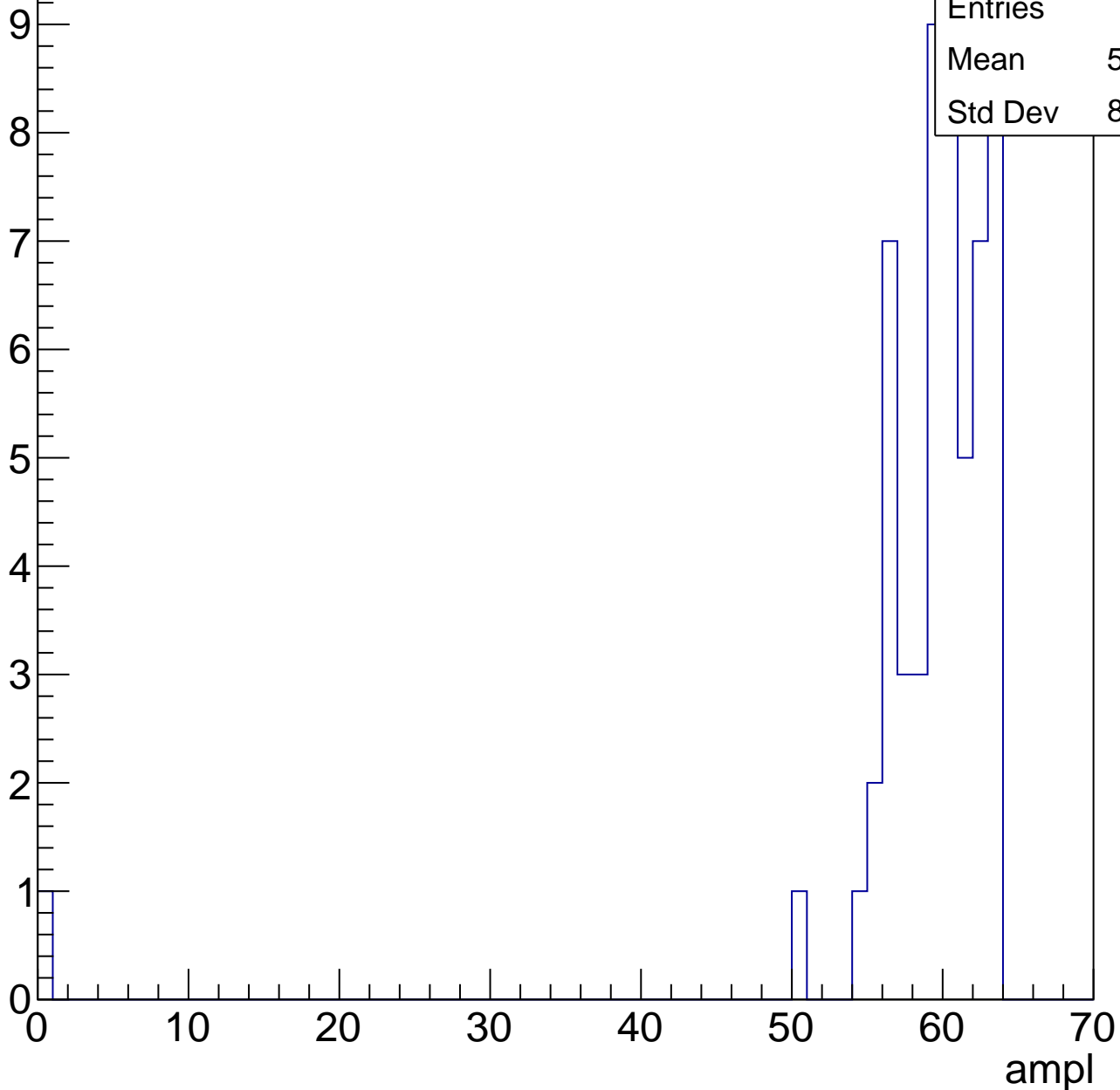
Entries	49
Mean	55.53
Std Dev	3.163

B1L103S, U12-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

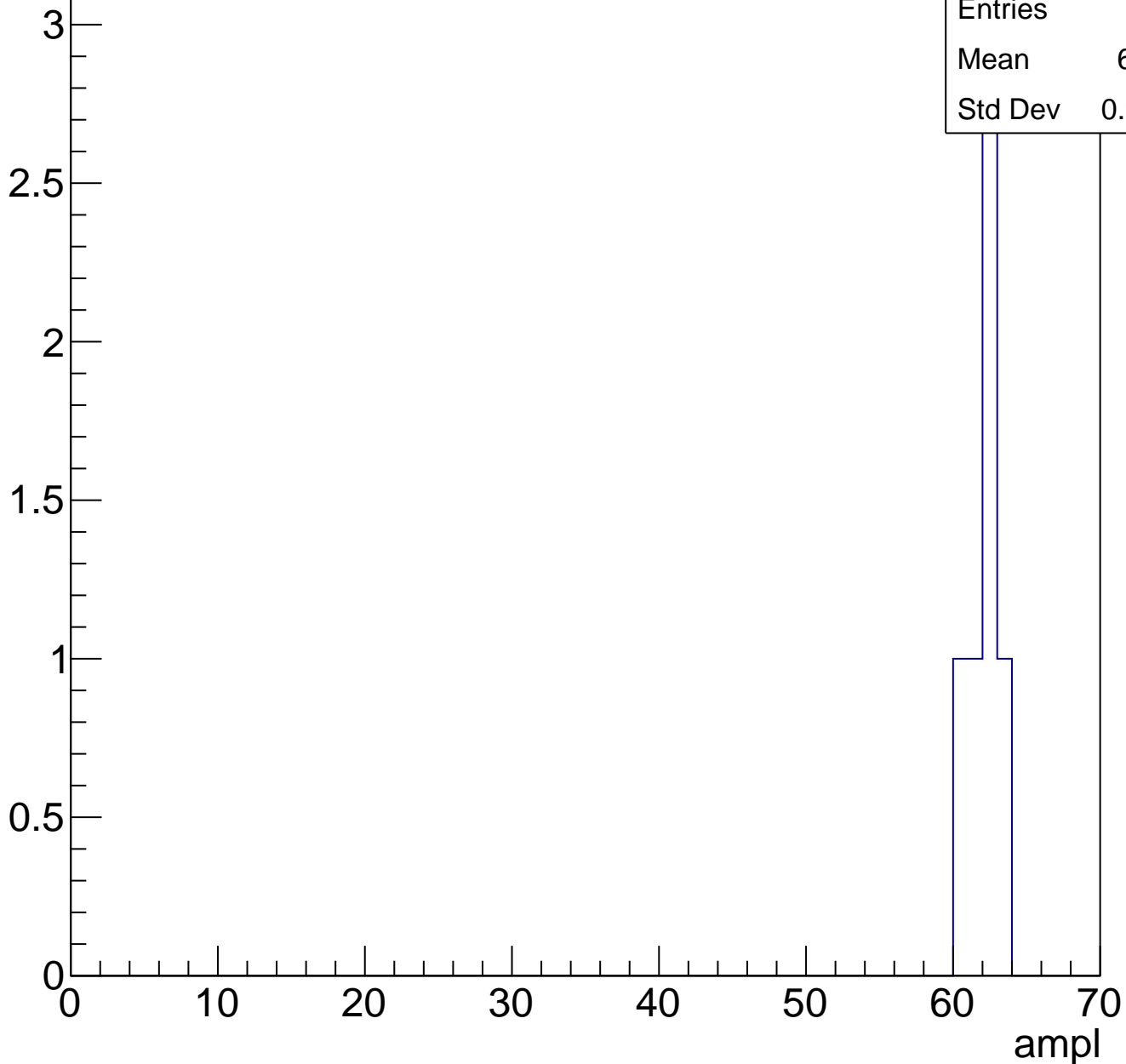
Entries	55
Mean	58.27
Std Dev	8.407



B1L103S, U12-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

B1L103S, U12-ch42, adc0

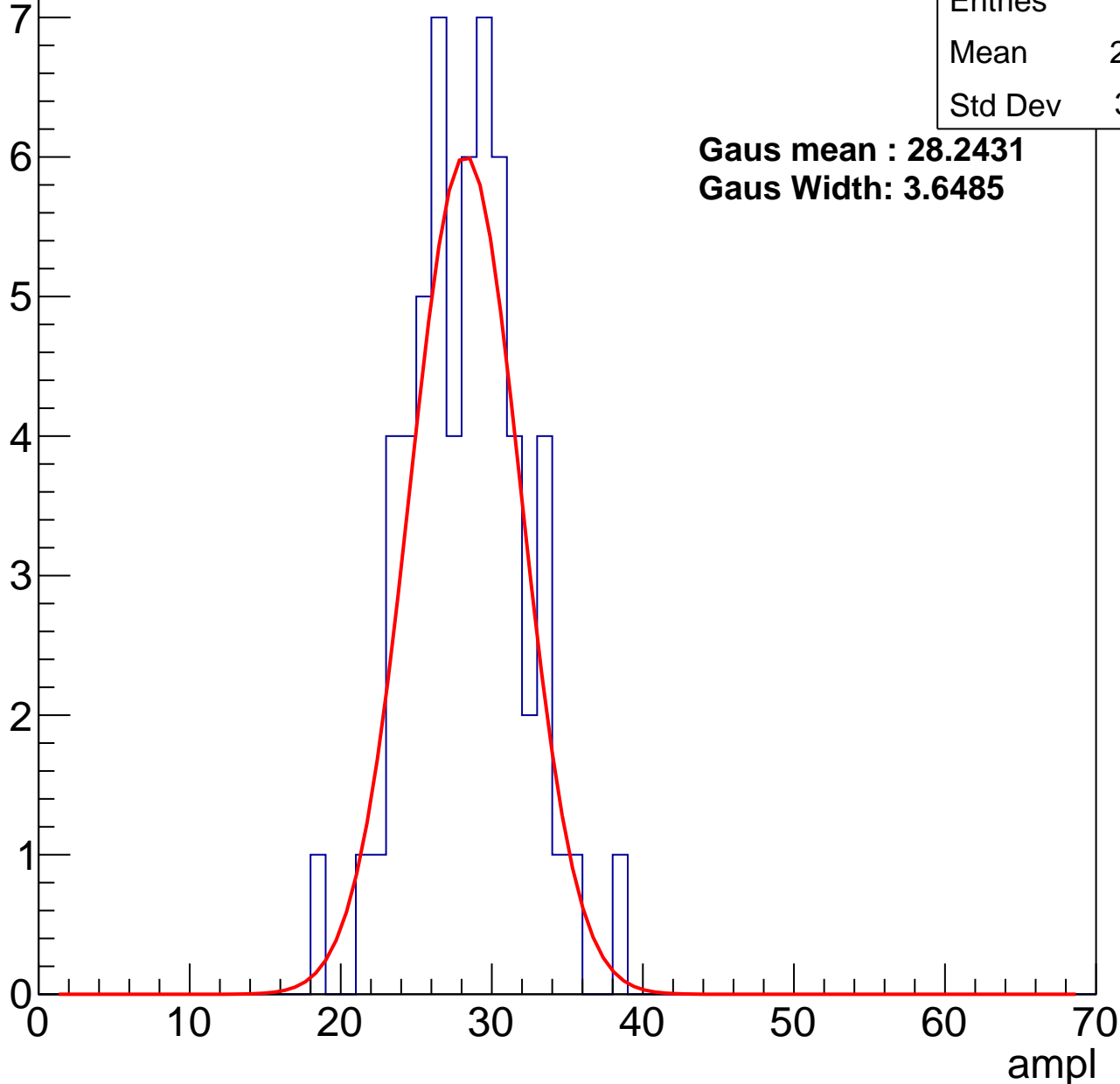
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	27.83
Std Dev	3.701

Gaus mean : 28.2431

Gaus Width: 3.6485



B1L103S, U12-ch42, adc1

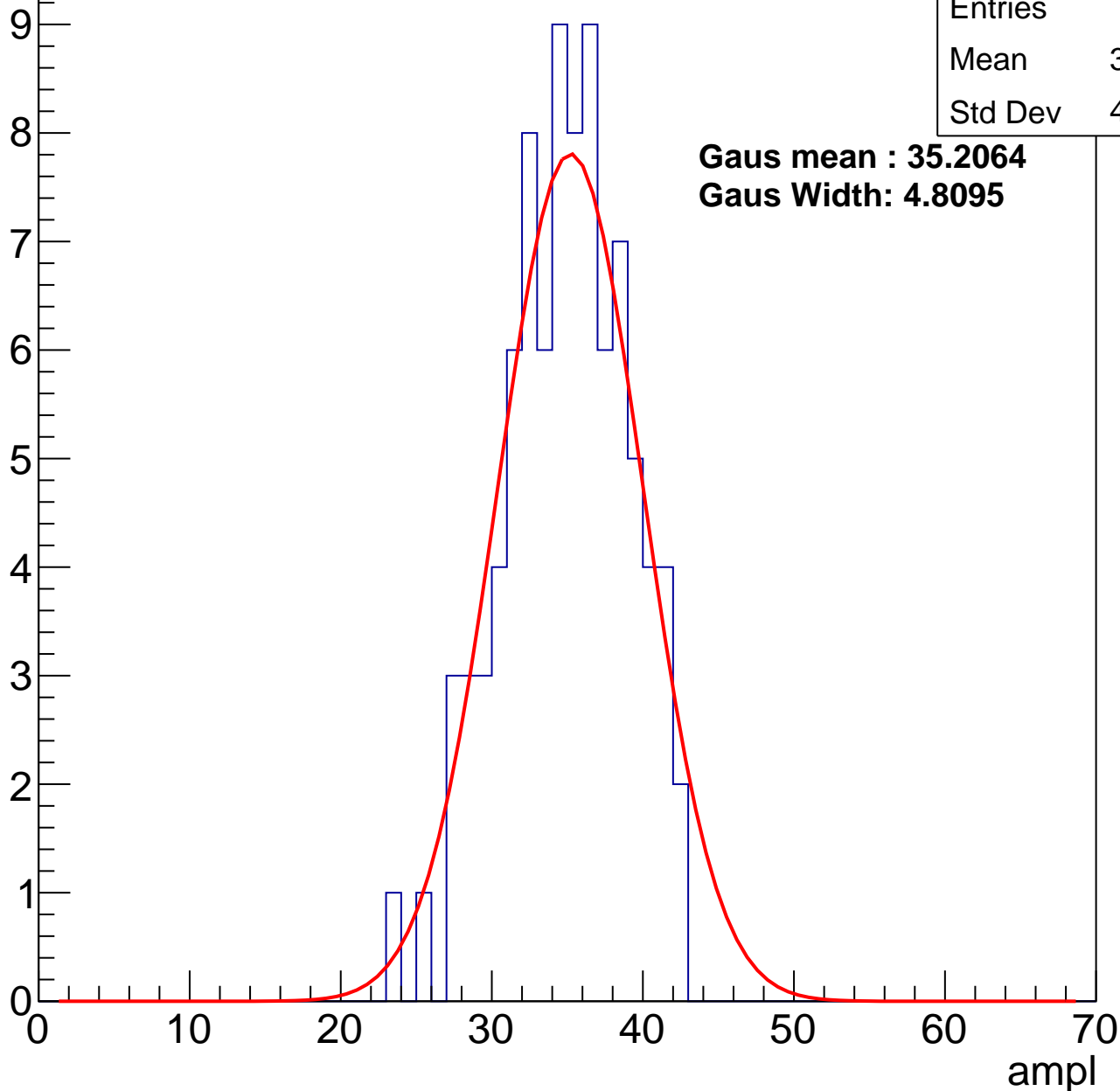
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	34.39
Std Dev	4.074

Gaus mean : 35.2064

Gaus Width: 4.8095



B1L103S, U12-ch42, adc2

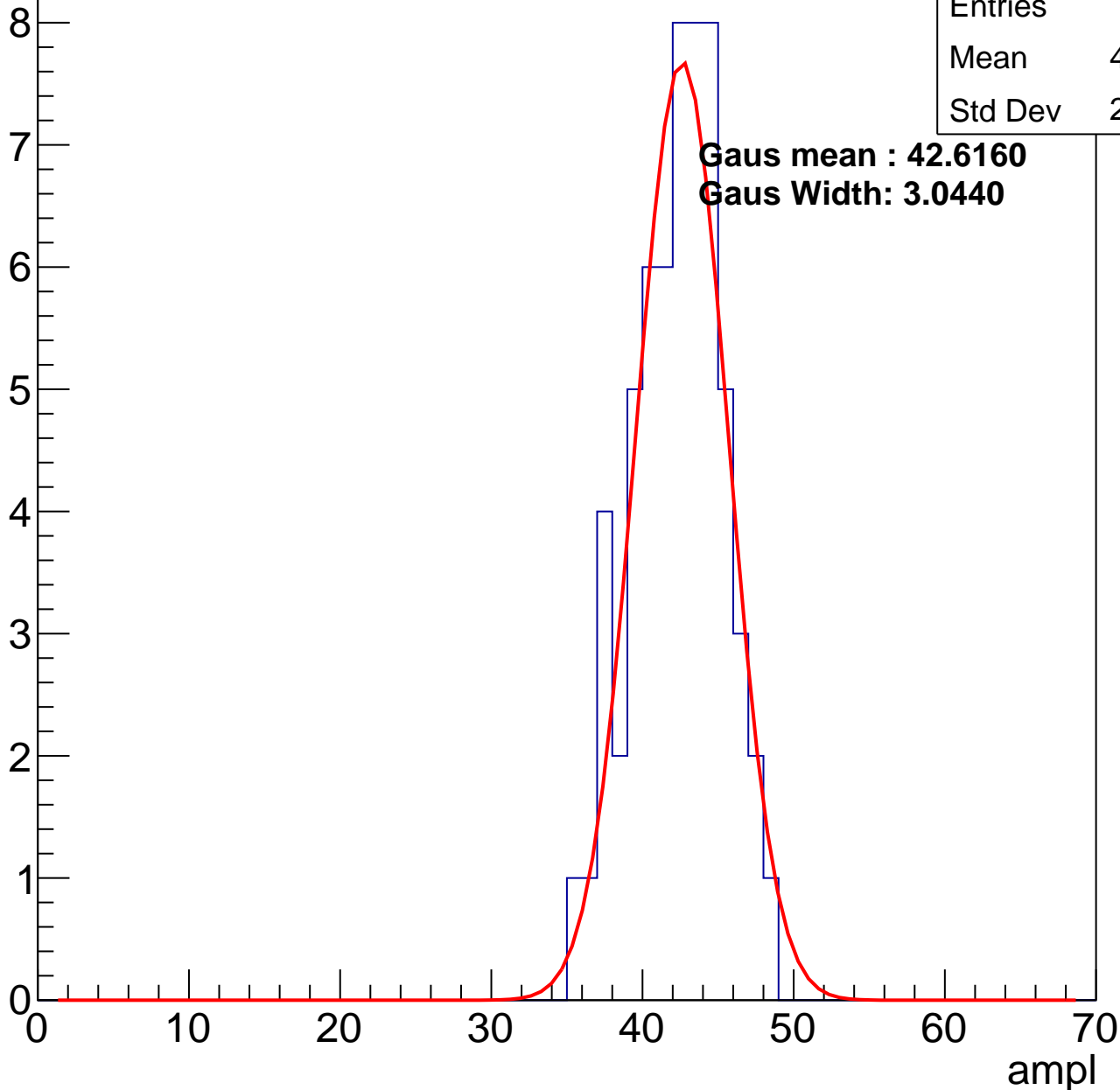
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	41.88
Std Dev	2.916

Gaus mean : 42.6160

Gaus Width: 3.0440



B1L103S, U12-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	49.13
Std Dev	3.155

Entry

10

8

6

4

2

0

0

10

20

30

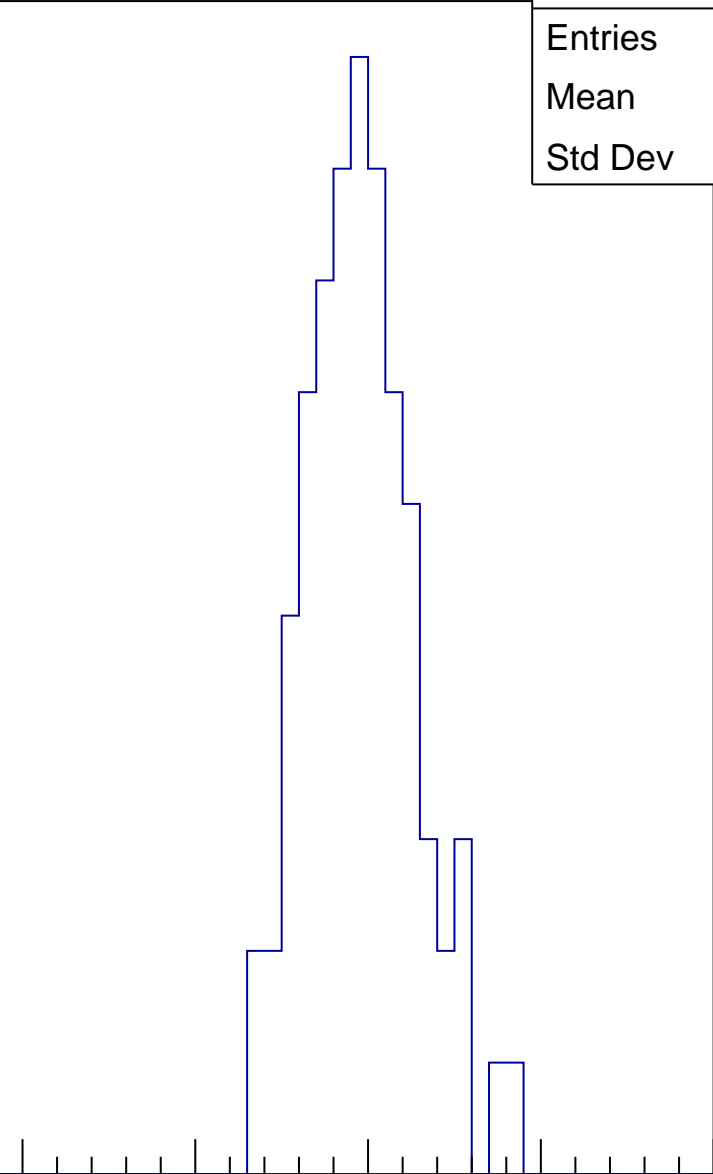
40

50

60

ampl

70

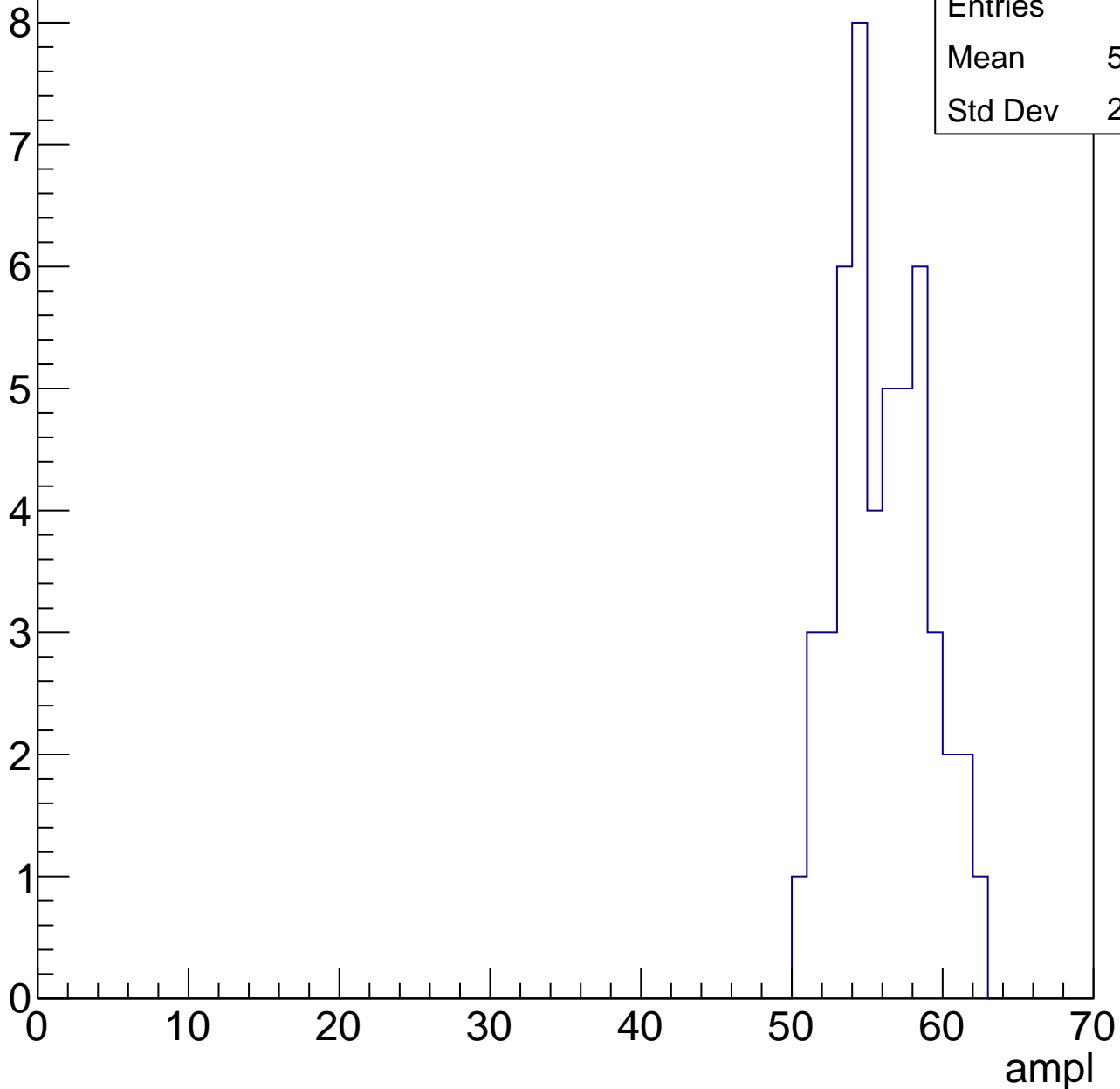


B1L103S, U12-ch42, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.57
Std Dev	2.907



B1L103S, U12-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	49
Mean	59.22
Std Dev	2.787

ampl

0

10

20

30

40

50

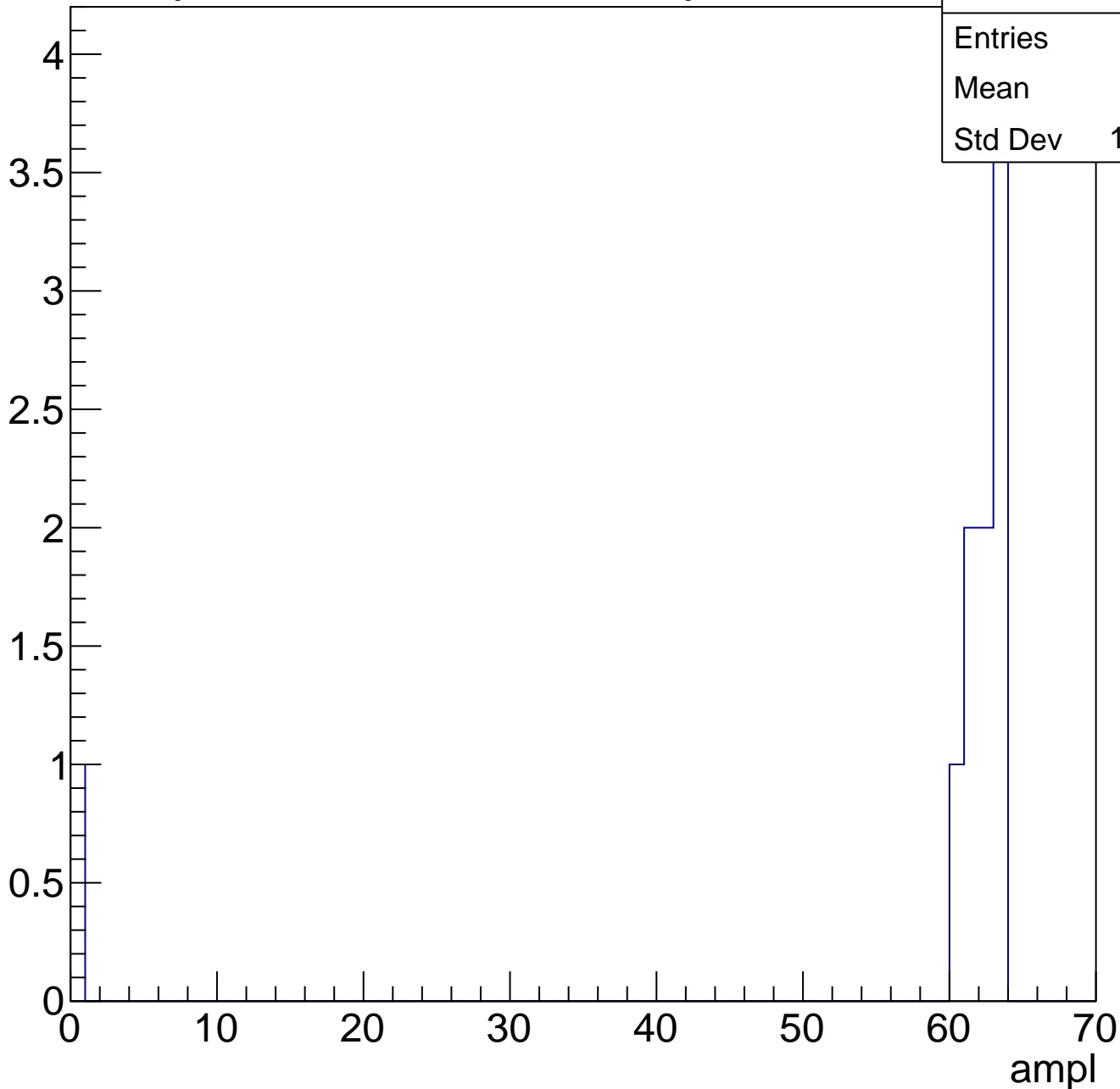
60

70

B1L103S, U12-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch43, adc0

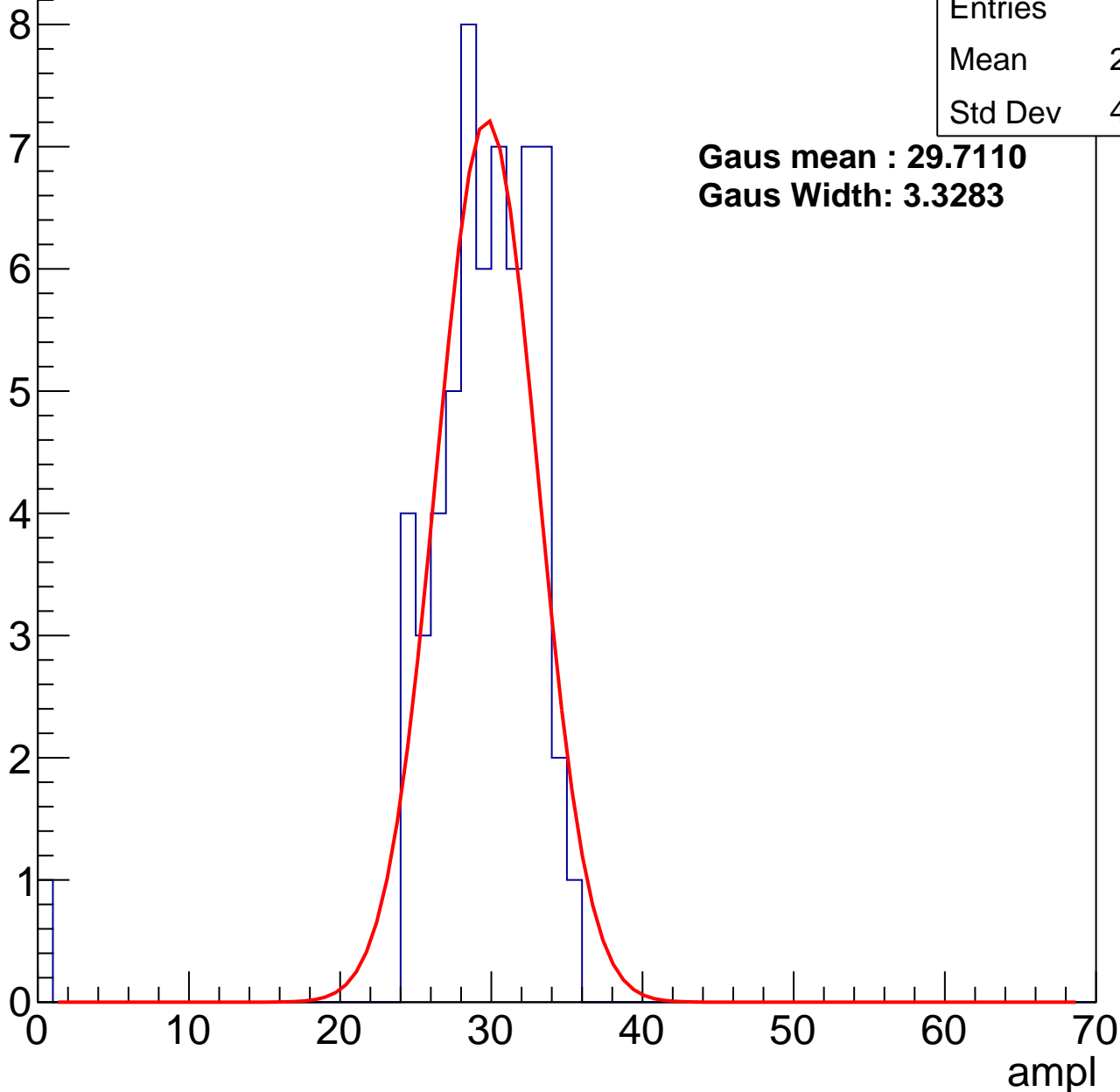
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.89
Std Dev	4.694

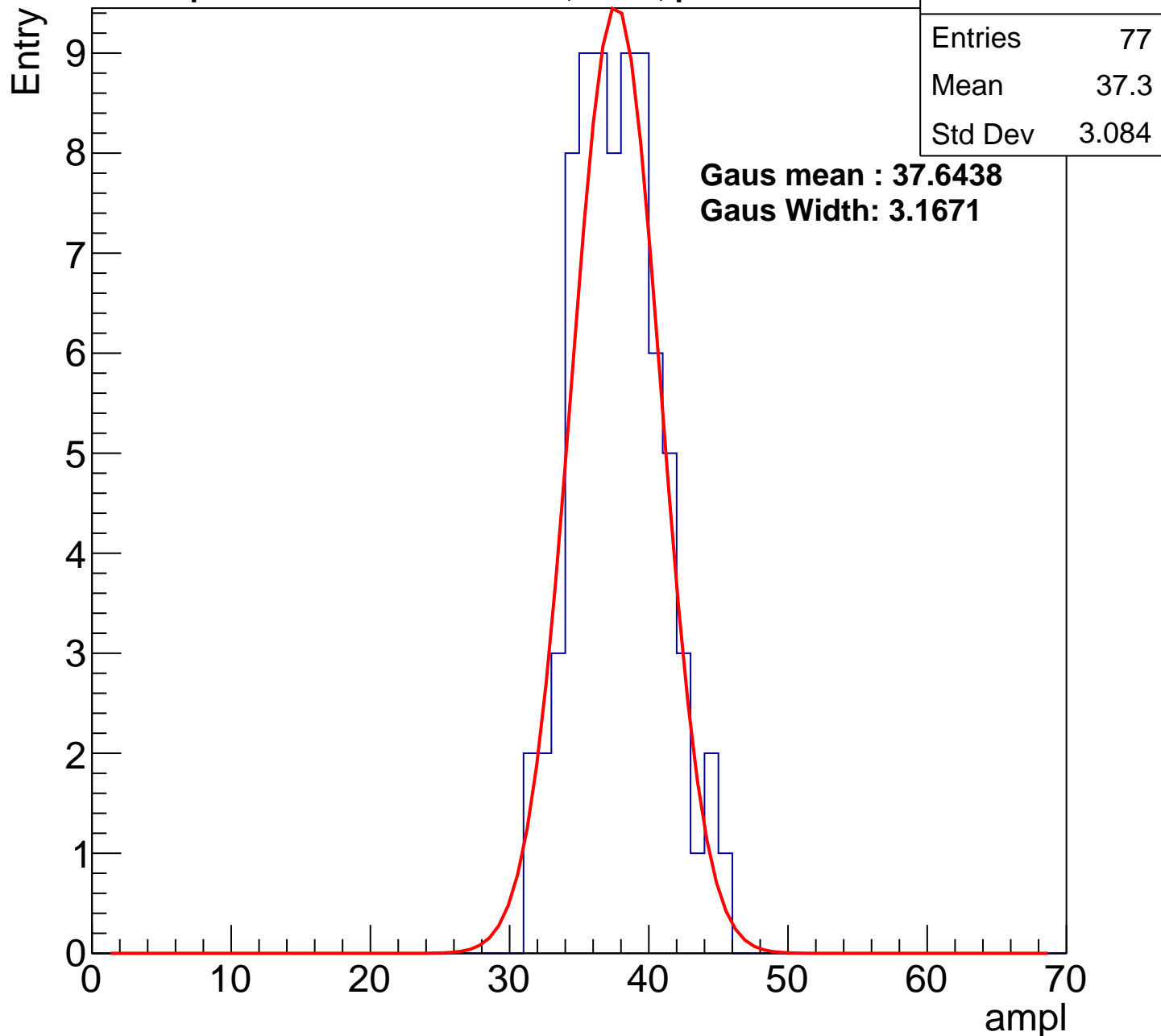
Gaus mean : 29.7110

Gaus Width: 3.3283



B1L103S, U12-ch43, adc1

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U12-ch43, adc2

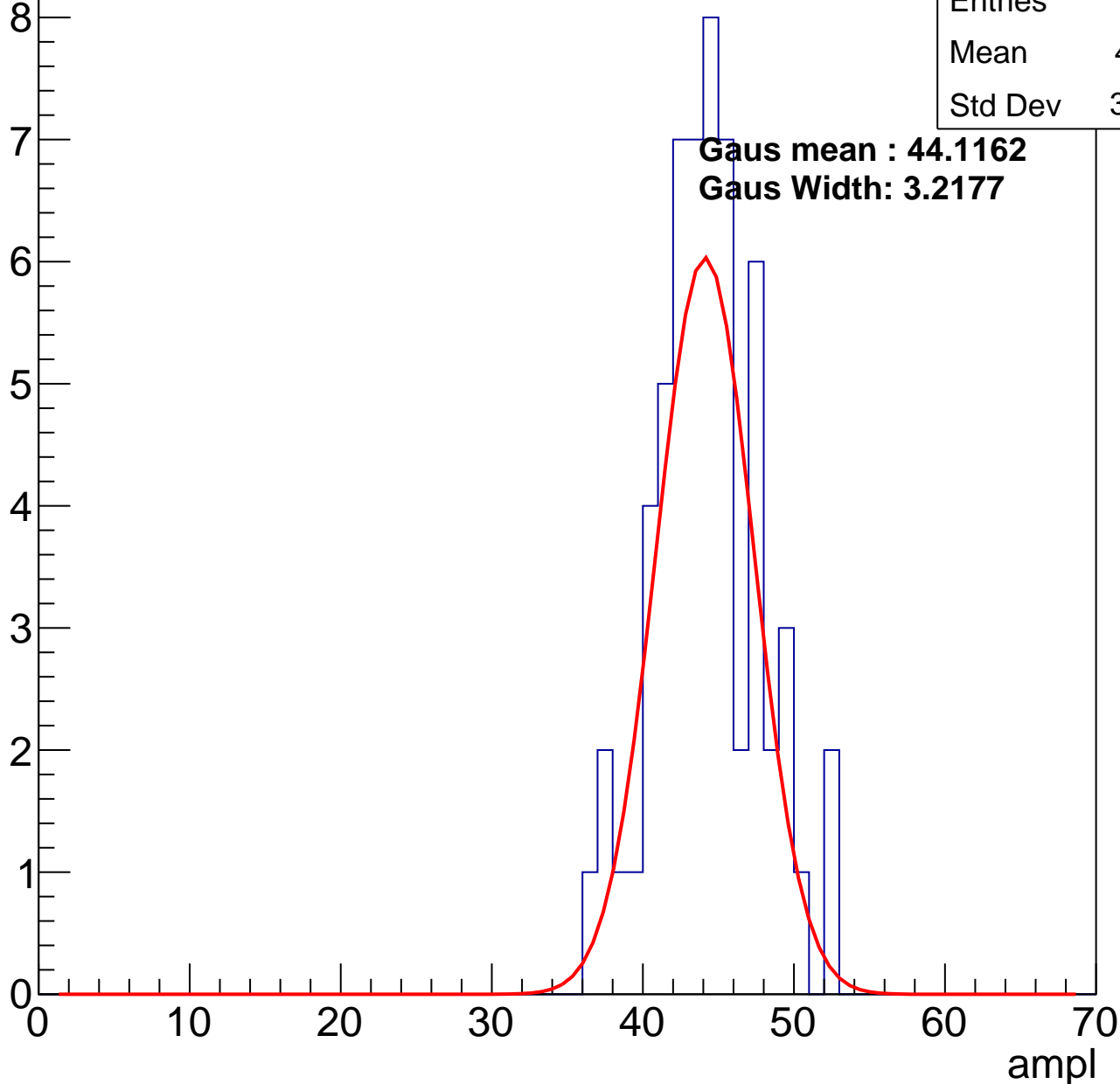
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.81
Std Dev	3.457

Gaus mean : 44.1162

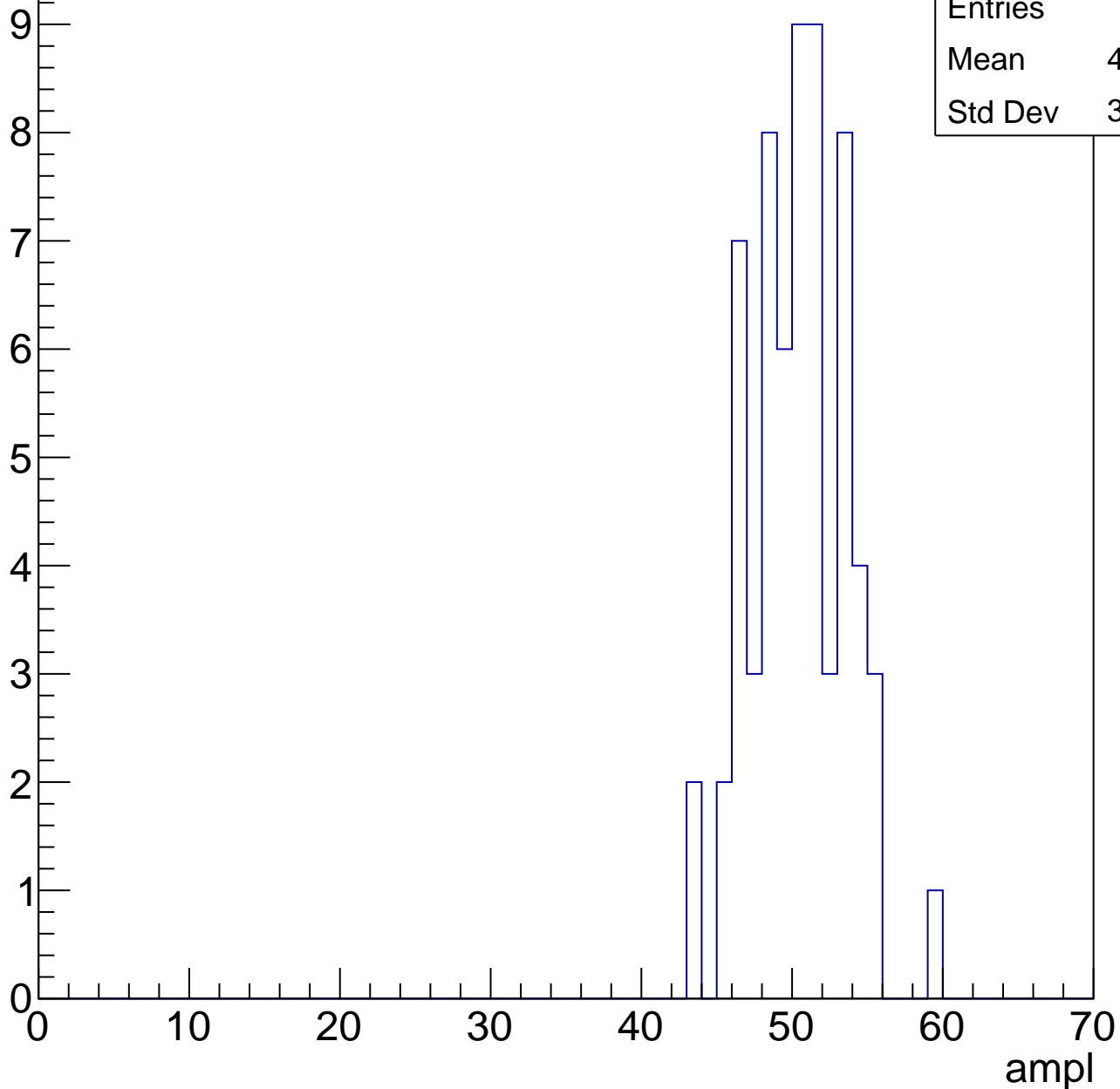
Gaus Width: 3.2177



B1L103S, U12-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	55.13
Std Dev	7.78

Entry

10

8

6

4

2

0

0

10

20

30

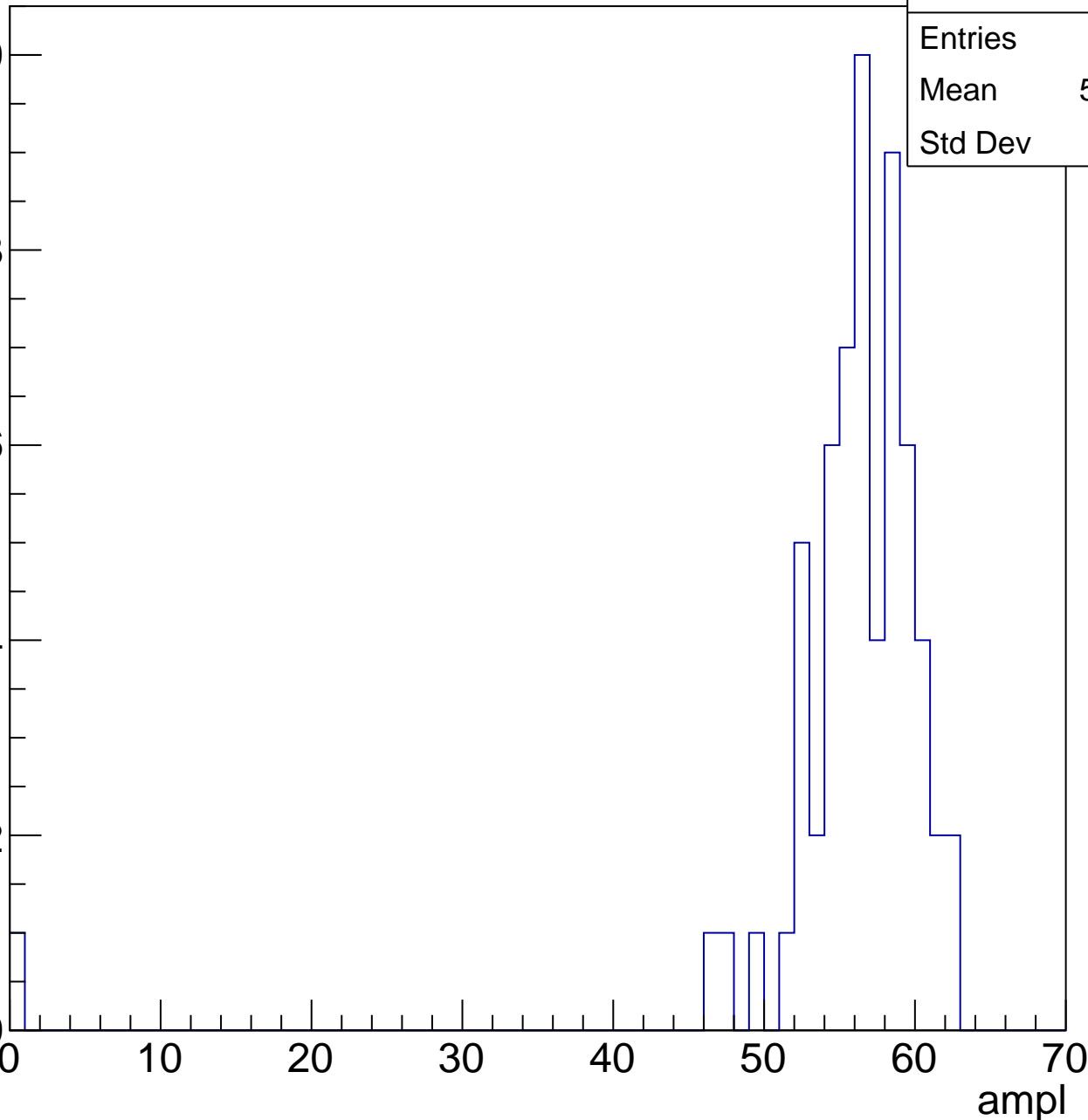
40

50

60

70

ampl

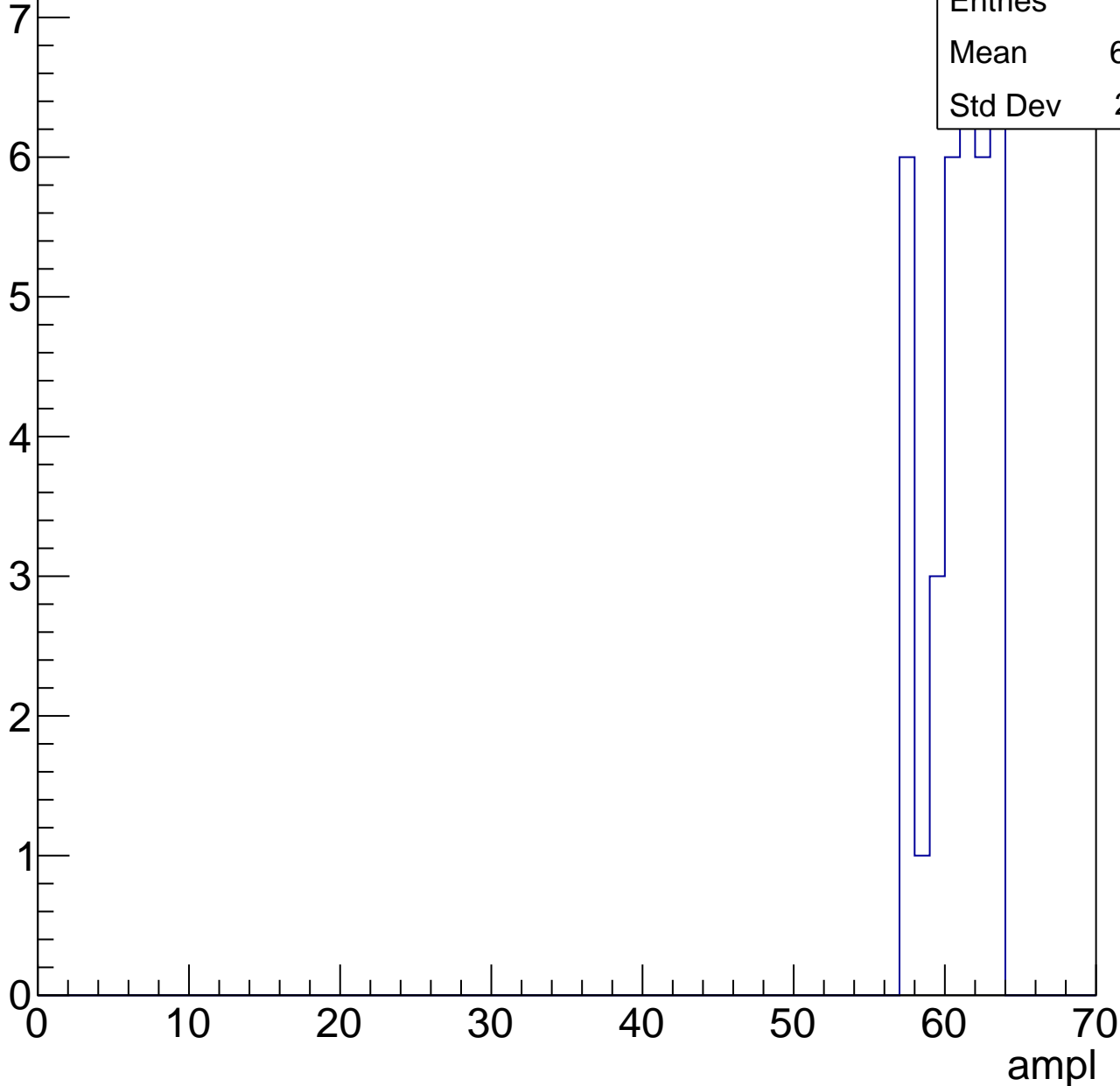


B1L103S, U12-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	60.47
Std Dev	2.021



B1L103S, U12-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch44, adc0

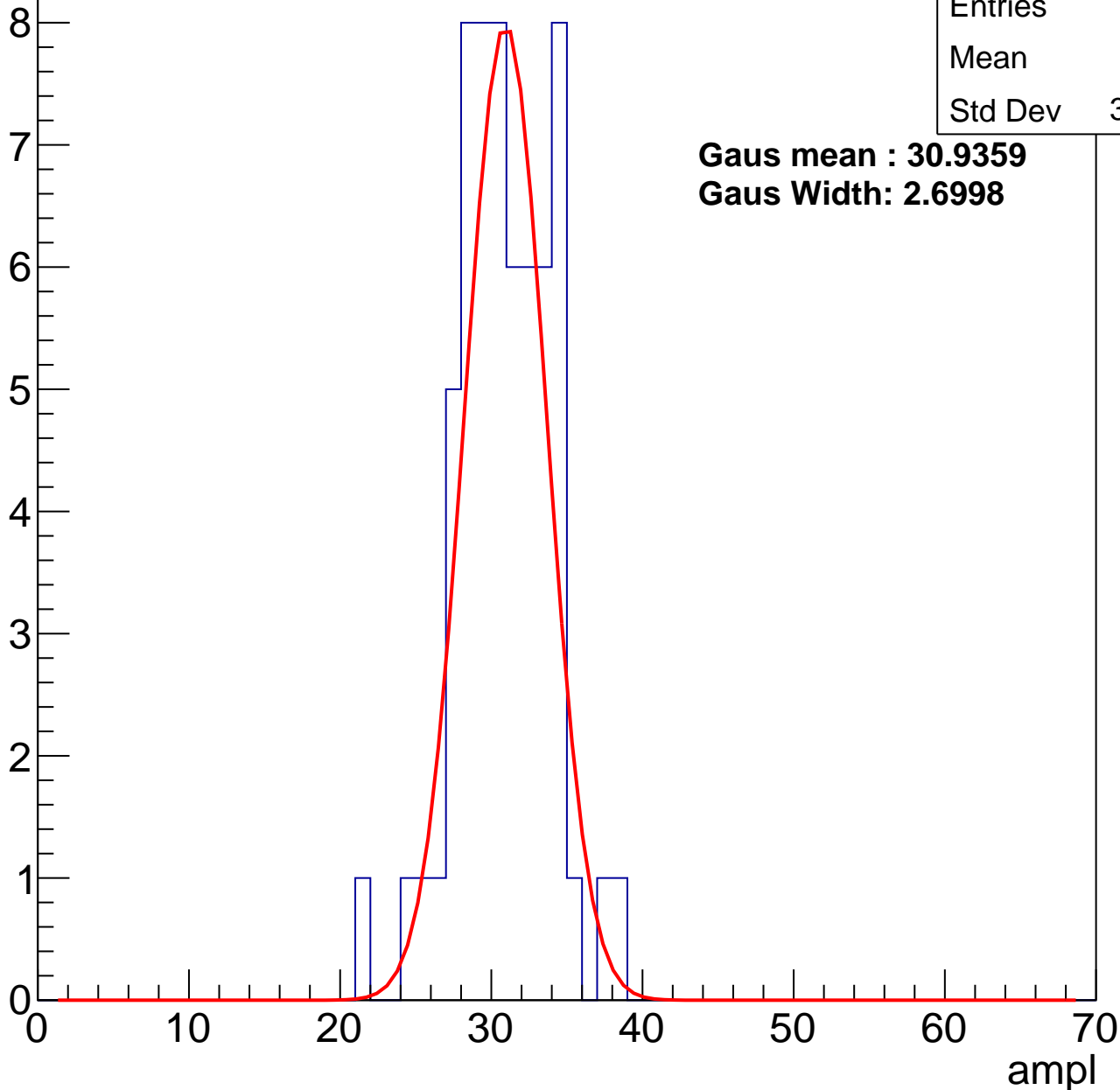
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	30.4
Std Dev	3.066

Gaus mean : 30.9359

Gaus Width: 2.6998



B1L103S, U12-ch44, adc1

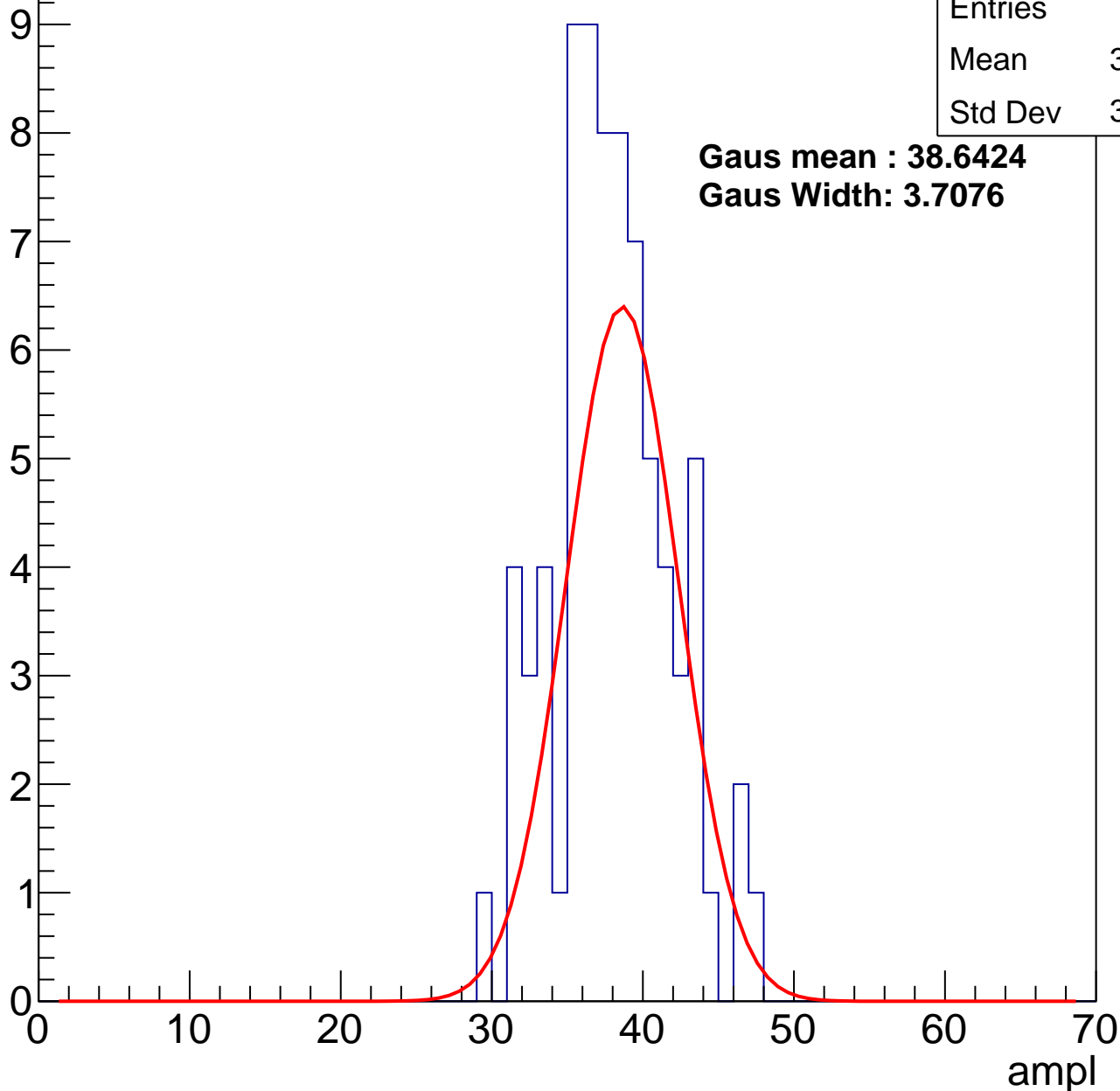
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.53
Std Dev	3.813

Gaus mean : 38.6424

Gaus Width: 3.7076



B1L103S, U12-ch44, adc2

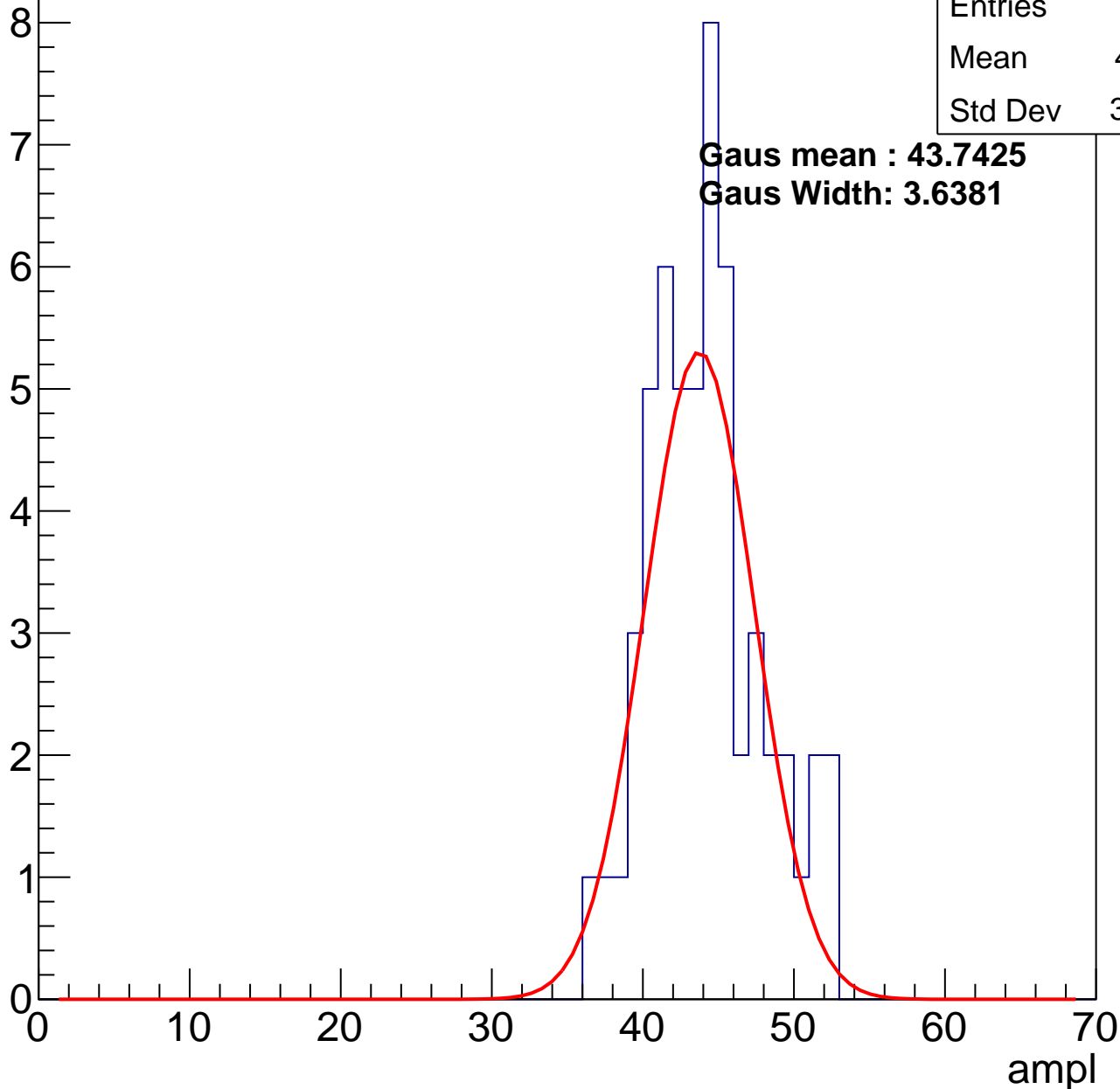
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.71
Std Dev	3.725

Gaus mean : 43.7425

Gaus Width: 3.6381

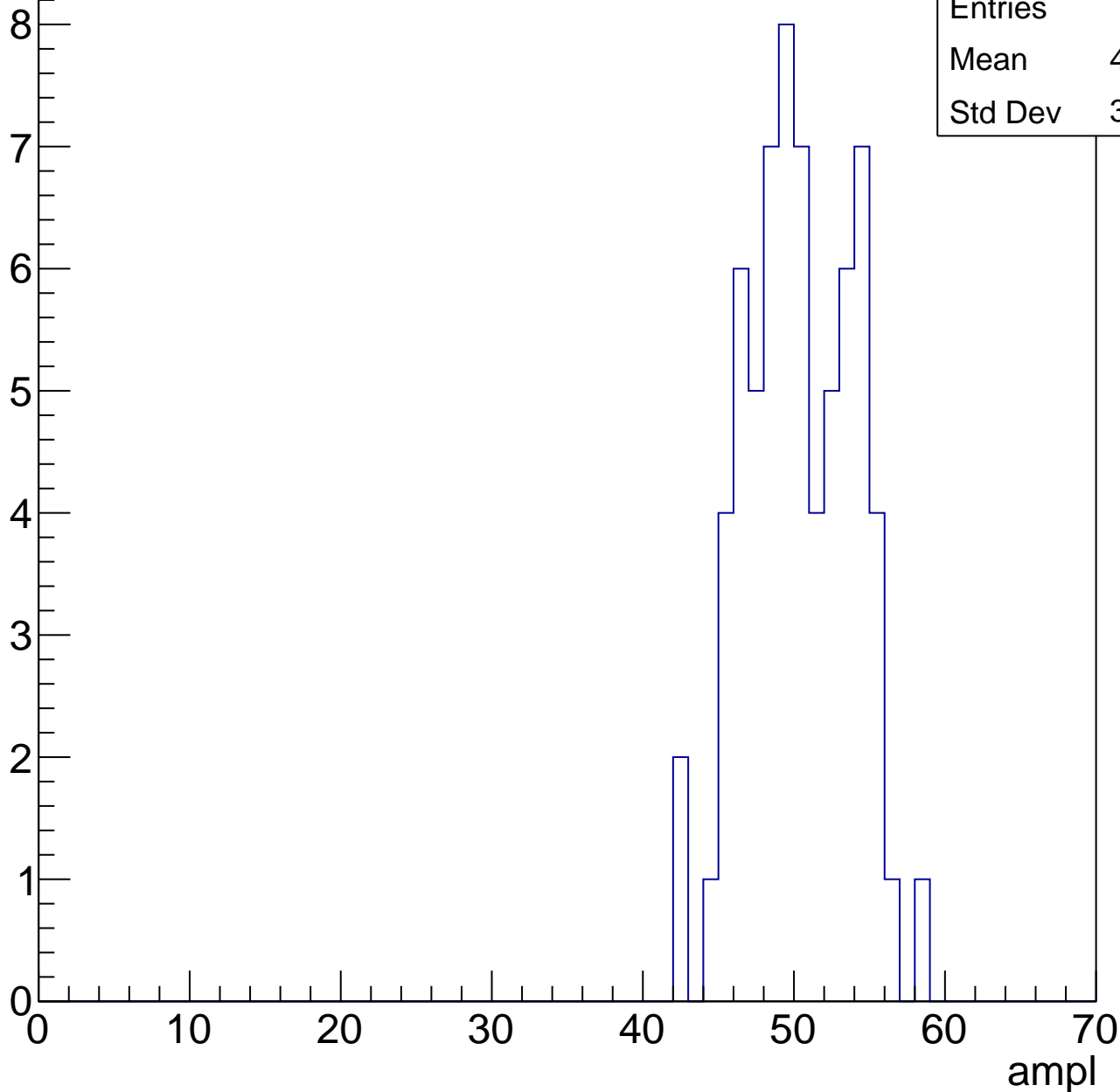


B1L103S, U12-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.87
Std Dev	3.493

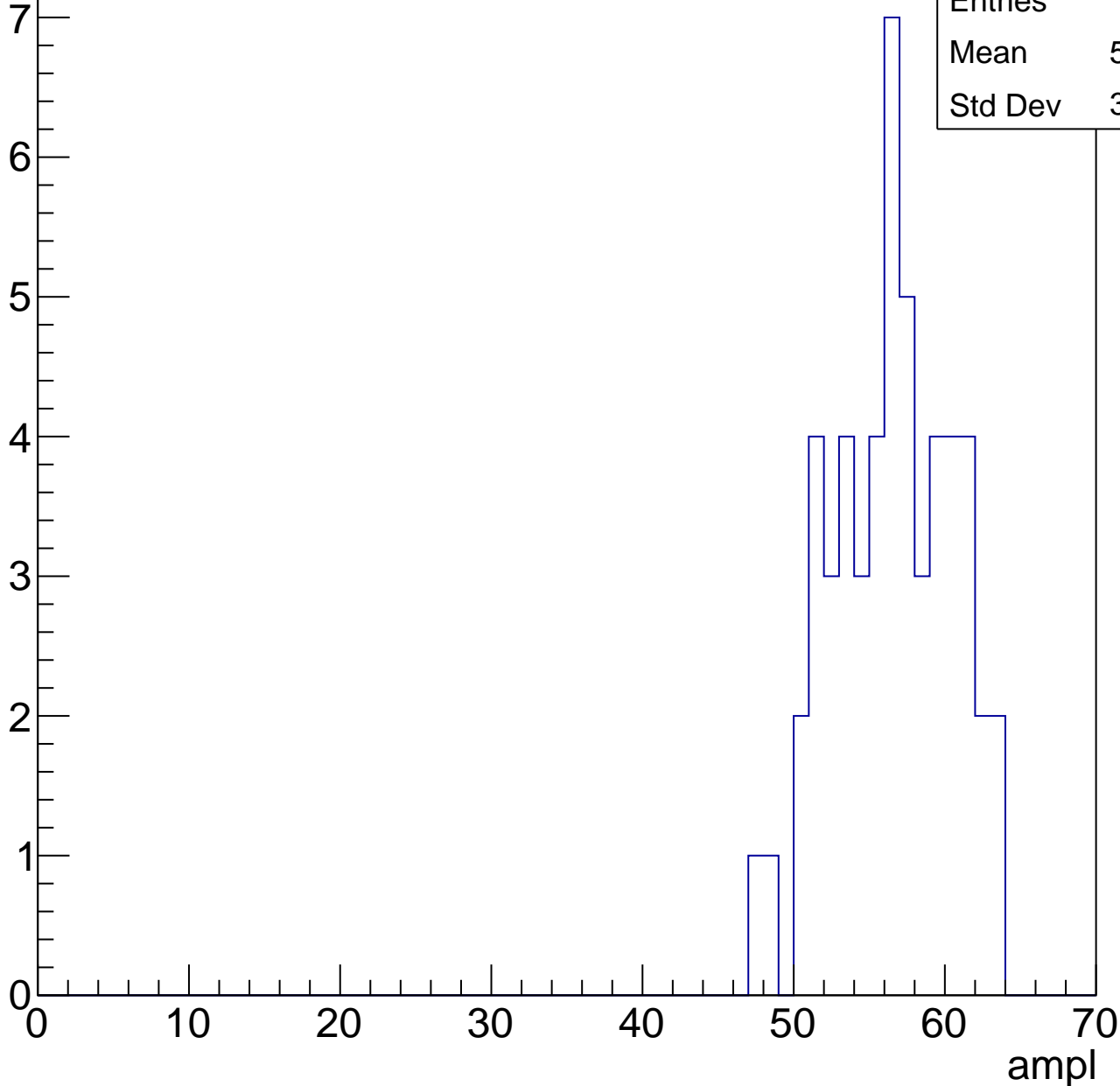


B1L103S, U12-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	56.04
Std Dev	3.895

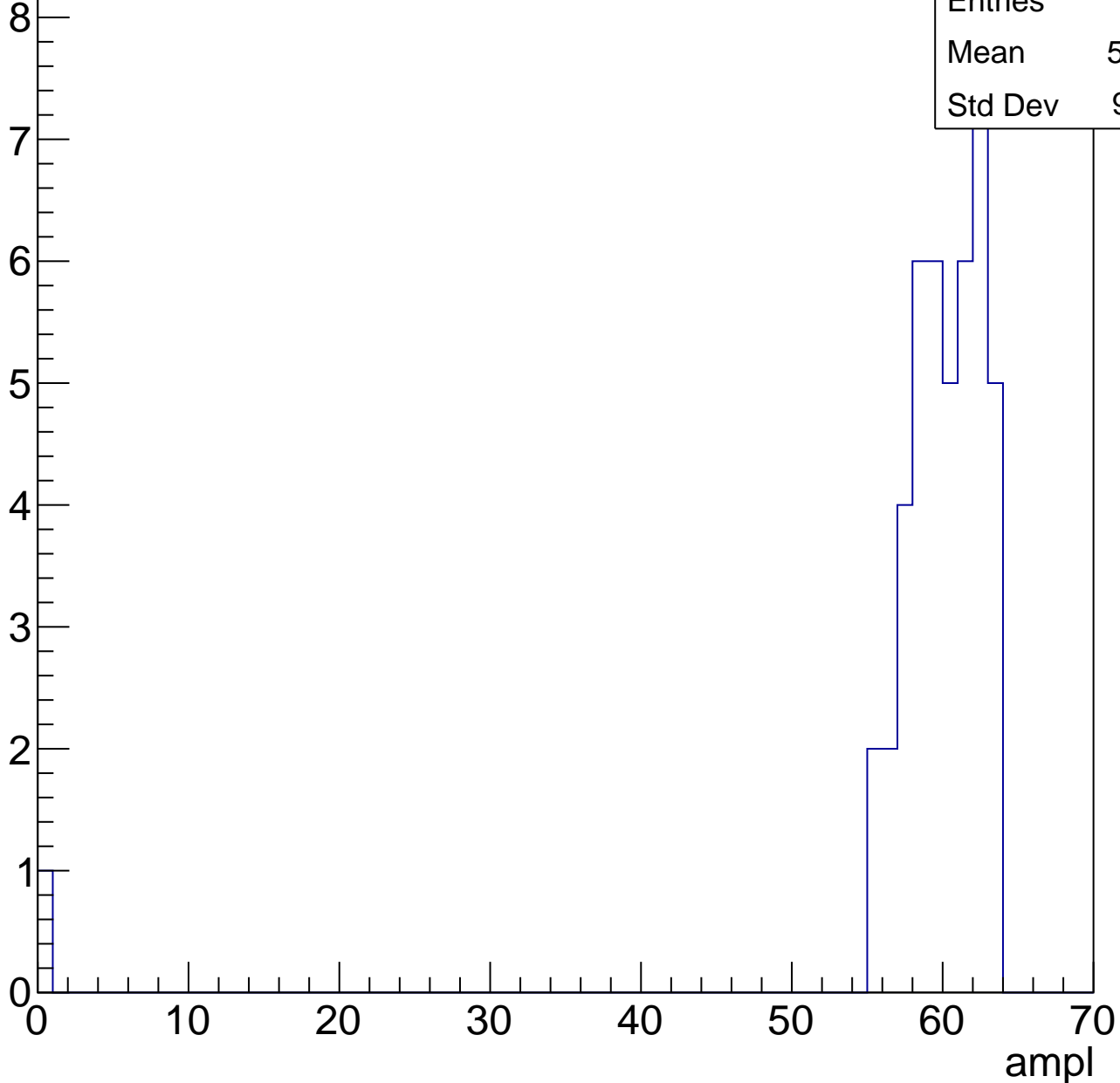


B1L103S, U12-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

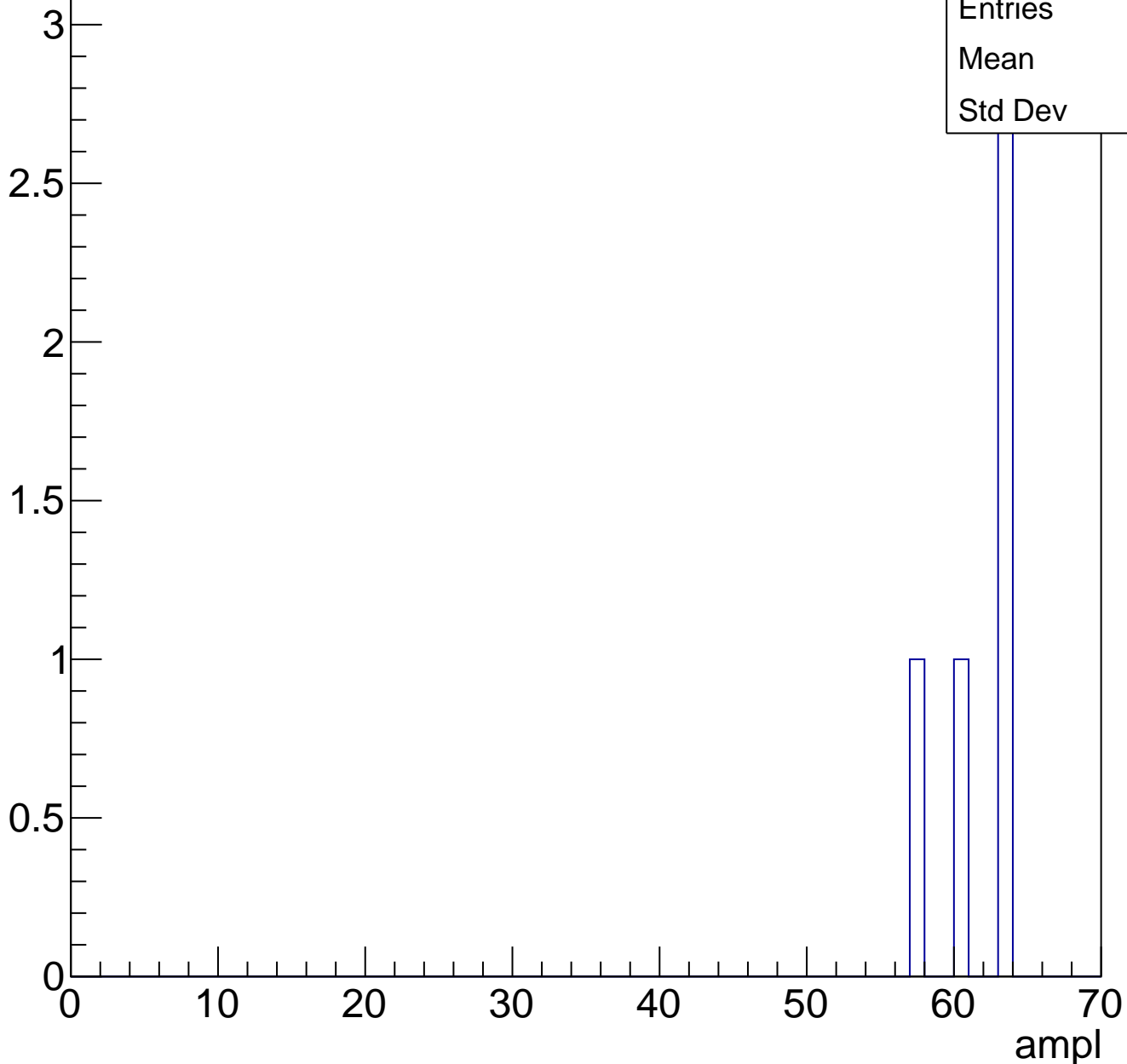
Entries	45
Mean	58.42
Std Dev	9.091



B1L103S, U12-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch45, adc0

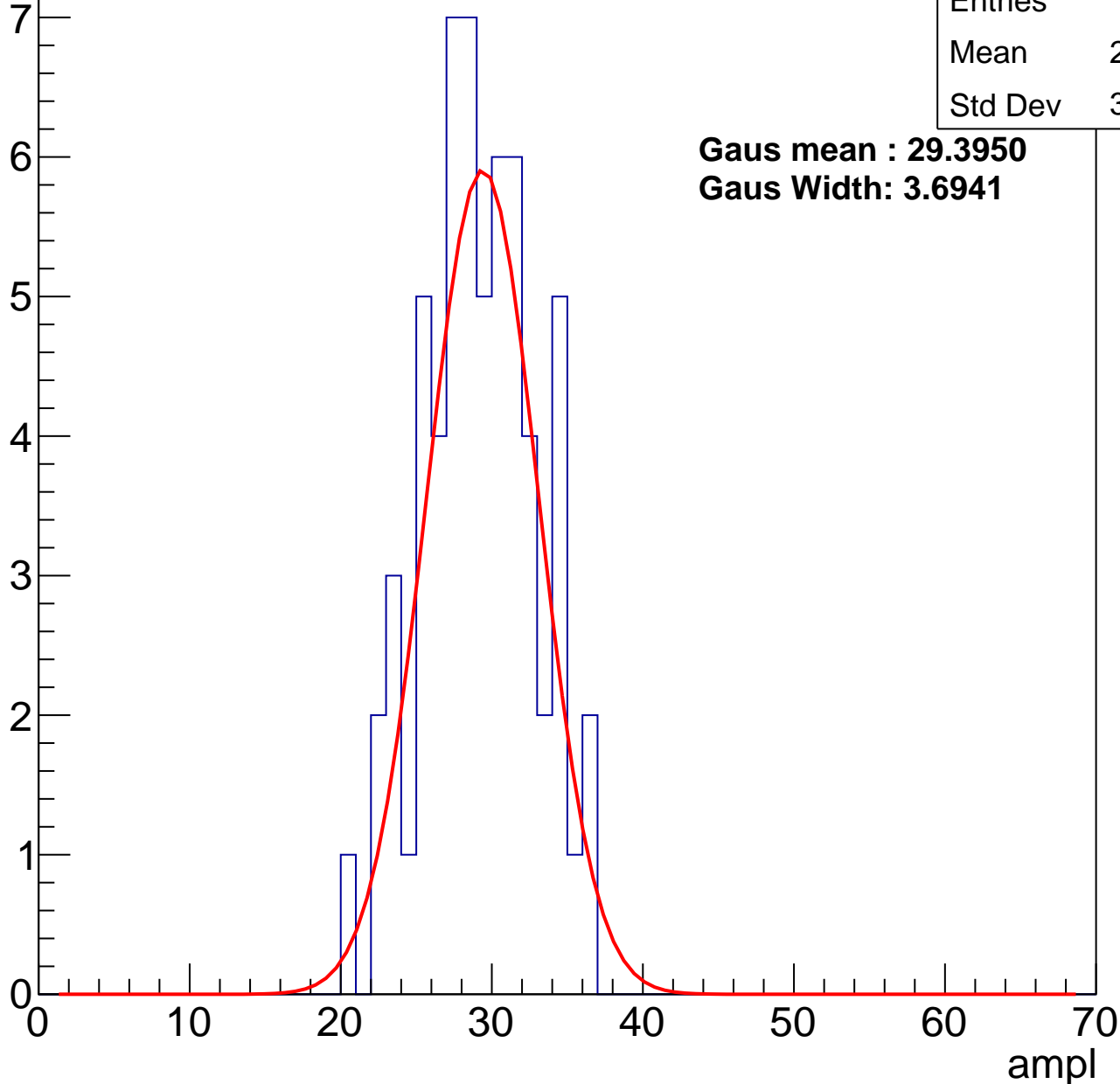
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.74
Std Dev	3.657

Gaus mean : 29.3950

Gaus Width: 3.6941



B1L103S, U12-ch45, adc1

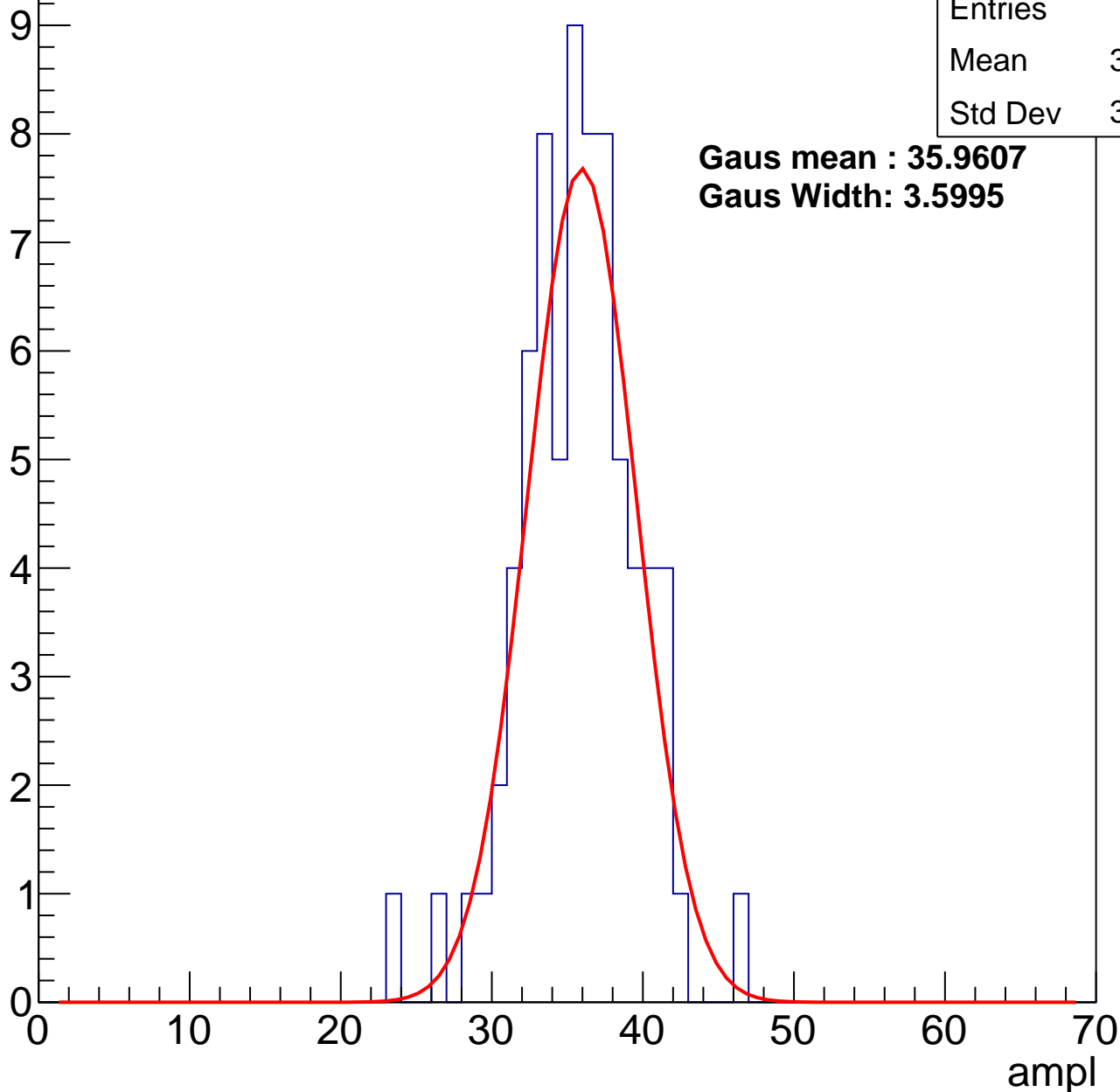
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.25
Std Dev	3.835

Gaus mean : 35.9607

Gaus Width: 3.5995



B1L103S, U12-ch45, adc2

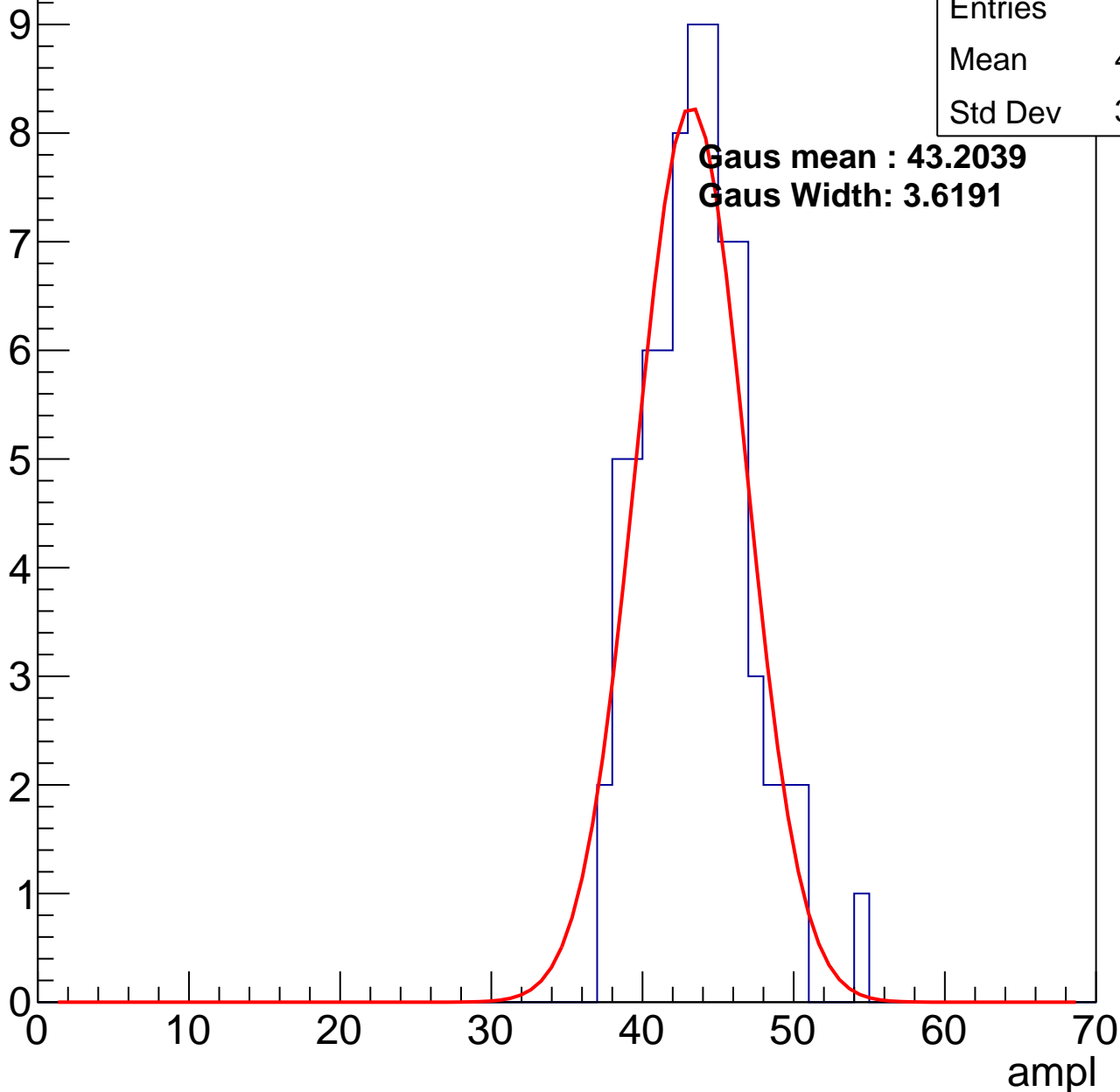
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	43.11
Std Dev	3.391

Gaus mean : 43.2039

Gaus Width: 3.6191

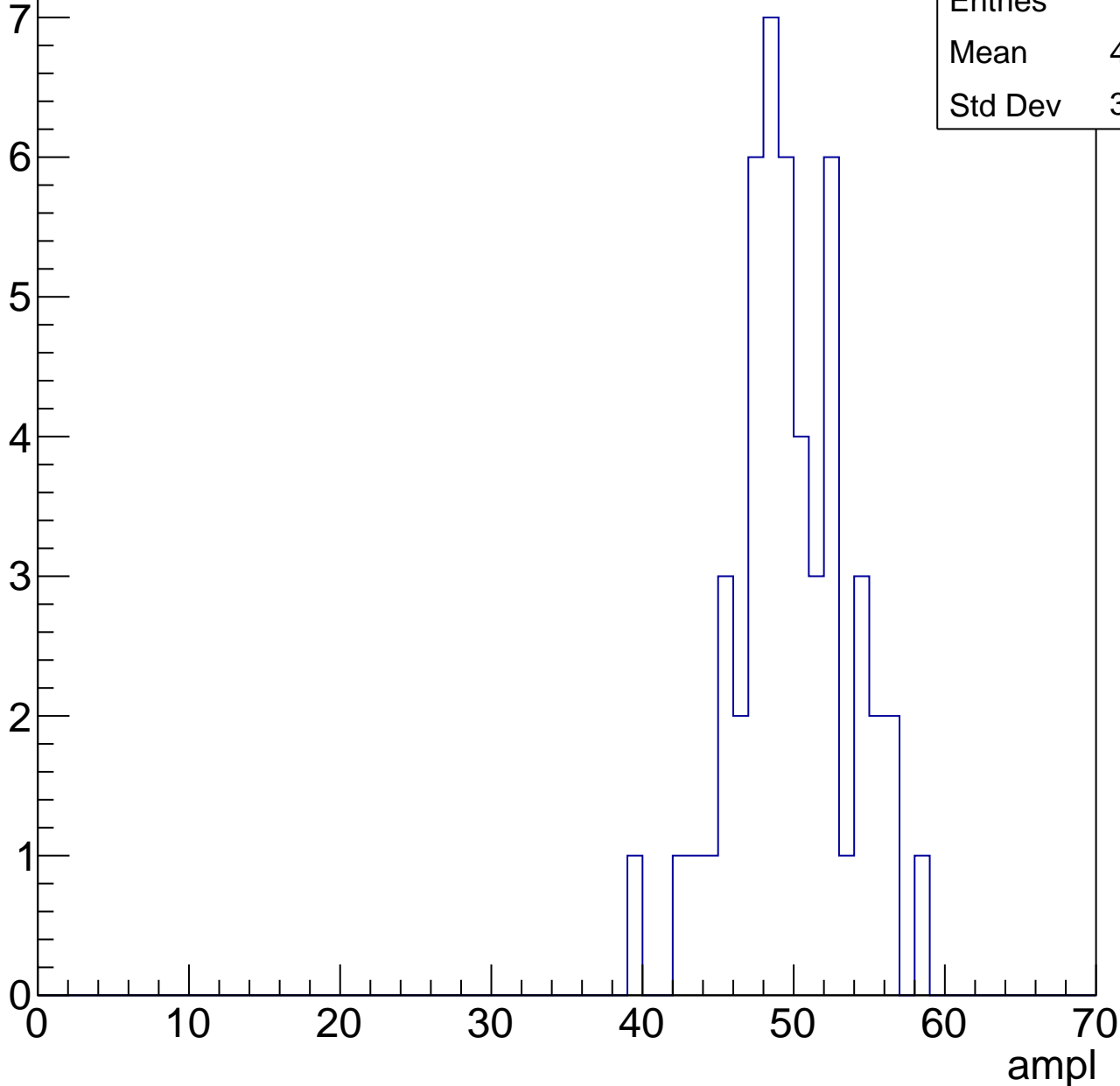


B1L103S, U12-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	49.34
Std Dev	3.777

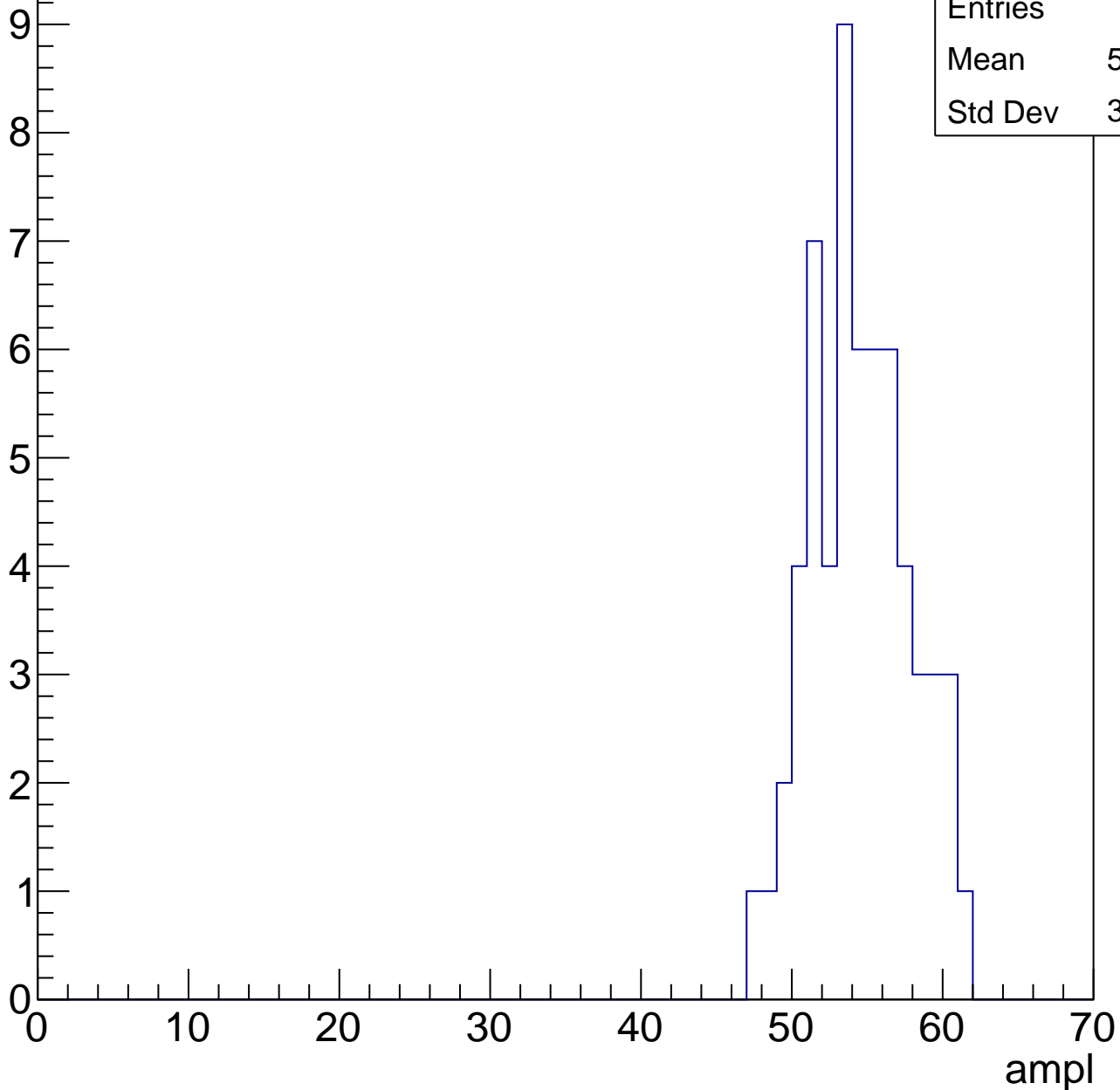


B1L103S, U12-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.08
Std Dev	3.247

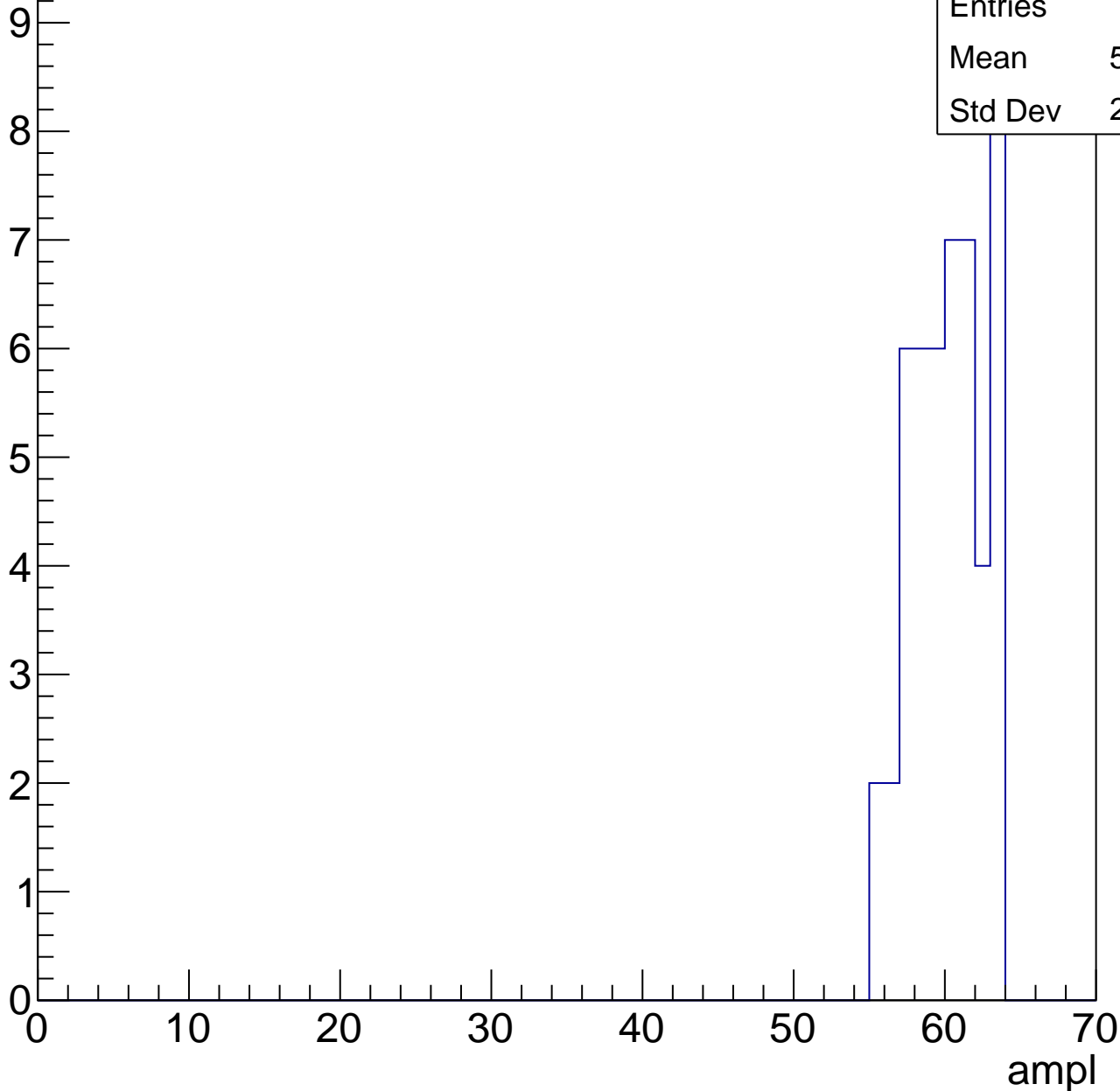


B1L103S, U12-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	59.76
Std Dev	2.335

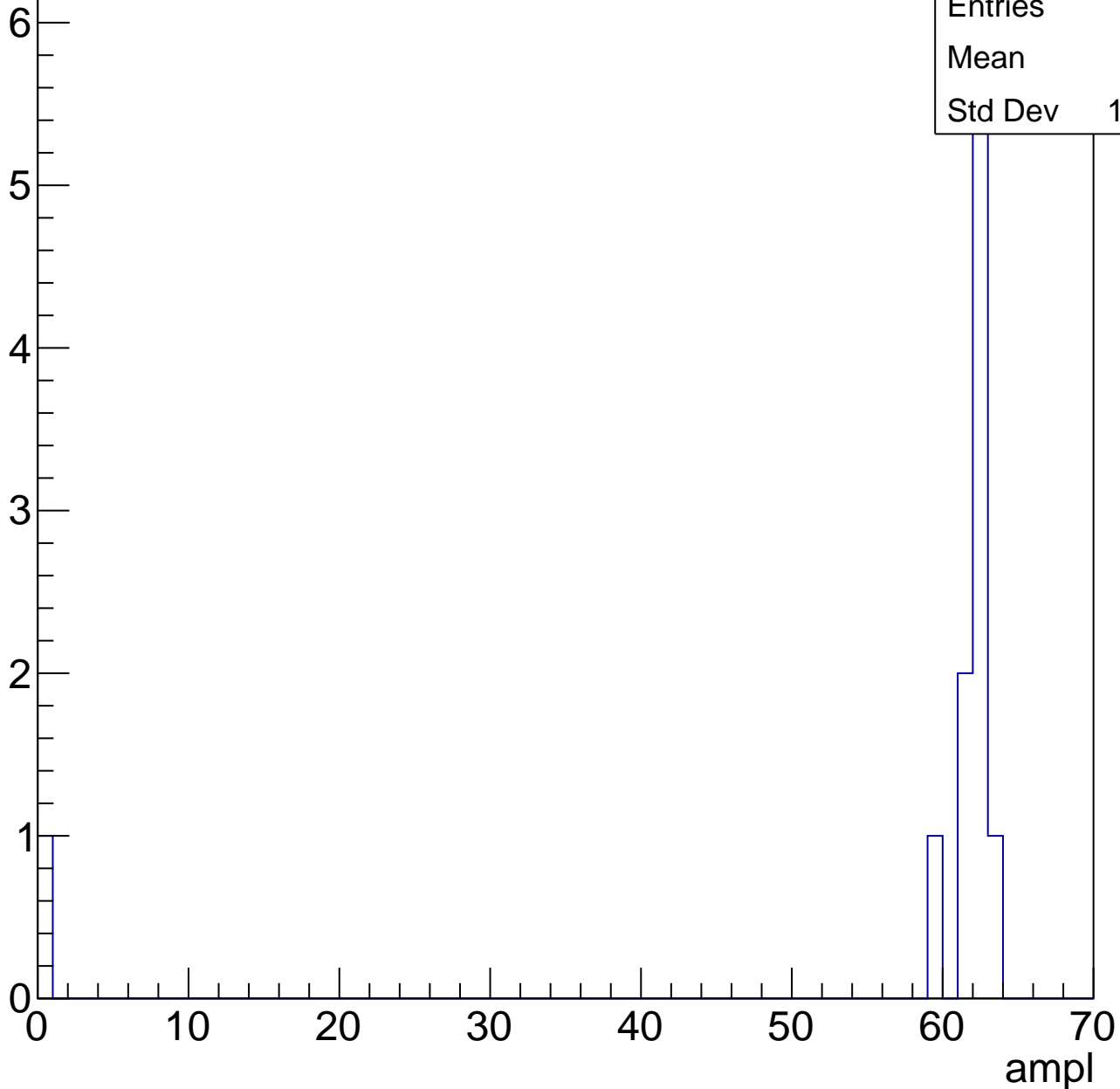


B1L103S, U12-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

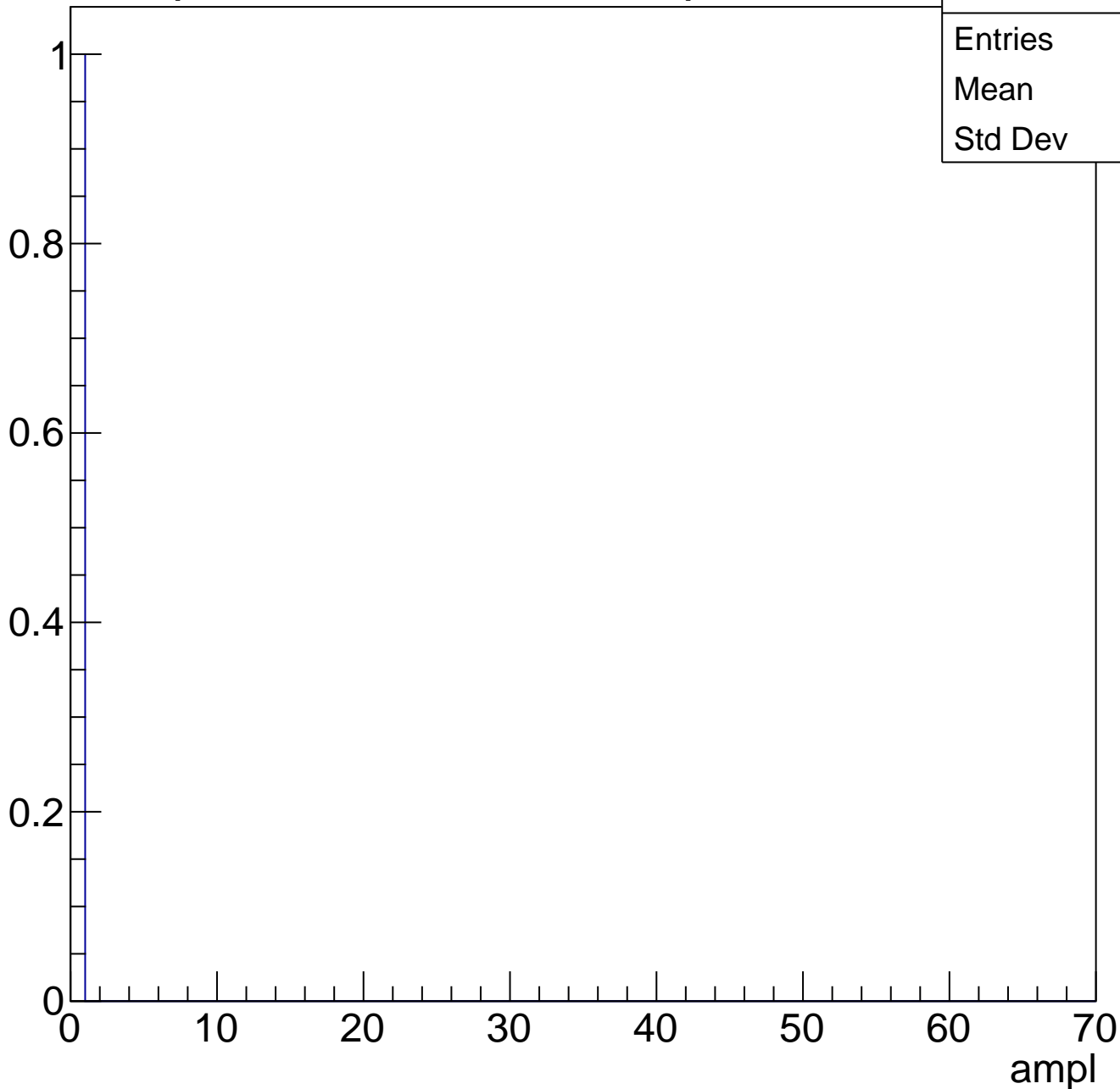
Entries	11
Mean	56
Std Dev	17.74



B1L103S, U12-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch46, adc0

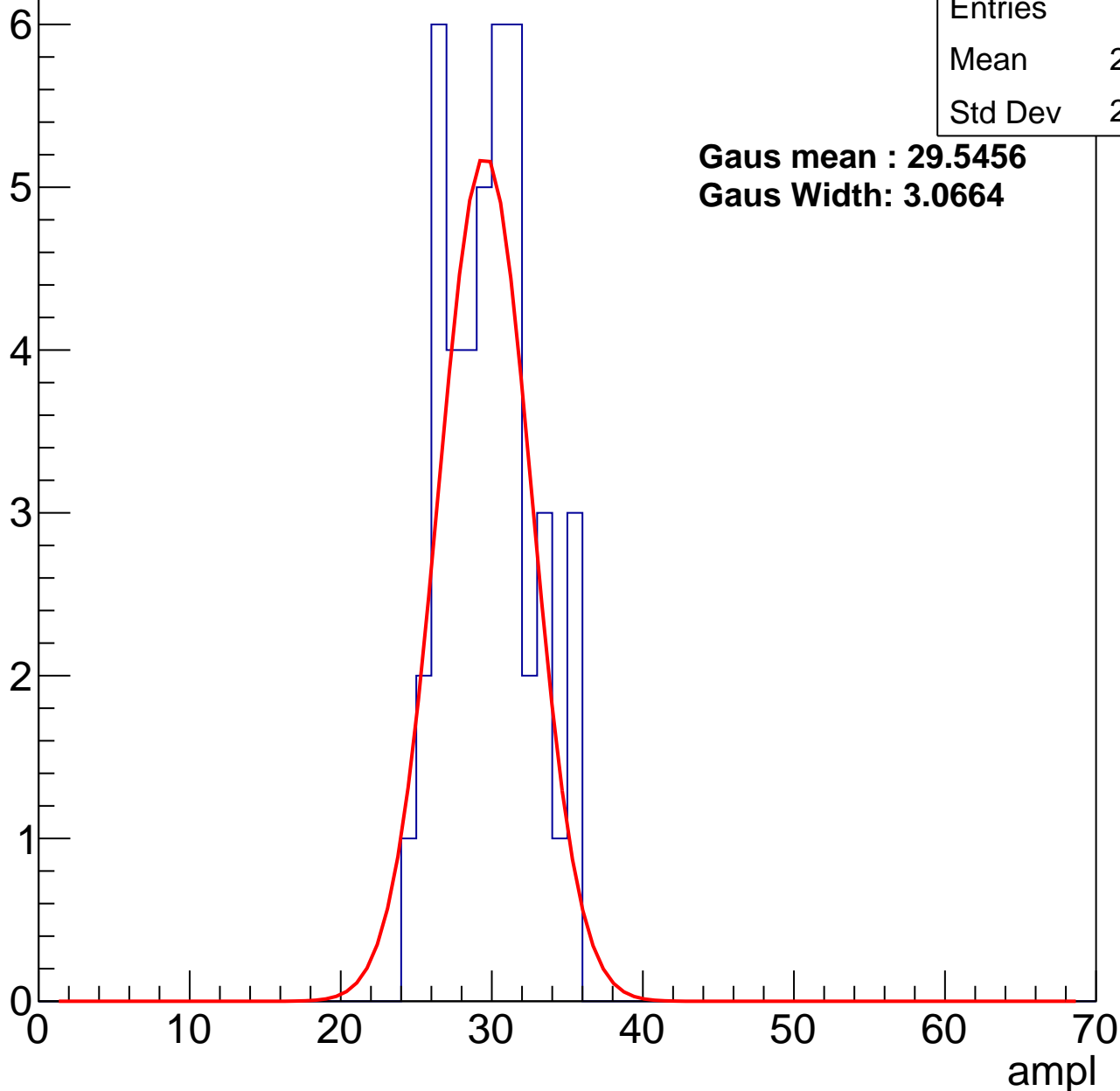
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	29.37
Std Dev	2.869

Gaus mean : 29.5456

Gaus Width: 3.0664



B1L103S, U12-ch46, adc1

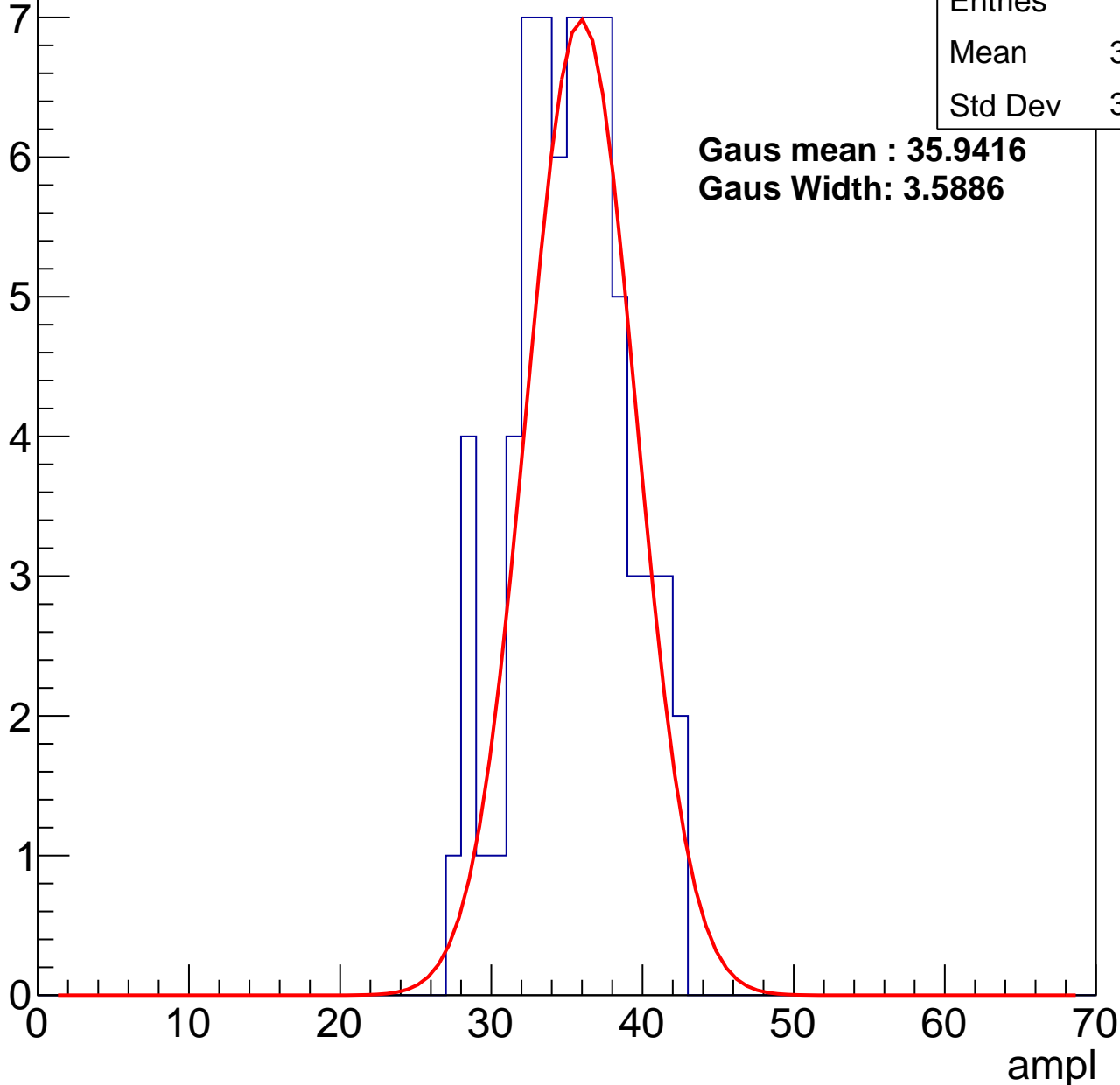
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	34.87
Std Dev	3.617

Gaus mean : 35.9416

Gaus Width: 3.5886



B1L103S, U12-ch46, adc2

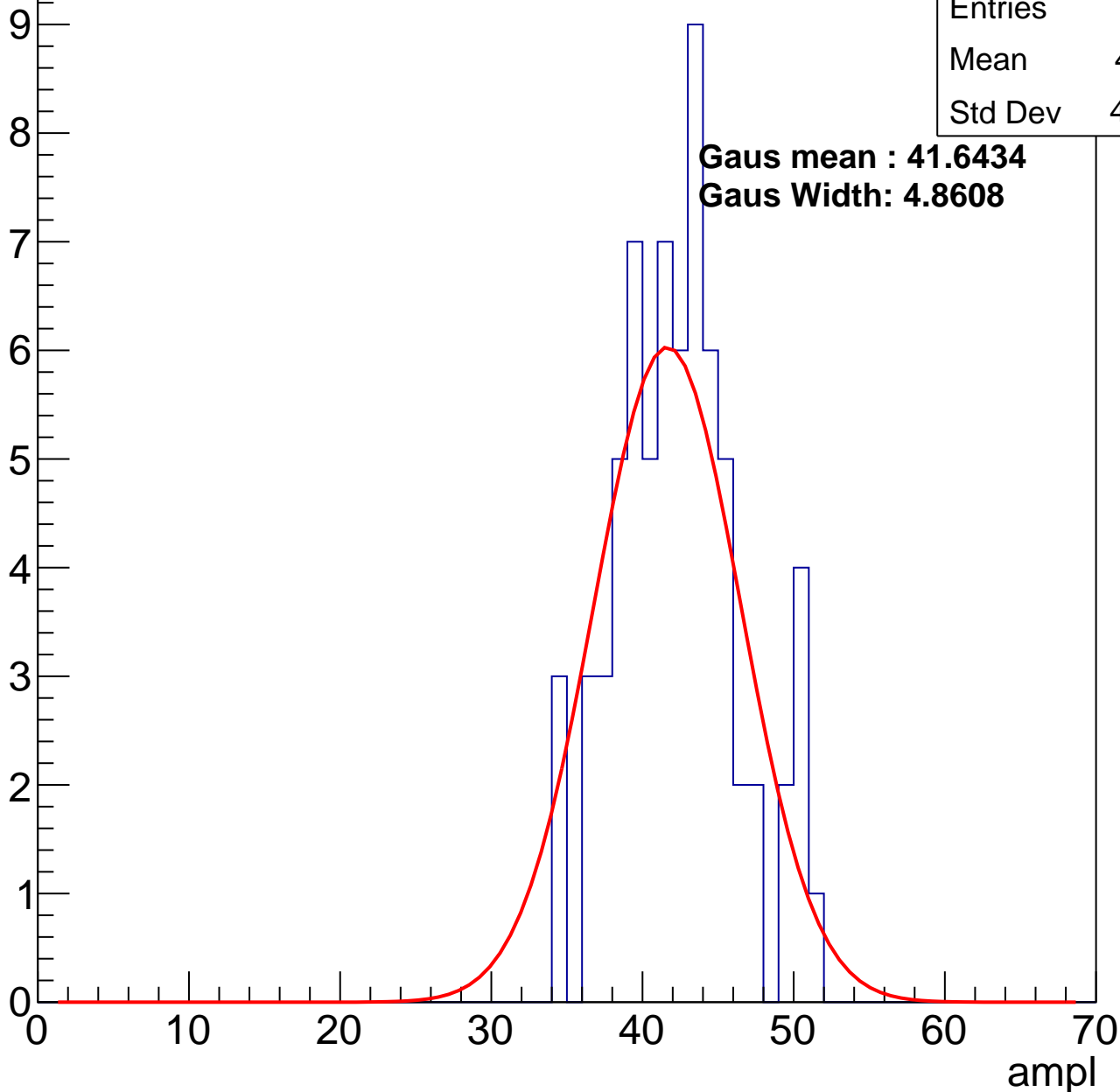
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	41.91
Std Dev	4.035

Gaus mean : 41.6434

Gaus Width: 4.8608

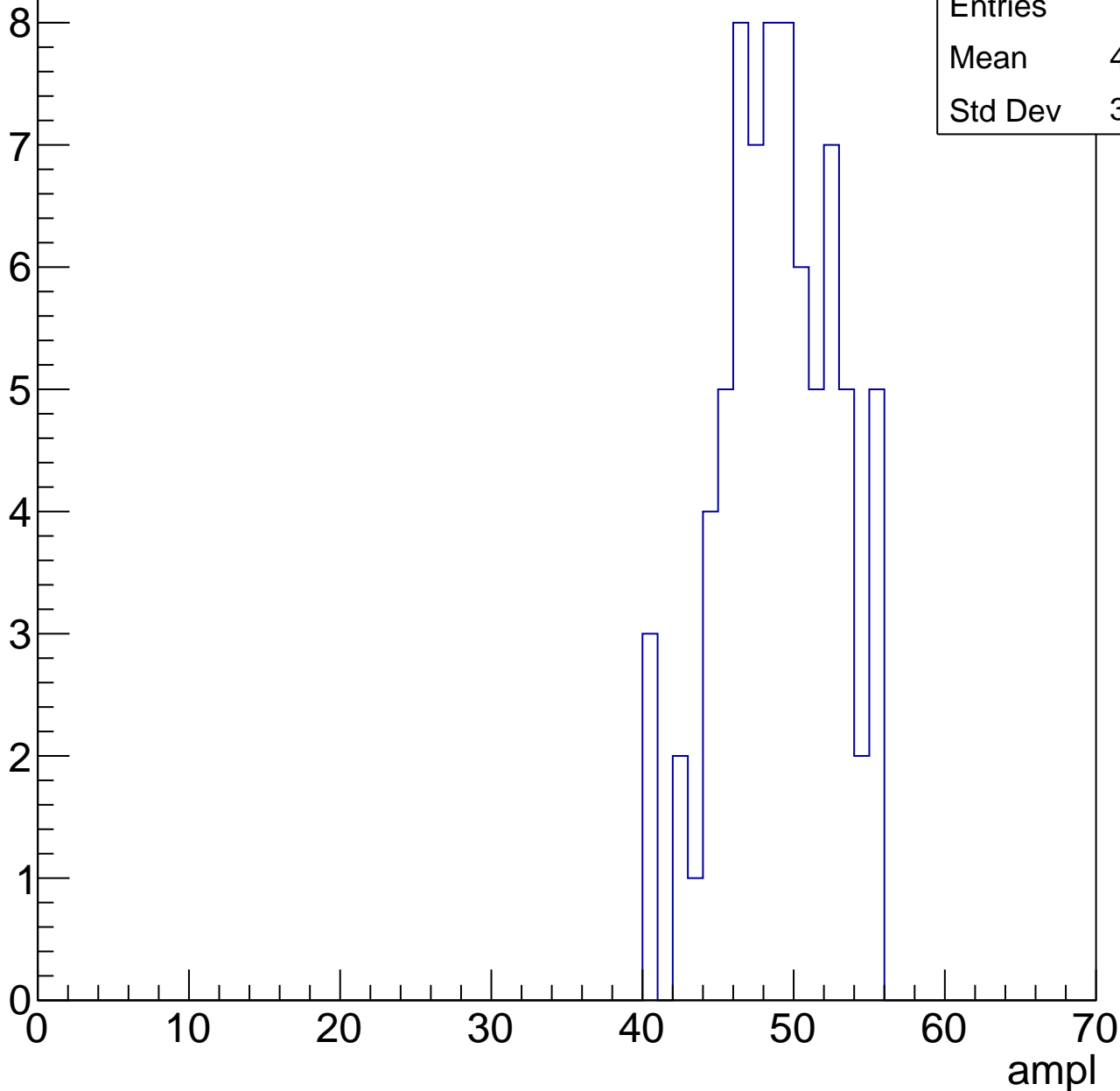


B1L103S, U12-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	48.53
Std Dev	3.712

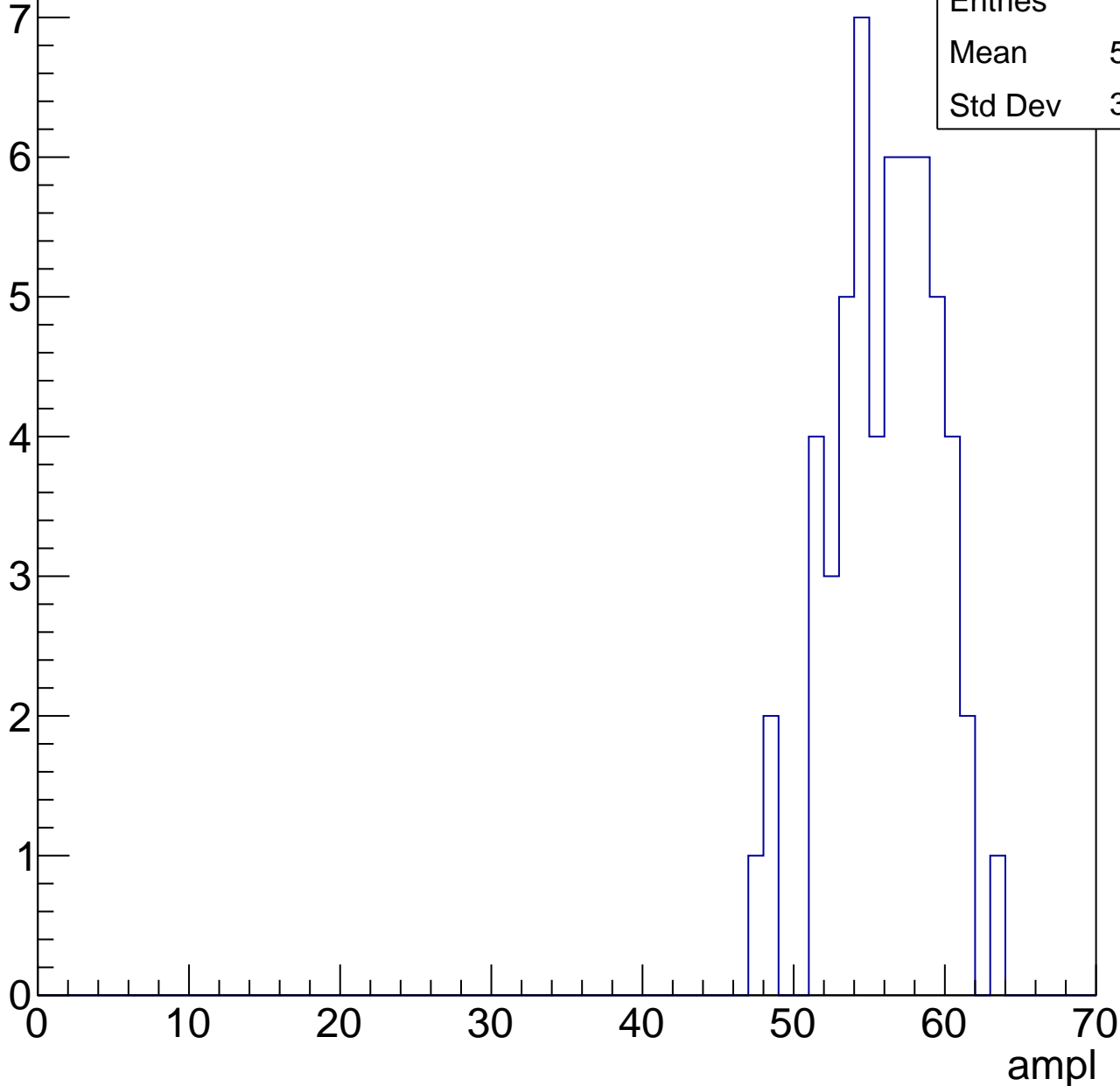


B1L103S, U12-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

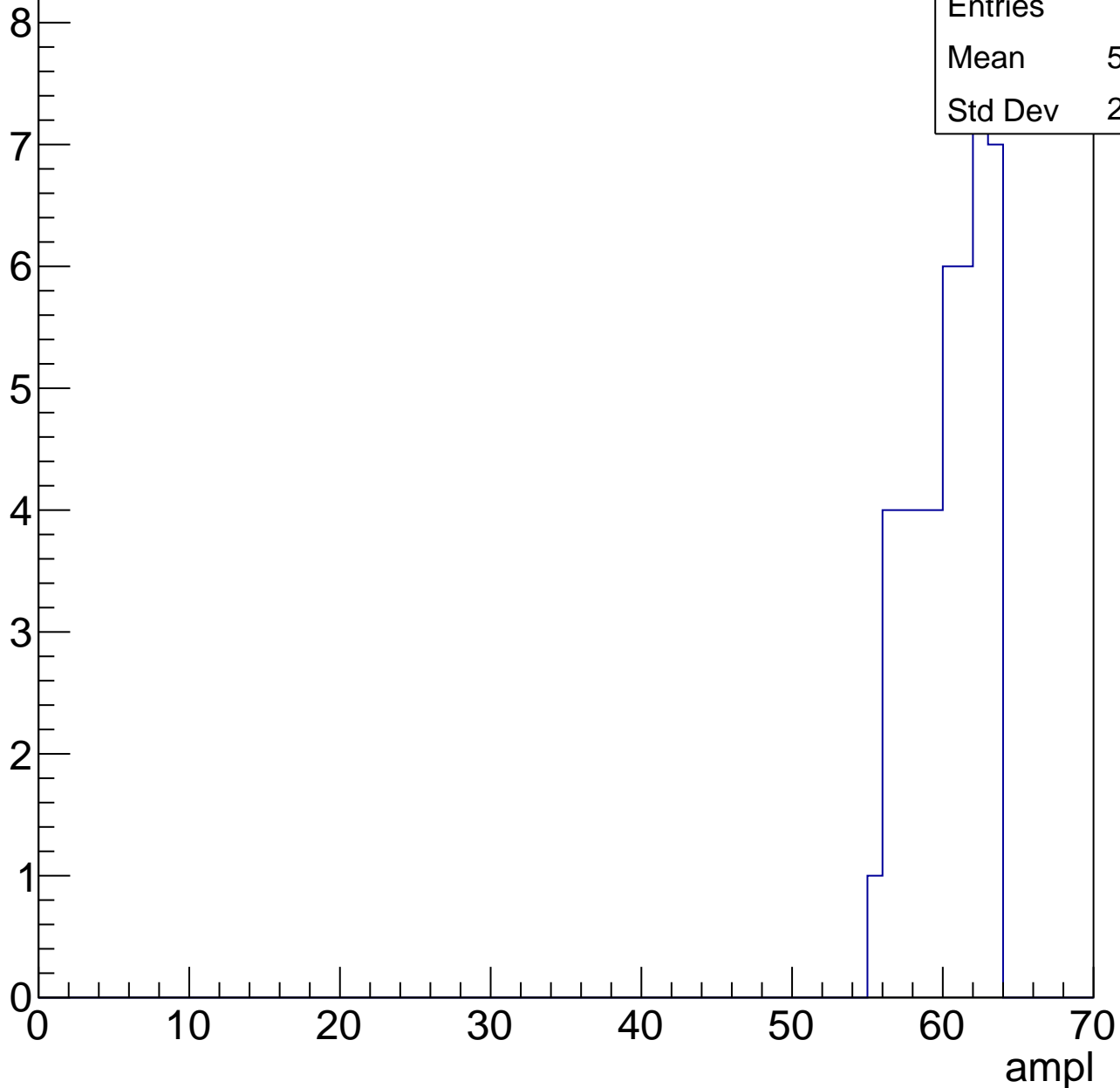
Entries	56
Mean	55.57
Std Dev	3.437



B1L103S, U12-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	44
Mean	59.95
Std Dev	2.364

B1L103S, U12-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

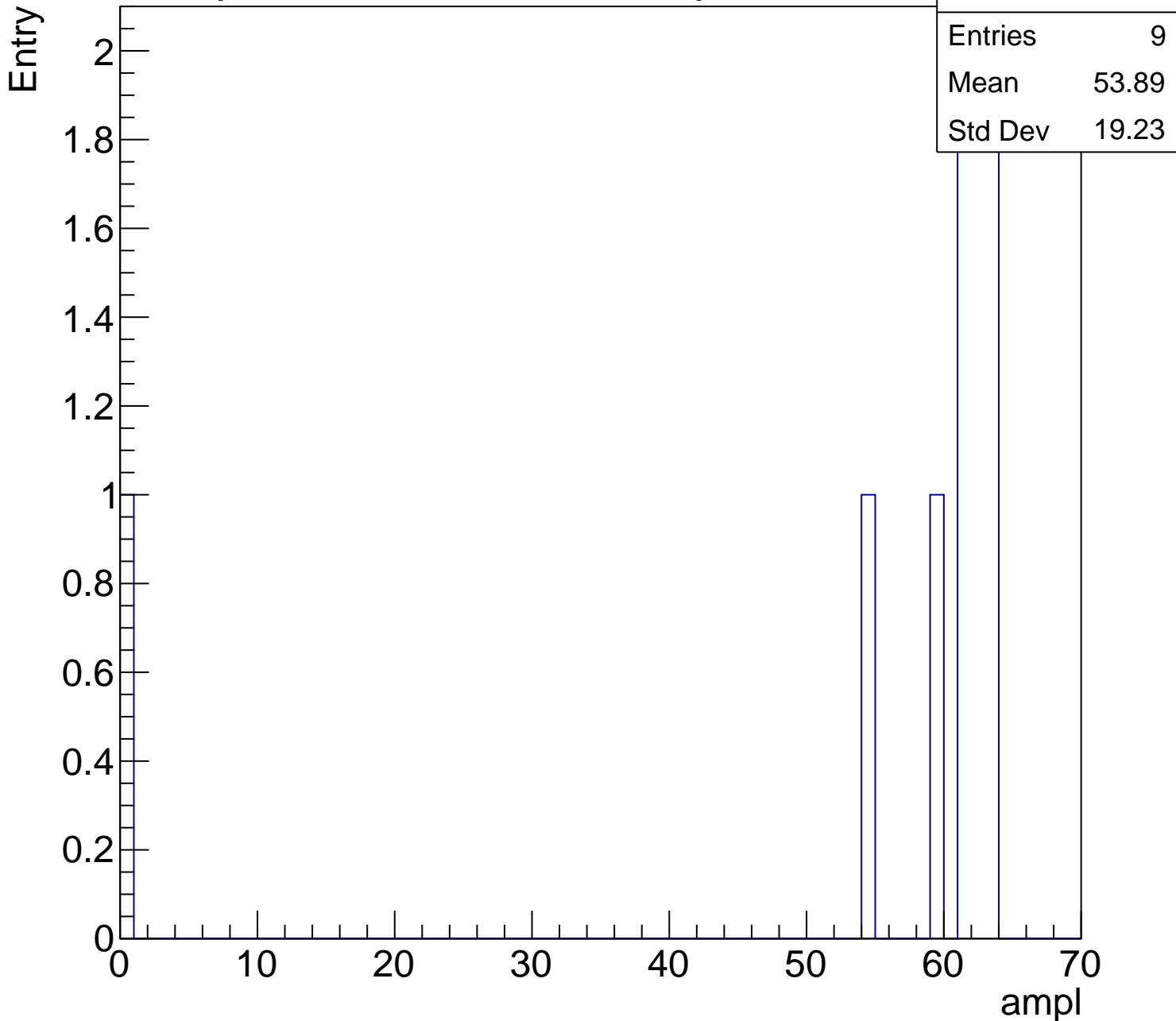
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	53.89
Std Dev	19.23

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch47, adc0

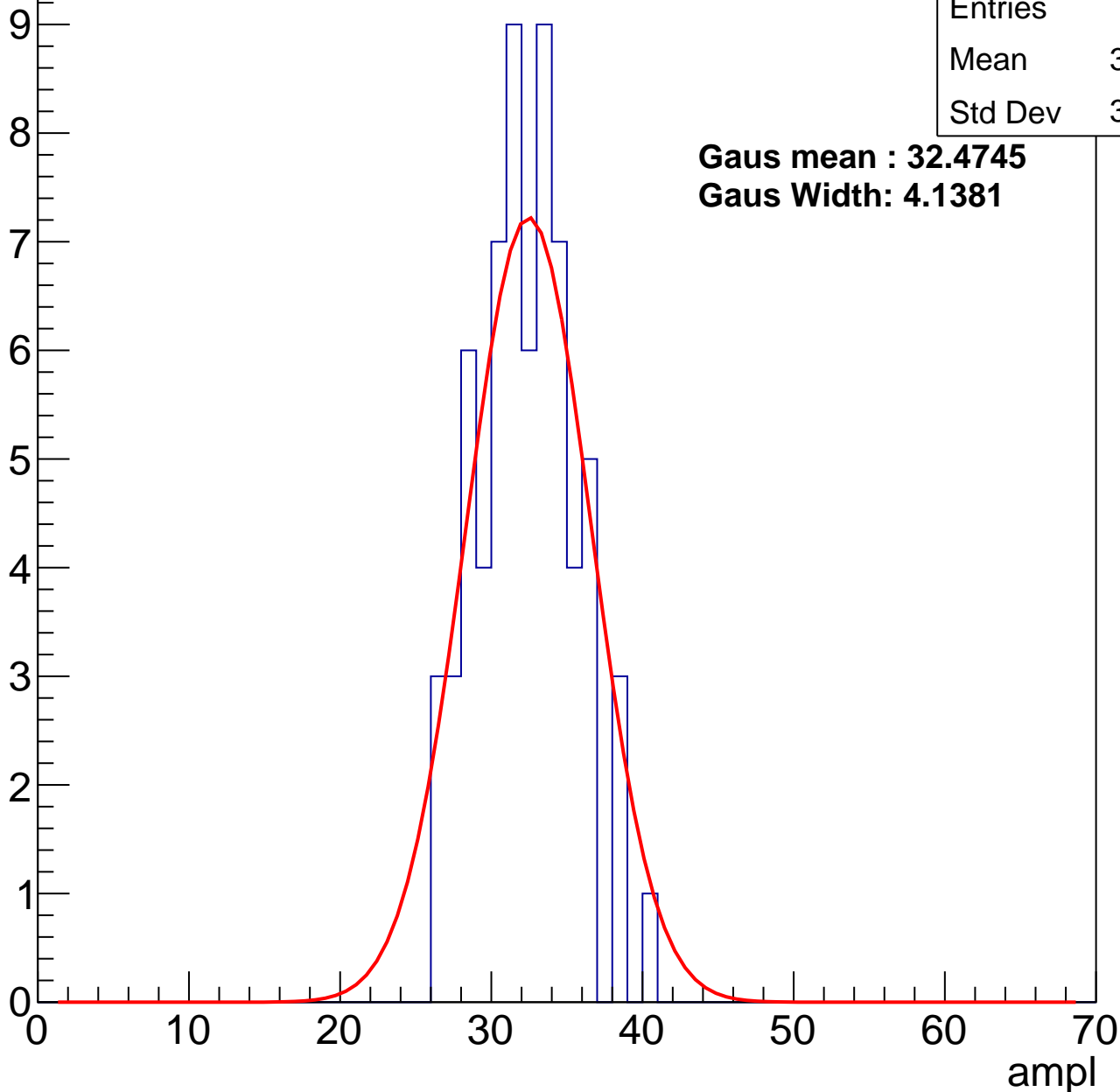
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	31.84
Std Dev	3.175

Gaus mean : 32.4745

Gaus Width: 4.1381



B1L103S, U12-ch47, adc1

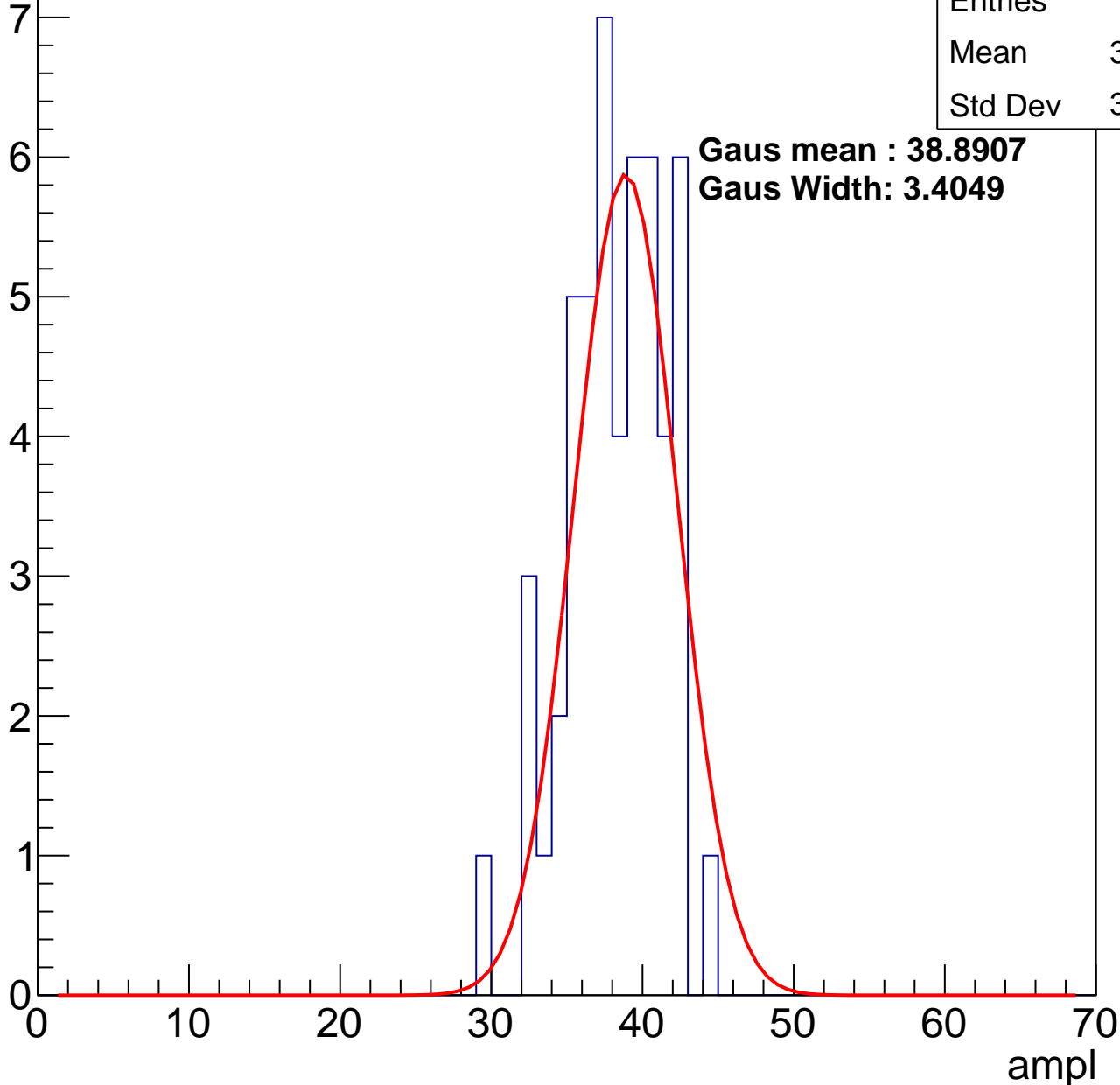
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	37.76
Std Dev	3.172

Gaus mean : 38.8907

Gaus Width: 3.4049



B1L103S, U12-ch47, adc2

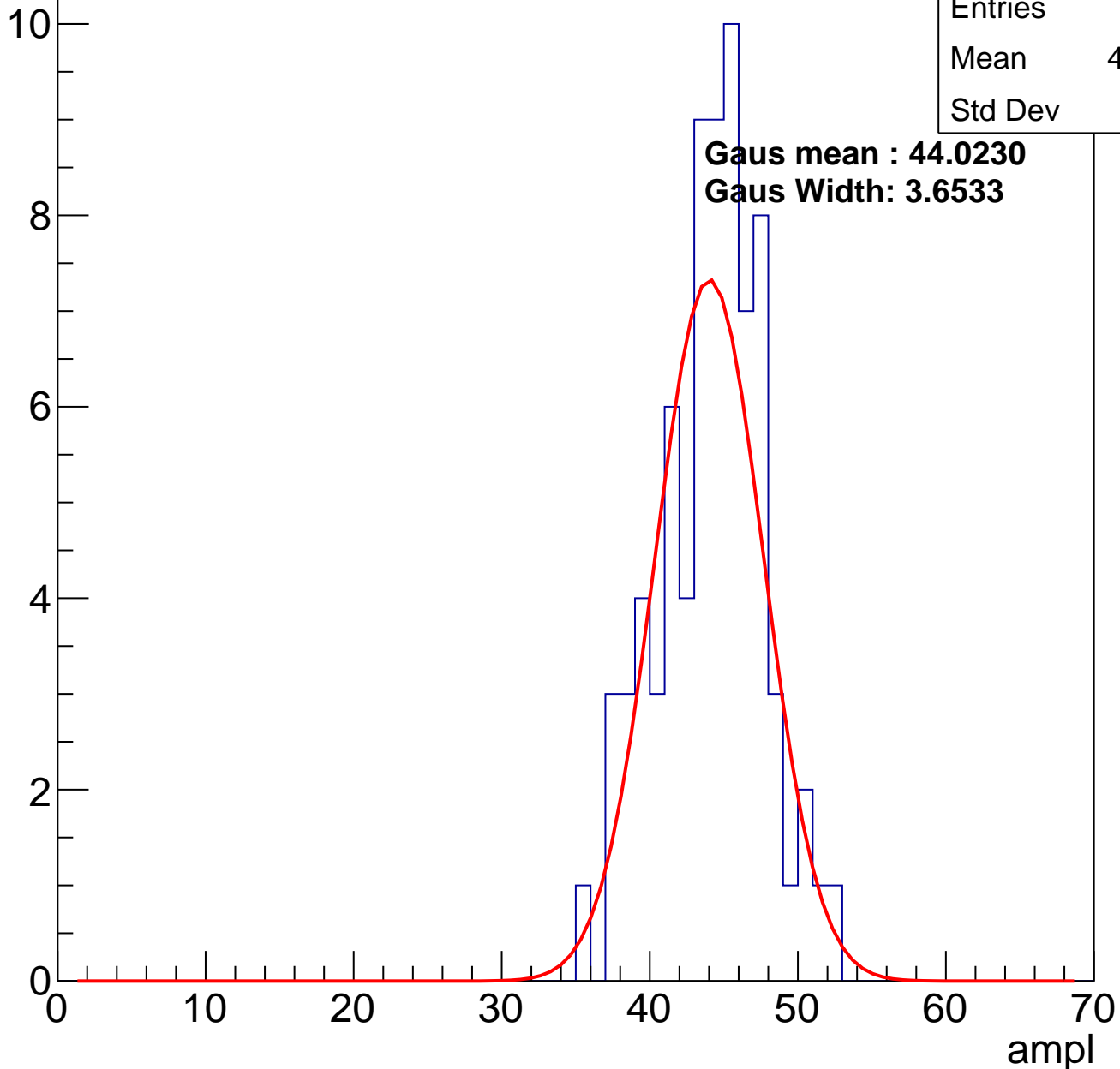
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	43.69
Std Dev	3.51

Gaus mean : 44.0230

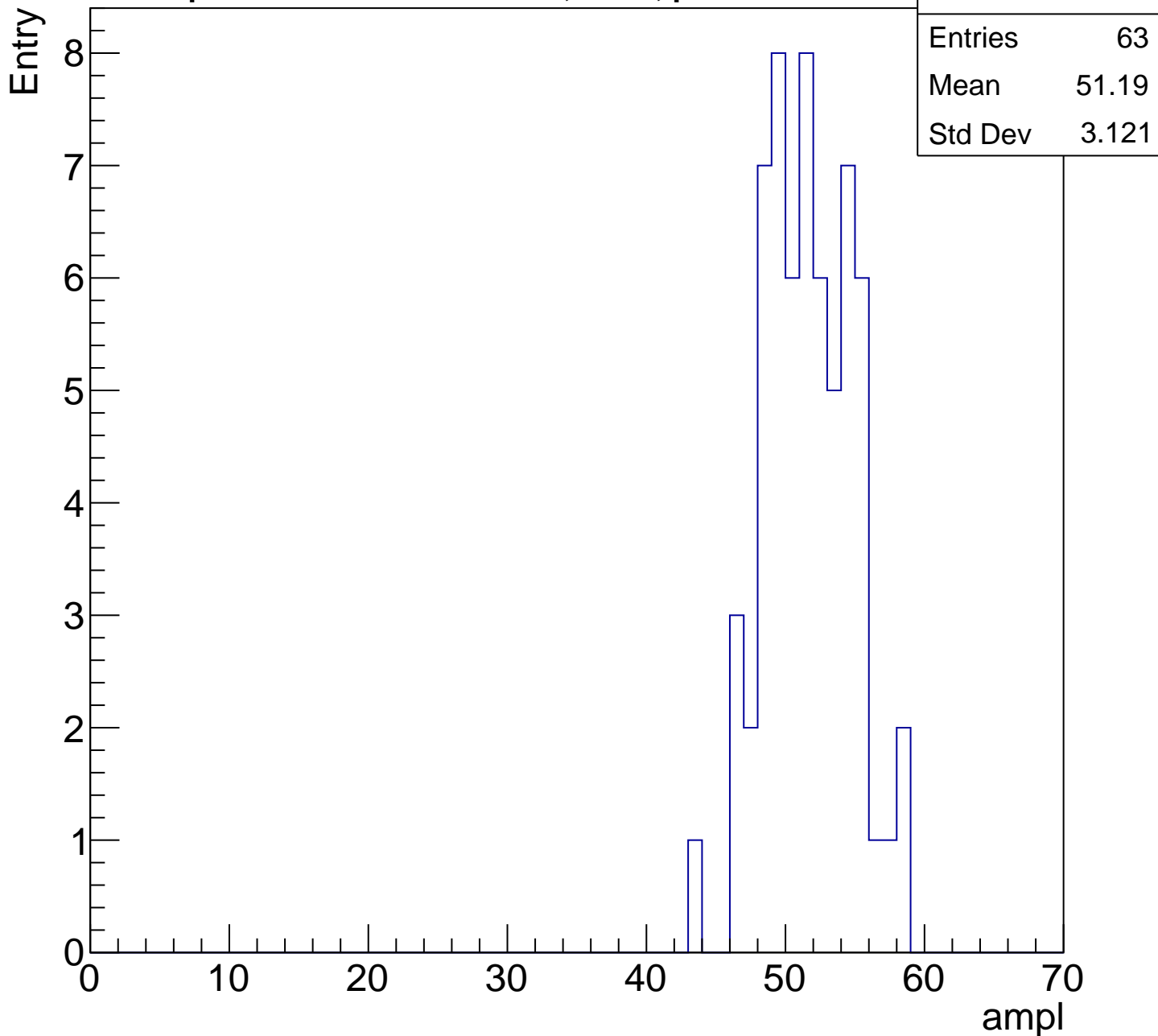
Gaus Width: 3.6533

Entry



B1L103S, U12-ch47, adc3

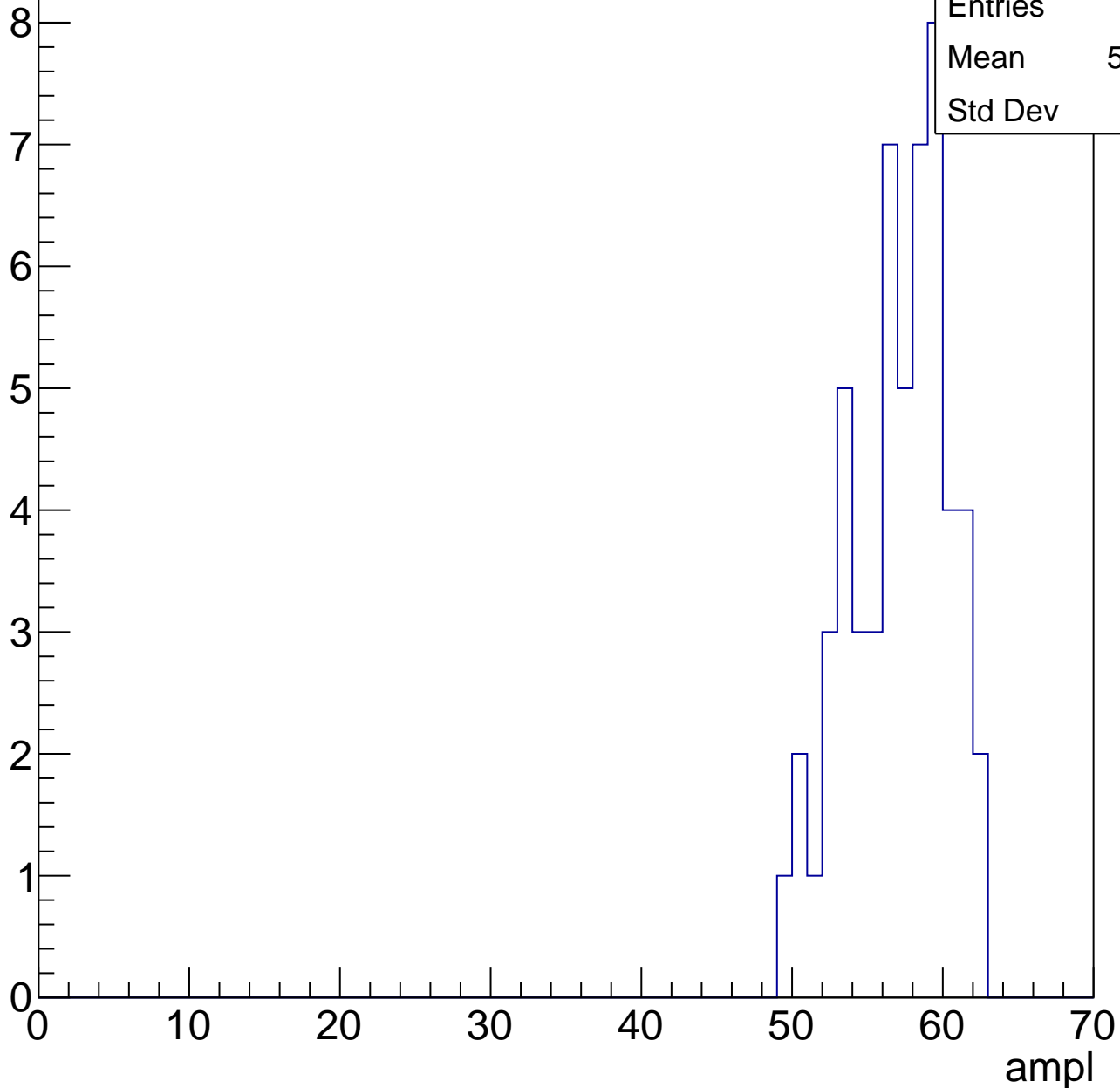
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U12-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

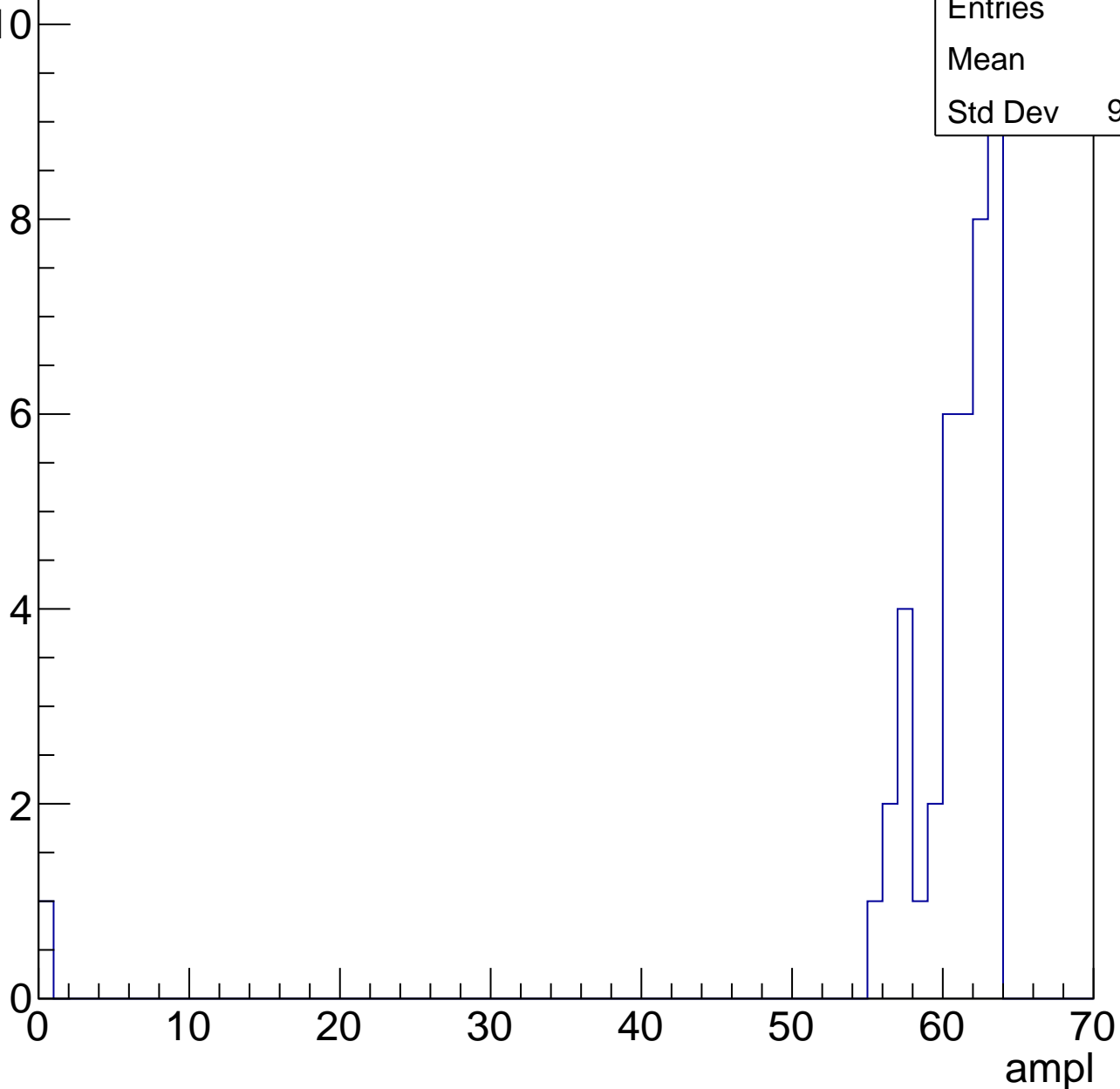


B1L103S, U12-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	59.1
Std Dev	9.619



B1L103S, U12-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

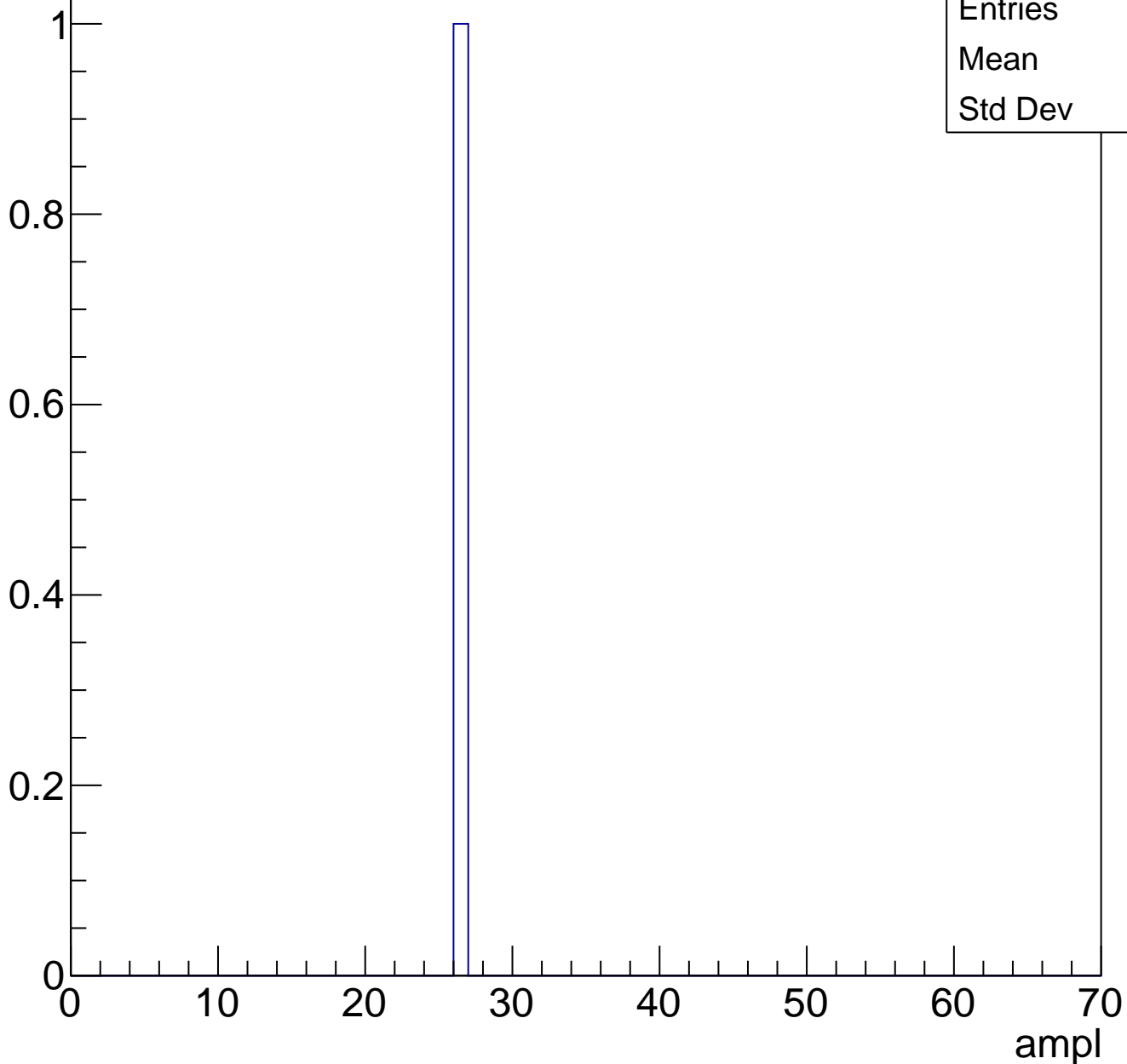


Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	26
Std Dev	0

B1L103S, U12-ch48, adc0

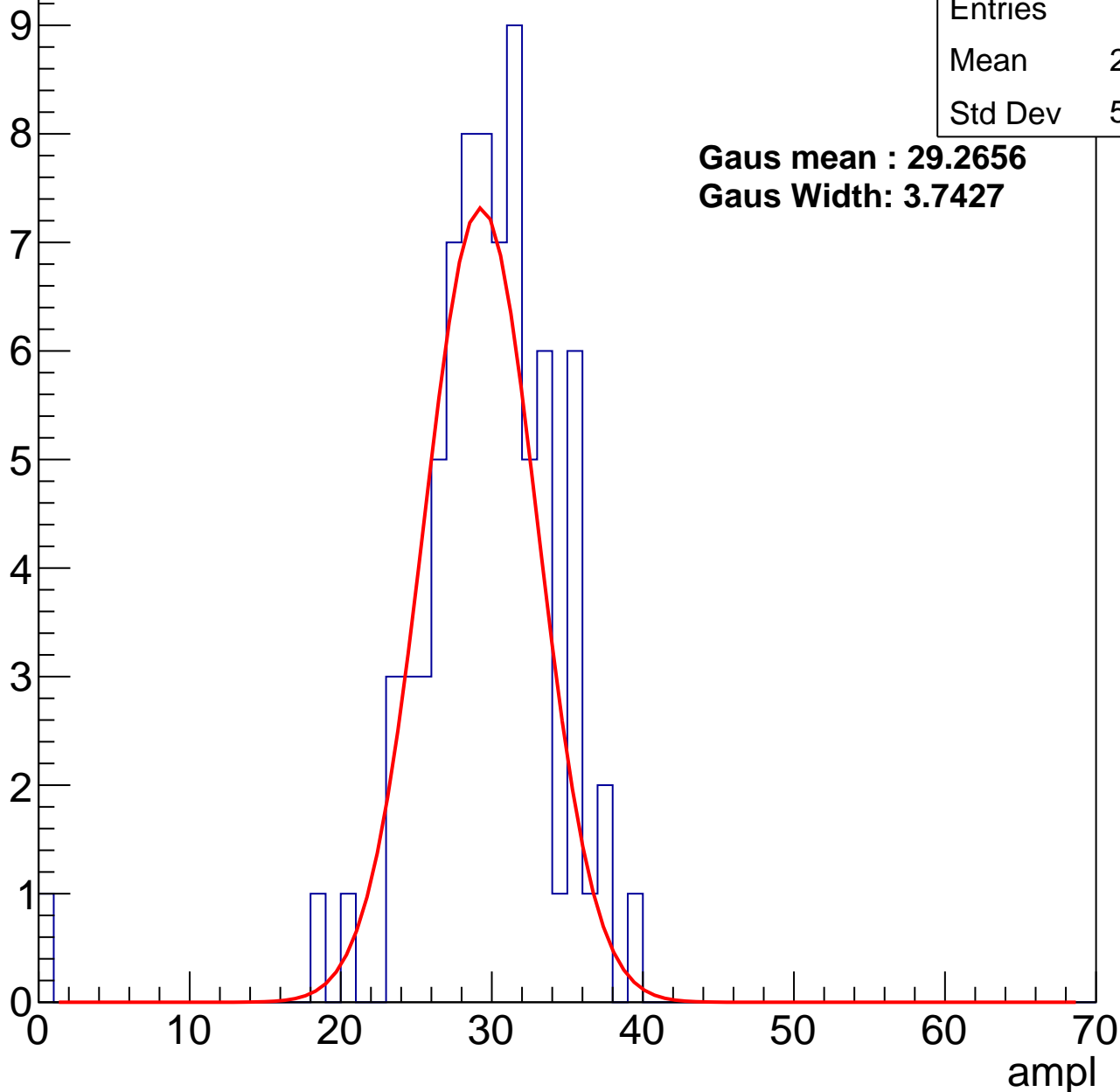
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.09
Std Dev	5.135

Gaus mean : 29.2656

Gaus Width: 3.7427



B1L103S, U12-ch48, adc1

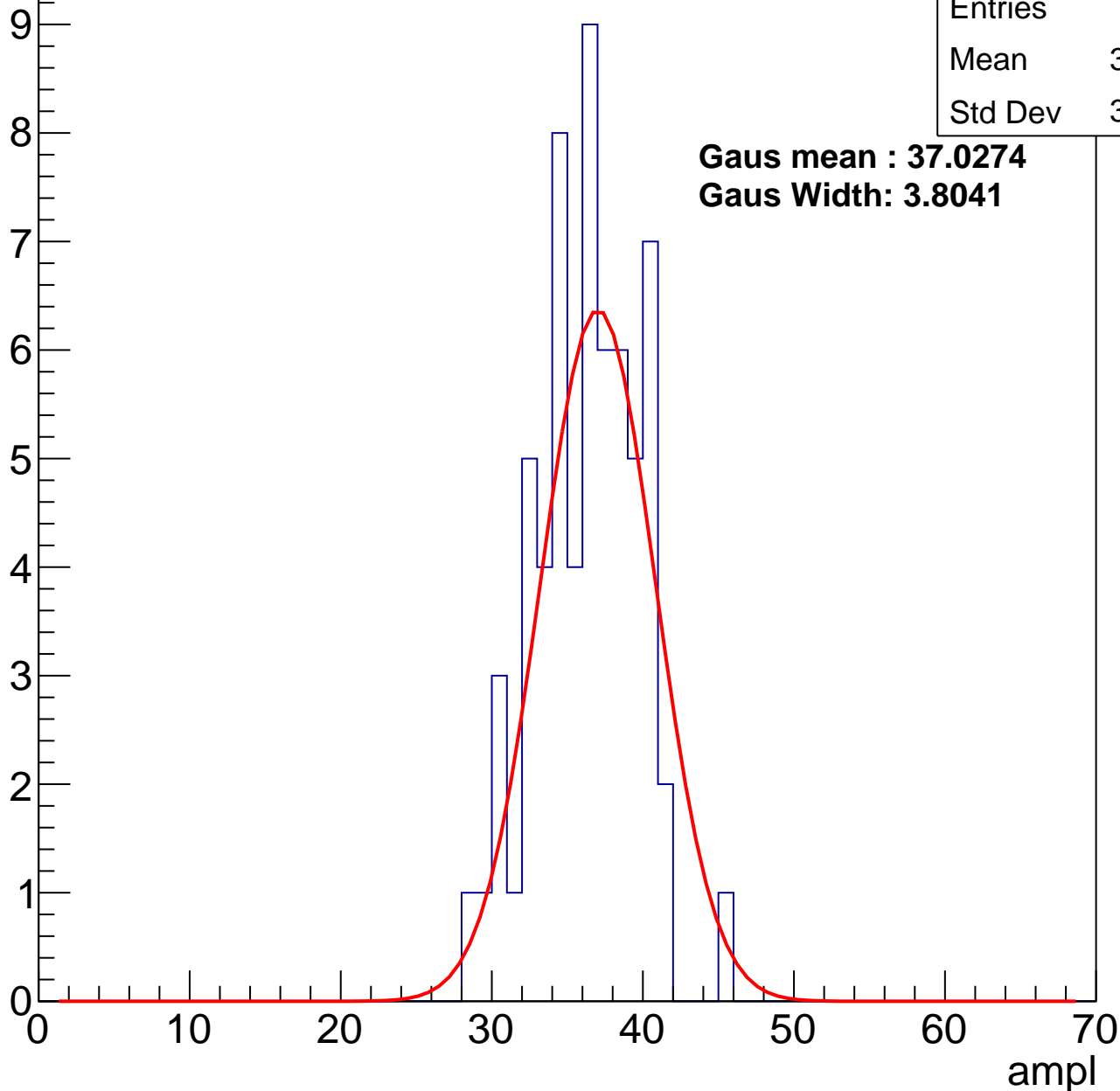
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.84
Std Dev	3.367

Gaus mean : 37.0274

Gaus Width: 3.8041



B1L103S, U12-ch48, adc2

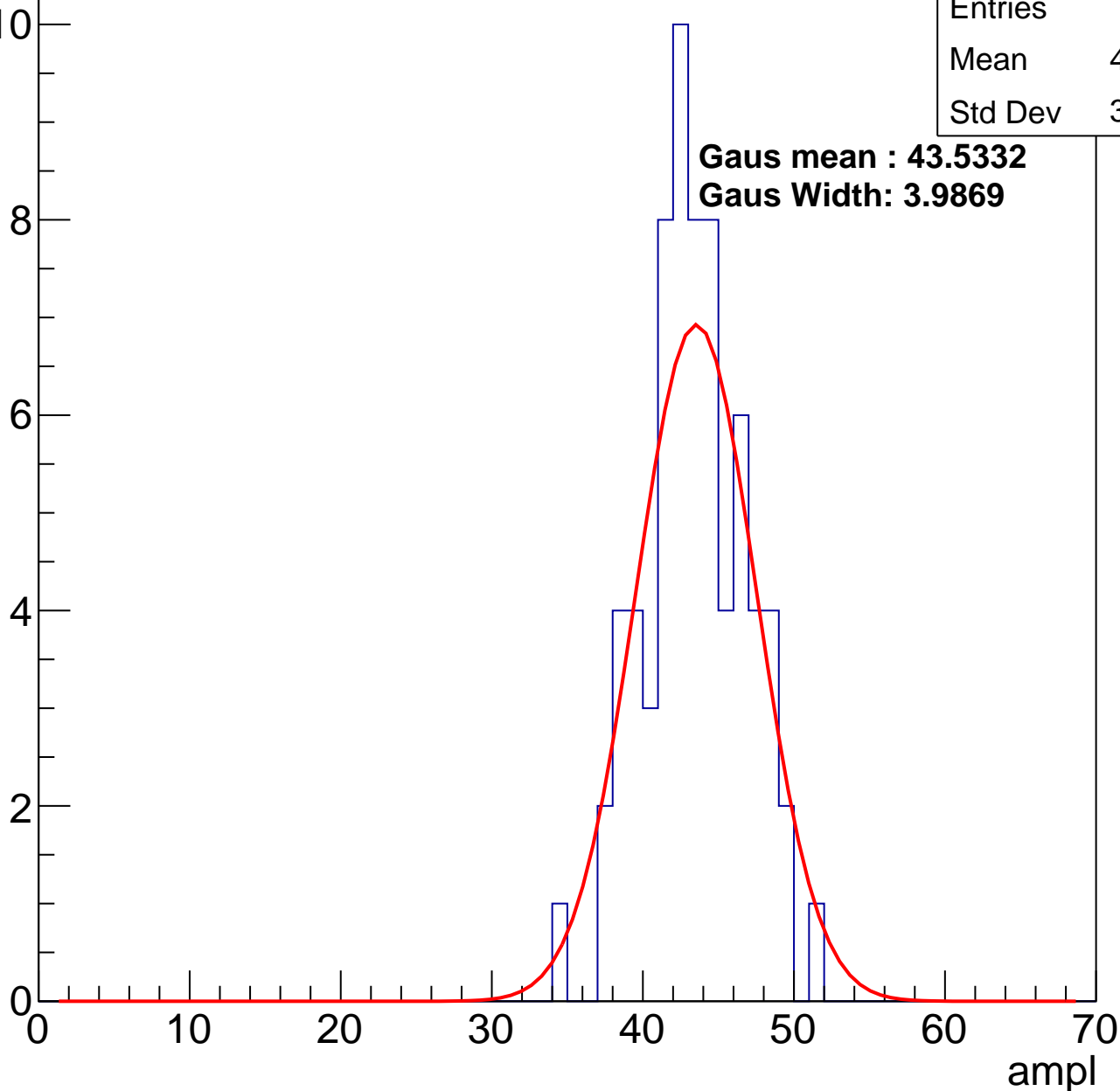
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	42.97
Std Dev	3.327

Gaus mean : 43.5332

Gaus Width: 3.9869

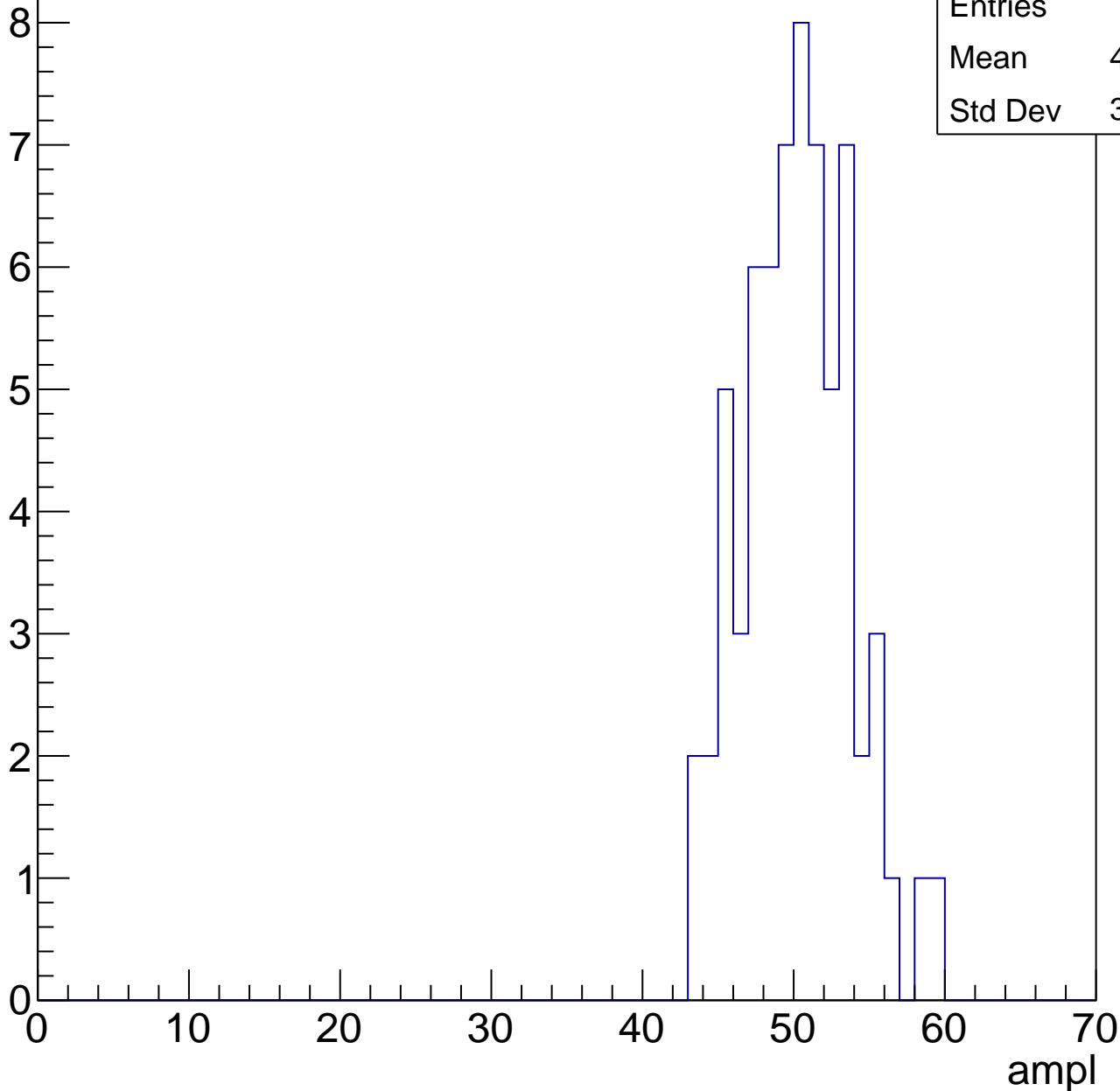


B1L103S, U12-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.76
Std Dev	3.486

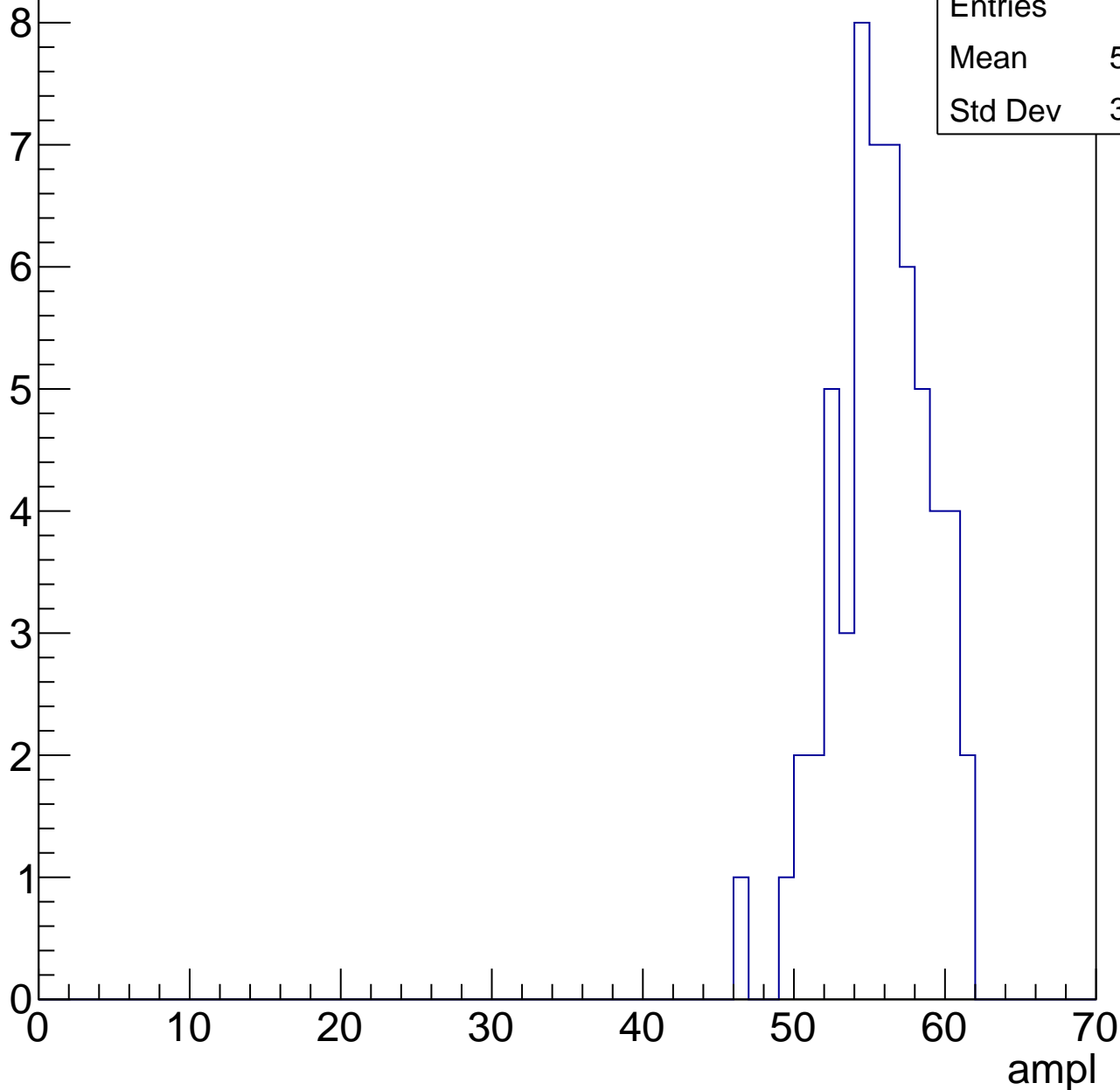


B1L103S, U12-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.35
Std Dev	3.159

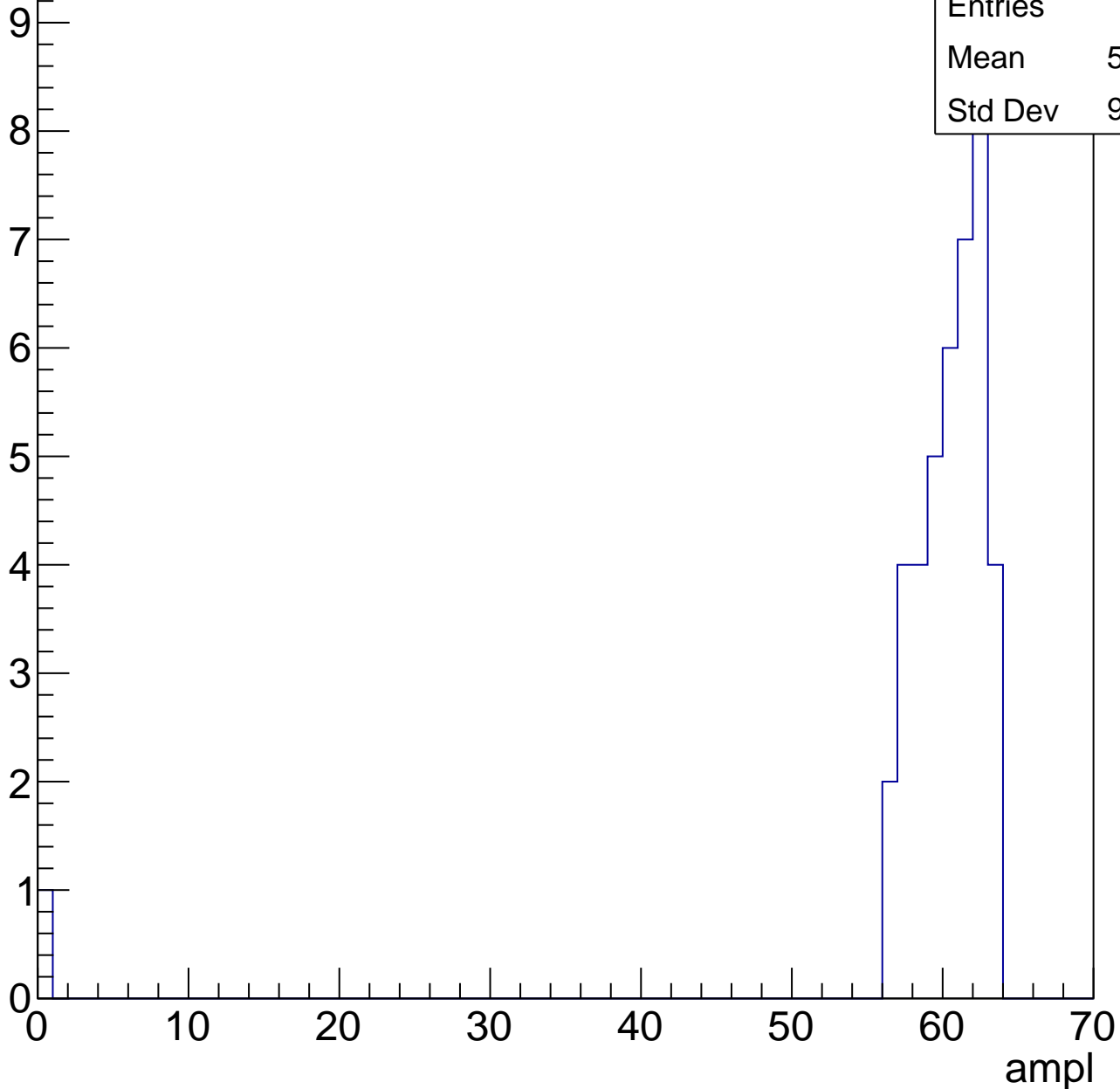


B1L103S, U12-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.67
Std Dev	9.377



B1L103S, U12-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

6

5

4

3

2

1

0

0

10

20

30

40

50

60

ampl

Entries	8
Mean	62.62
Std Dev	0.696

B1L103S, U12-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch49, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	29.12
Std Dev	4.671

Gaus mean : 30.4357

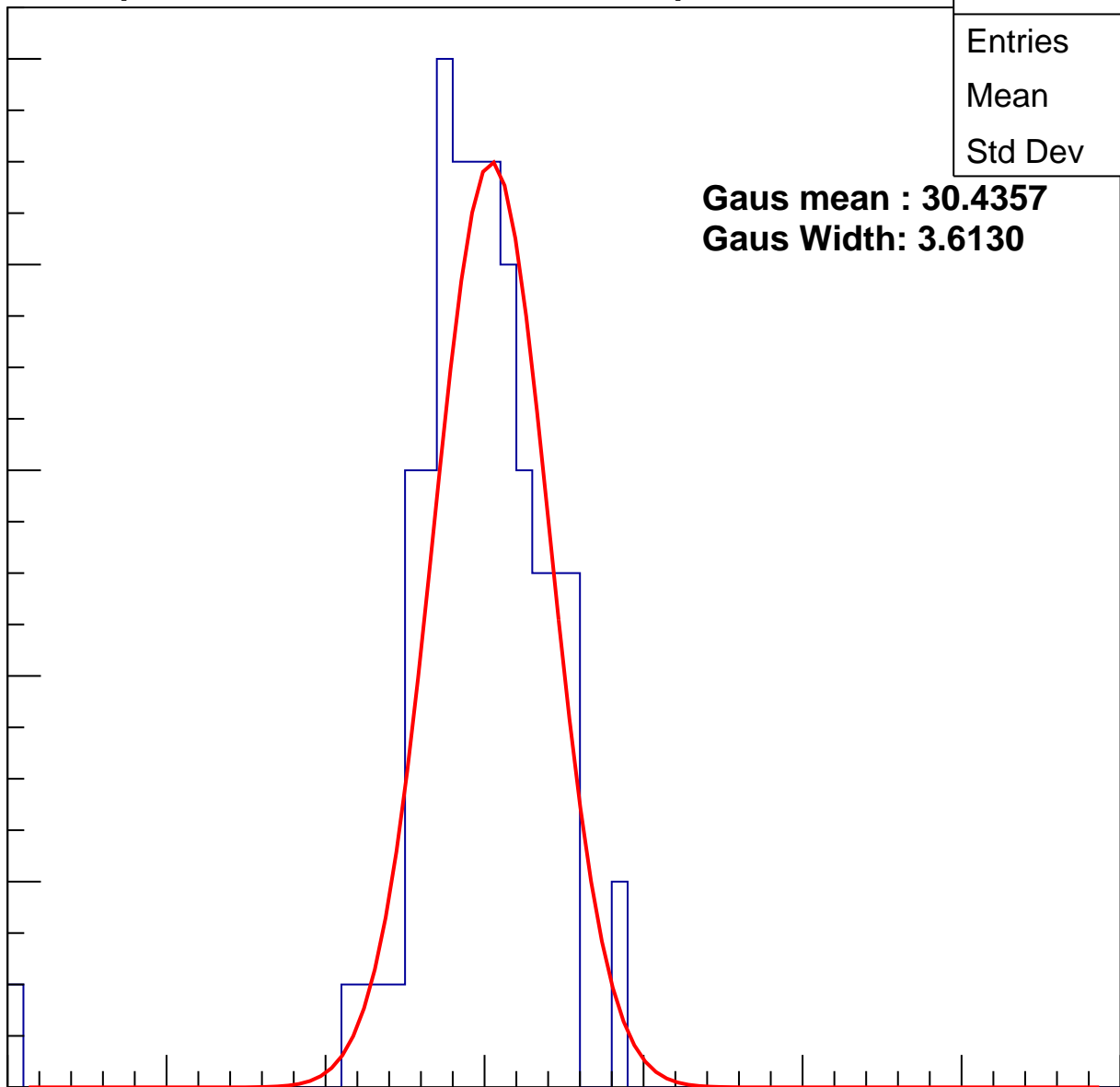
Gaus Width: 3.6130

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch49, adc1

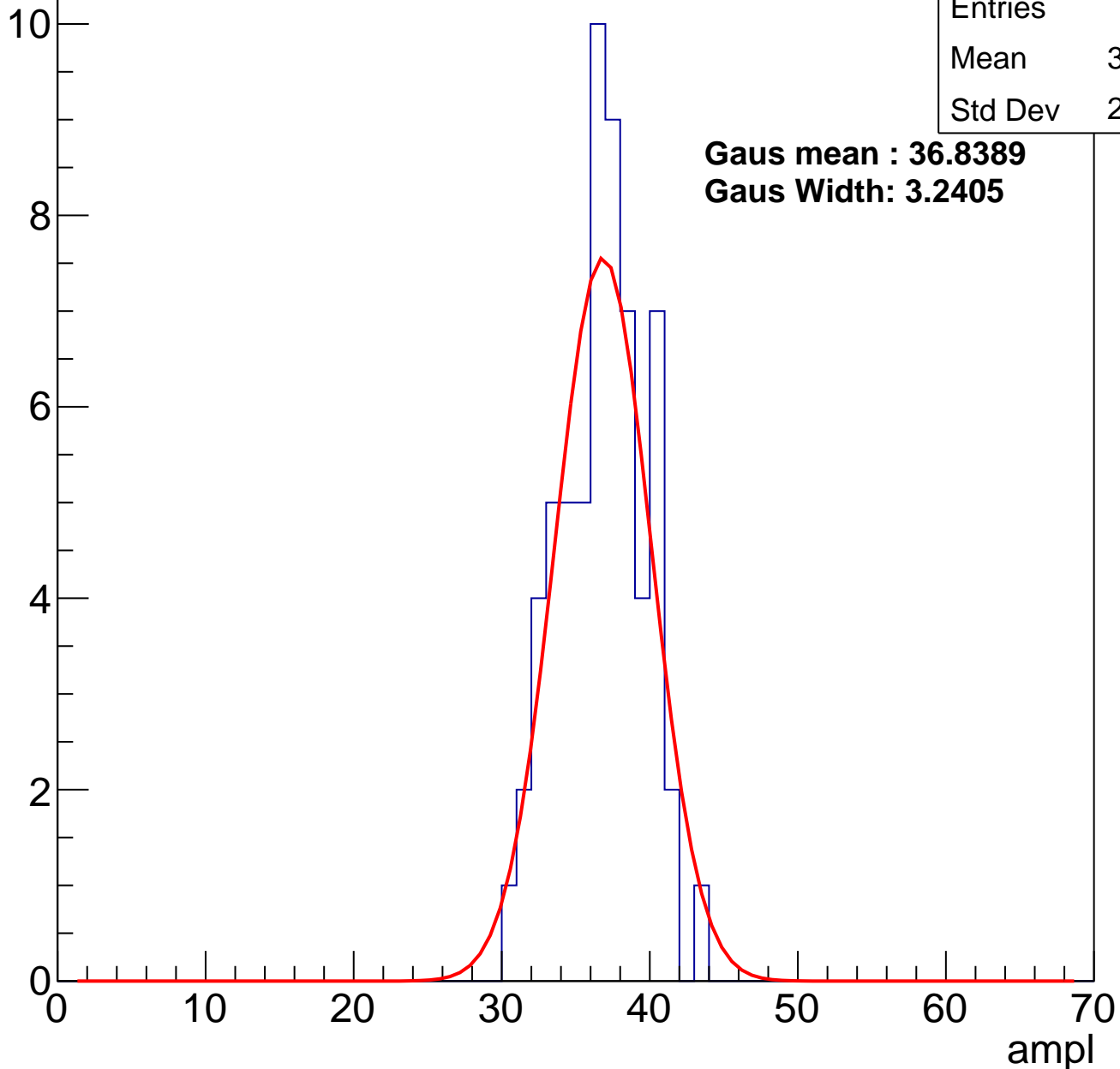
calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	36.29
Std Dev	2.836

Gaus mean : 36.8389

Gaus Width: 3.2405

Entry

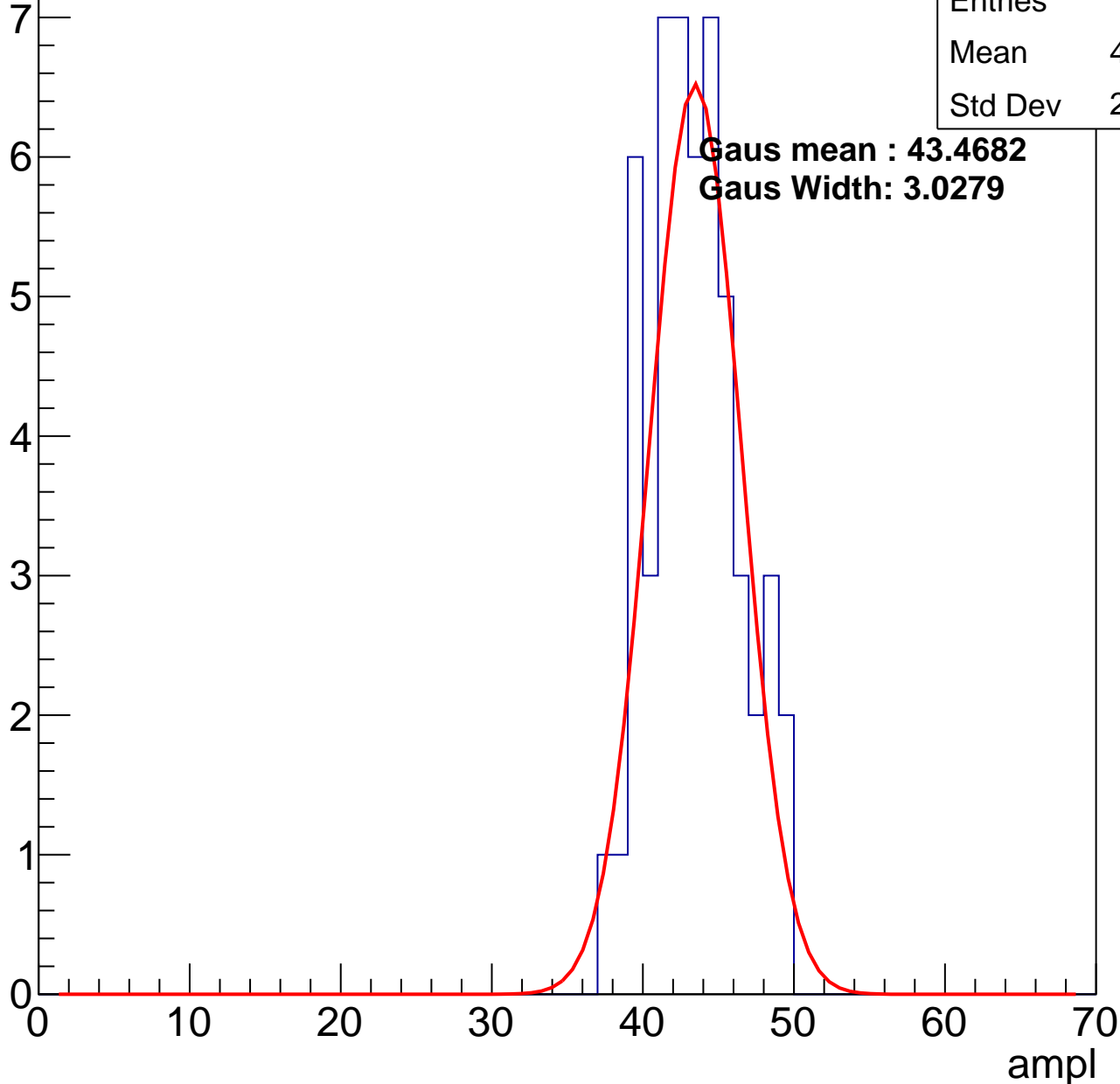


B1L103S, U12-ch49, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.92
Std Dev	2.919



B1L103S, U12-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

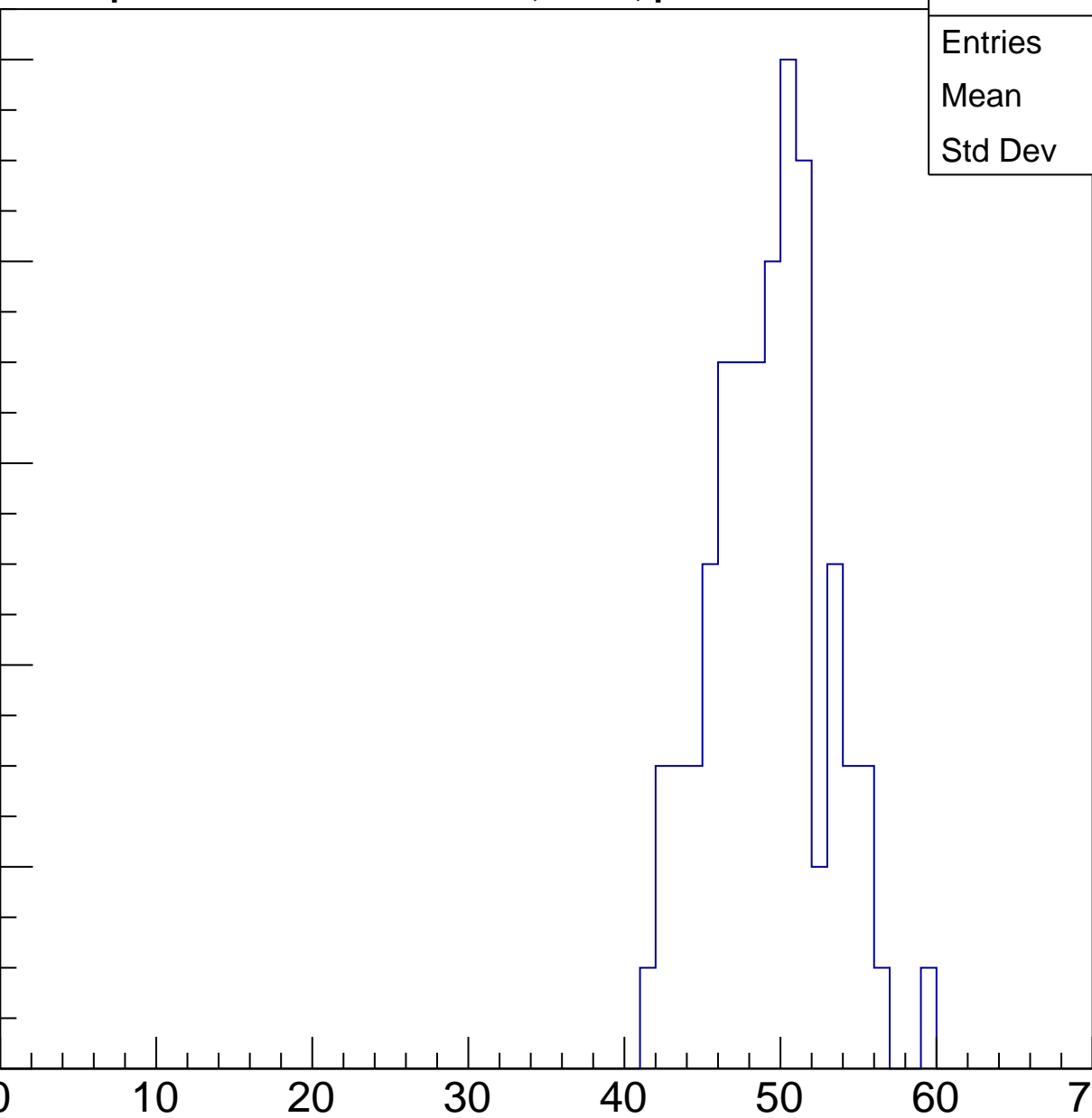
Entries	78
Mean	48.74
Std Dev	3.657

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

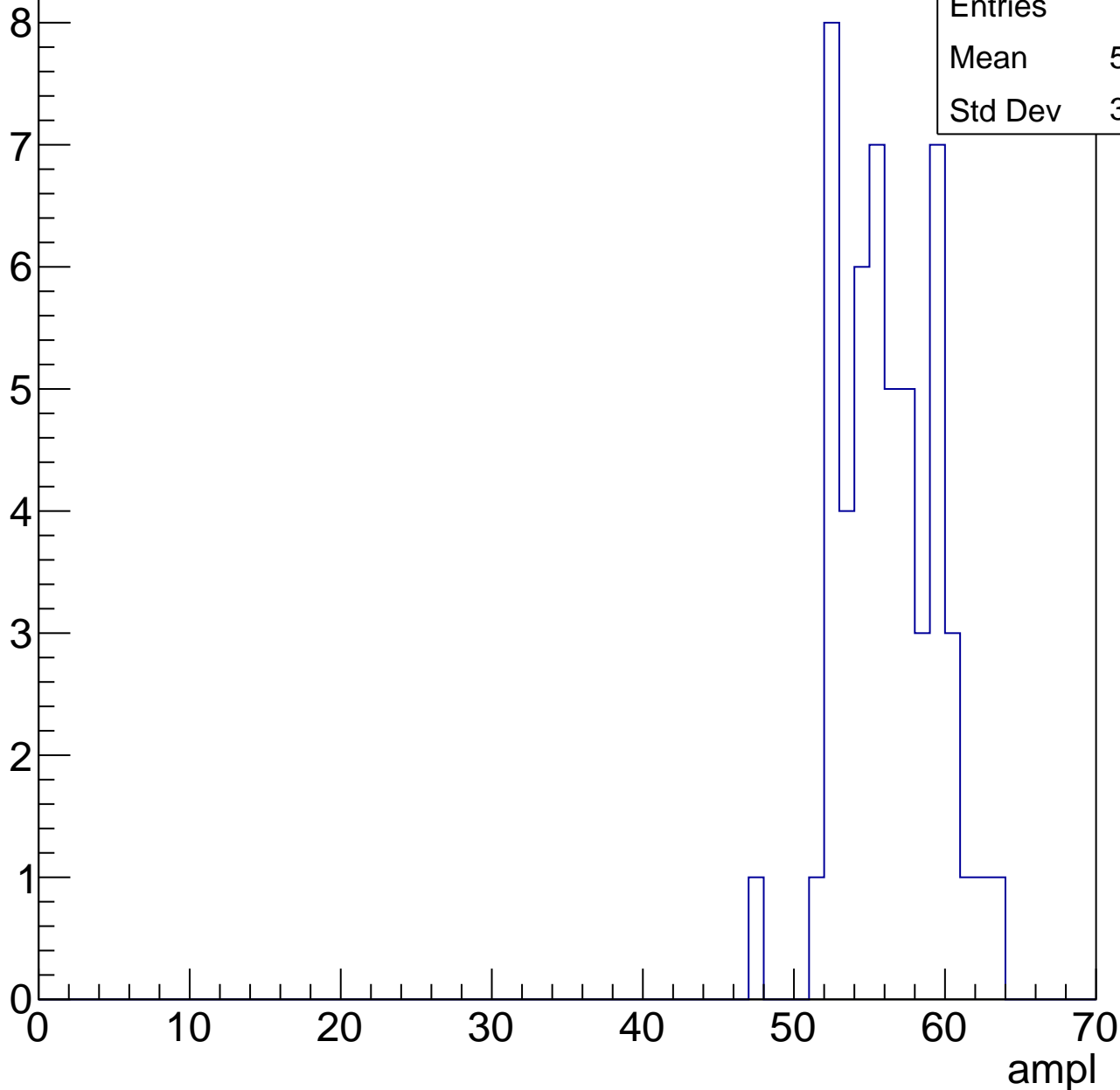


B1L103S, U12-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.72
Std Dev	3.176

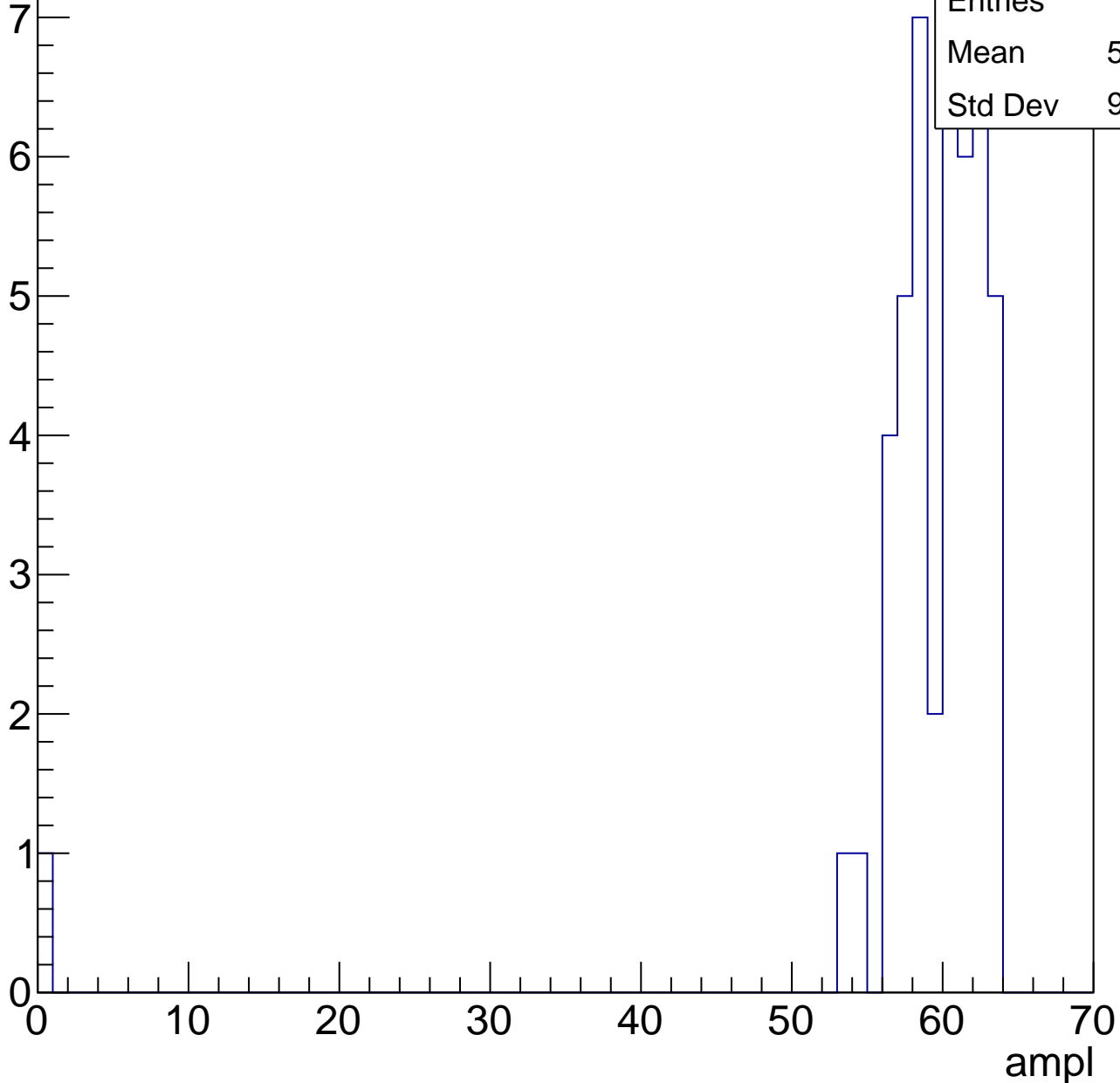


B1L103S, U12-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

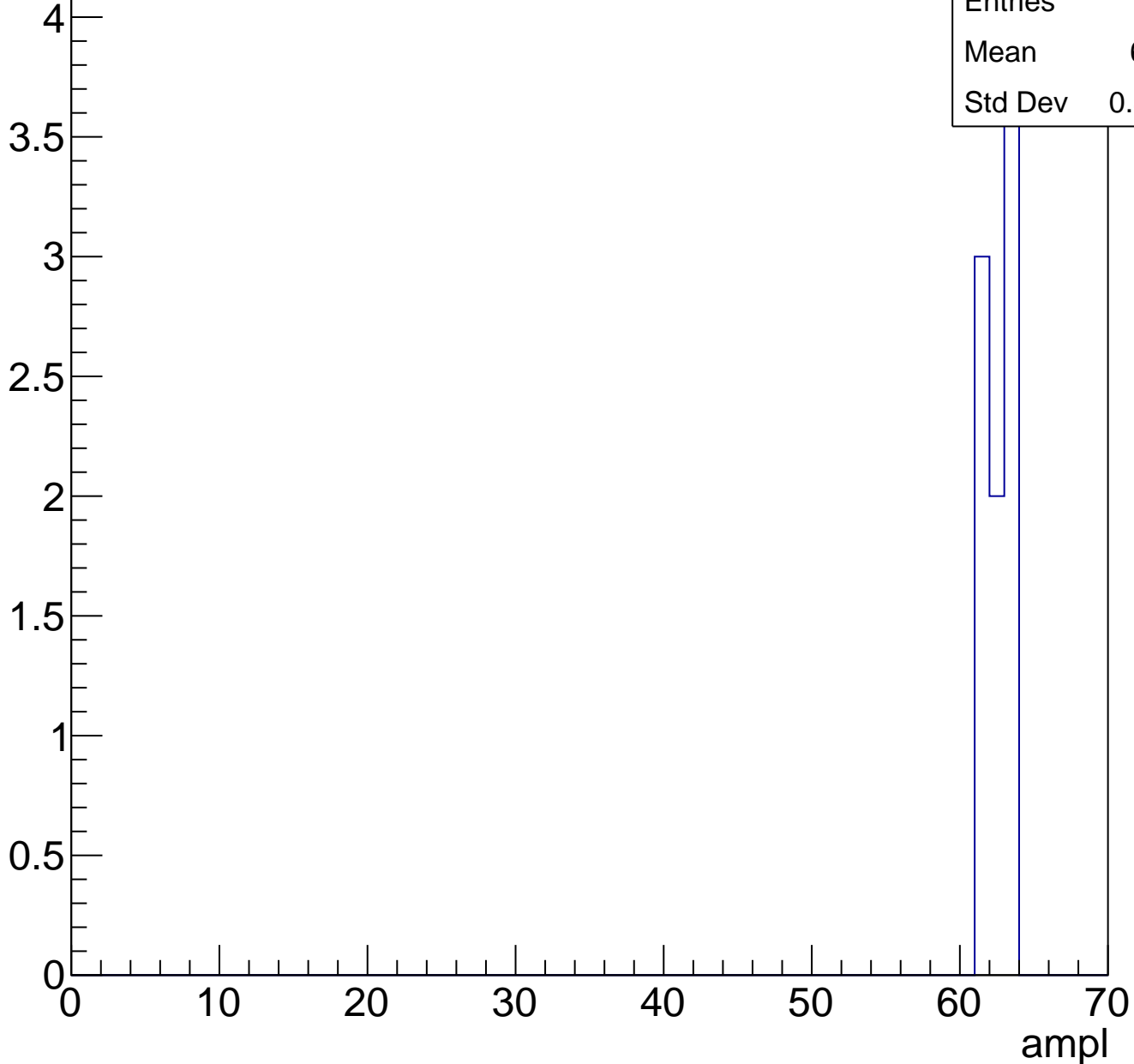
Entries	46
Mean	58.15
Std Dev	9.024



B1L103S, U12-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch50, adc0

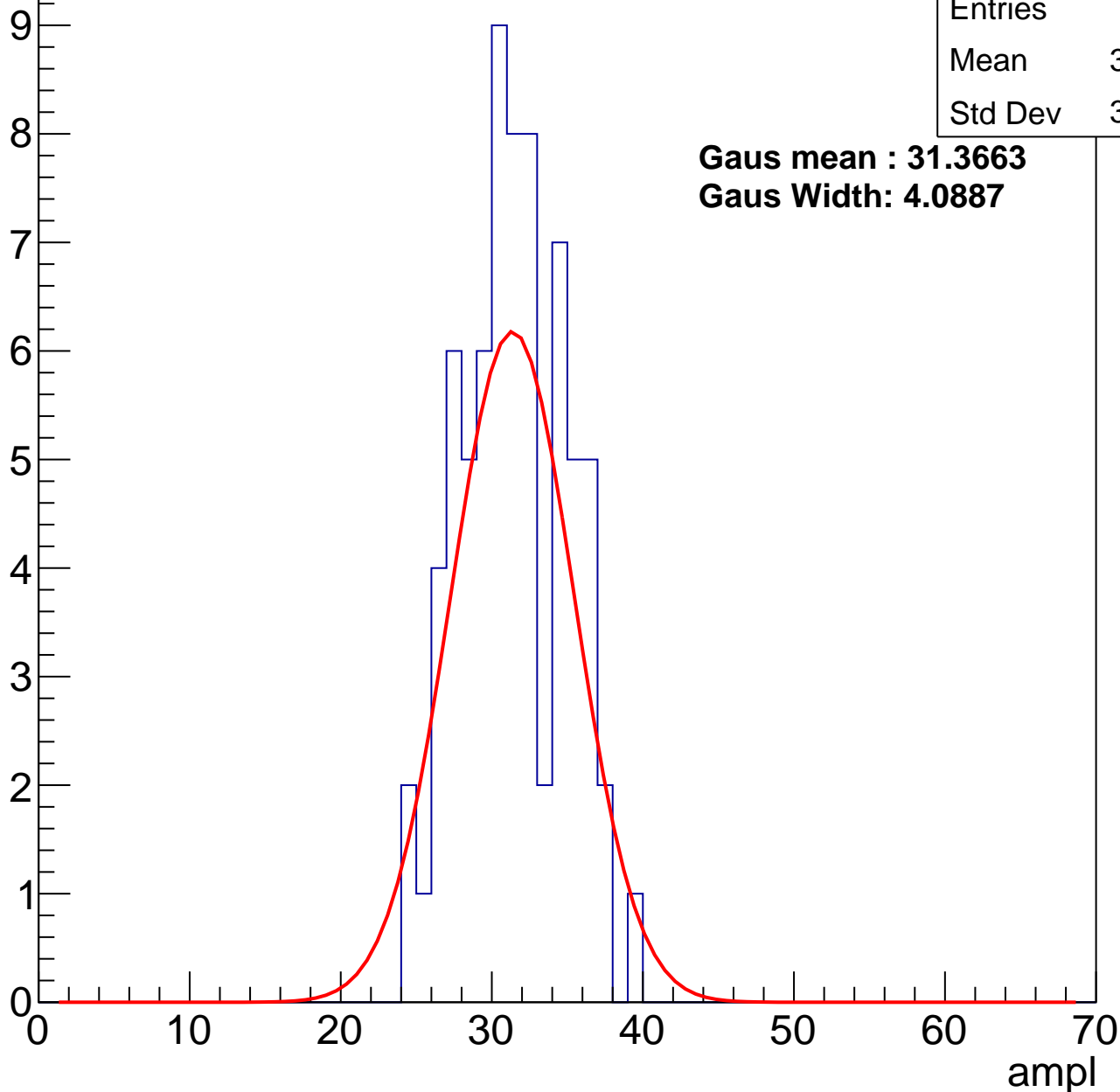
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.97
Std Dev	3.415

Gaus mean : 31.3663

Gaus Width: 4.0887



B1L103S, U12-ch50, adc1

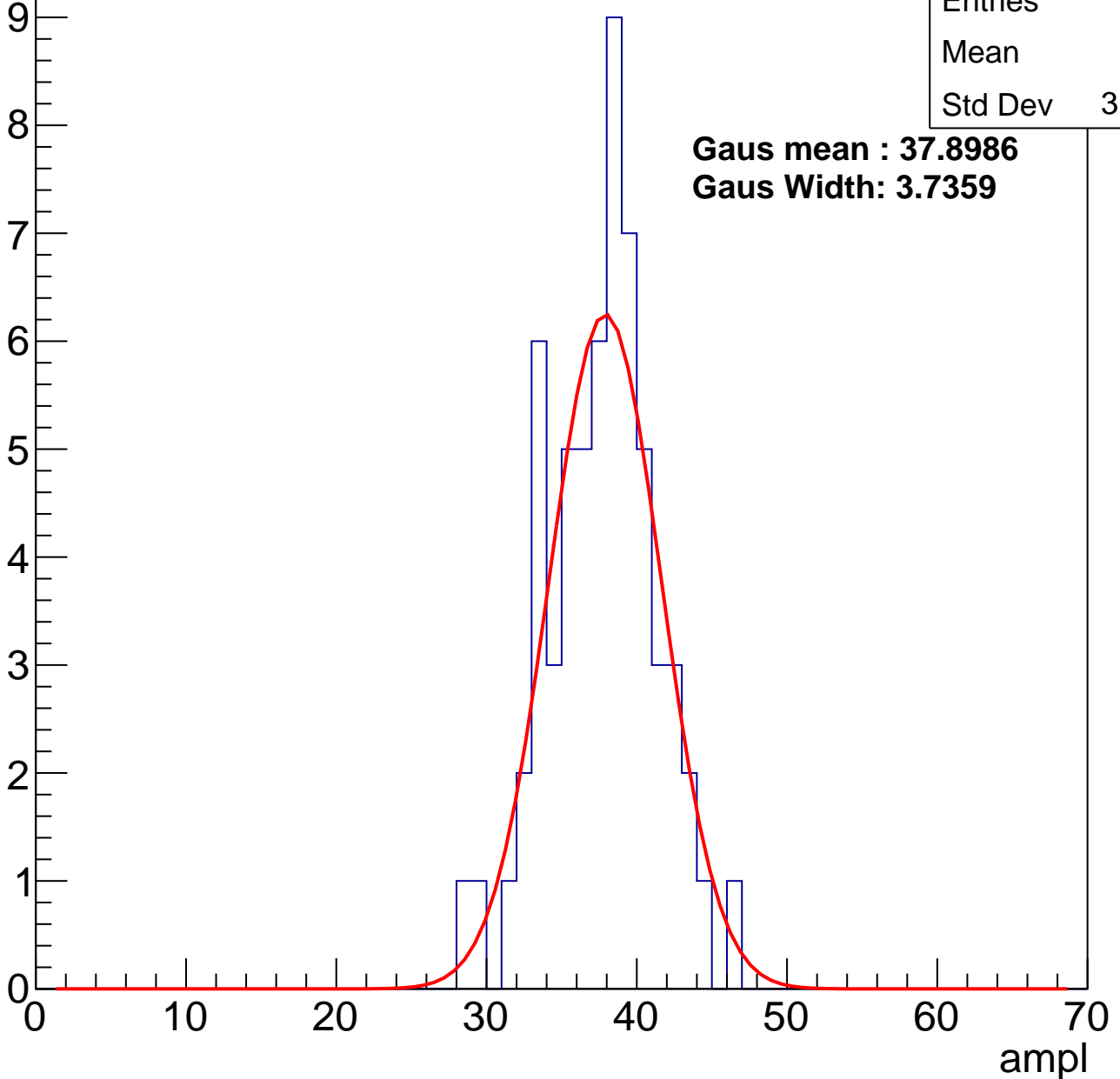
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.2
Std Dev	3.584

Gaus mean : 37.8986

Gaus Width: 3.7359



B1L103S, U12-ch50, adc2

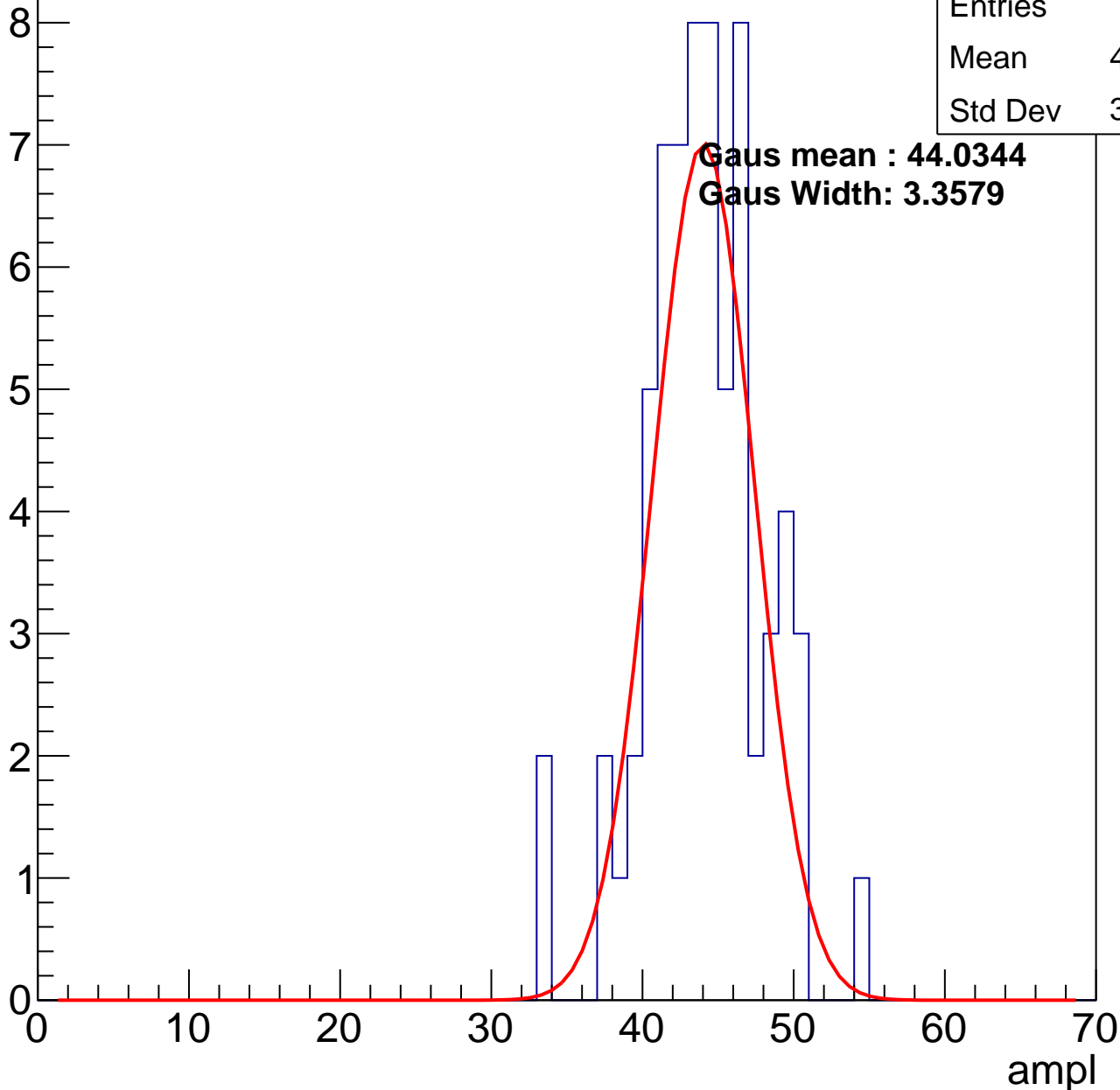
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.59
Std Dev	3.843

Gaus mean : 44.0344

Gaus Width: 3.3579

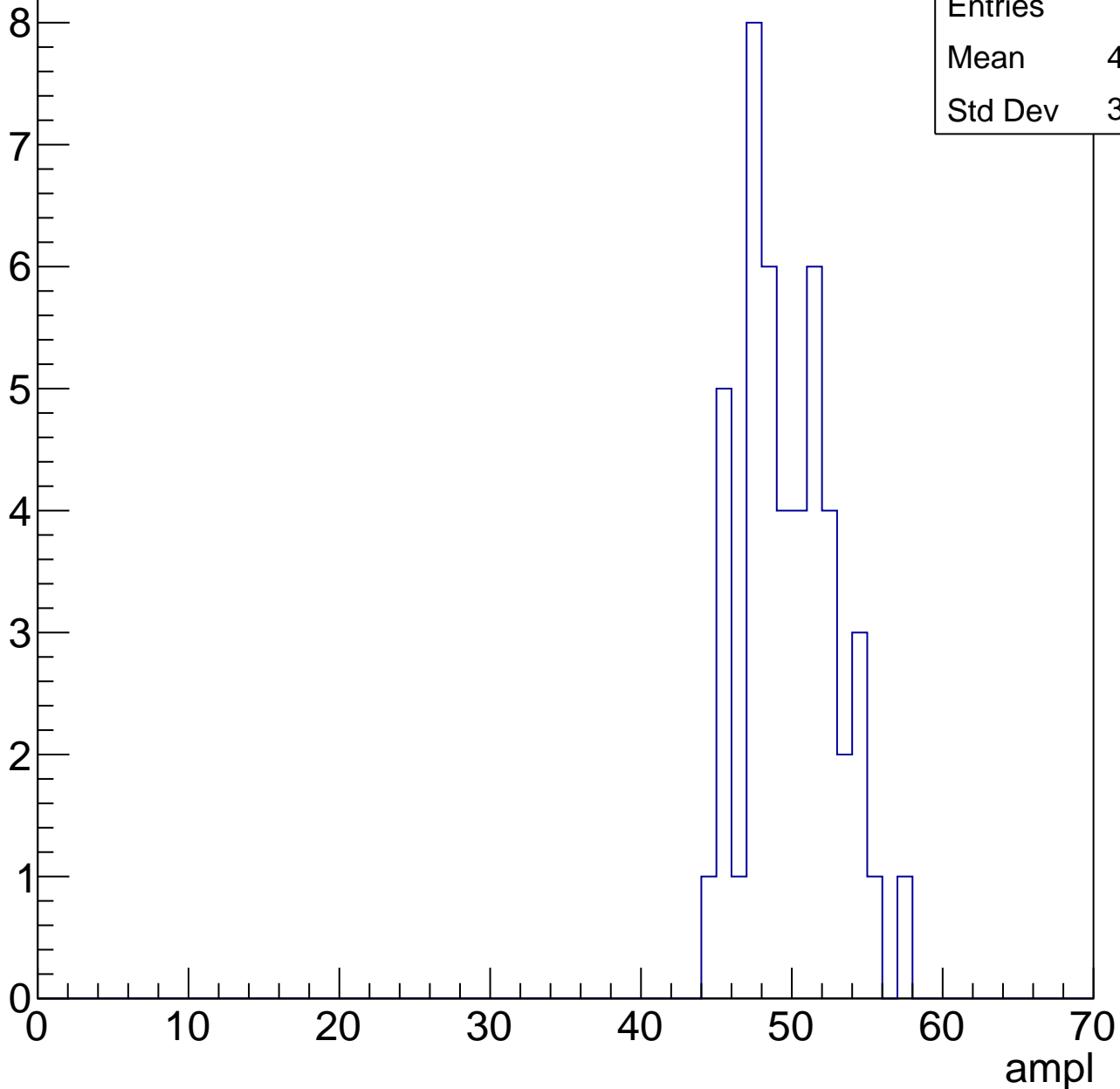


B1L103S, U12-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	49.33
Std Dev	3.015

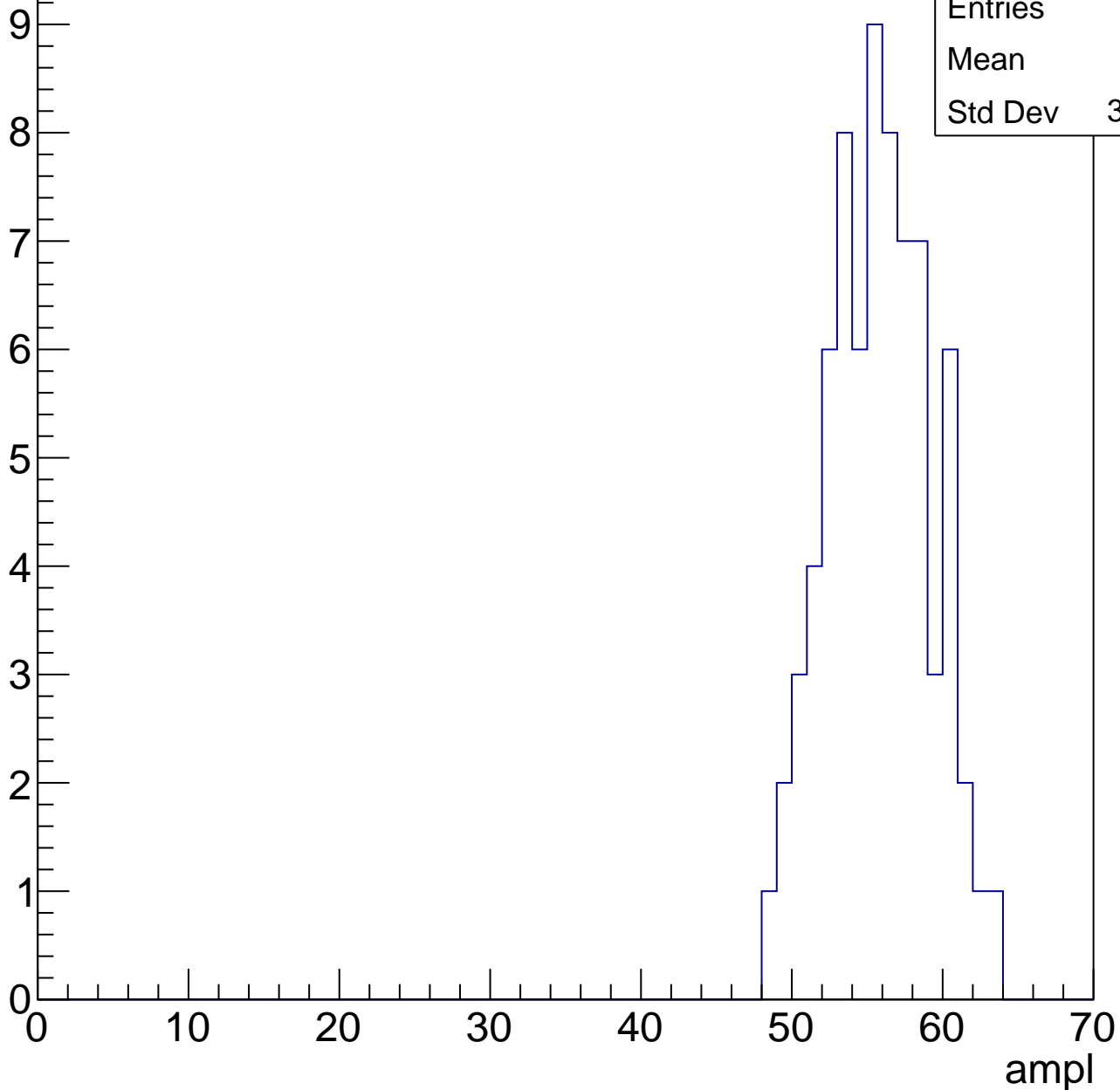


B1L103S, U12-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	55.3
Std Dev	3.344

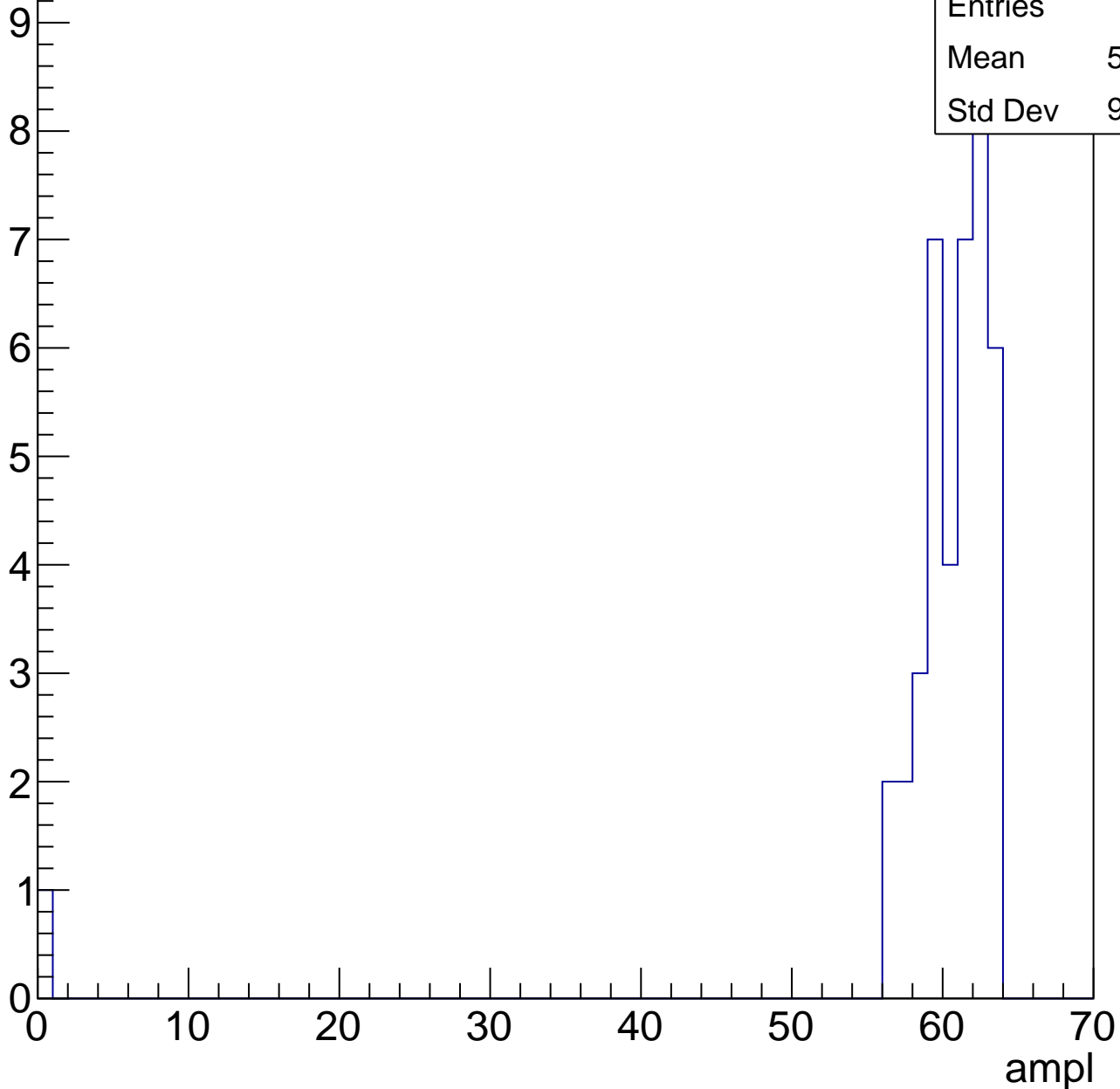


B1L103S, U12-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	58.93
Std Dev	9.524



B1L103S, U12-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

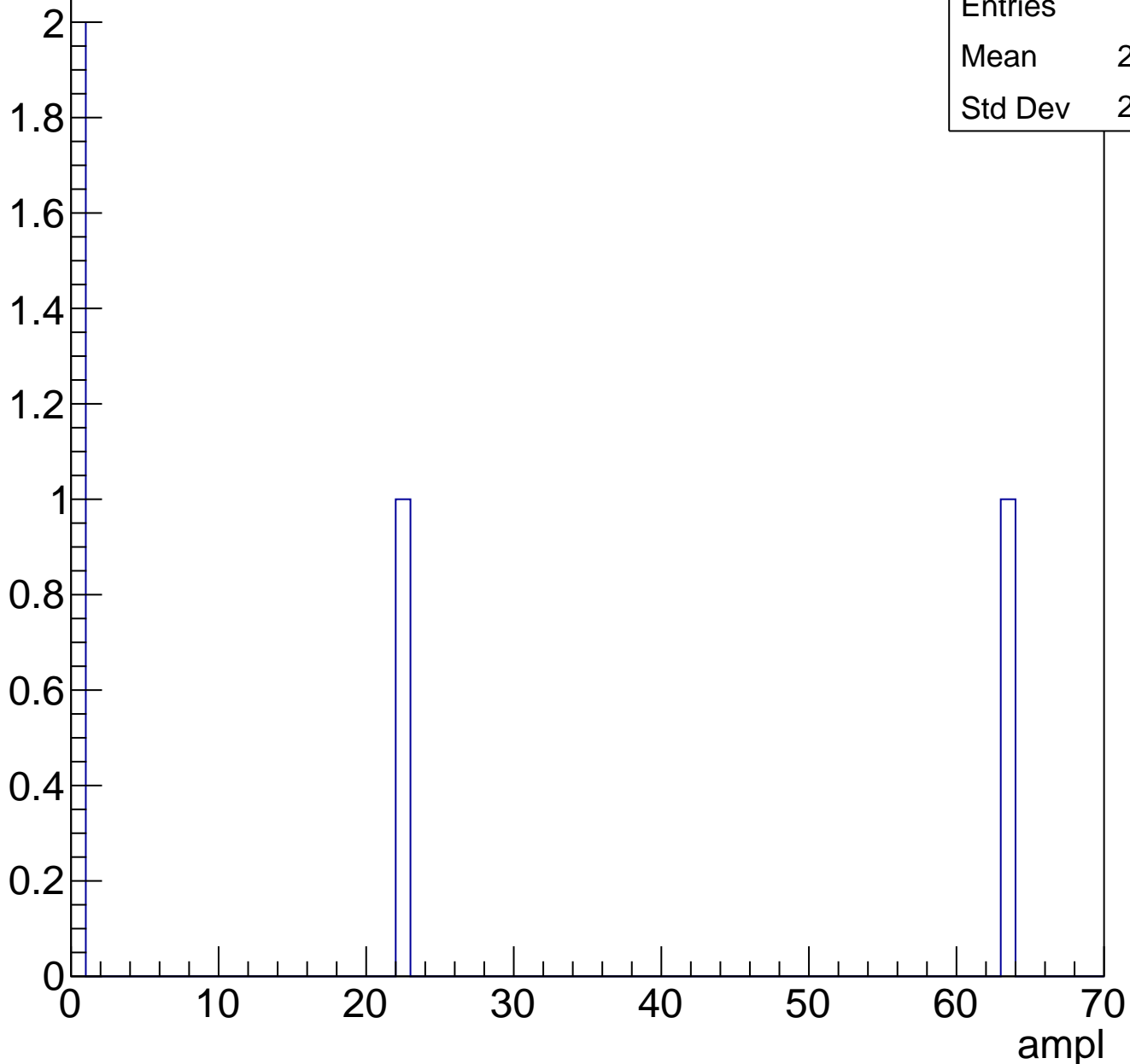


Entries	3
Mean	62.33
Std Dev	0.9428

B1L103S, U12-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B1L103S, U12-ch51, adc0

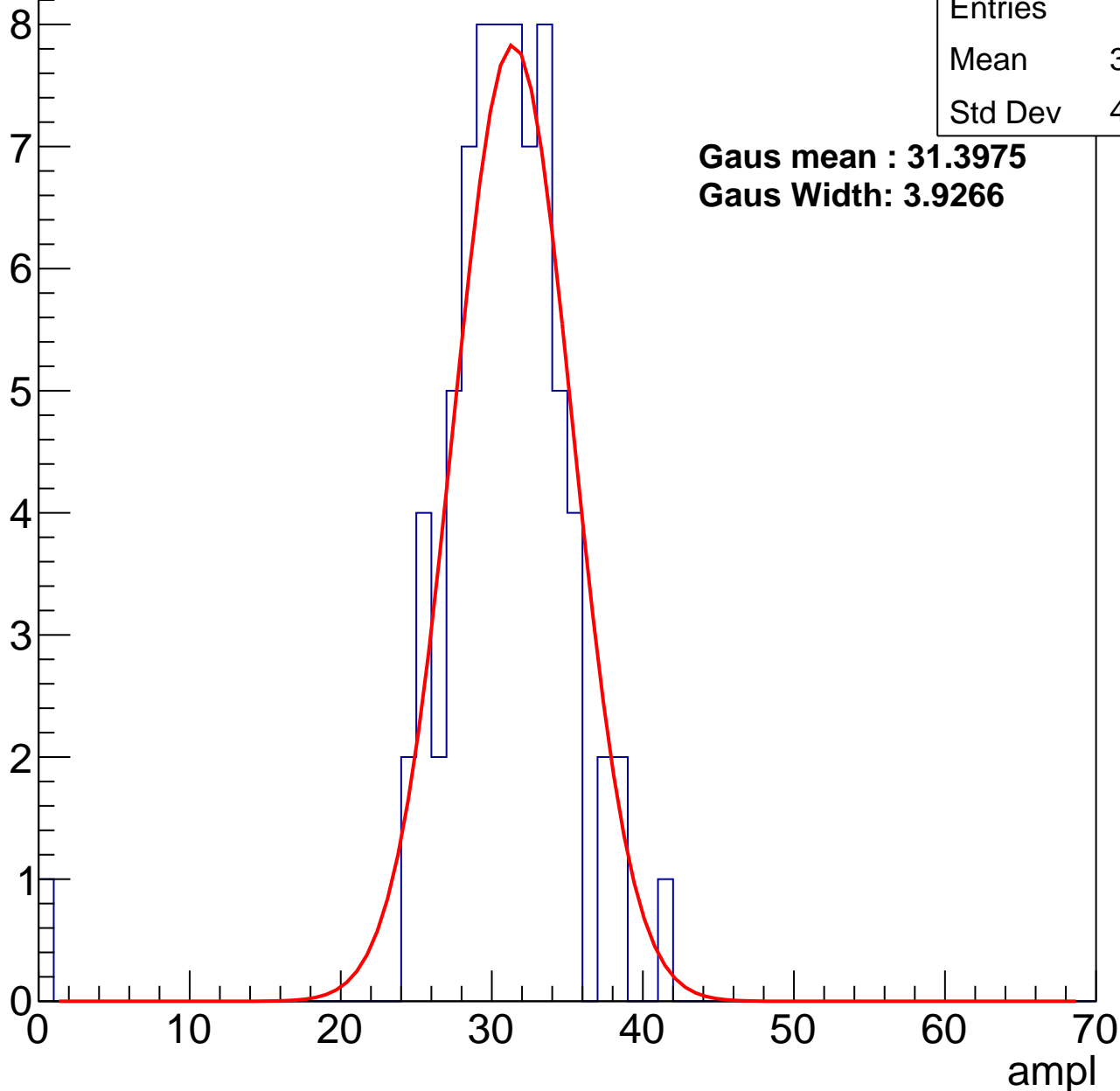
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.27
Std Dev	4.952

Gaus mean : 31.3975

Gaus Width: 3.9266



B1L103S, U12-ch51, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	38.11
Std Dev	3.286

Gaus mean : 38.5266

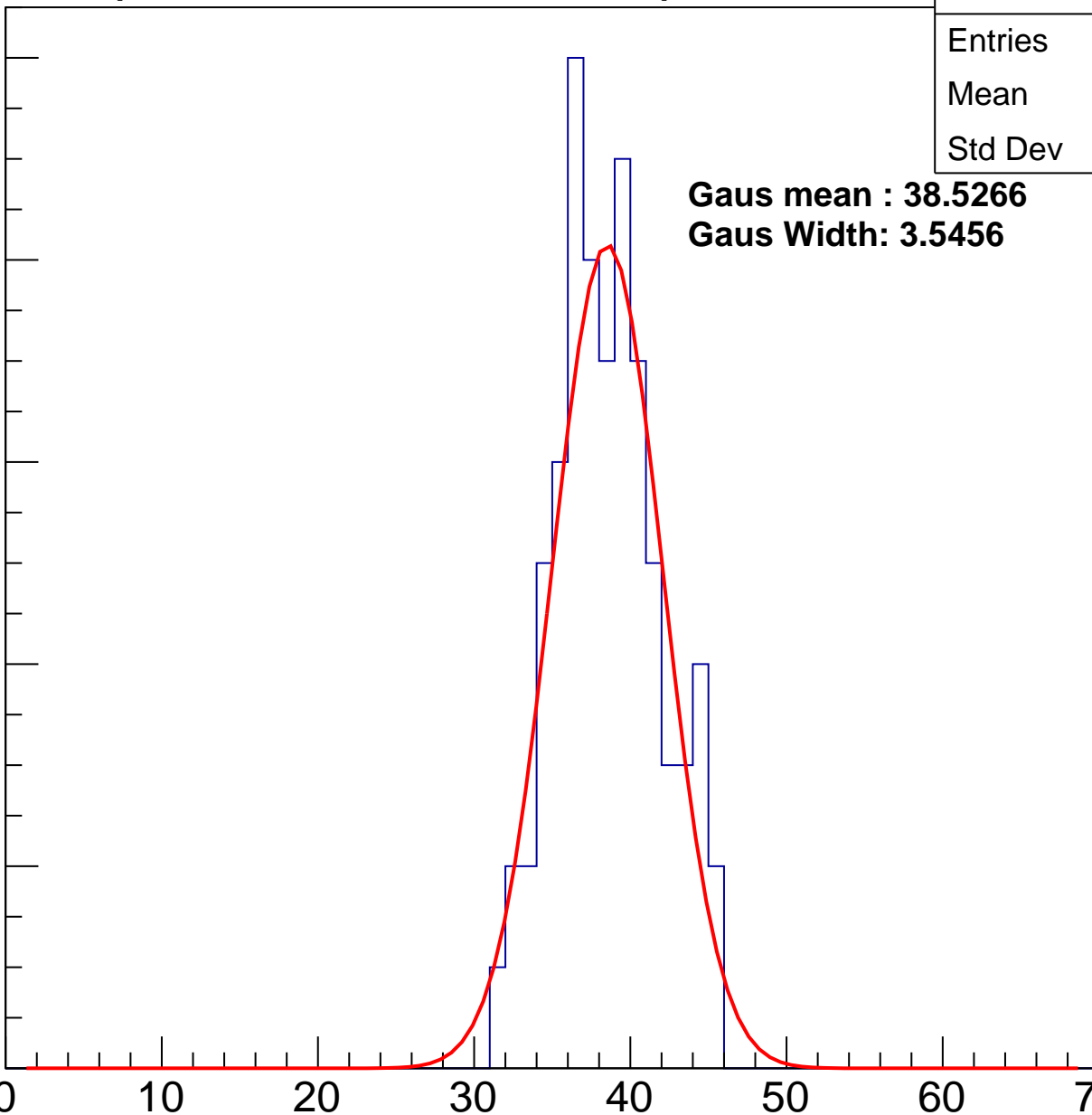
Gaus Width: 3.5456

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch51, adc2

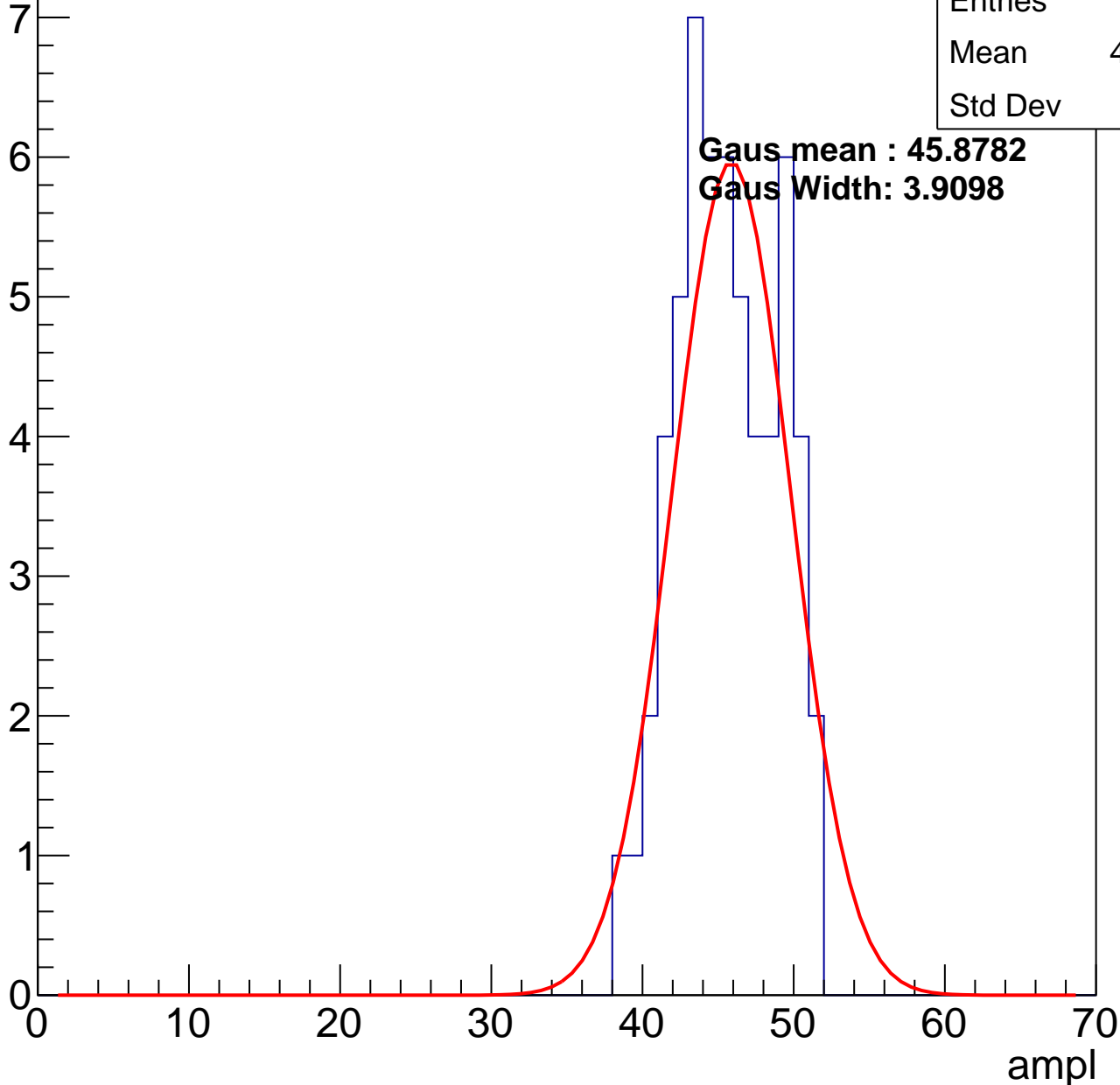
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	45.12
Std Dev	3.25

Gaus mean : 45.8782

Gaus Width: 3.9098

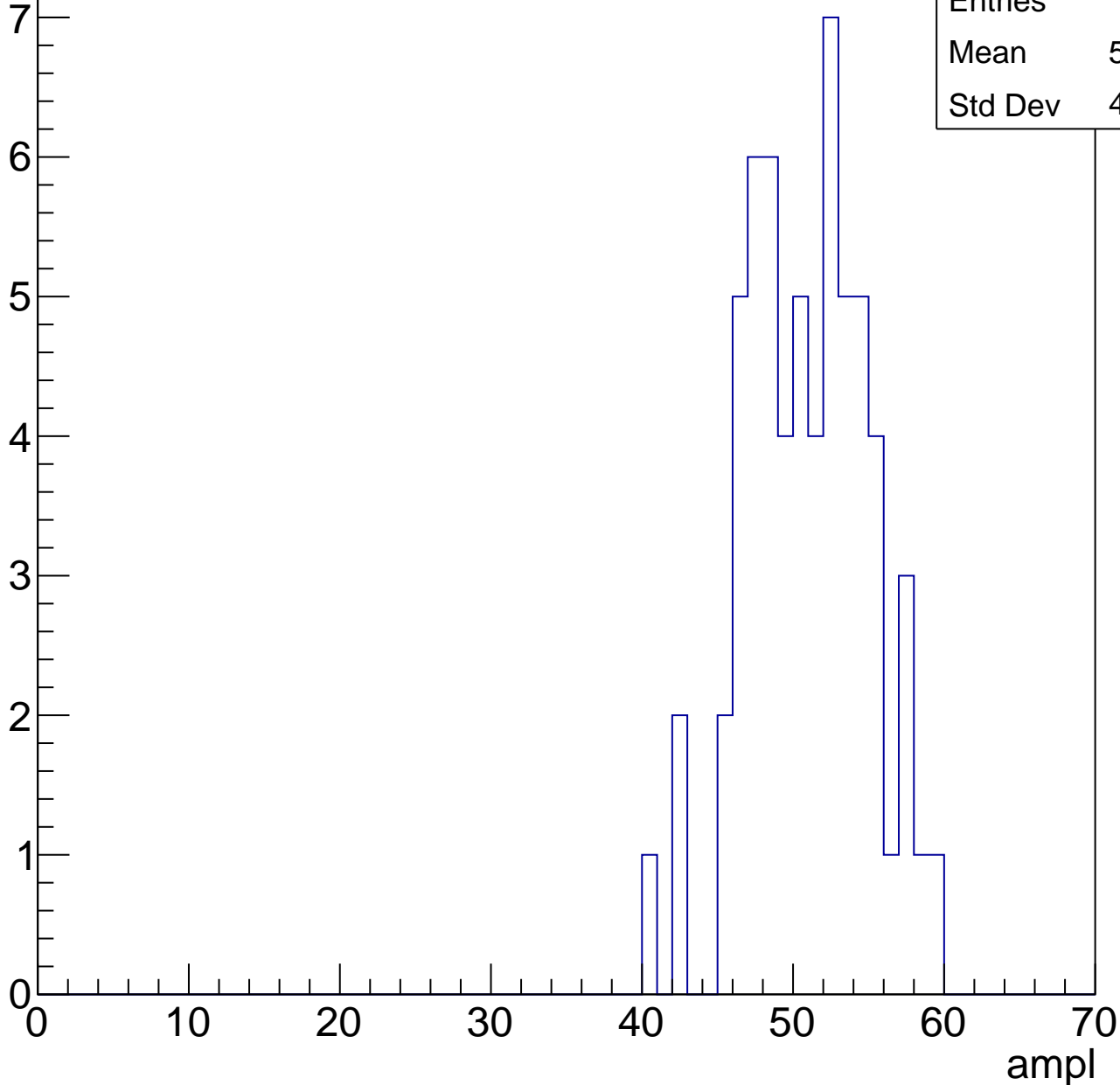


B1L103S, U12-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	50.44
Std Dev	4.043

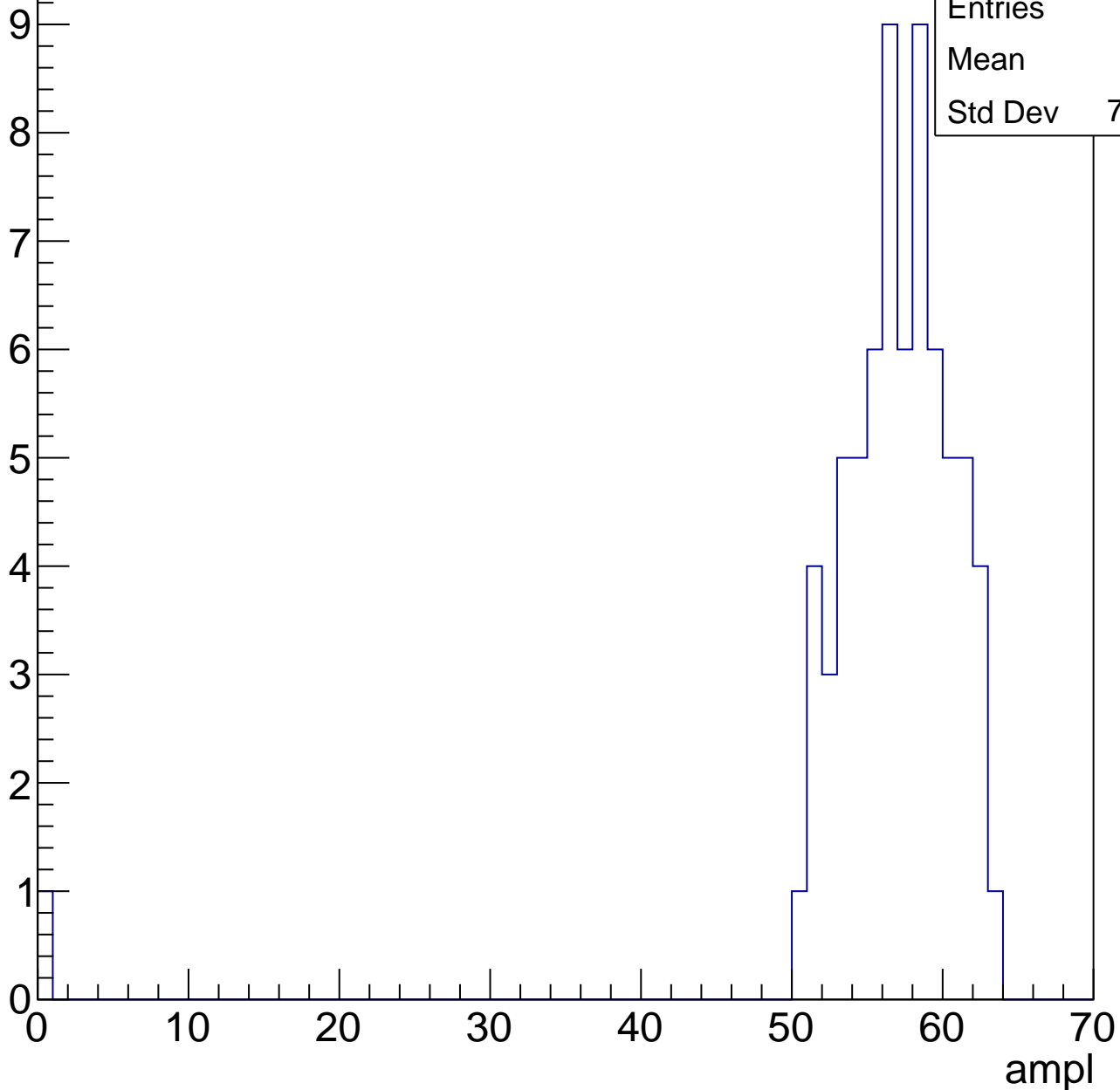


B1L103S, U12-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	55.9
Std Dev	7.449

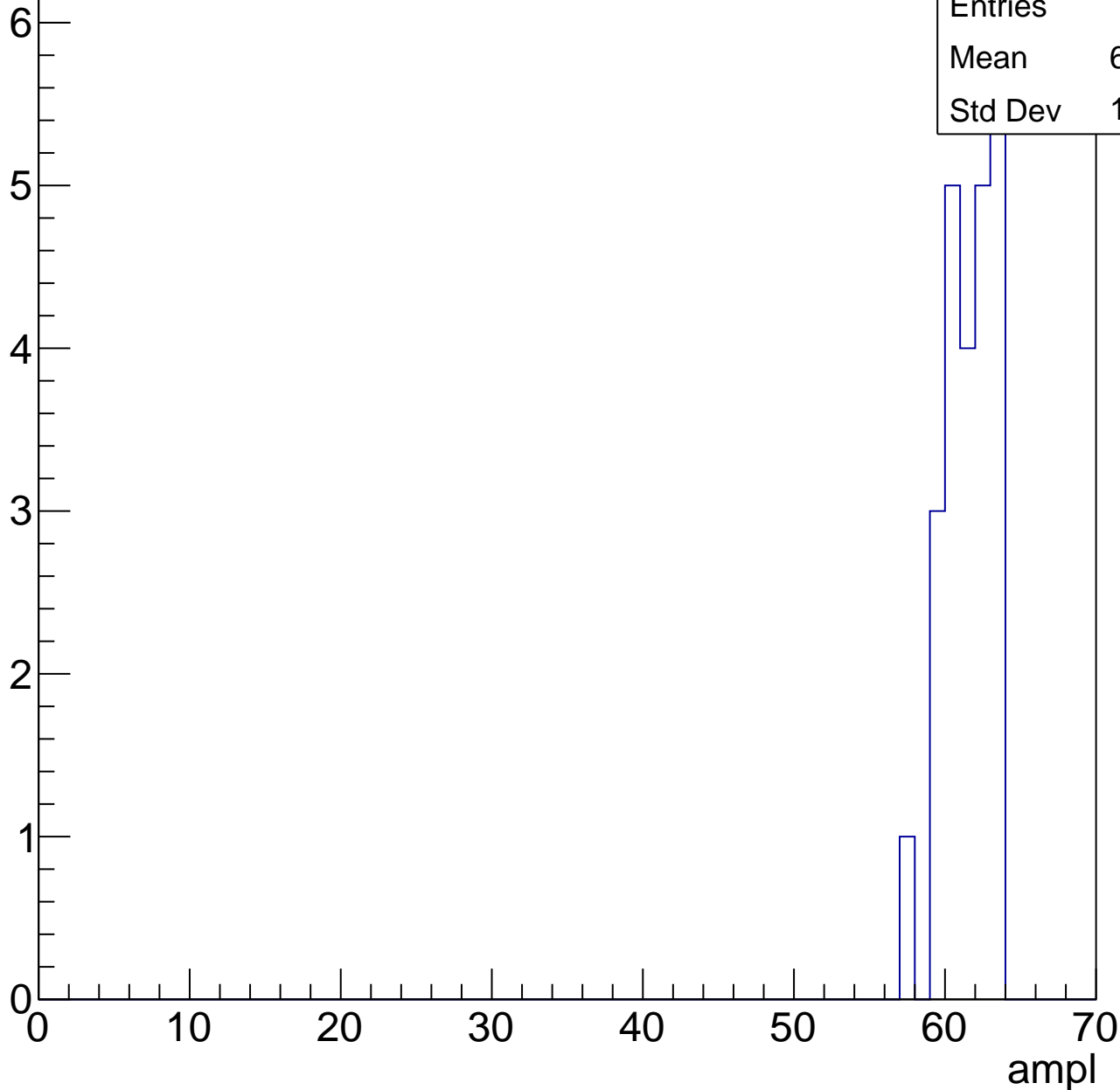


B1L103S, U12-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	61.08
Std Dev	1.605



B1L103S, U12-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch52, adc0

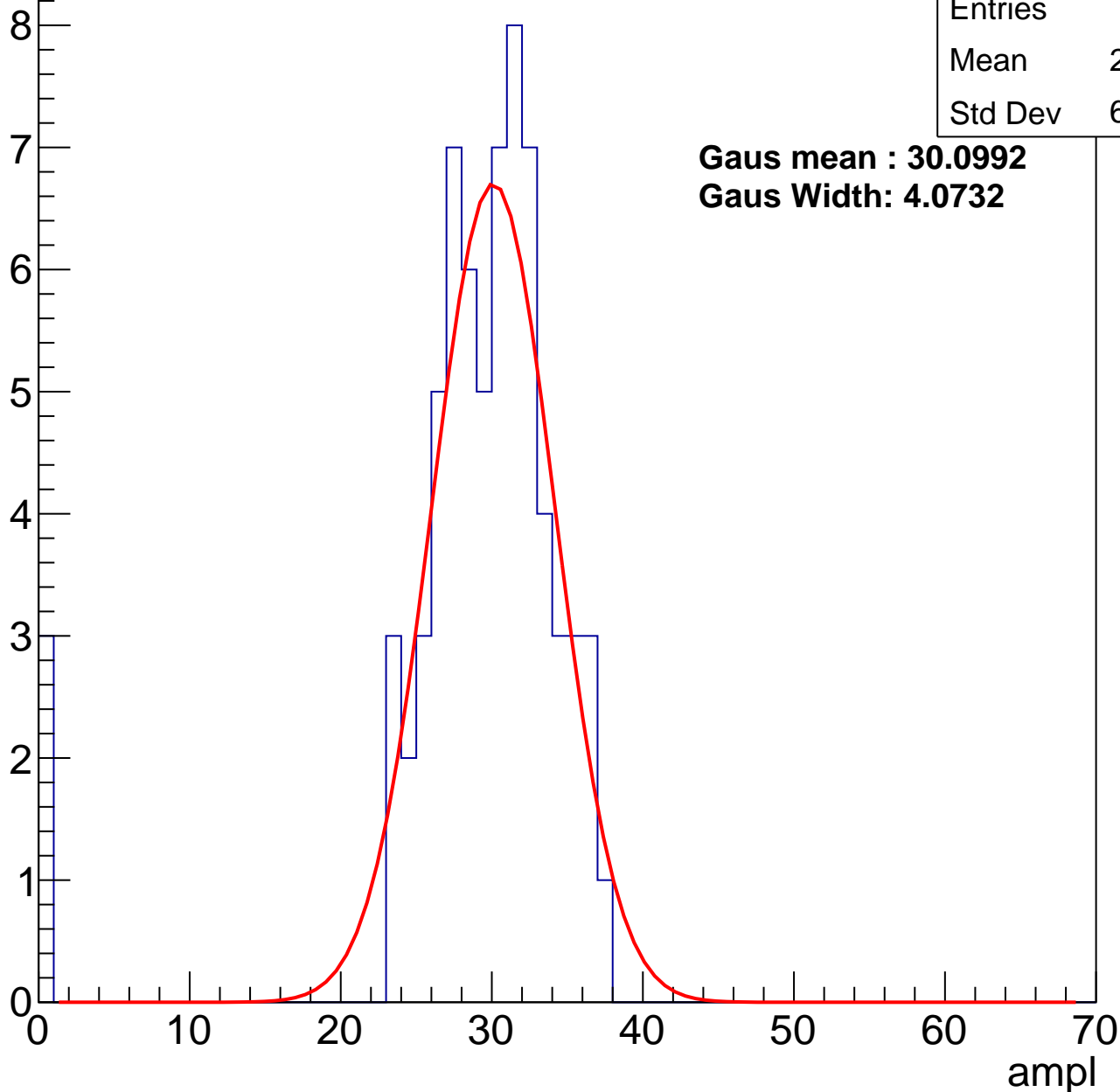
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.43
Std Dev	6.913

Gaus mean : 30.0992

Gaus Width: 4.0732



B1L103S, U12-ch52, adc1

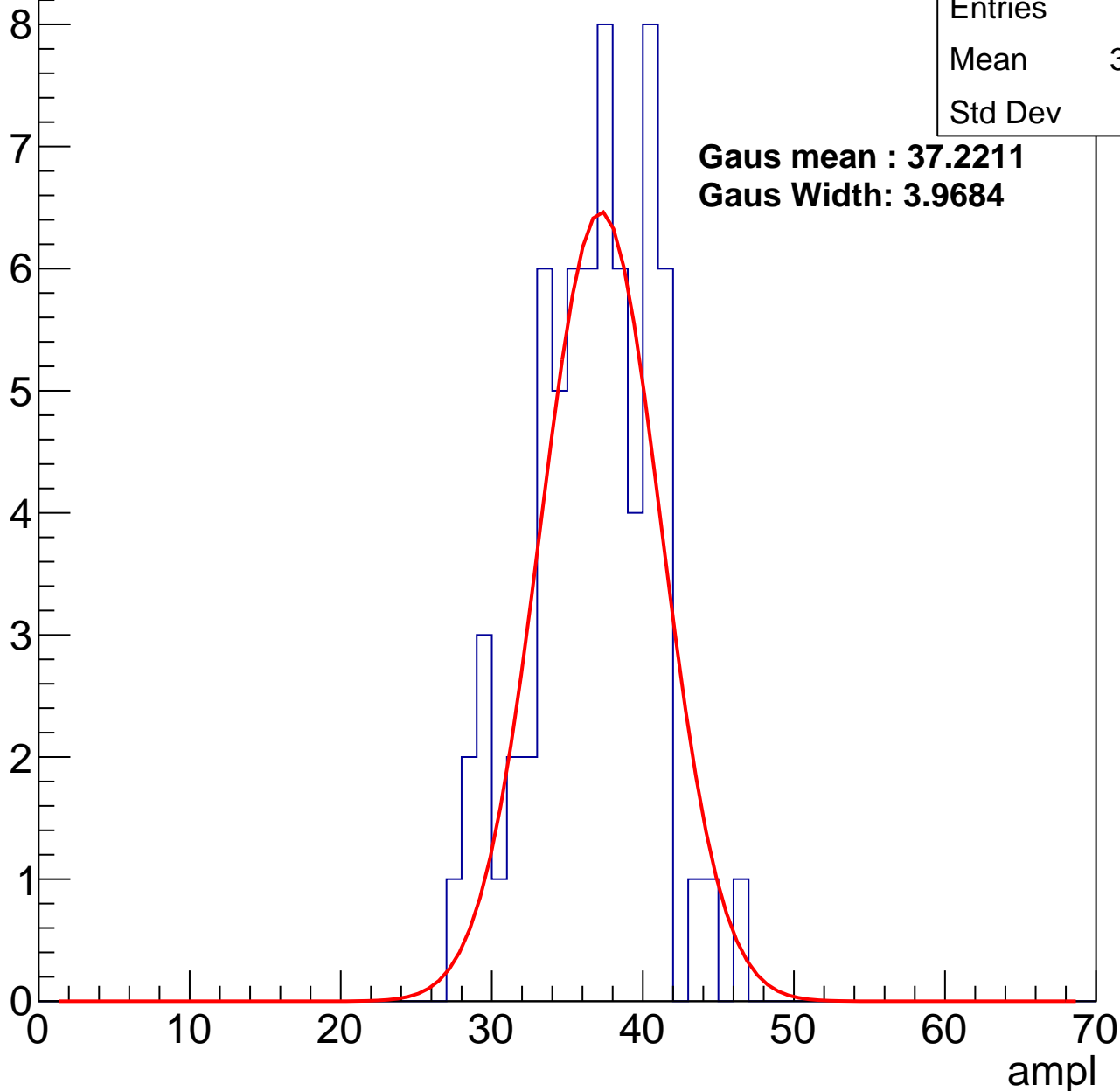
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.22
Std Dev	4.01

Gaus mean : 37.2211

Gaus Width: 3.9684



B1L103S, U12-ch52, adc2

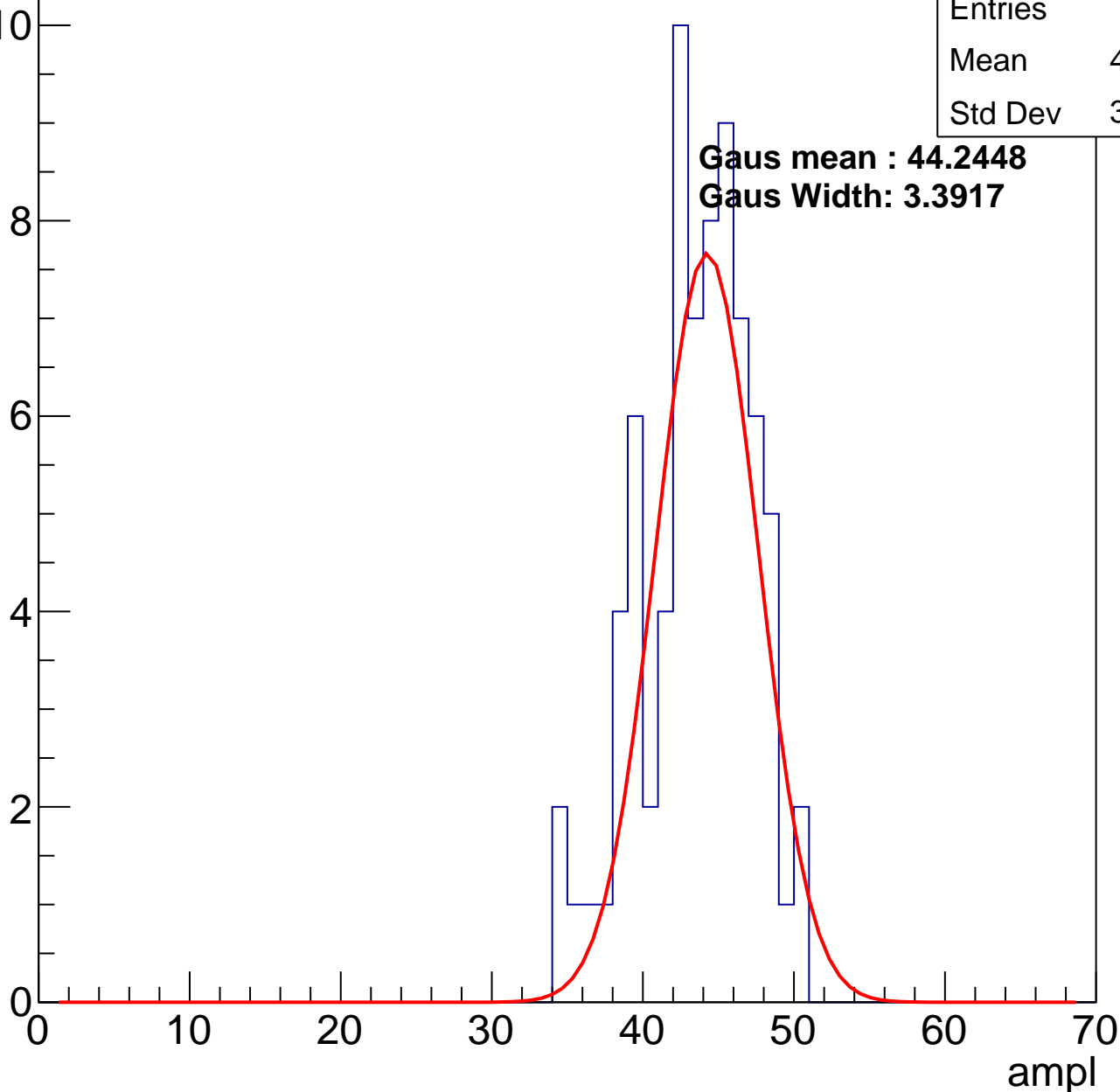
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.12
Std Dev	3.649

Gaus mean : 44.2448

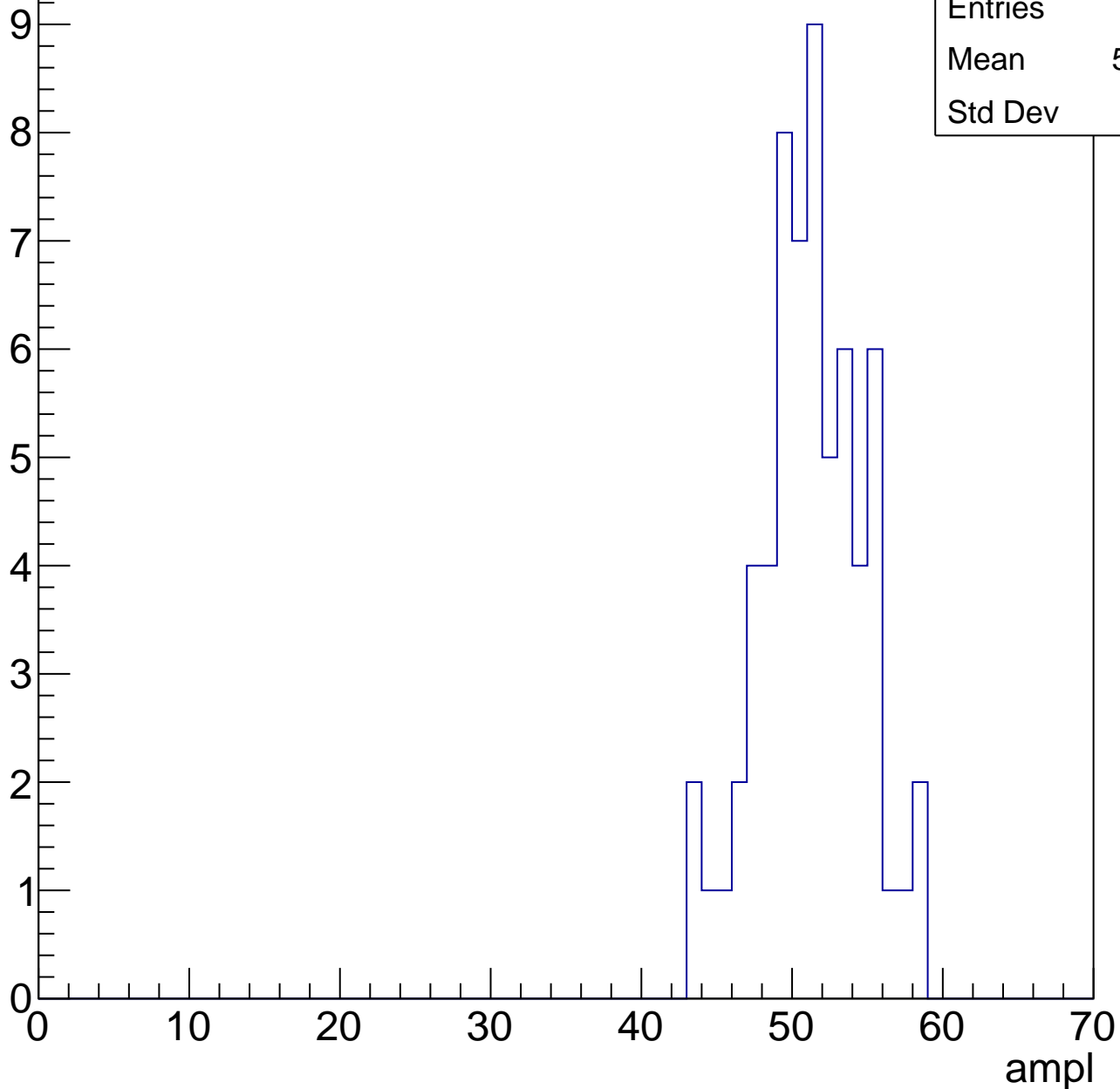
Gaus Width: 3.3917



B1L103S, U12-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



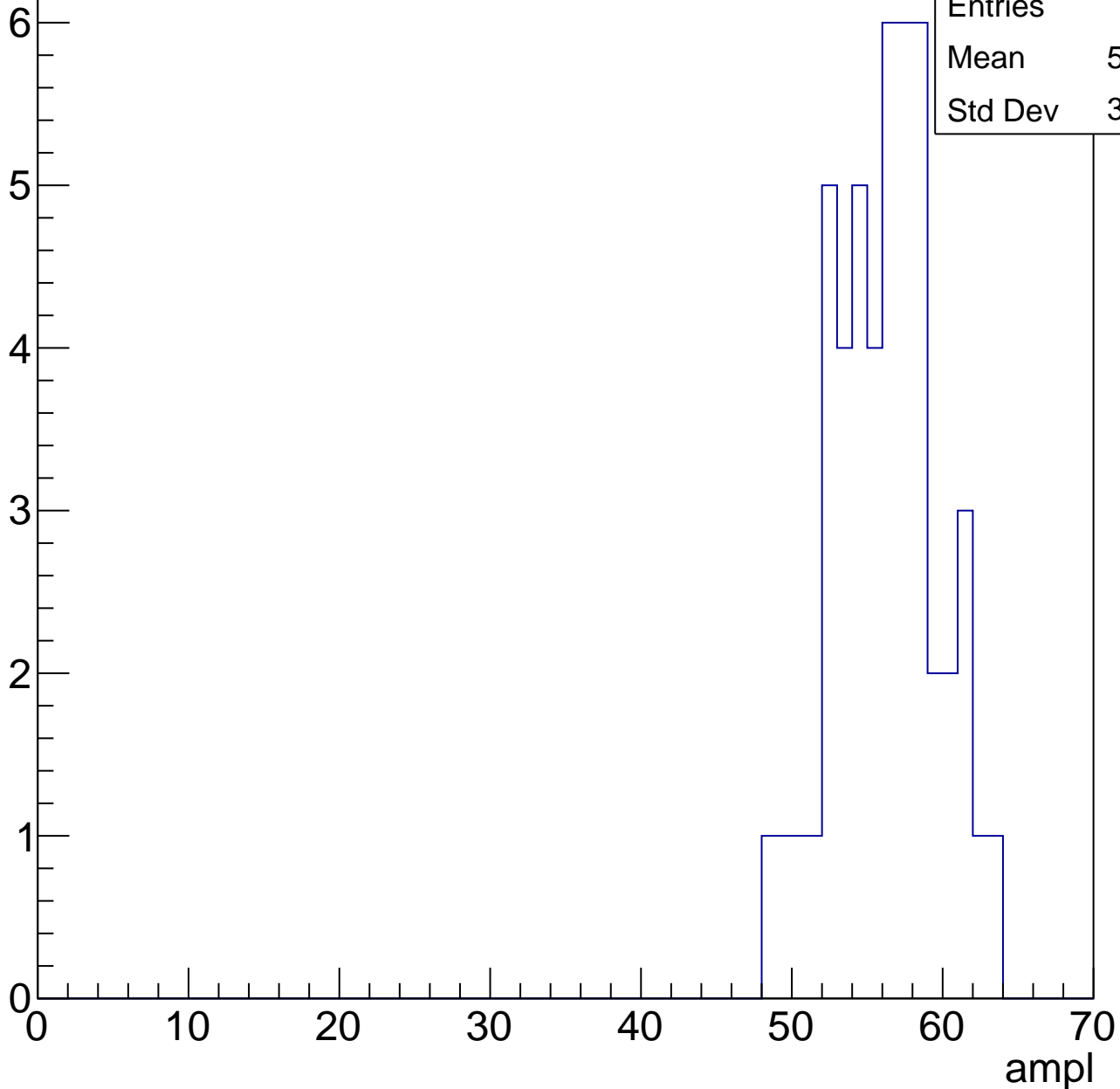
Entries	63
Mean	50.81
Std Dev	3.38

B1L103S, U12-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.76
Std Dev	3.354

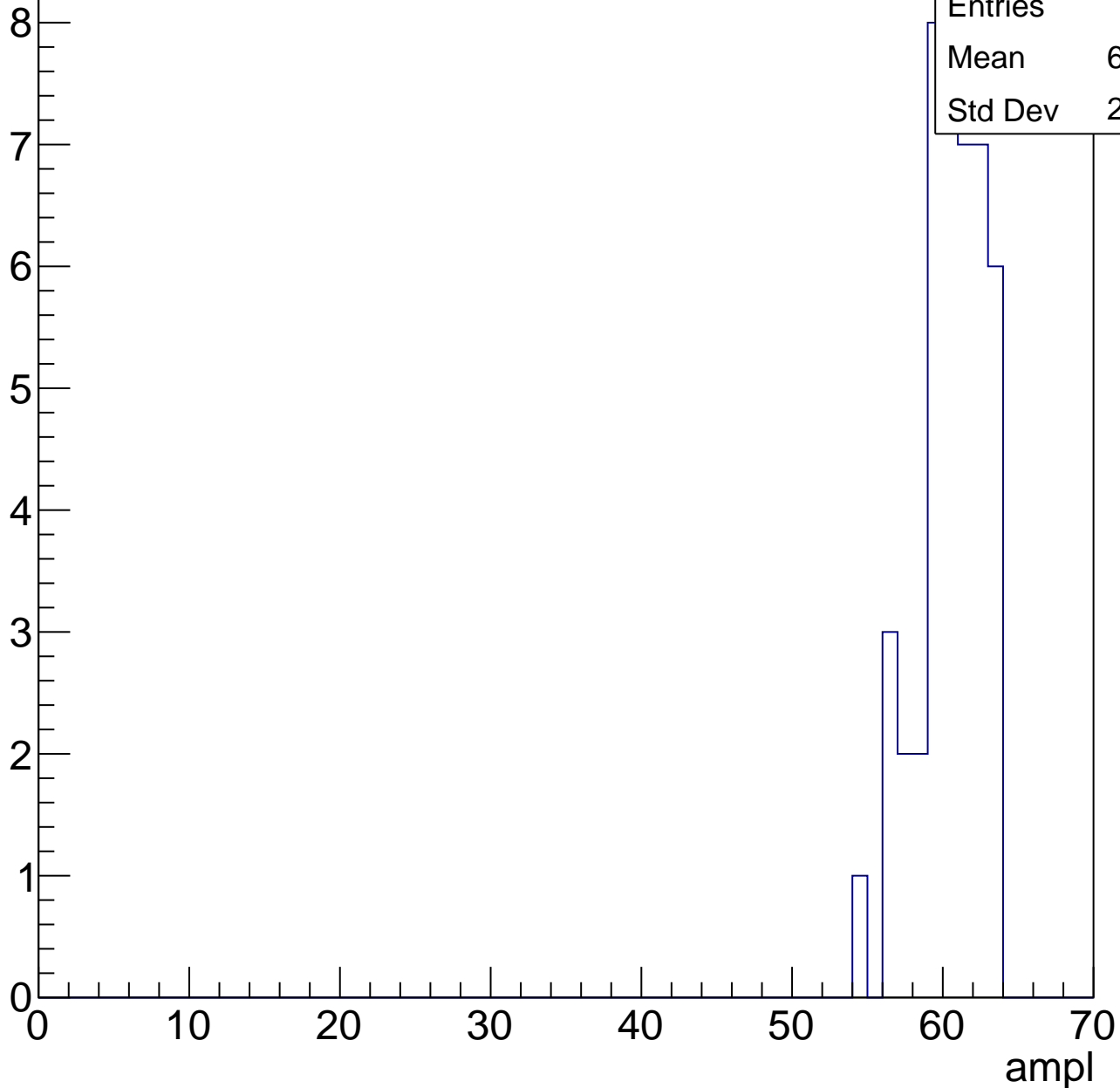


B1L103S, U12-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

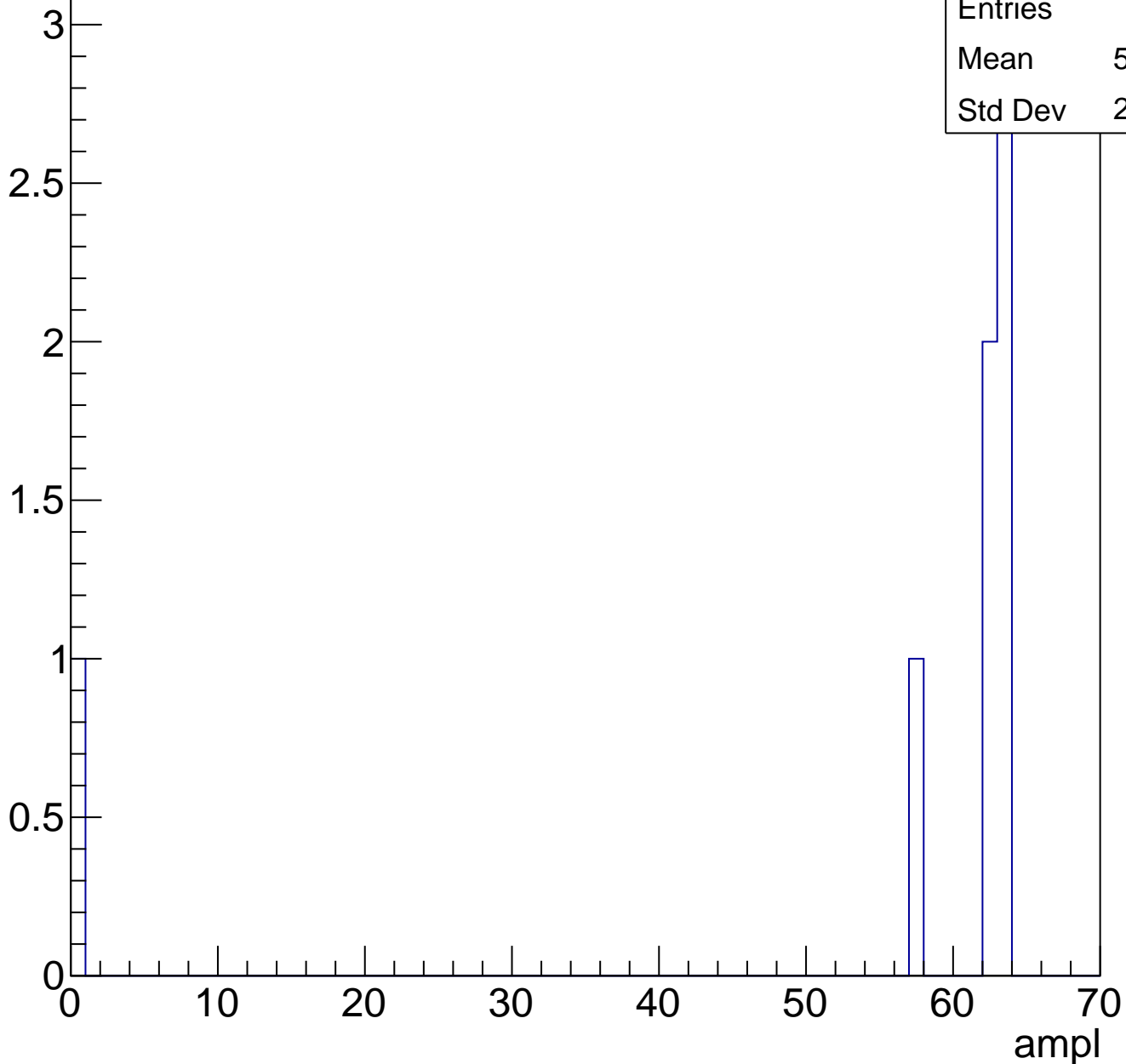
Entries	44
Mean	60.07
Std Dev	2.168



B1L103S, U12-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch53, adc0

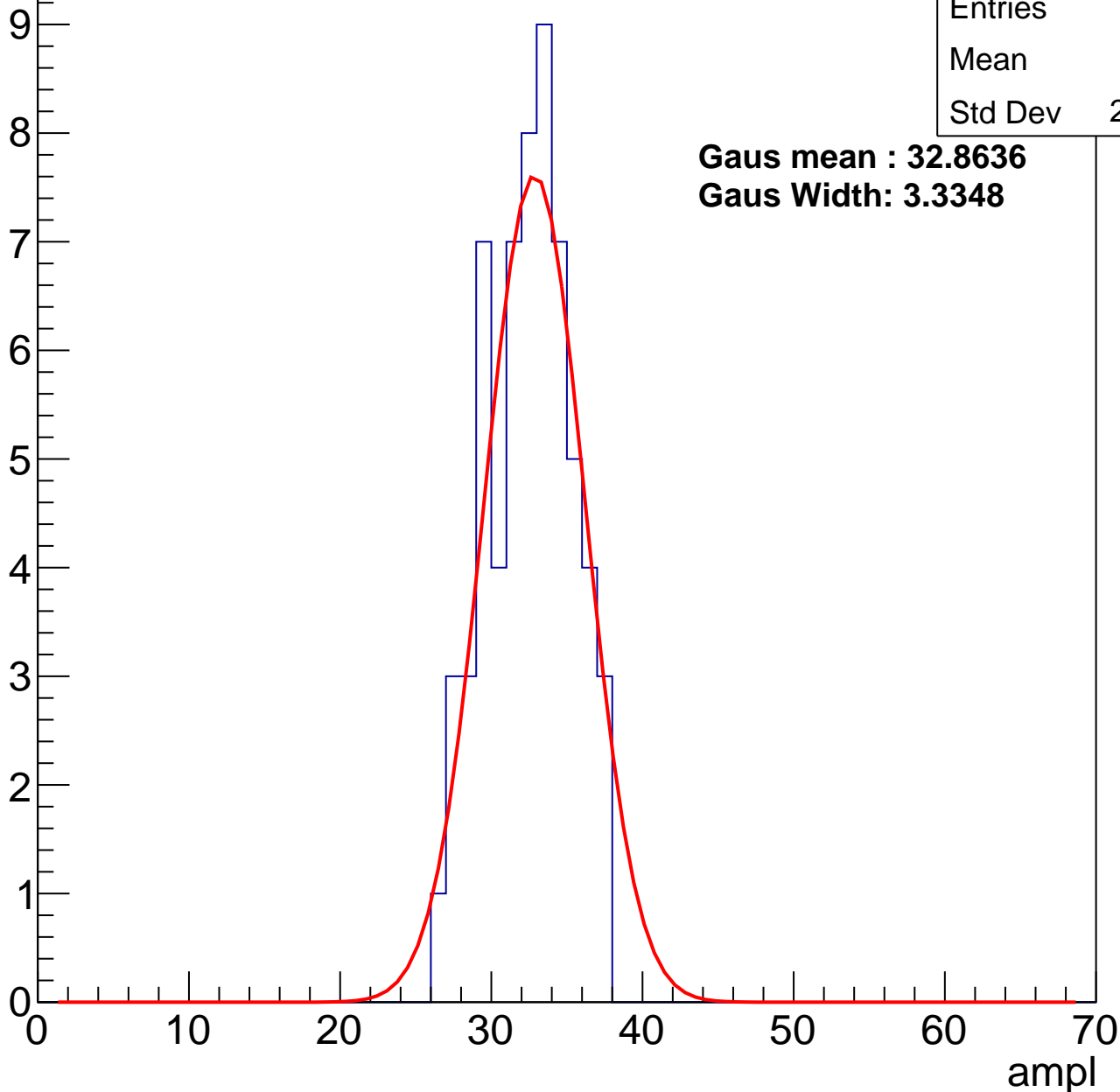
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	32
Std Dev	2.764

Gaus mean : 32.8636

Gaus Width: 3.3348



B1L103S, U12-ch53, adc1

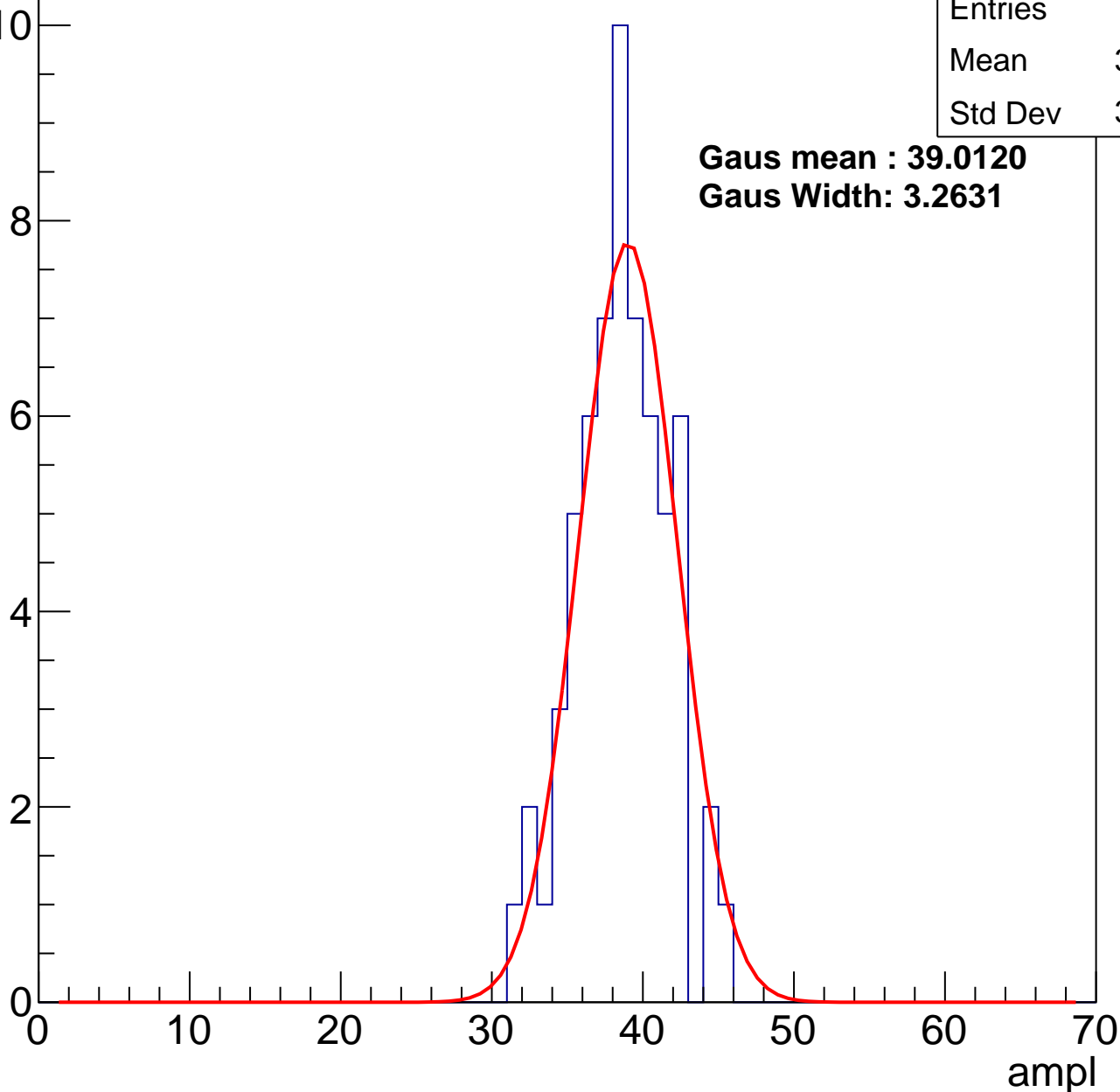
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	38.11
Std Dev	3.011

Gaus mean : 39.0120

Gaus Width: 3.2631



B1L103S, U12-ch53, adc2

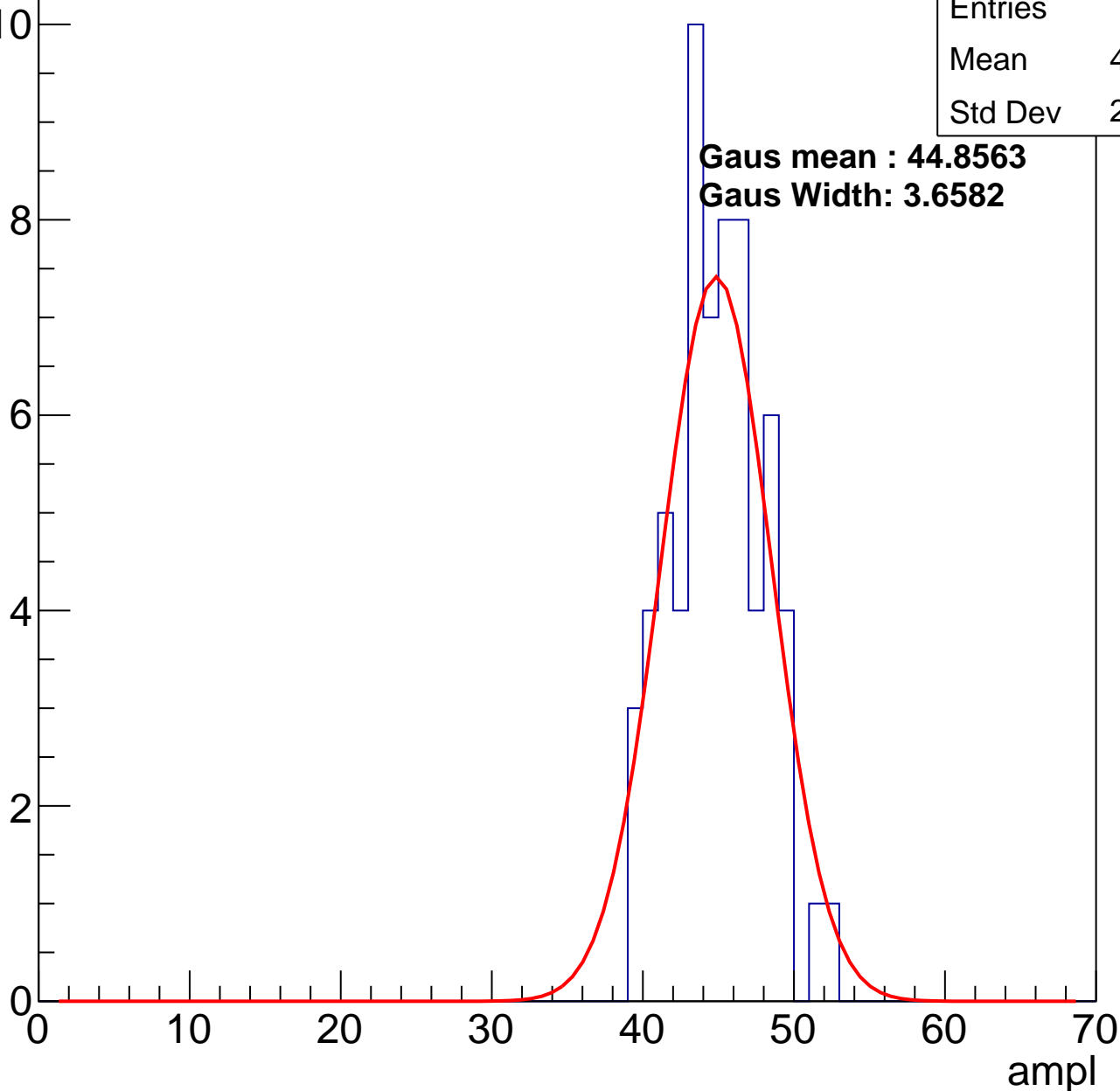
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.48
Std Dev	2.988

Gaus mean : 44.8563

Gaus Width: 3.6582

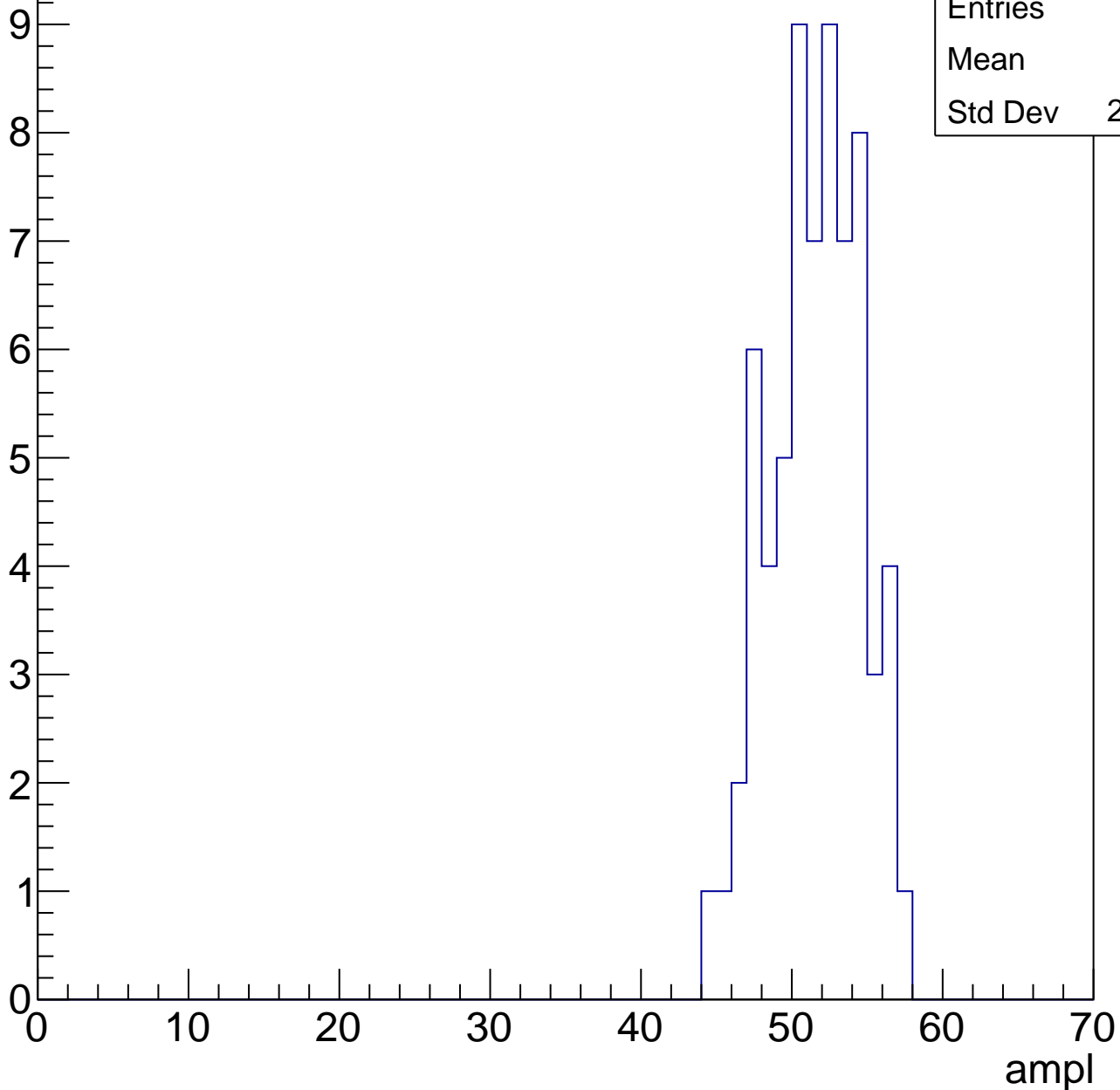


B1L103S, U12-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	51.1
Std Dev	2.963

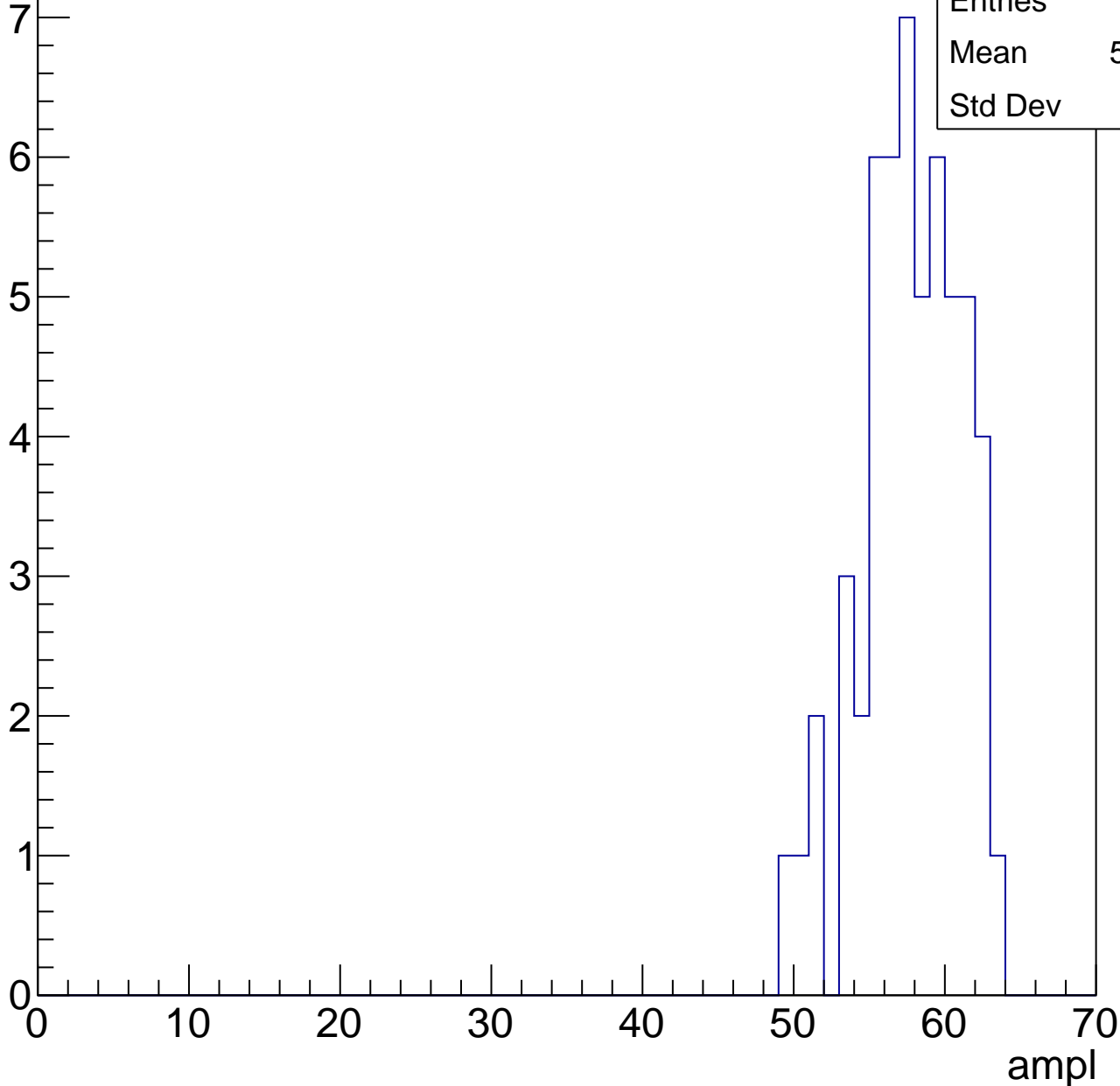


B1L103S, U12-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	57.28
Std Dev	3.24

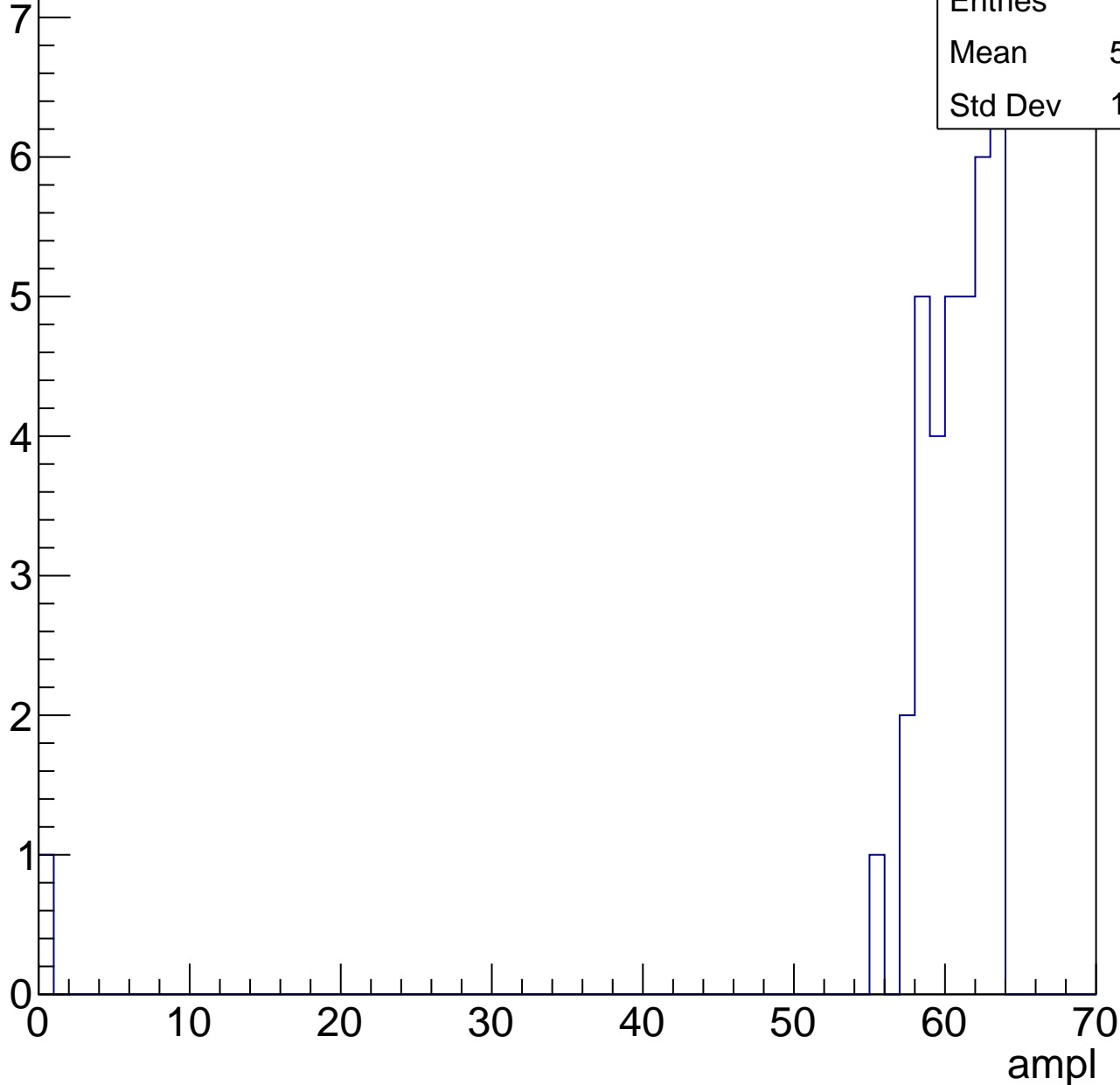


B1L103S, U12-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.69
Std Dev	10.13



B1L103S, U12-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

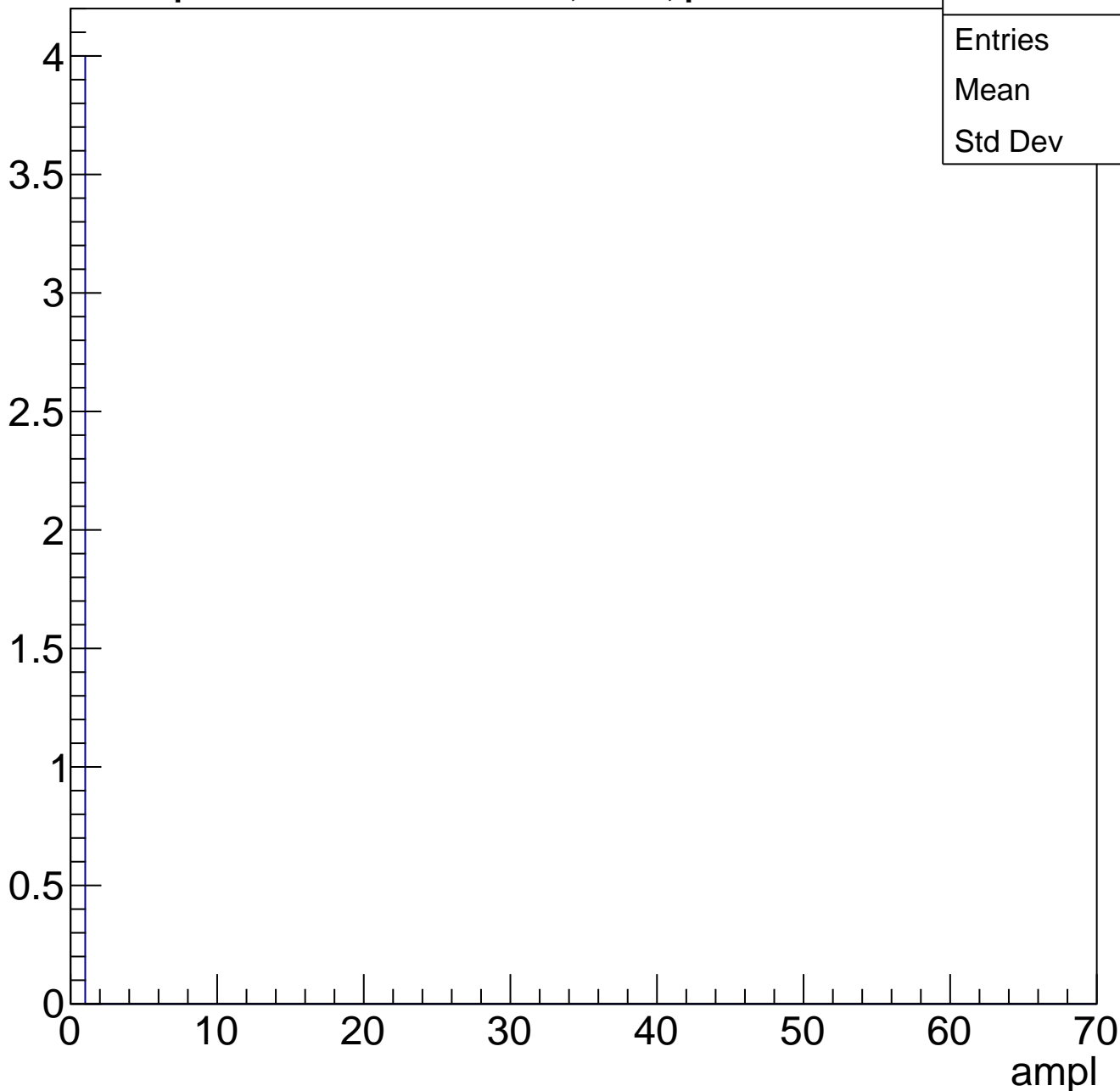


Entries	2
Mean	63
Std Dev	0

B1L103S, U12-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L103S, U12-ch54, adc0

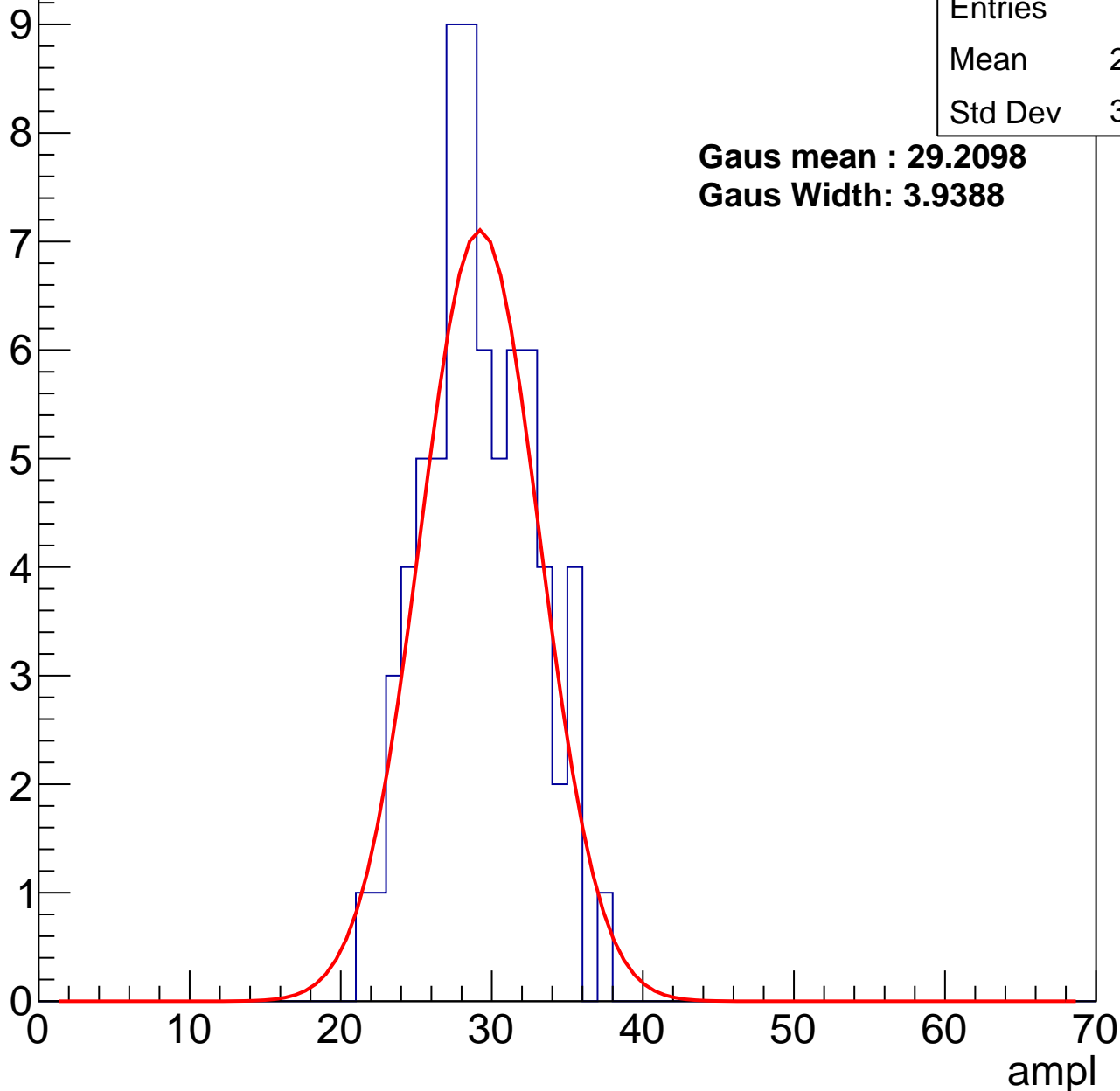
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.69
Std Dev	3.539

Gaus mean : 29.2098

Gaus Width: 3.9388



B1L103S, U12-ch54, adc1

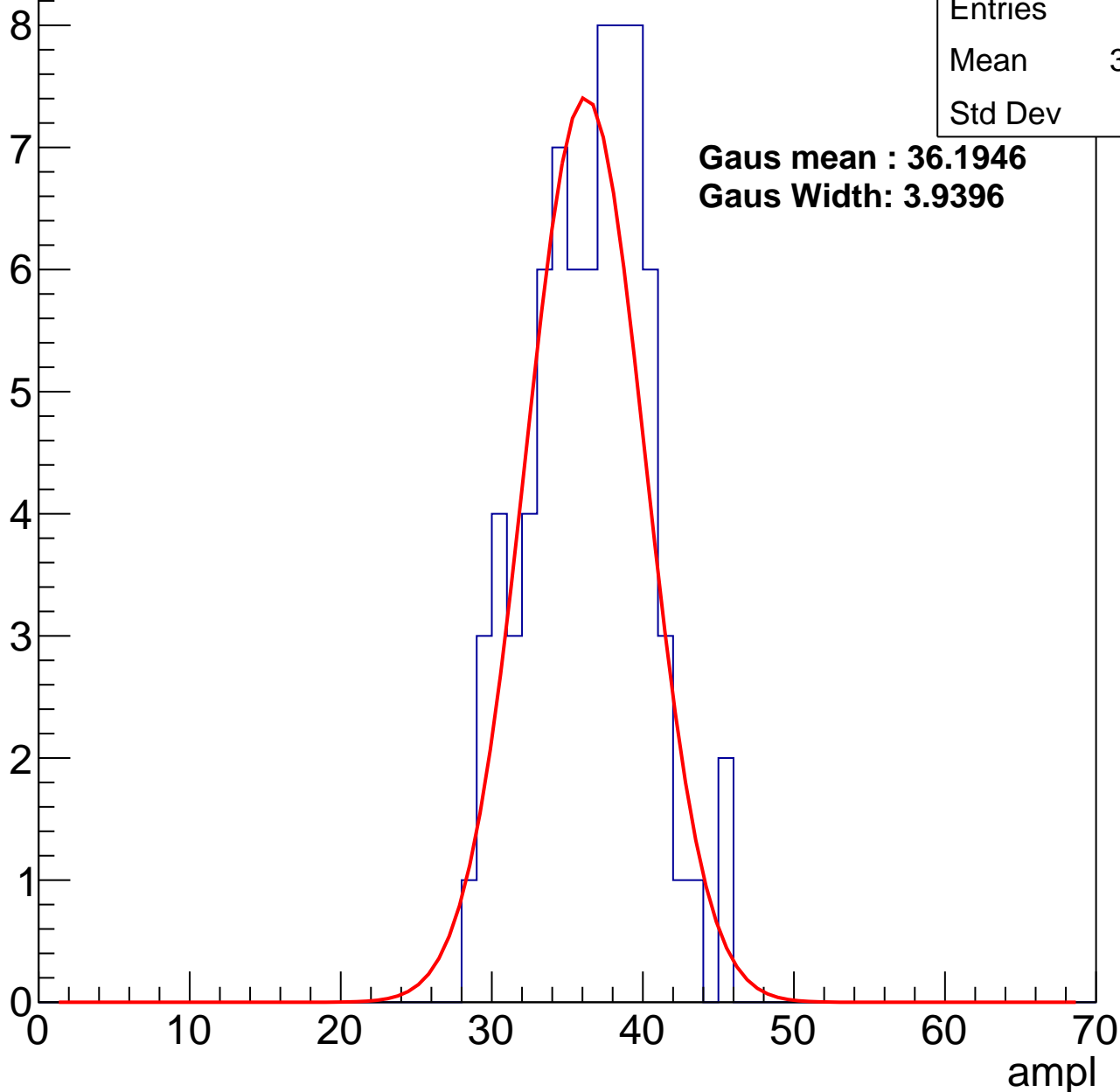
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	35.95
Std Dev	3.8

Gaus mean : 36.1946

Gaus Width: 3.9396



B1L103S, U12-ch54, adc2

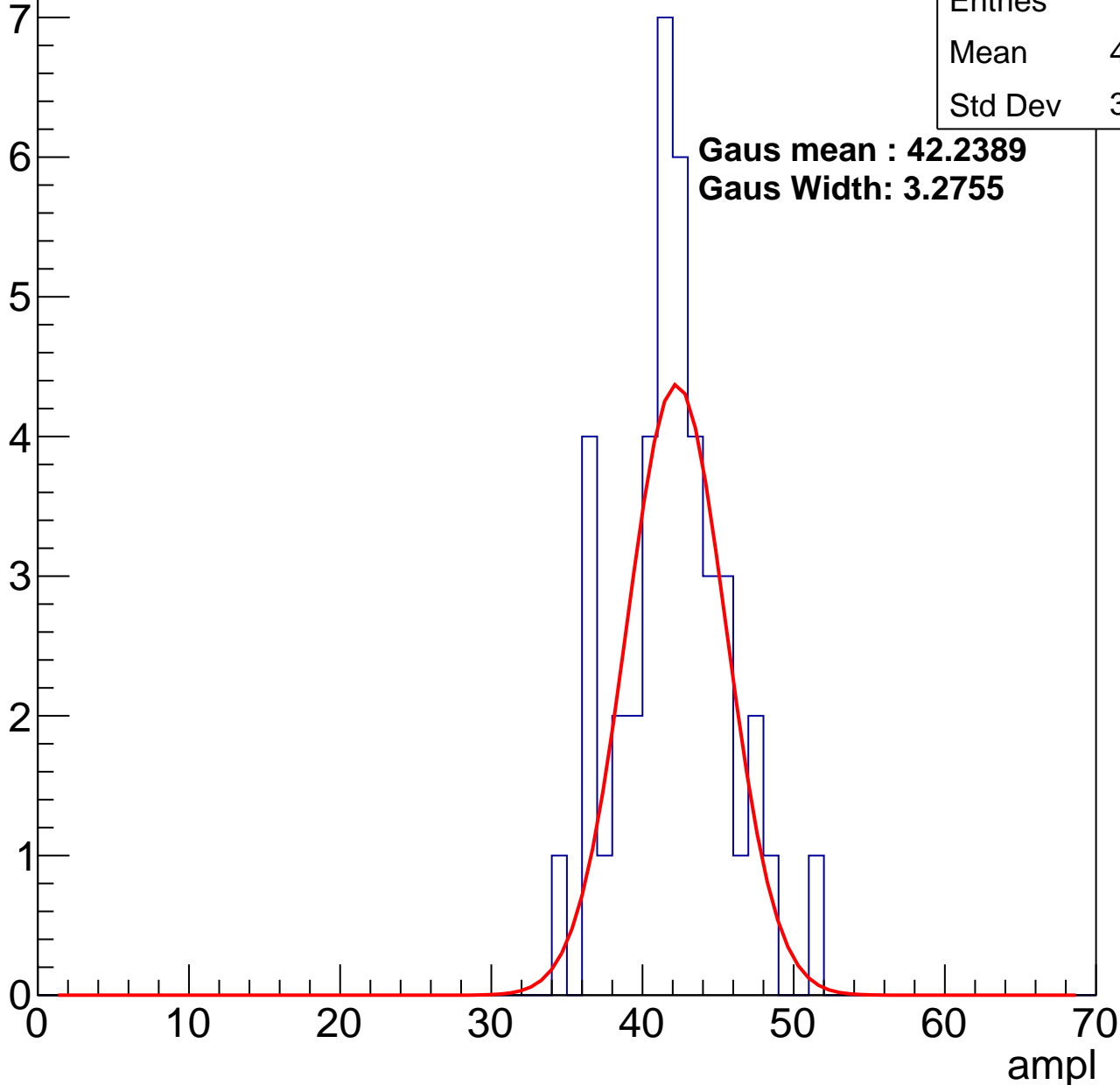
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	41.57
Std Dev	3.533

Gaus mean : 42.2389

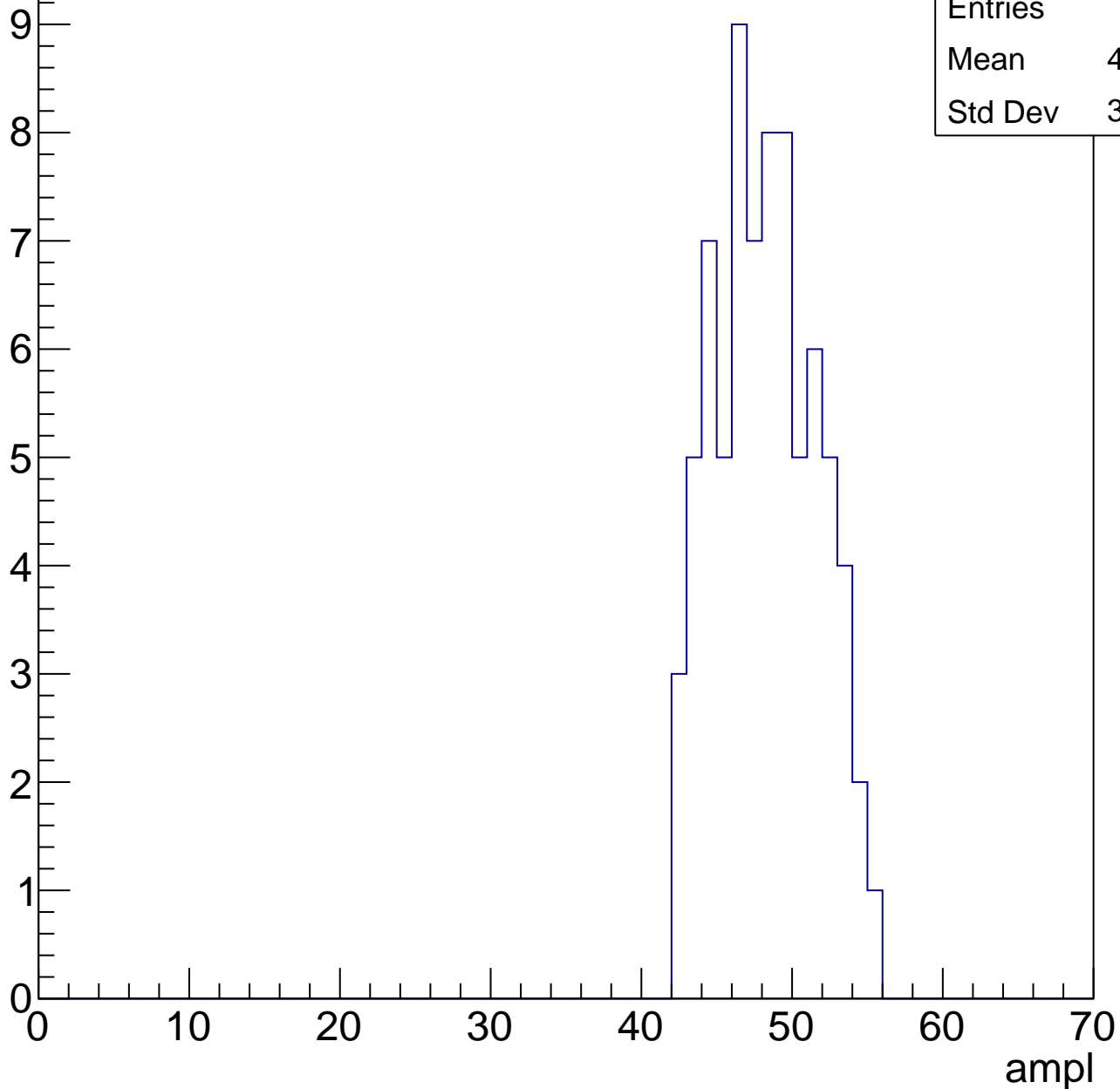
Gaus Width: 3.2755



B1L103S, U12-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



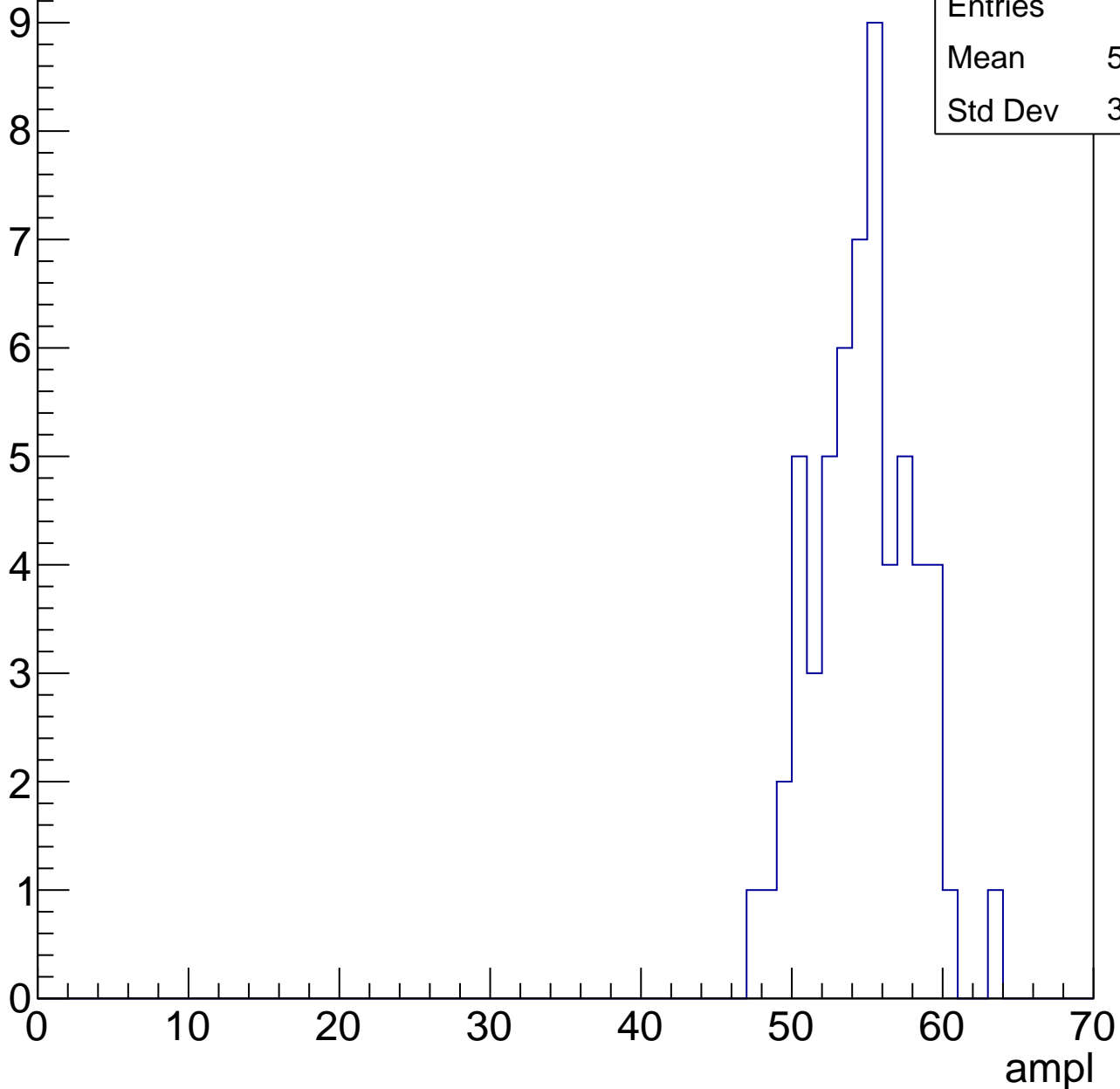
Entries	75
Mean	47.79
Std Dev	3.292

B1L103S, U12-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

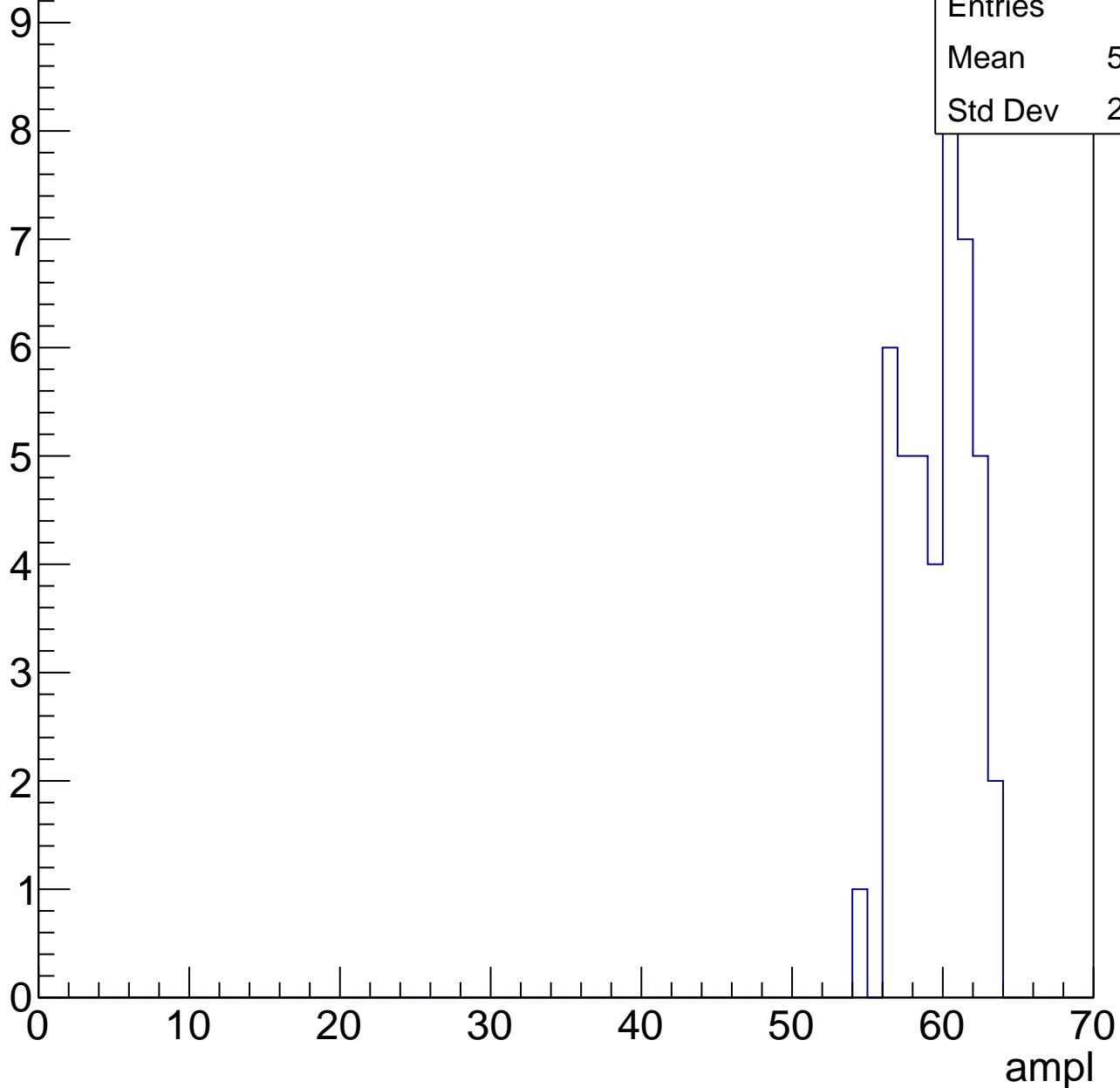
Entries	58
Mean	54.26
Std Dev	3.257



B1L103S, U12-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

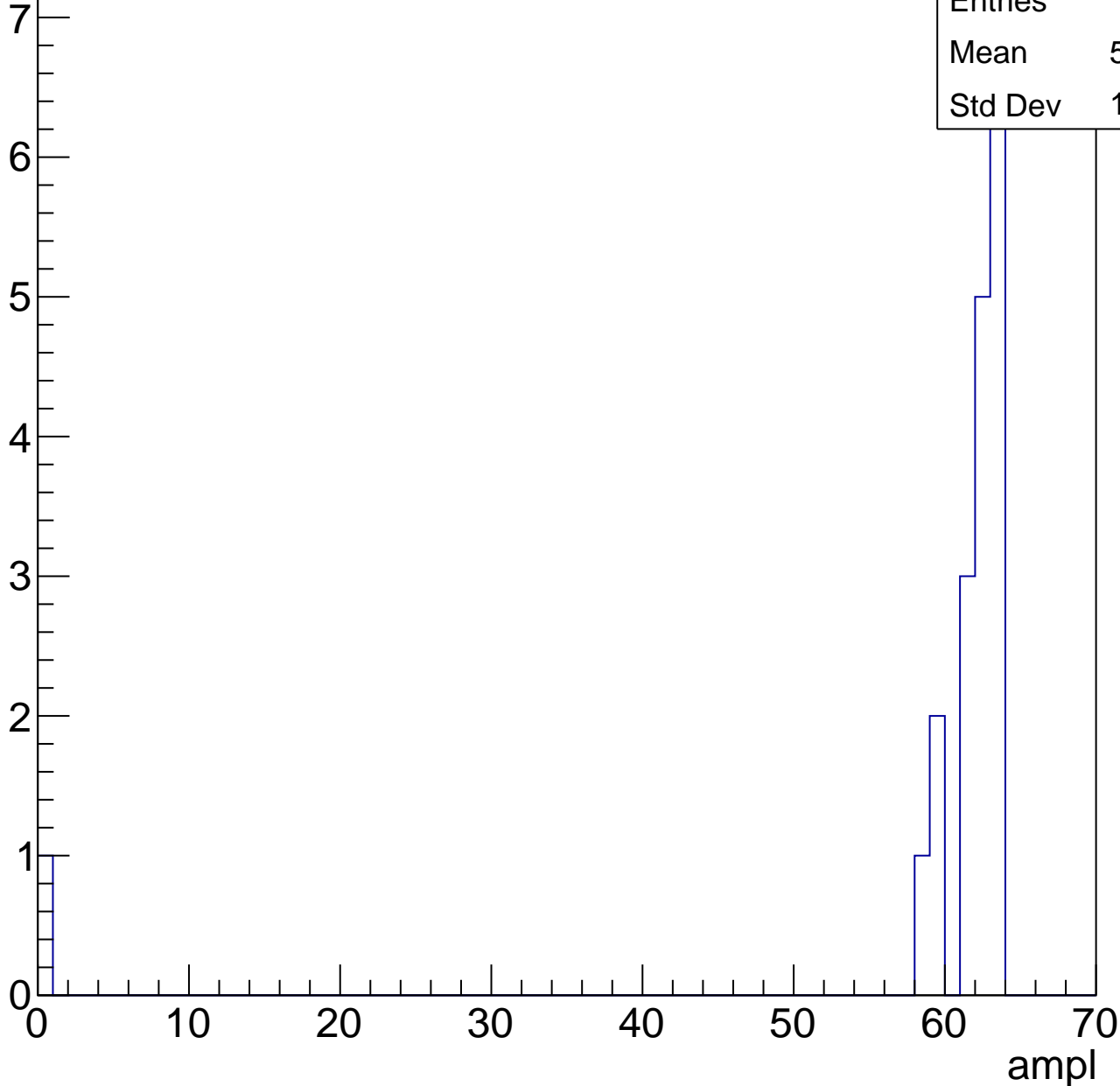


B1L103S, U12-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

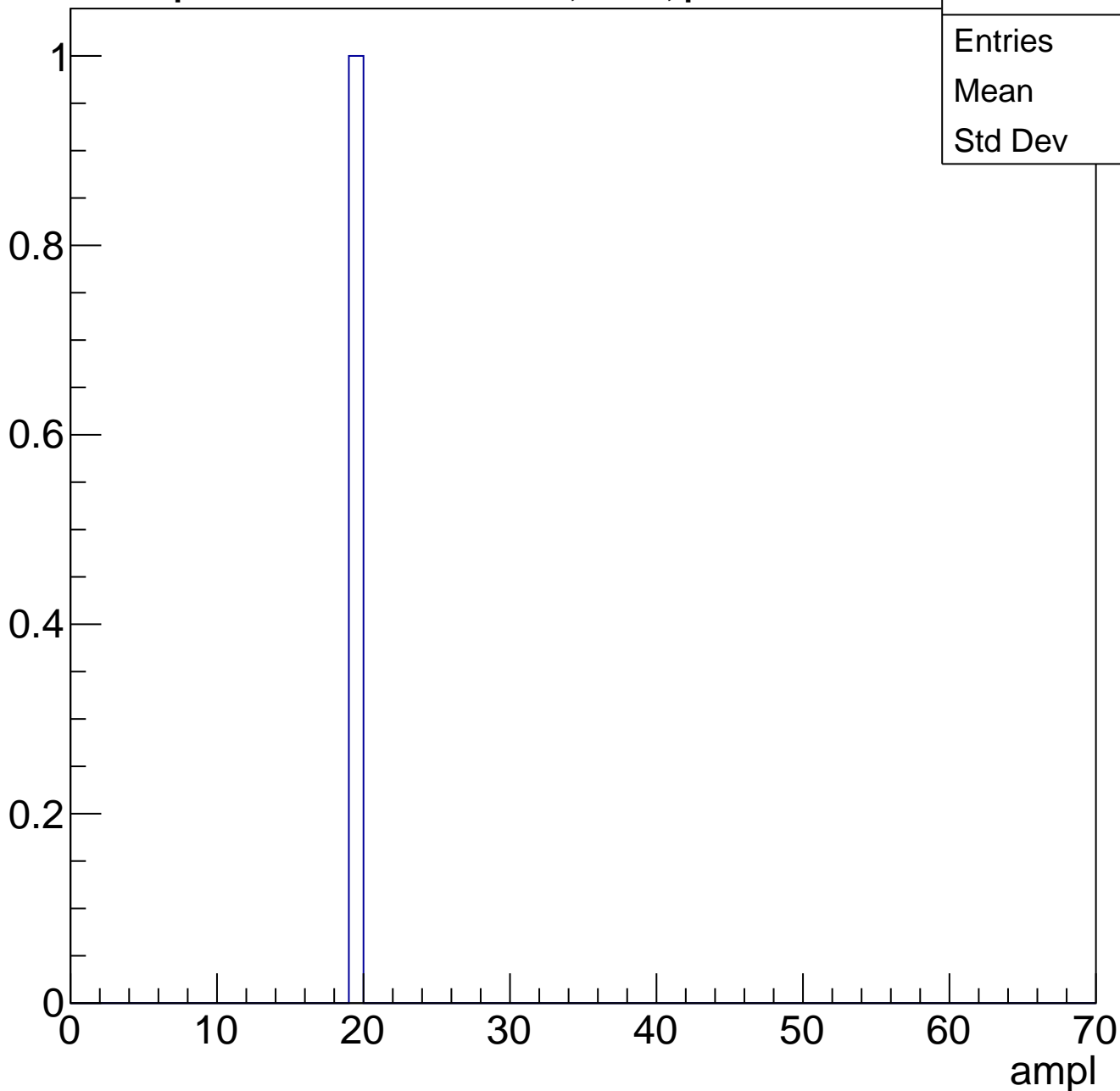
Entries	19
Mean	58.42
Std Dev	13.85



B1L103S, U12-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	19
Std Dev	0

B1L103S, U12-ch55, adc0

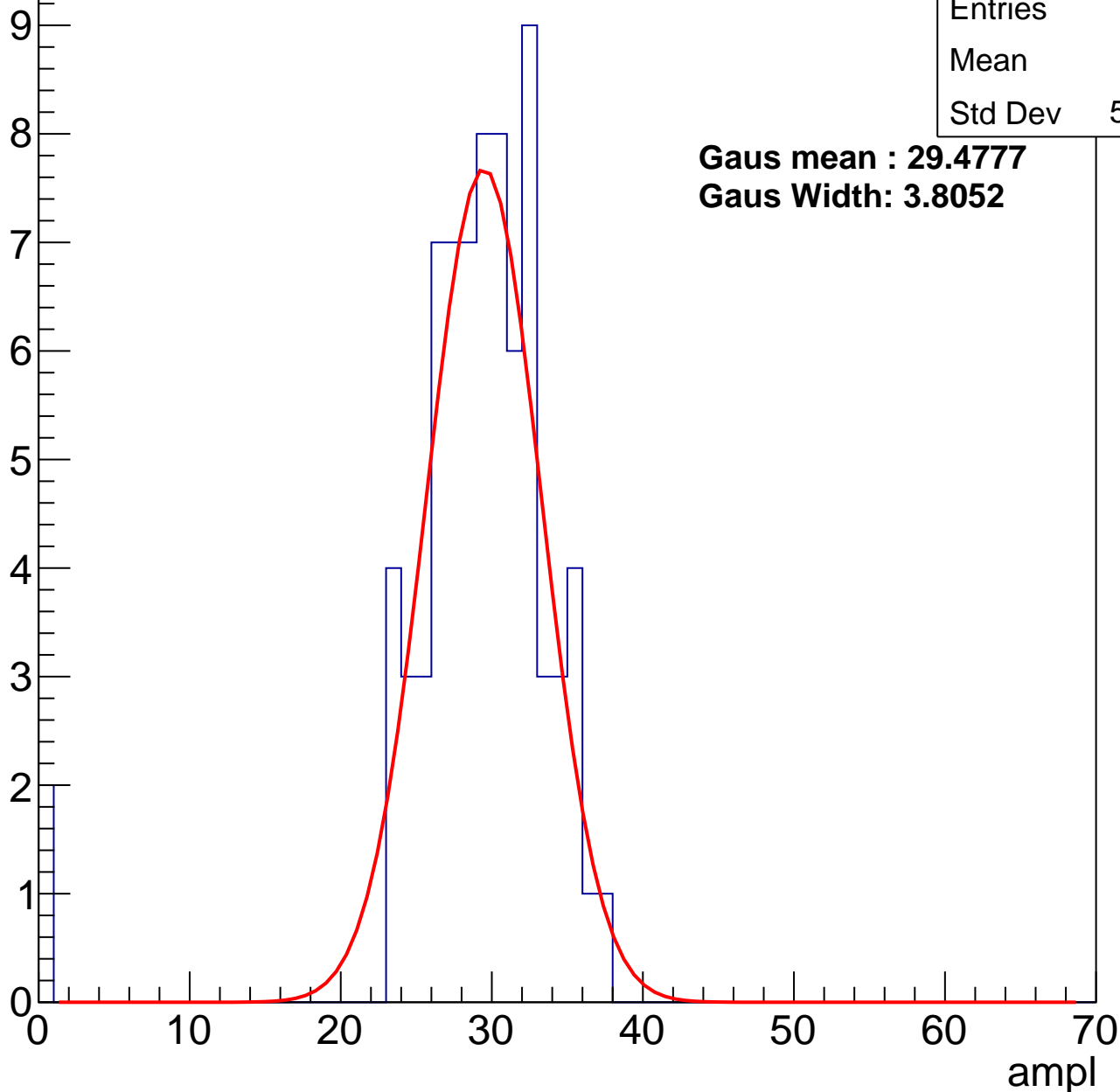
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.5
Std Dev	5.759

Gaus mean : 29.4777

Gaus Width: 3.8052



B1L103S, U12-ch55, adc1

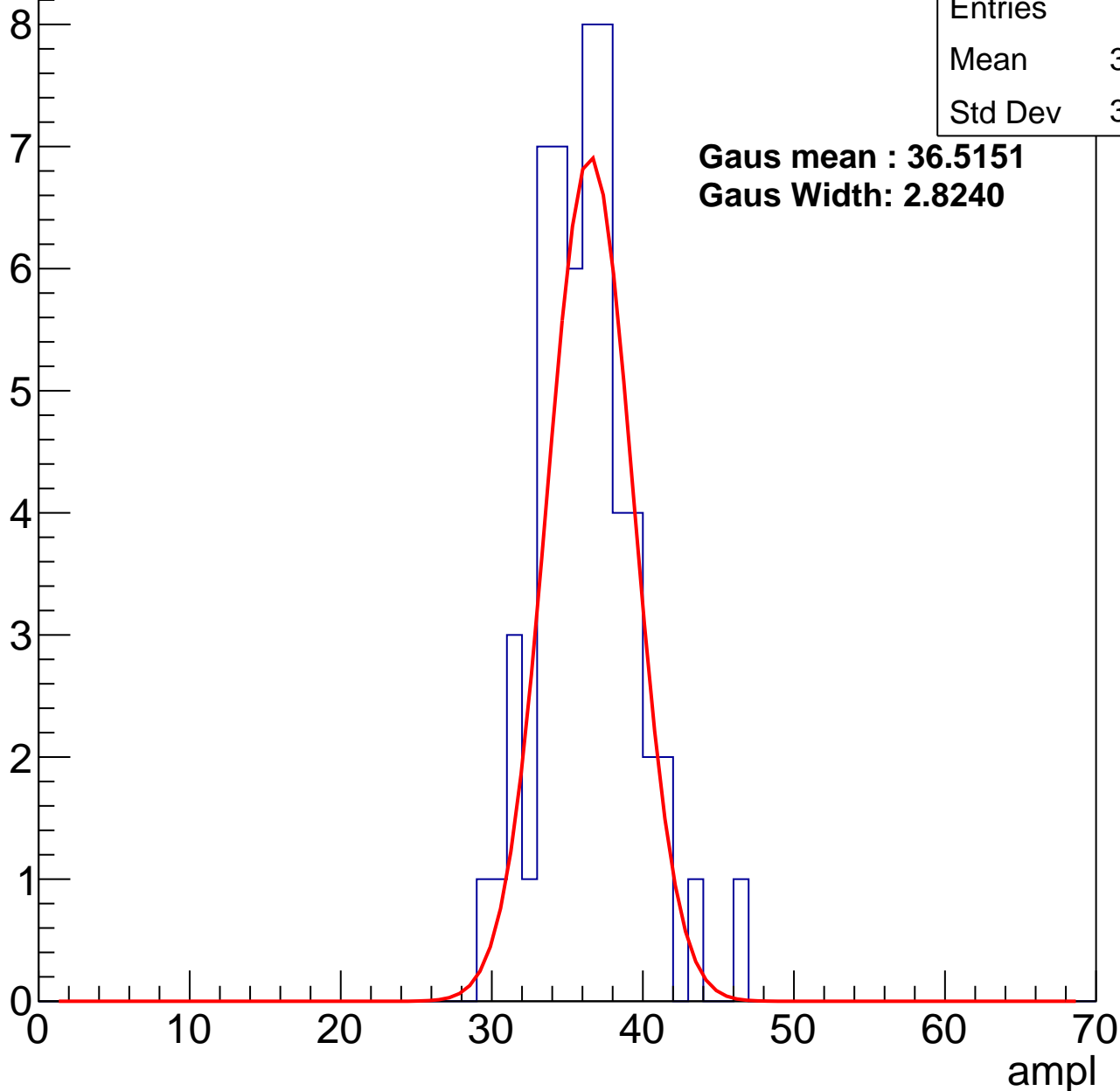
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	35.82
Std Dev	3.169

Gaus mean : 36.5151

Gaus Width: 2.8240



B1L103S, U12-ch55, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

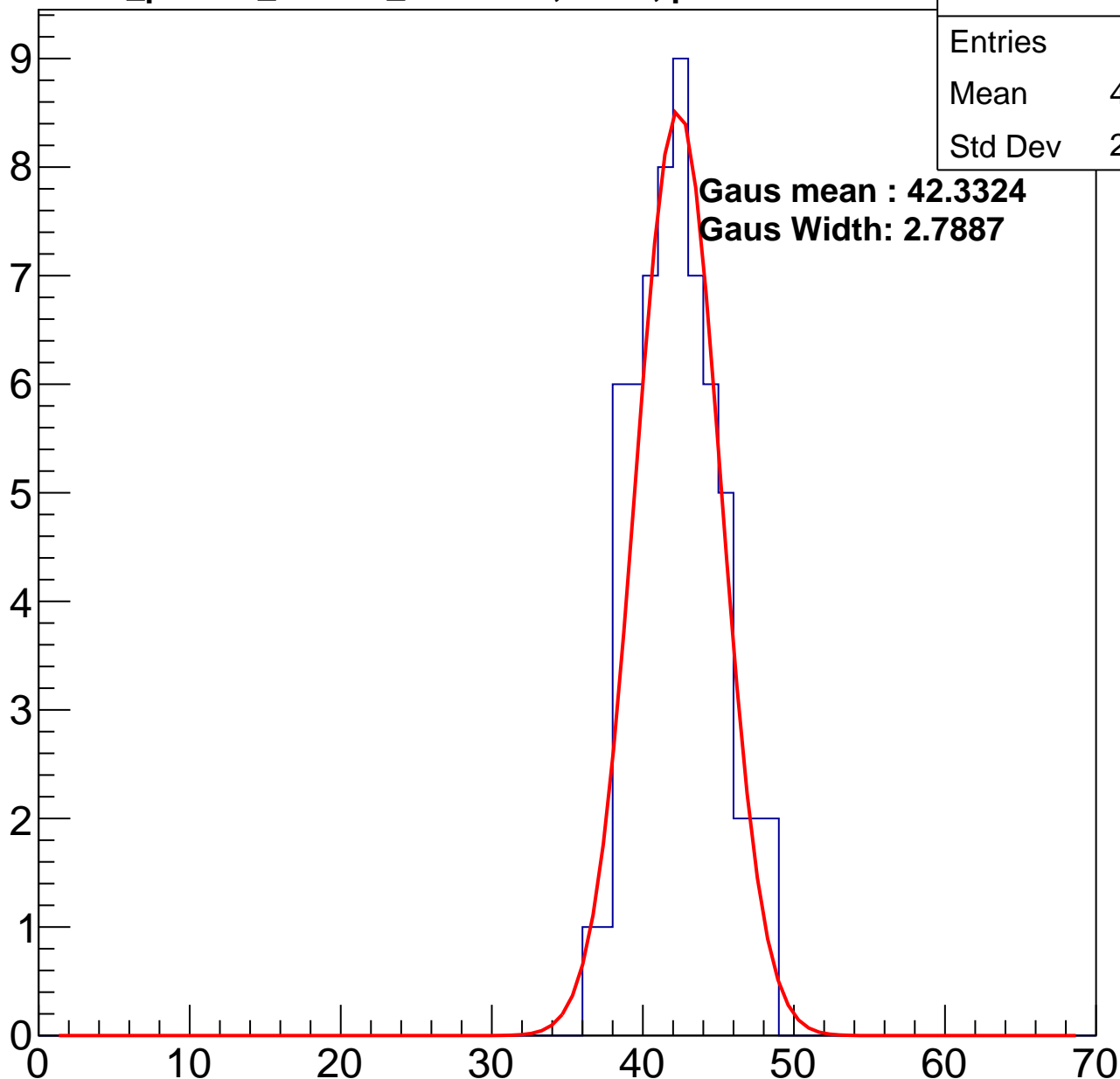
9
8
7
6
5
4
3
2
1
0

Entries	62
Mean	41.82
Std Dev	2.768

Gaus mean : 42.3324

Gaus Width: 2.7887

ampl

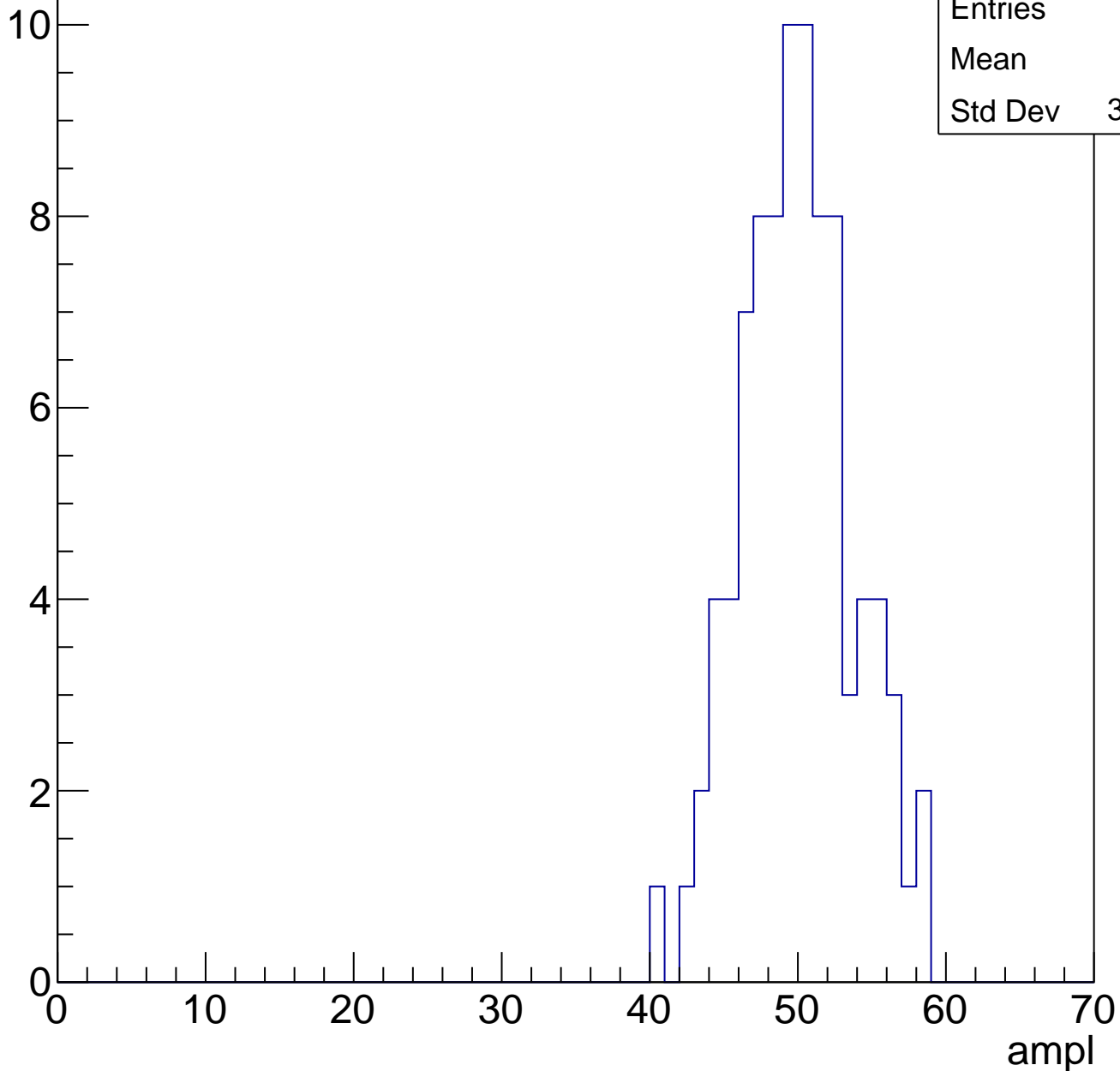


B1L103S, U12-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	49.5
Std Dev	3.733

Entry

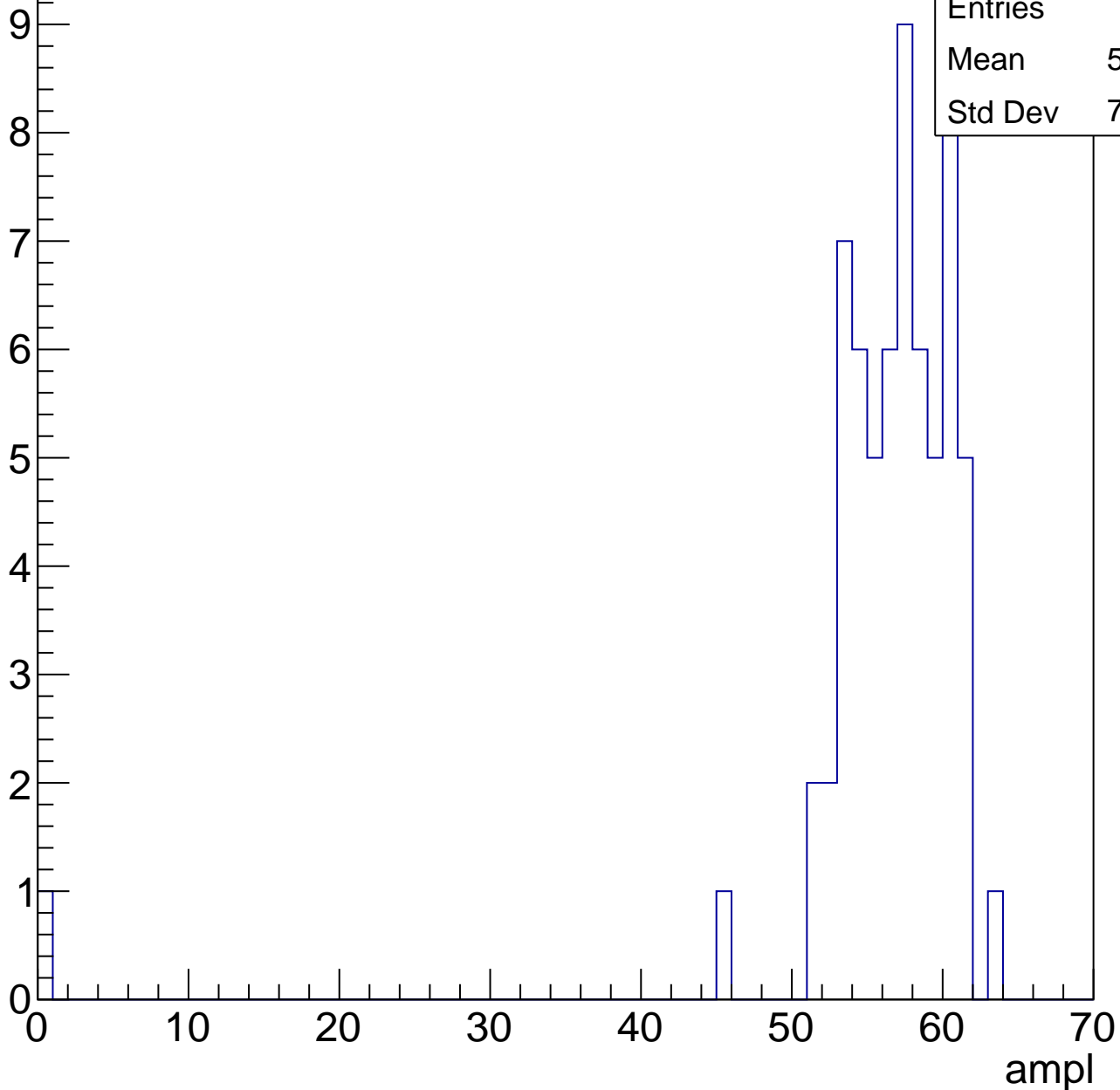


B1L103S, U12-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	55.64
Std Dev	7.708

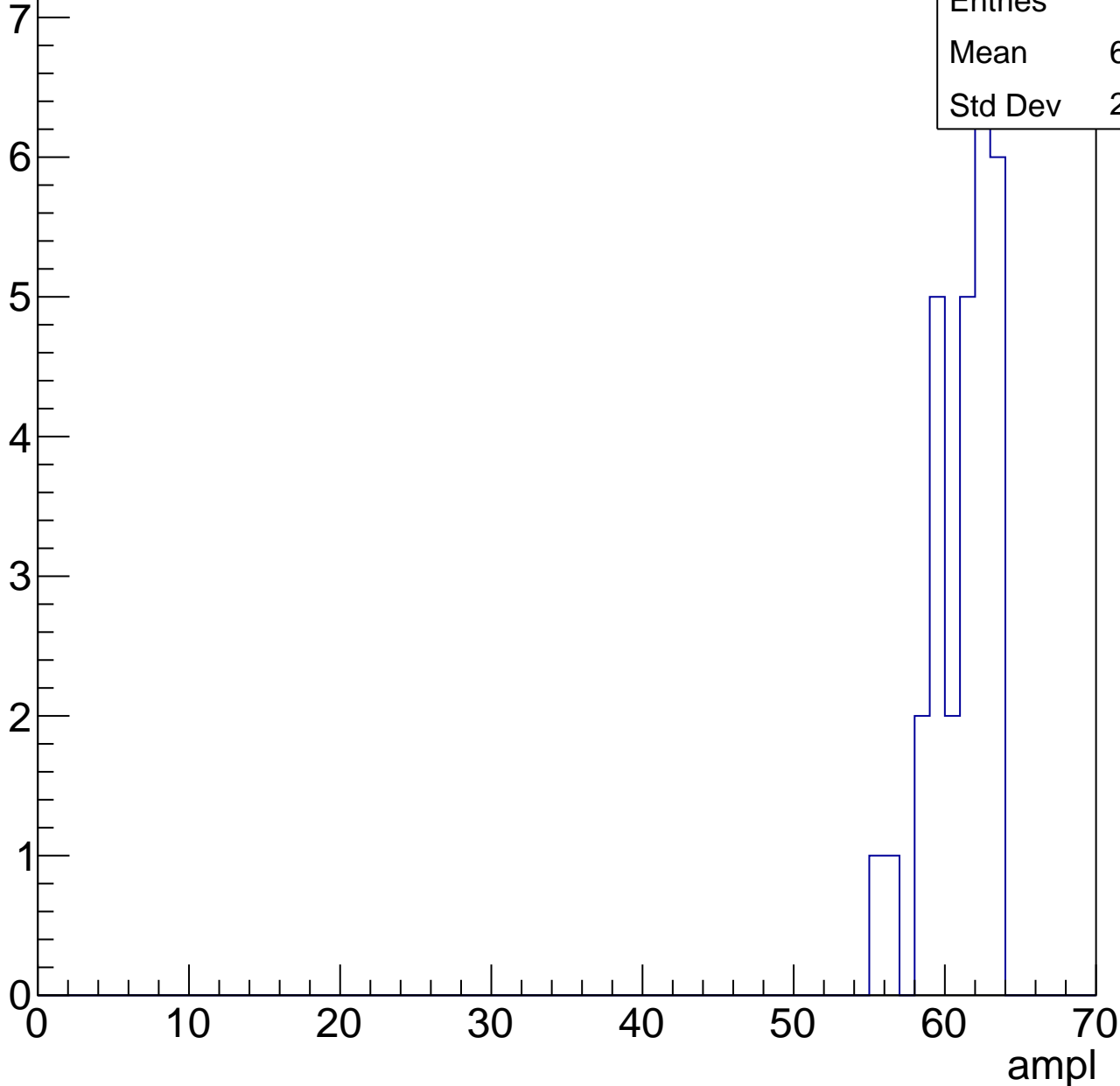


B1L103S, U12-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	60.66
Std Dev	2.105



B1L103S, U12-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch56, adc0

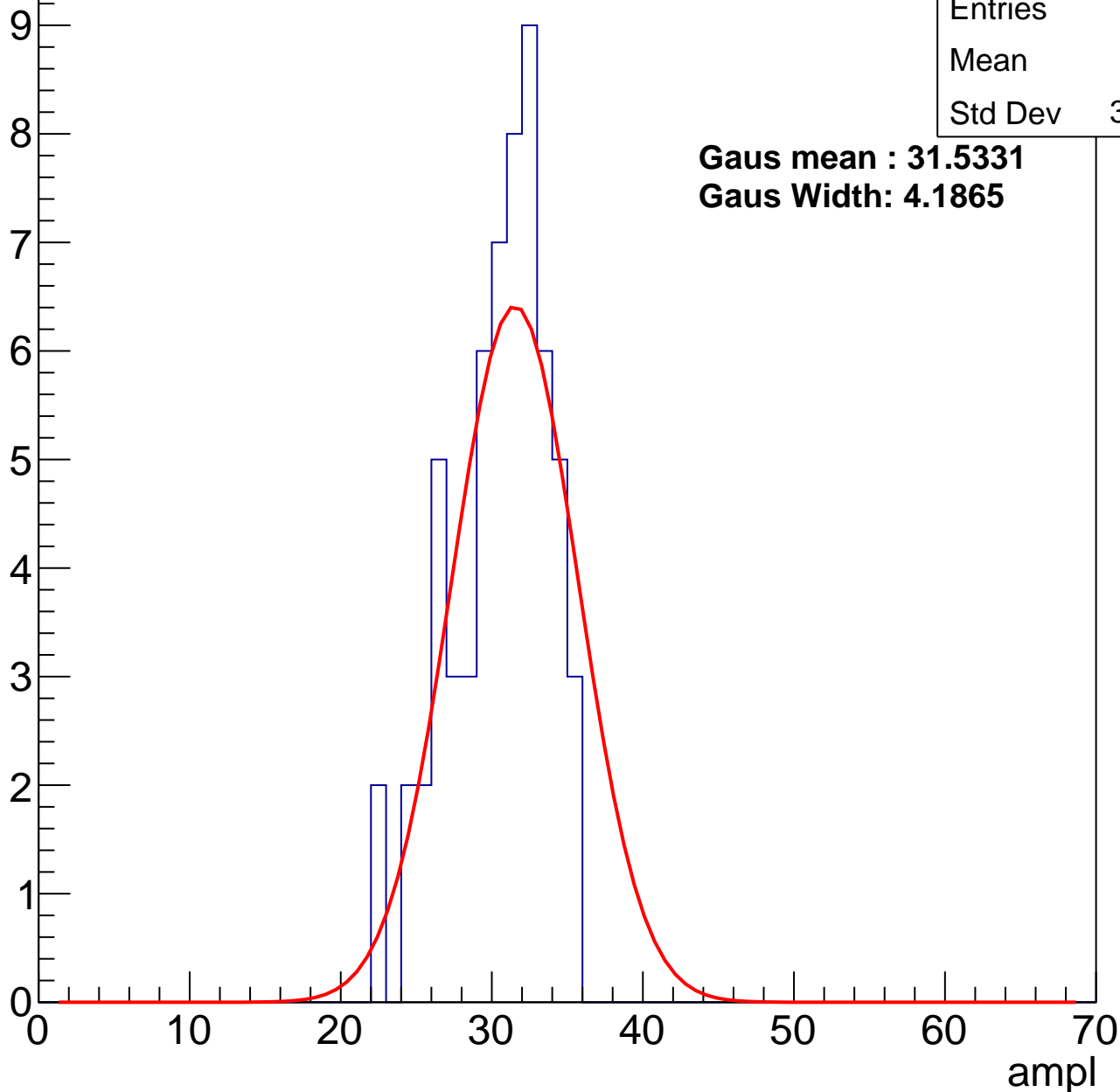
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	30
Std Dev	3.209

Gaus mean : 31.5331

Gaus Width: 4.1865



B1L103S, U12-ch56, adc1

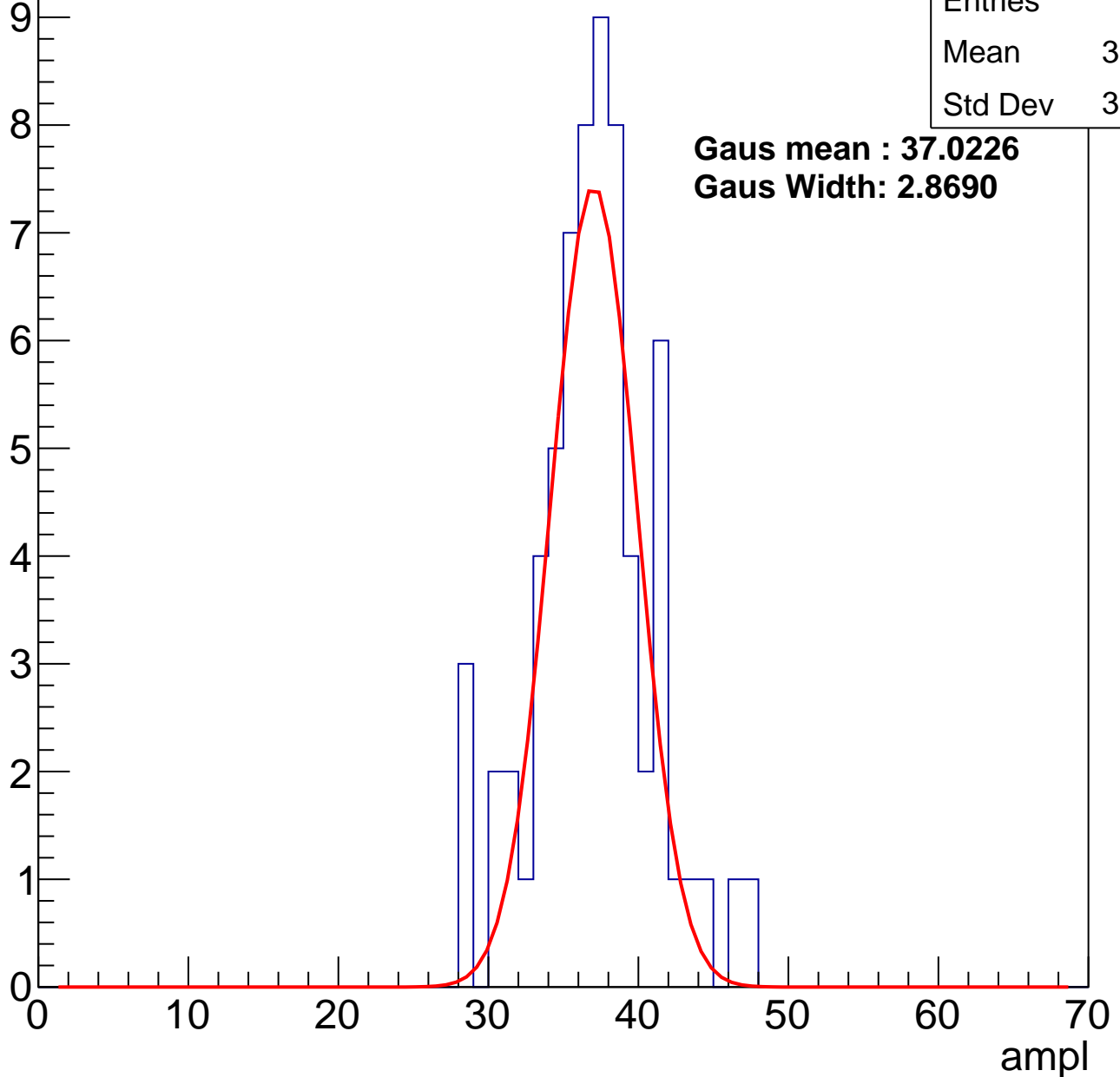
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.58
Std Dev	3.893

Gaus mean : 37.0226

Gaus Width: 2.8690



B1L103S, U12-ch56, adc2

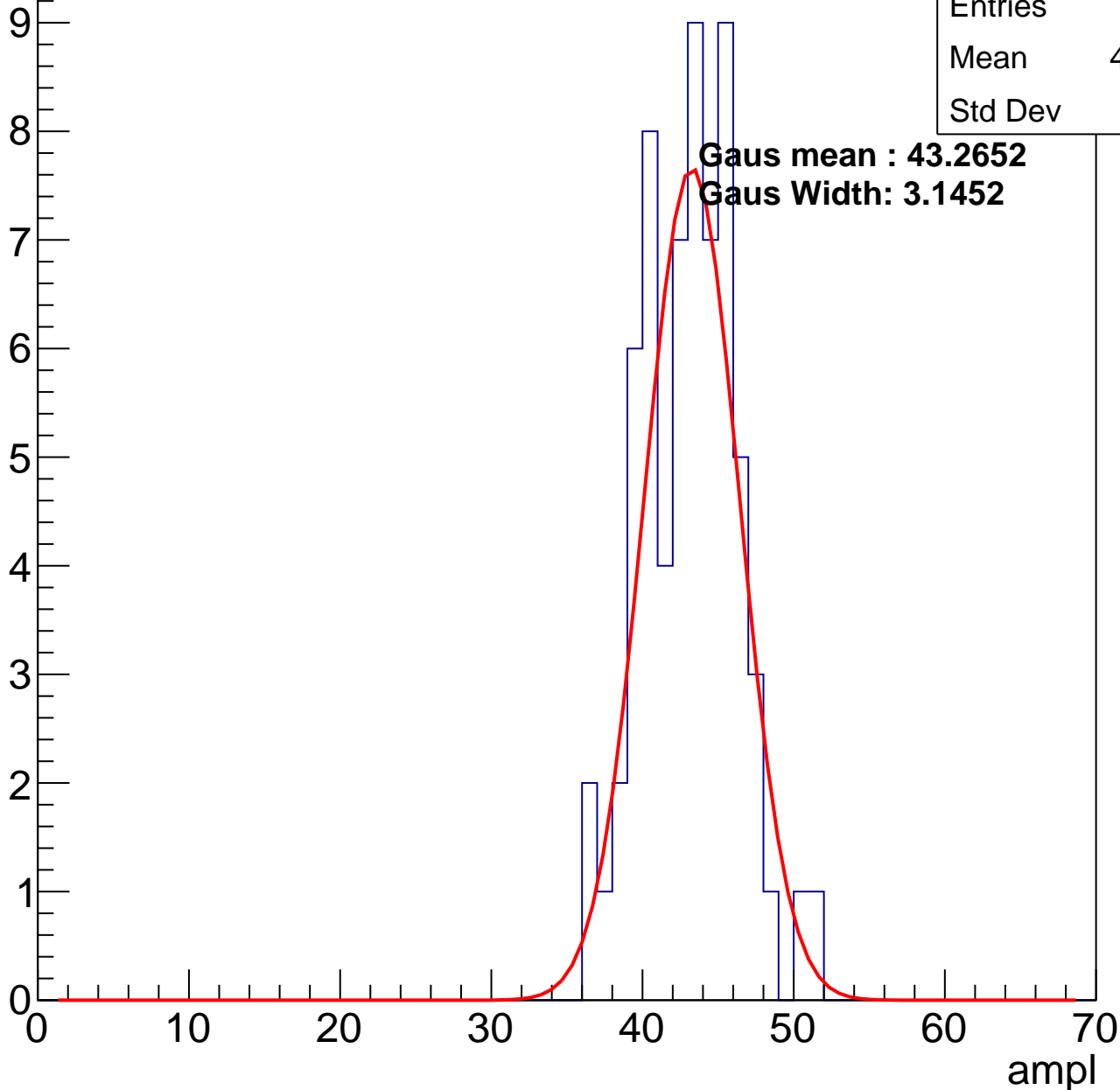
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.68
Std Dev	3.12

Gaus mean : 43.2652

Gaus Width: 3.1452

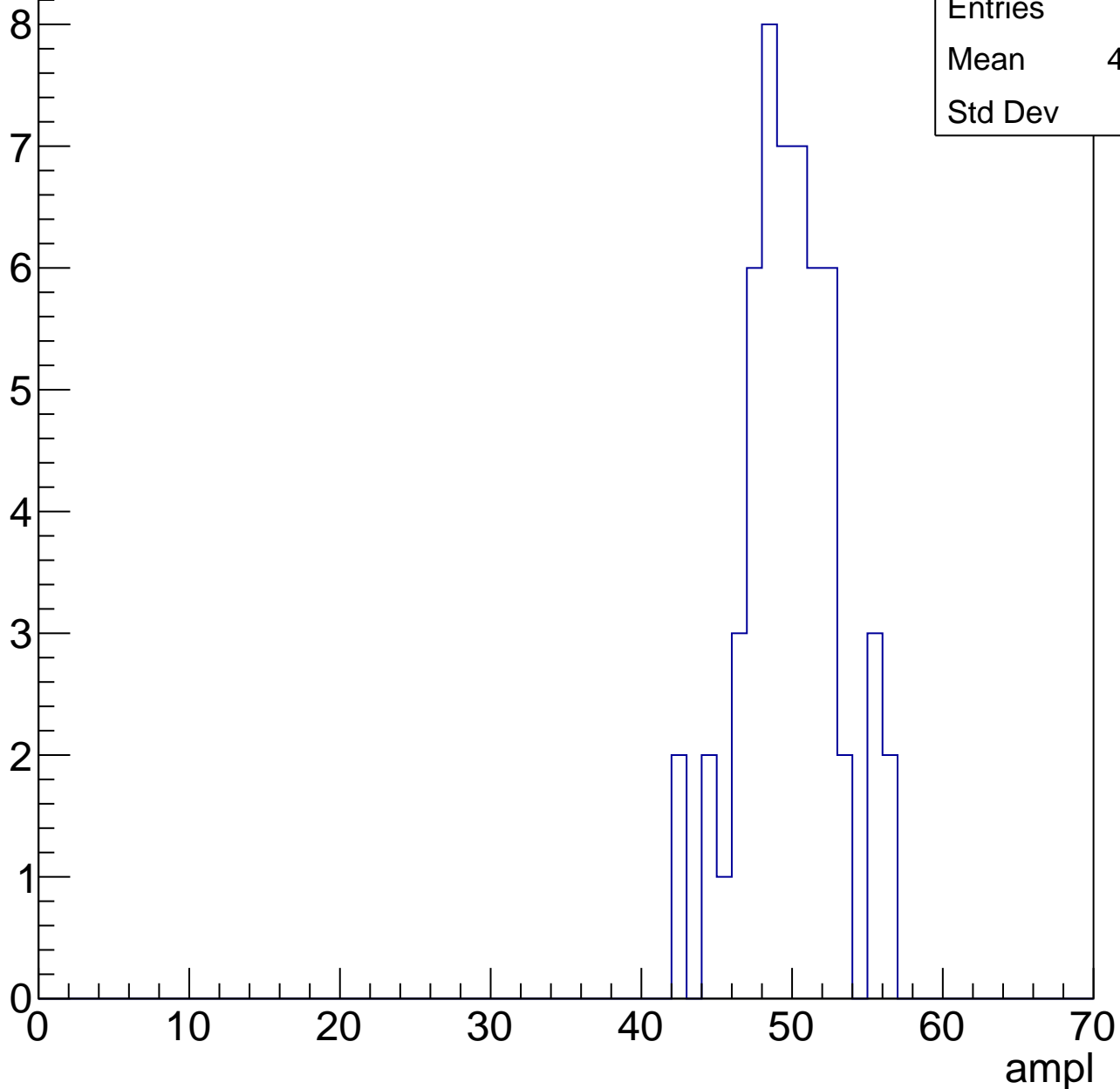


B1L103S, U12-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49.36
Std Dev	3.13

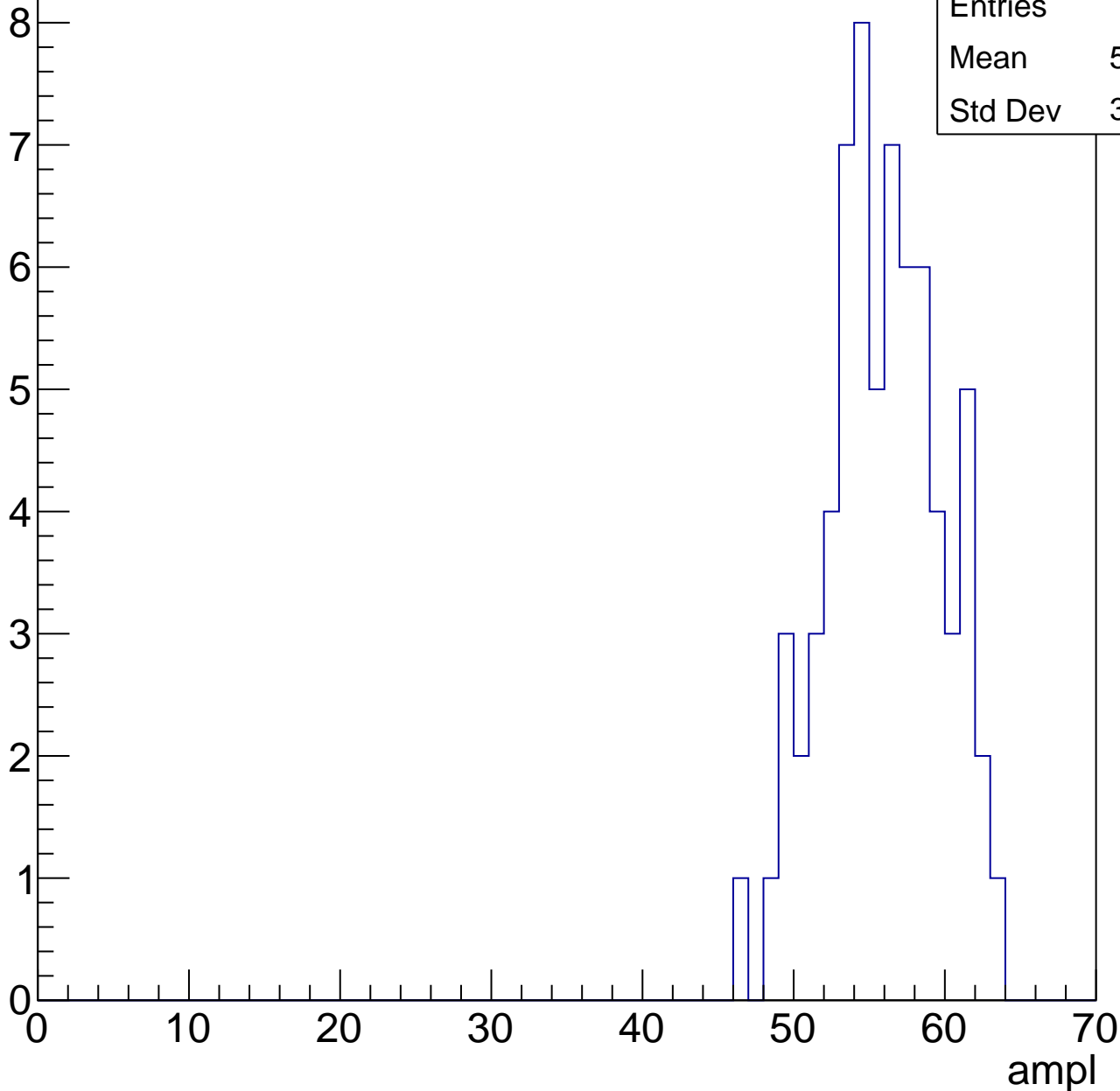


B1L103S, U12-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.44
Std Dev	3.759

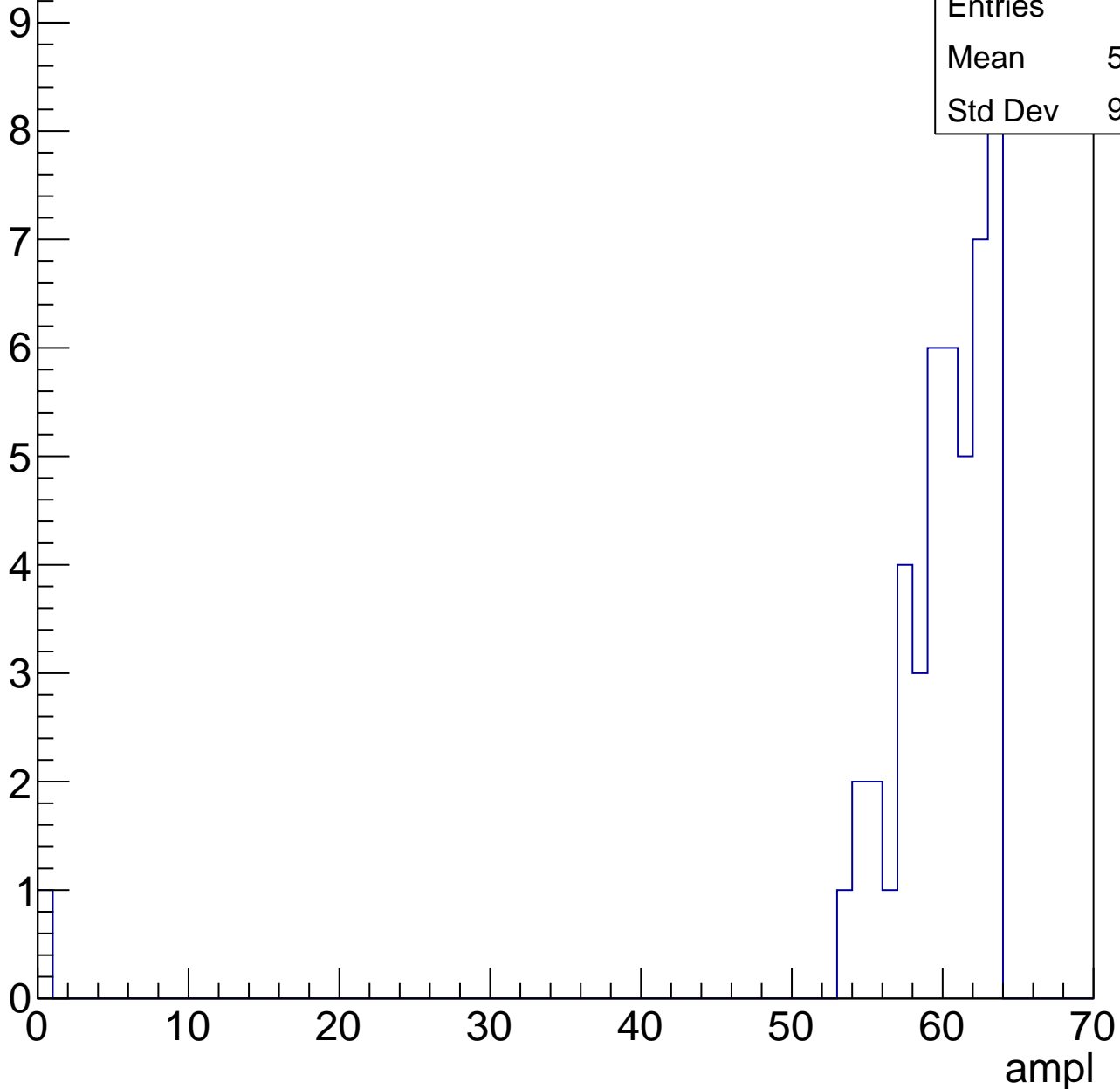


B1L103S, U12-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

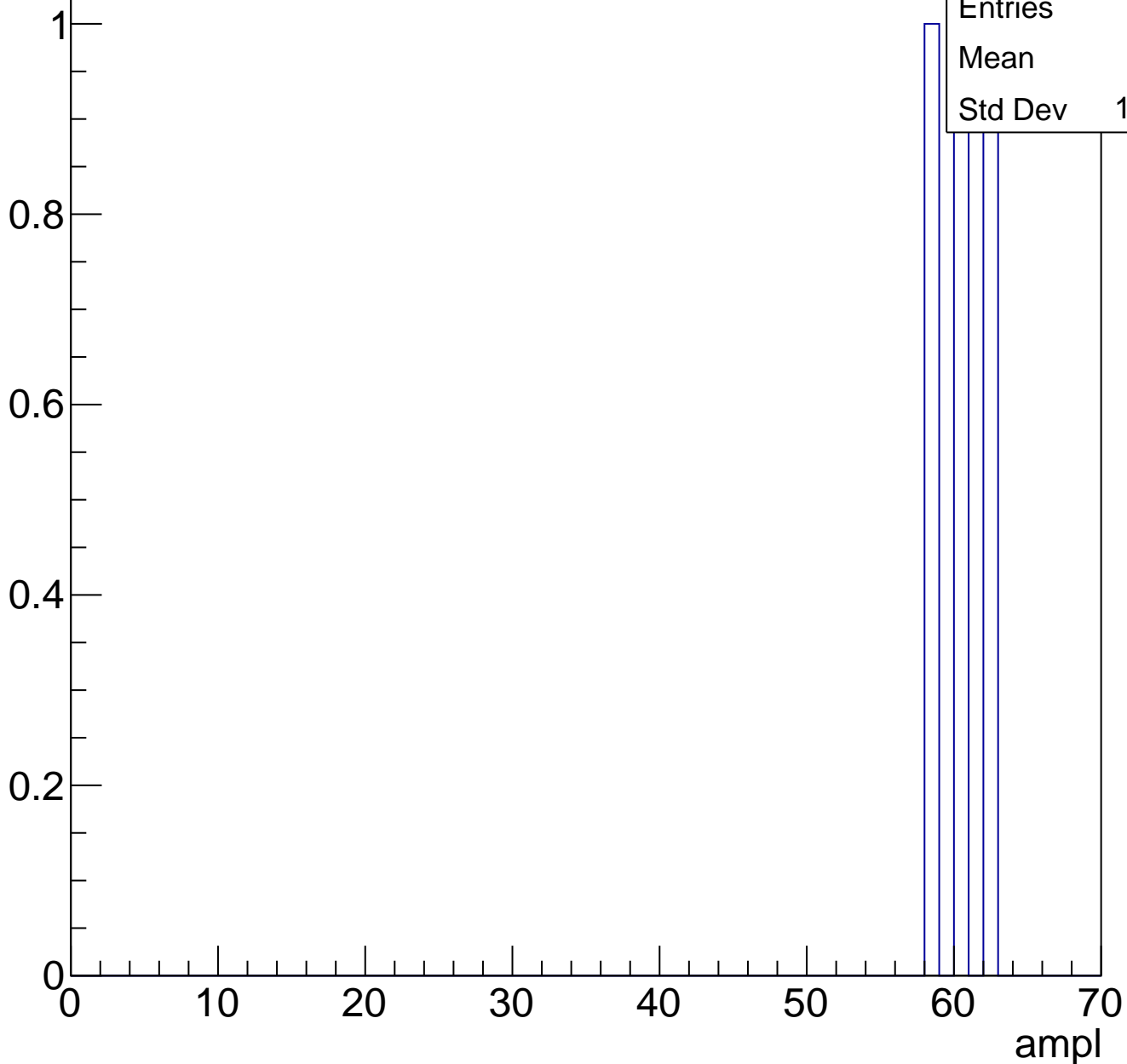
Entries	47
Mean	58.49
Std Dev	9.048



B1L103S, U12-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch57, adc0

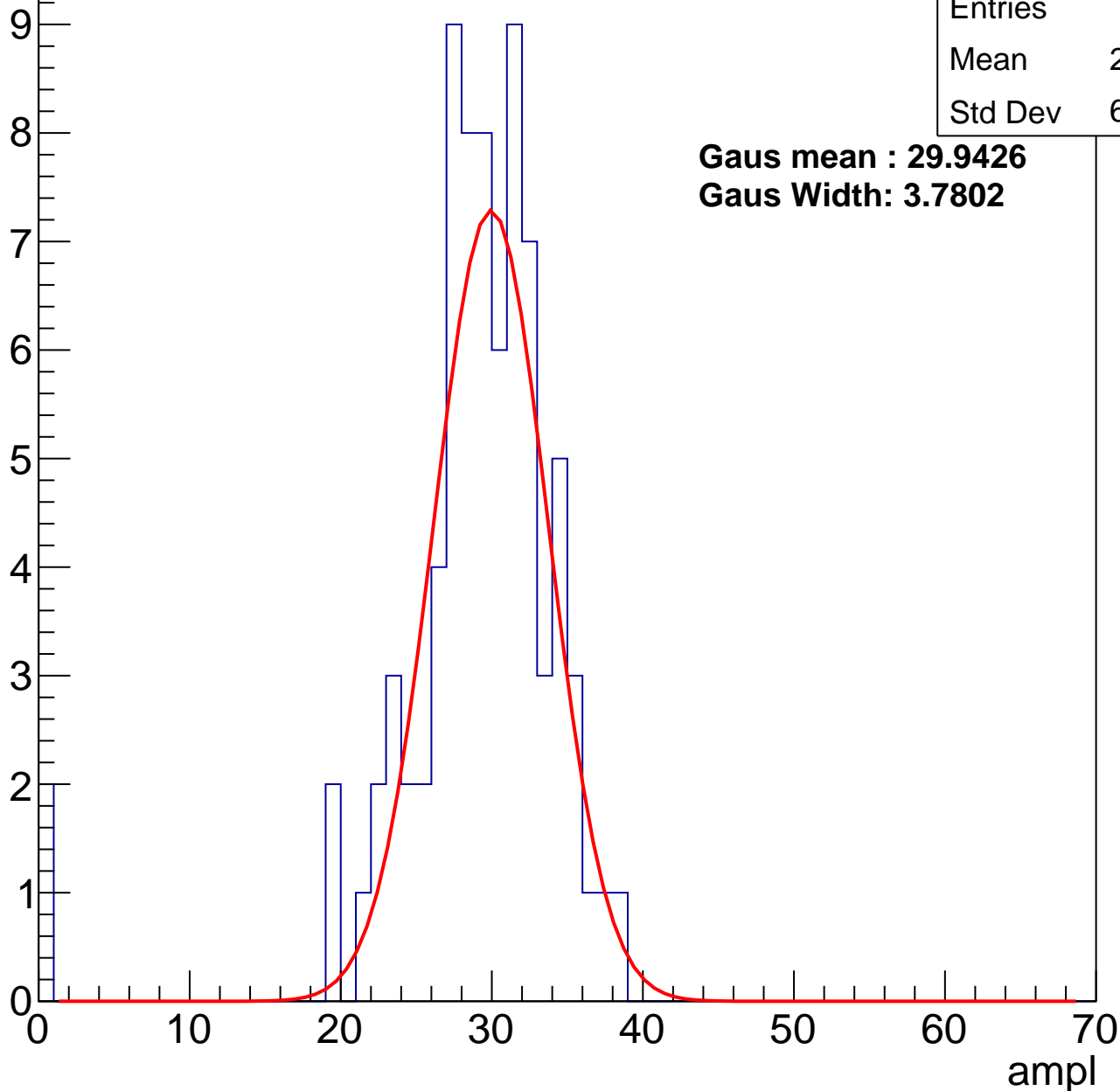
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.37
Std Dev	6.019

Gaus mean : 29.9426

Gaus Width: 3.7802



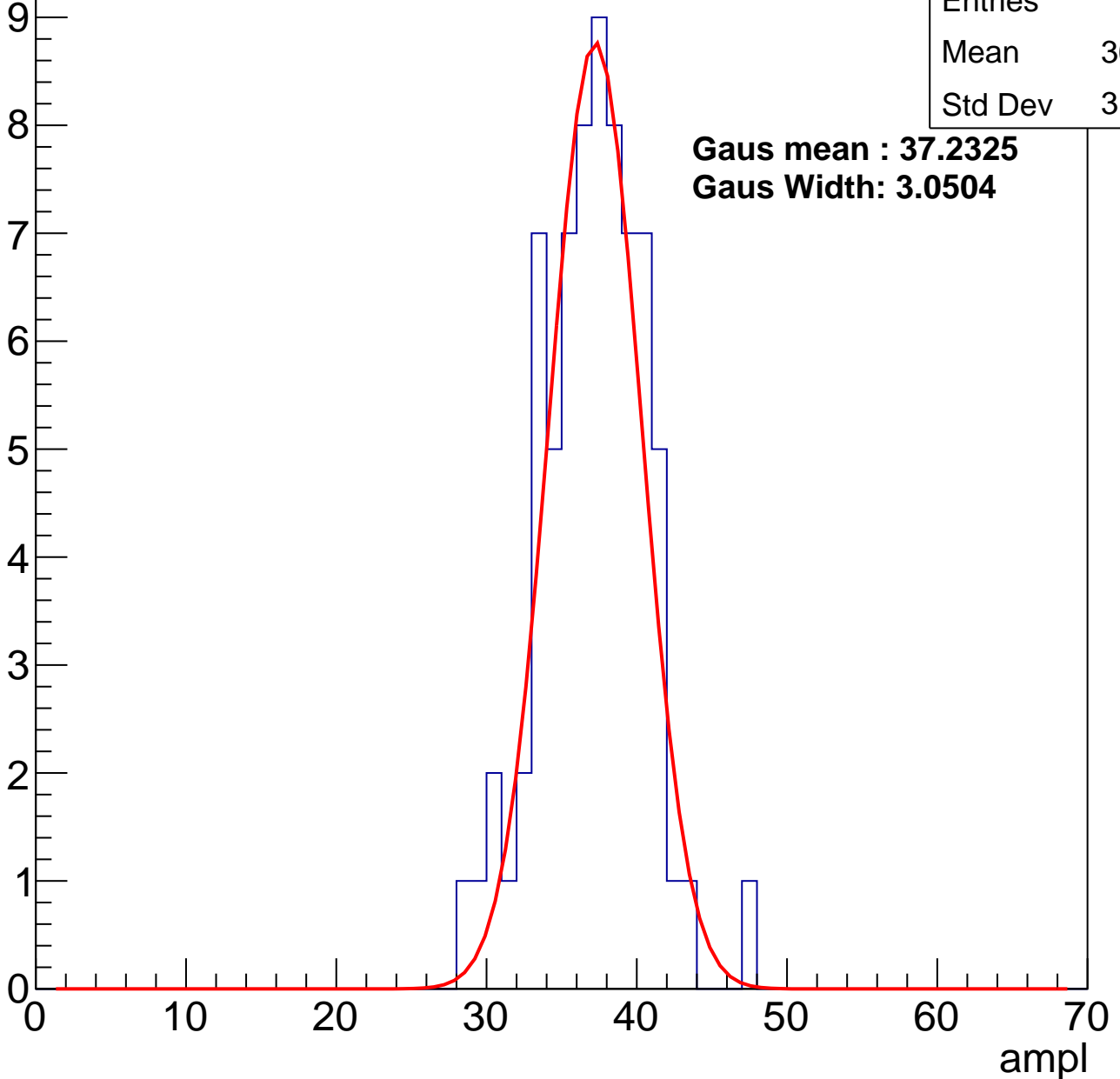
B1L103S, U12-ch57, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	36.62
Std Dev	3.415

Gaus mean : 37.2325
Gaus Width: 3.0504



B1L103S, U12-ch57, adc2

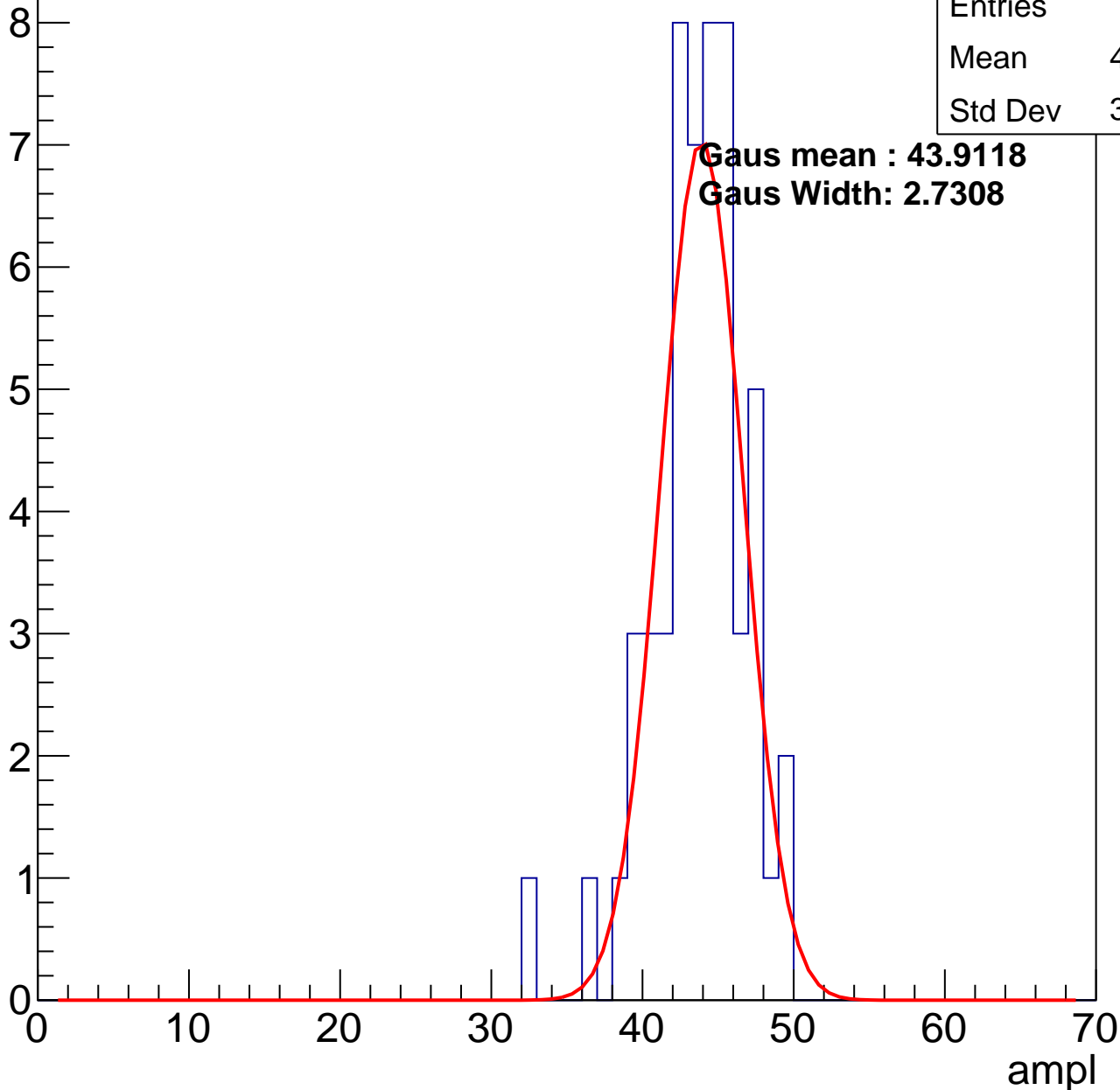
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	43.22
Std Dev	3.137

Gaus mean : 43.9118

Gaus Width: 2.7308

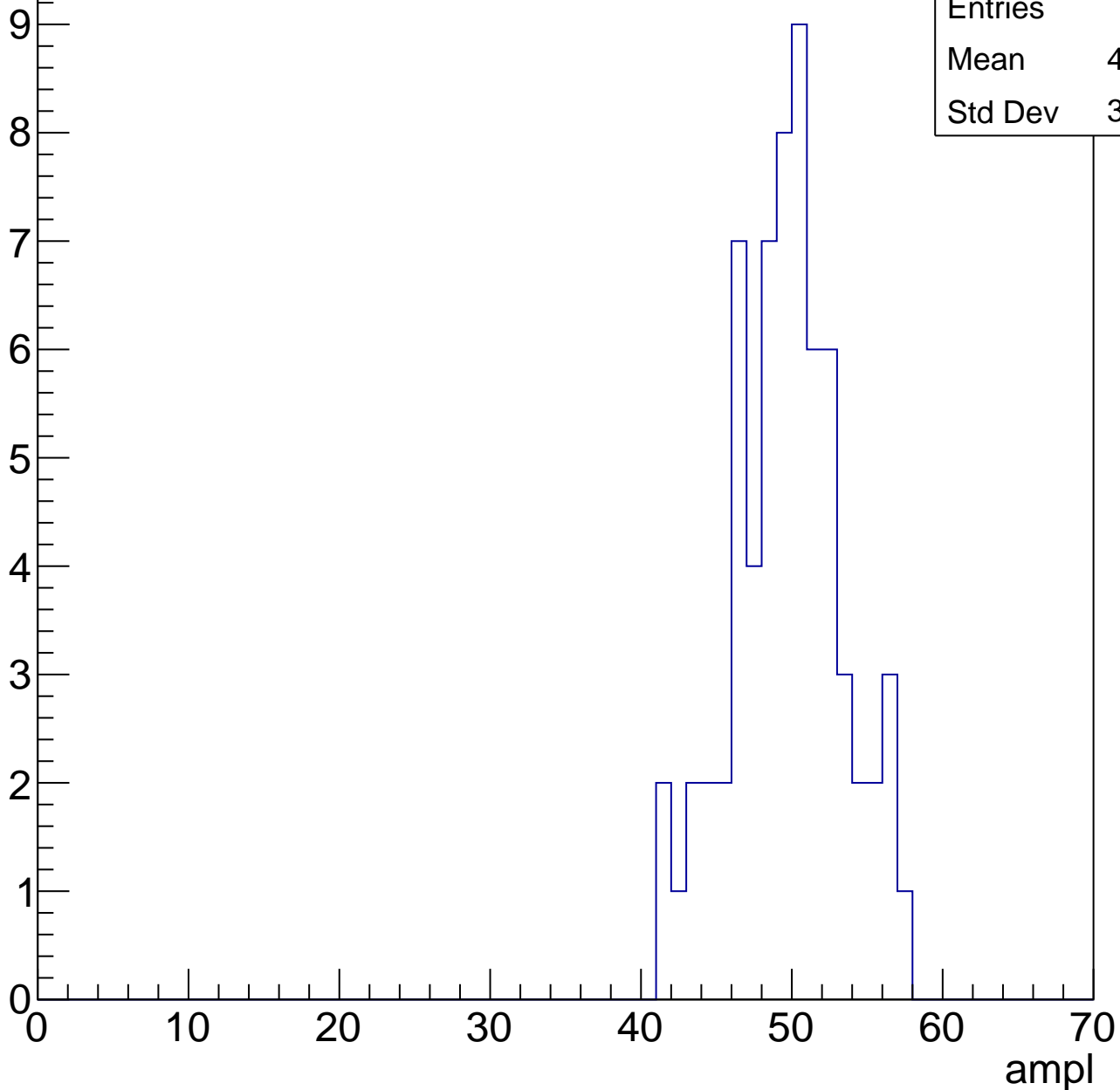


B1L103S, U12-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.19
Std Dev	3.629

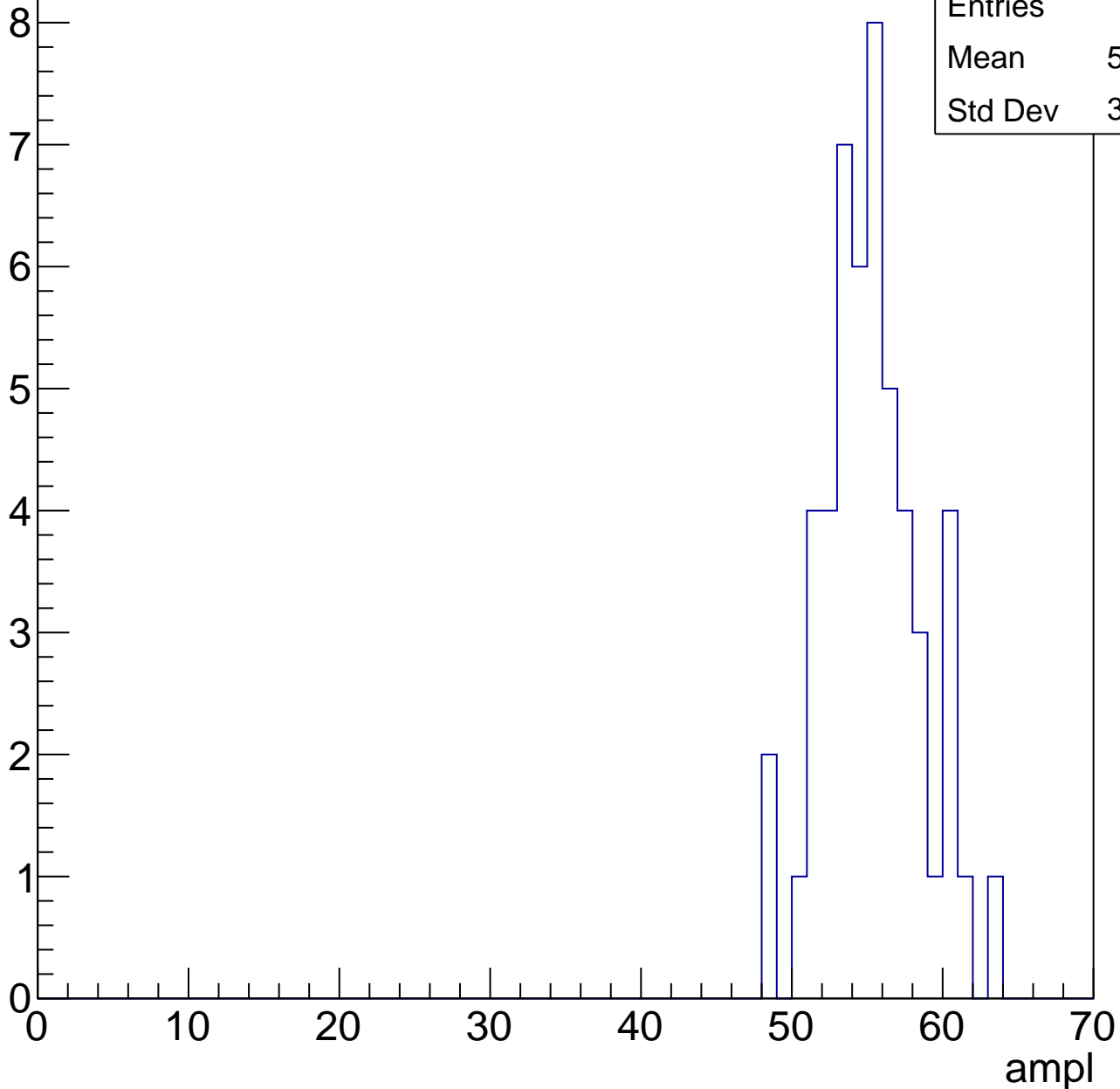


B1L103S, U12-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

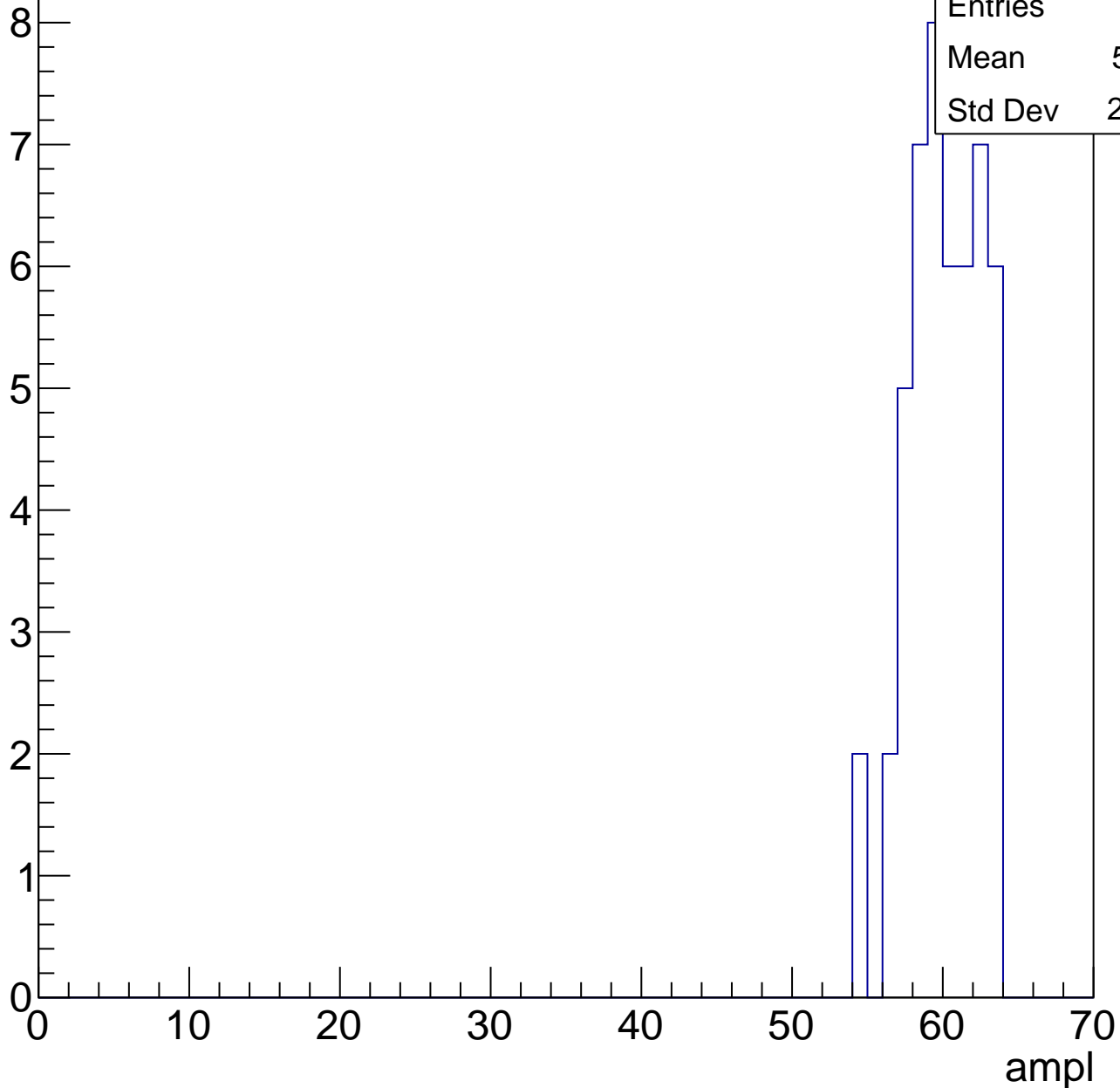
Entries	51
Mean	54.86
Std Dev	3.193



B1L103S, U12-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

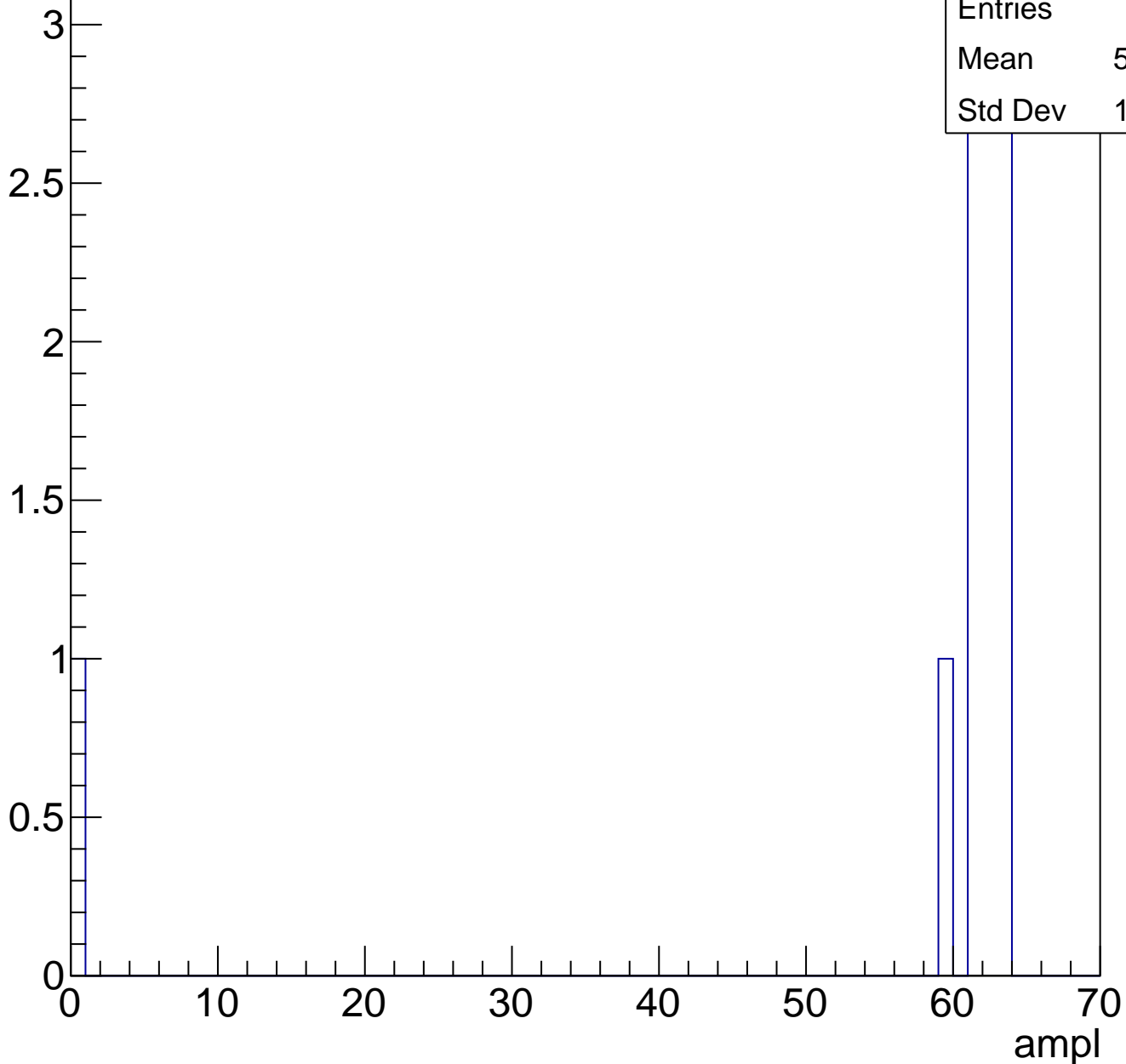
Entry



B1L103S, U12-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch58, adc0

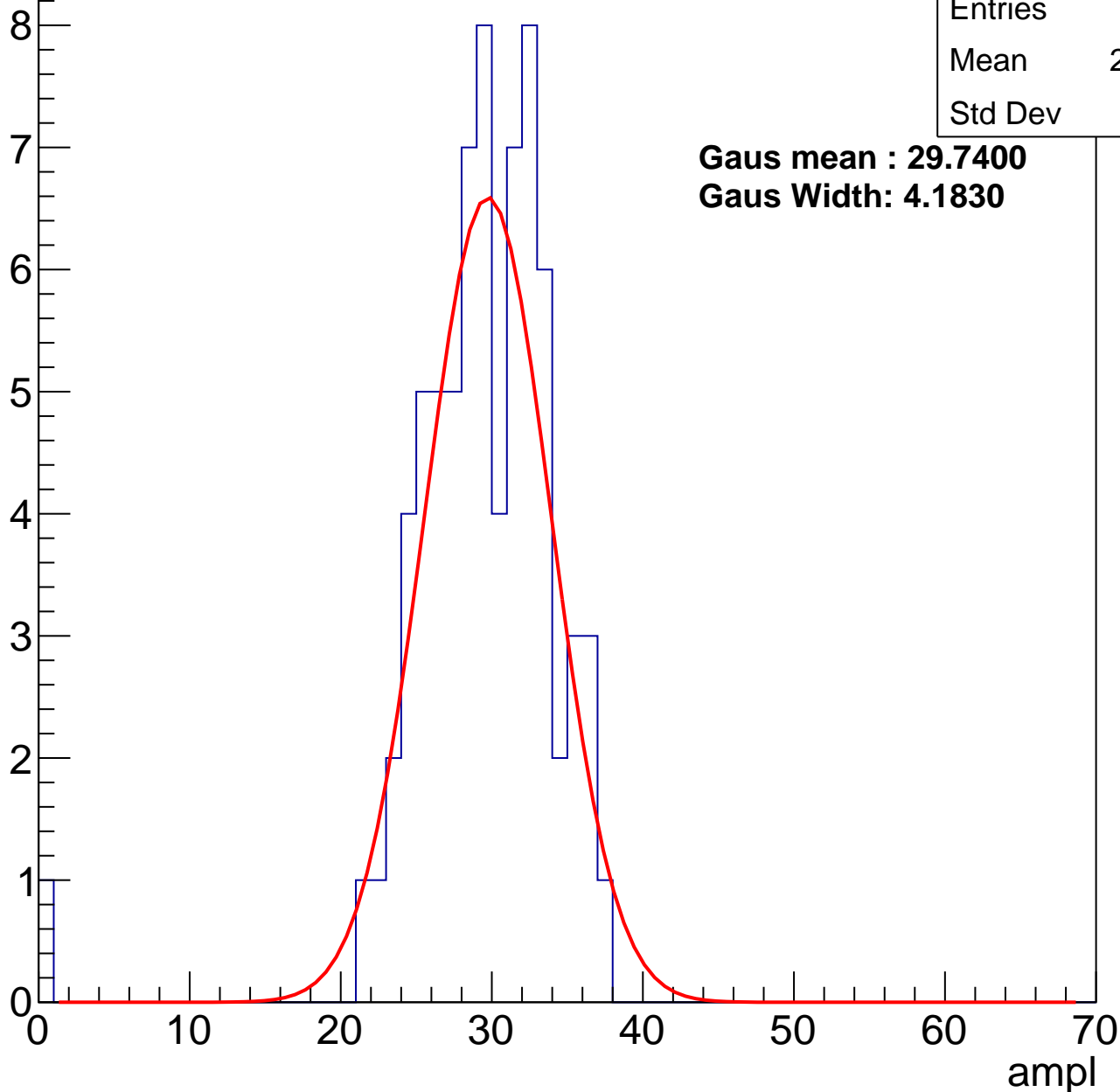
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.93
Std Dev	5.03

Gaus mean : 29.7400

Gaus Width: 4.1830



B1L103S, U12-ch58, adc1

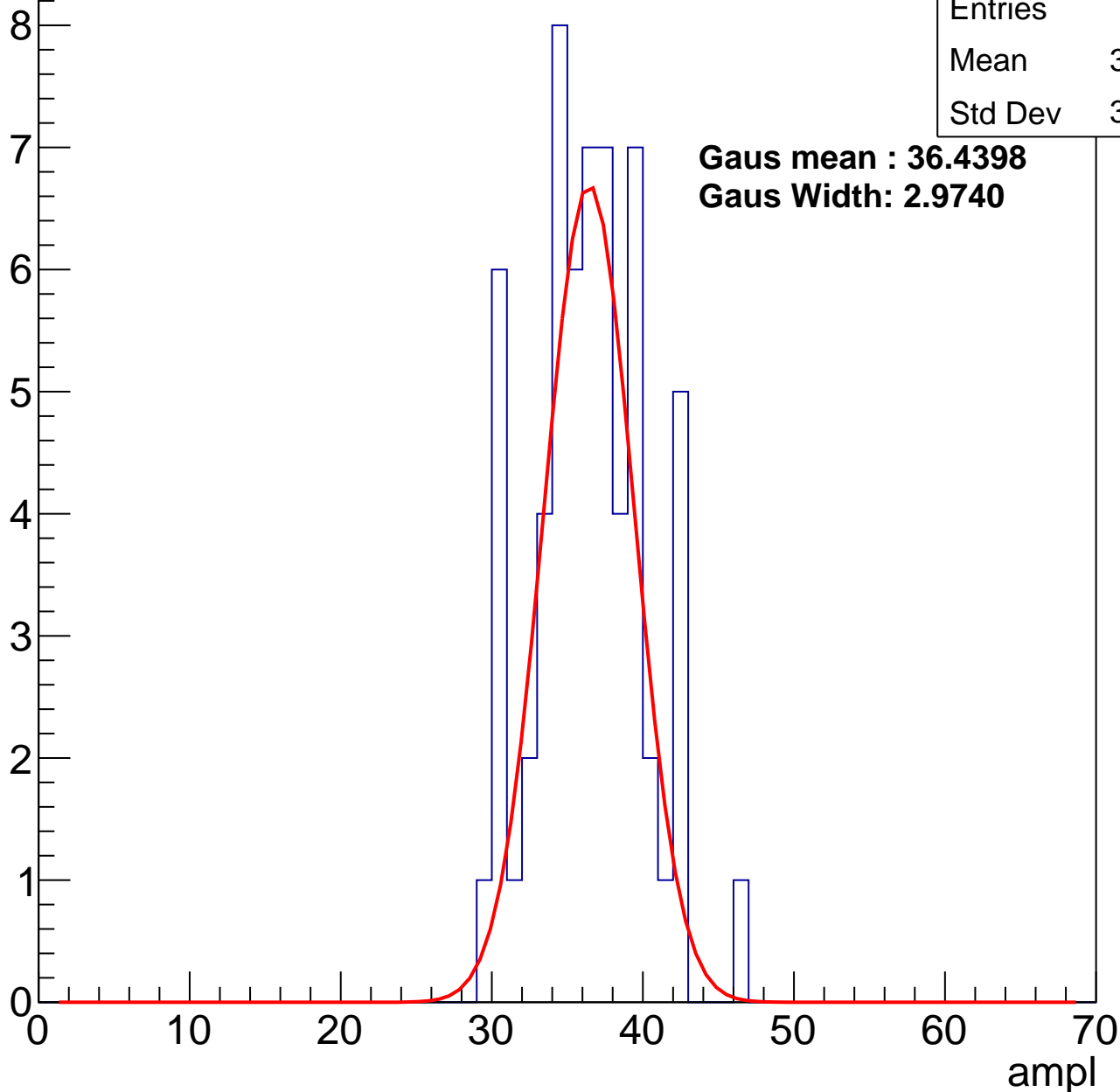
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.98
Std Dev	3.634

Gaus mean : 36.4398

Gaus Width: 2.9740



B1L103S, U12-ch58, adc2

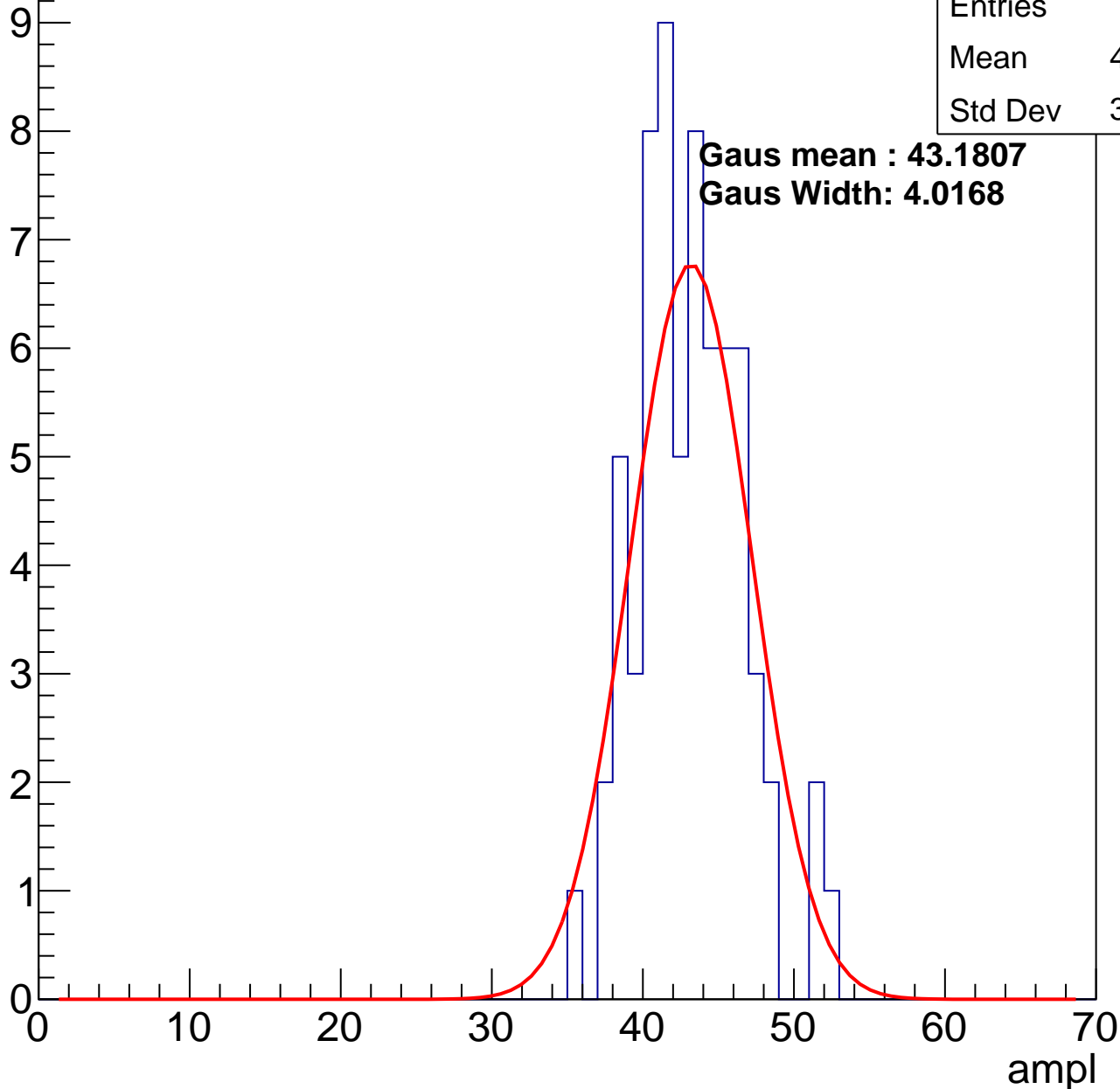
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.69
Std Dev	3.469

Gaus mean : 43.1807

Gaus Width: 4.0168

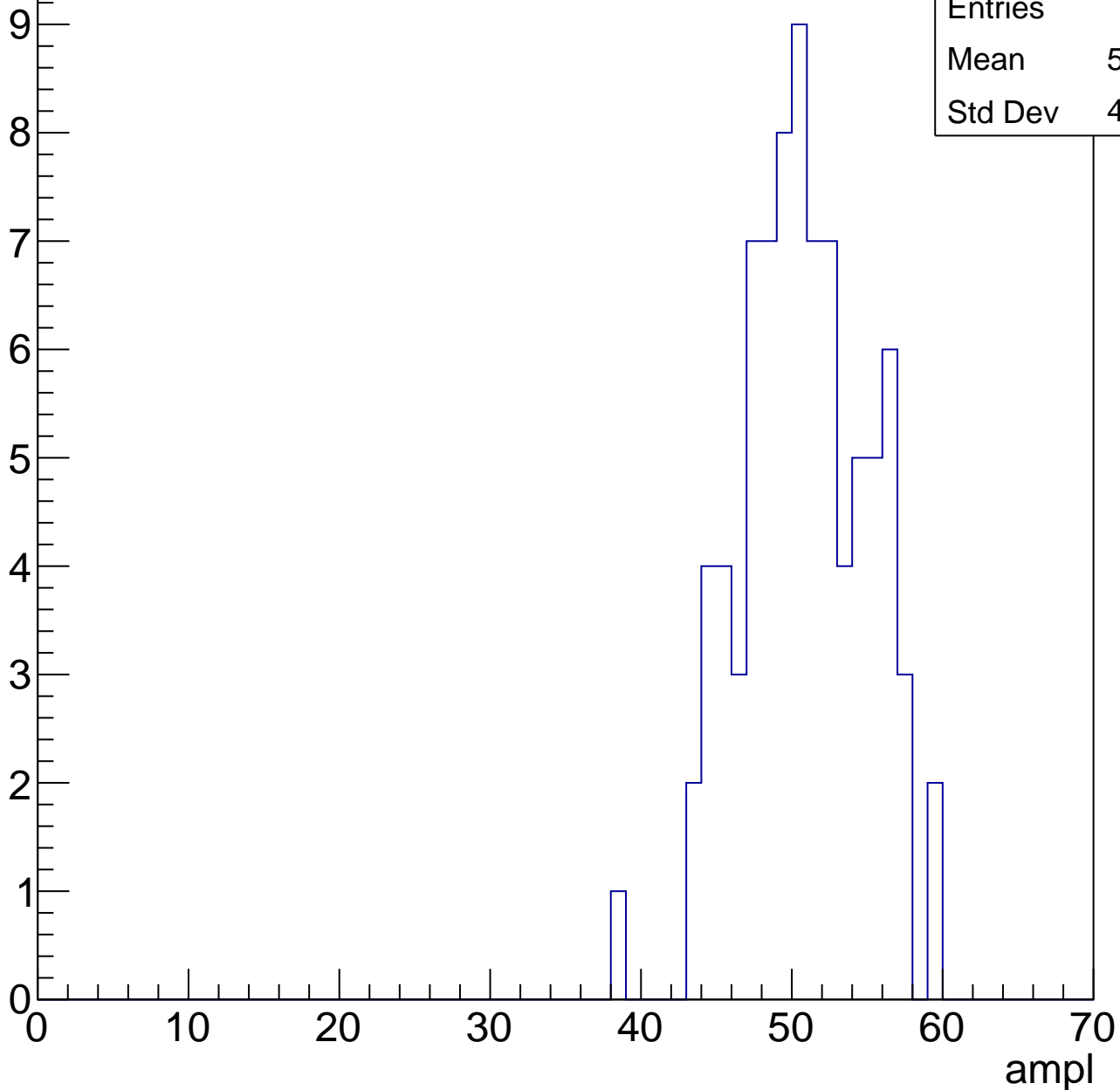


B1L103S, U12-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	50.33
Std Dev	4.118

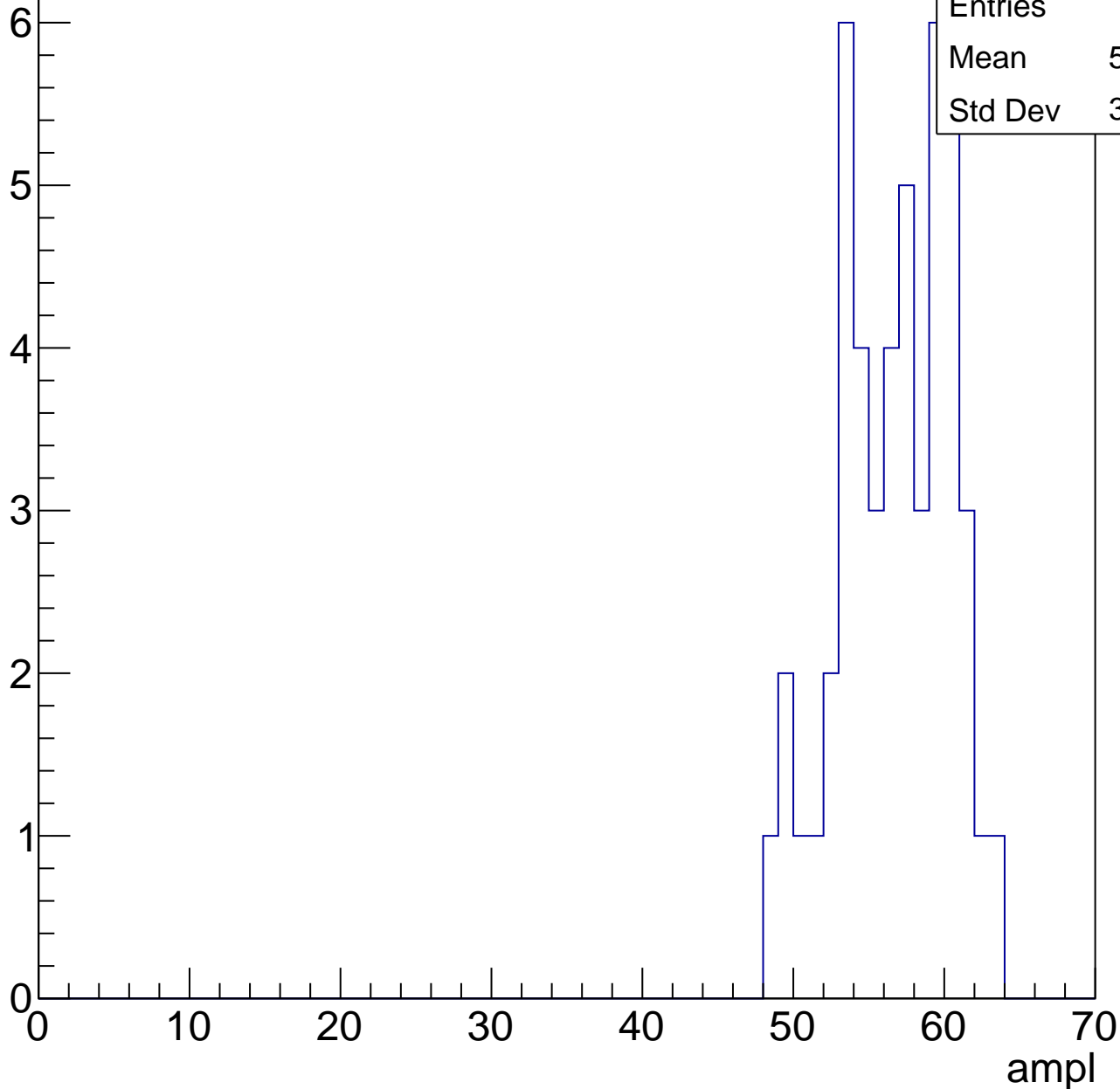


B1L103S, U12-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	56.22
Std Dev	3.649

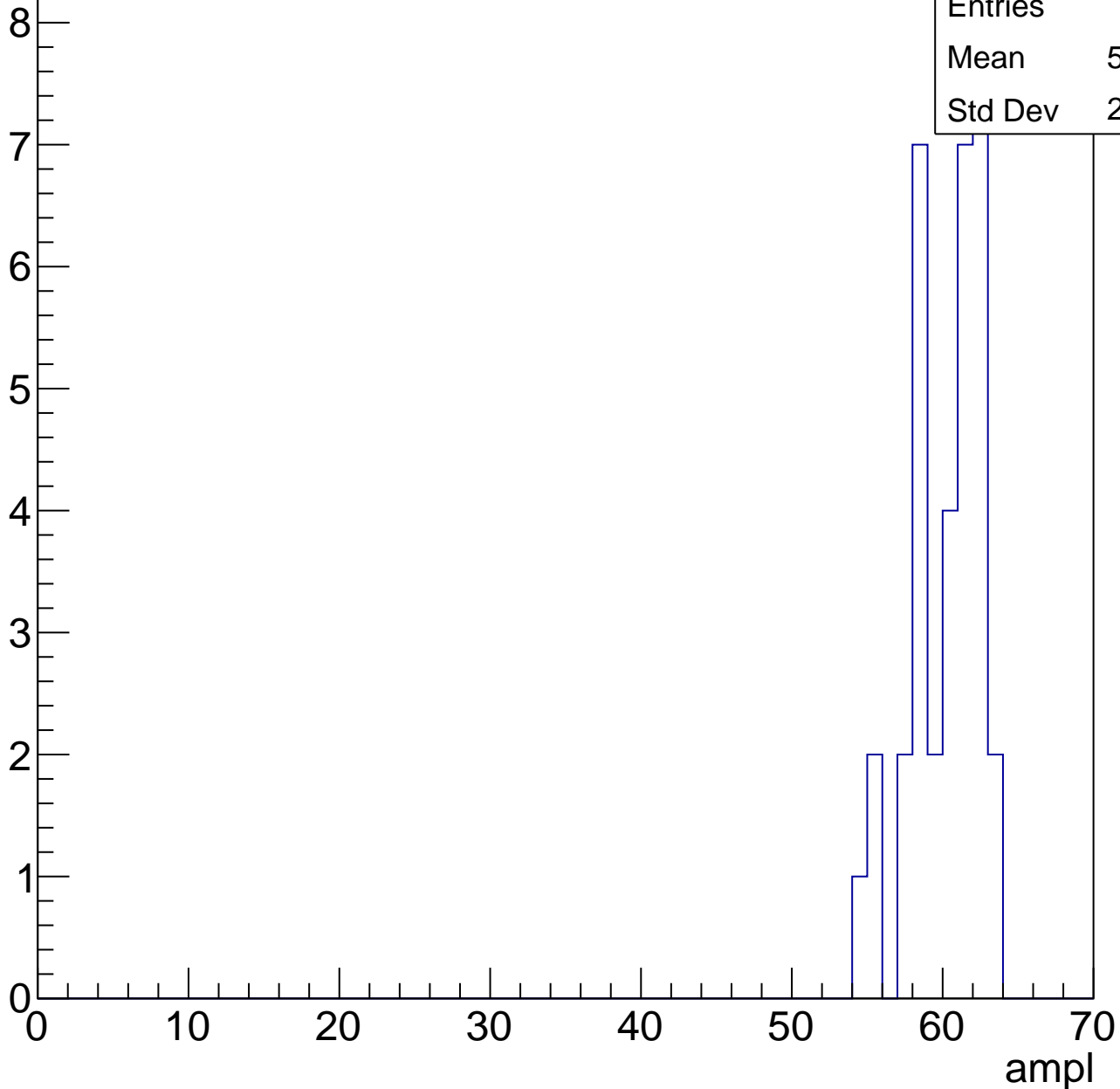


B1L103S, U12-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	59.74
Std Dev	2.322

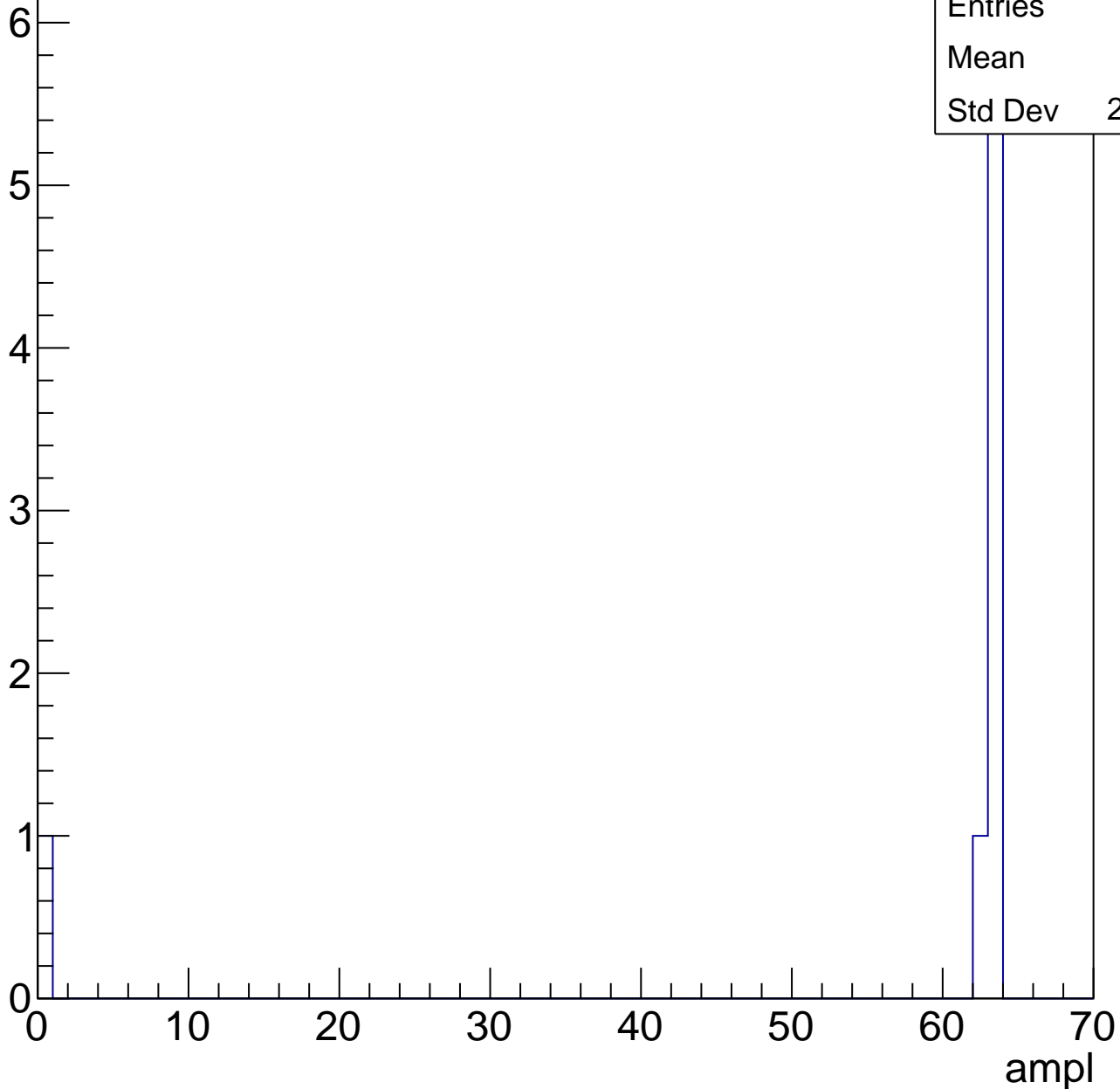


B1L103S, U12-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	55
Std Dev	20.79



B1L103S, U12-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch59, adc0

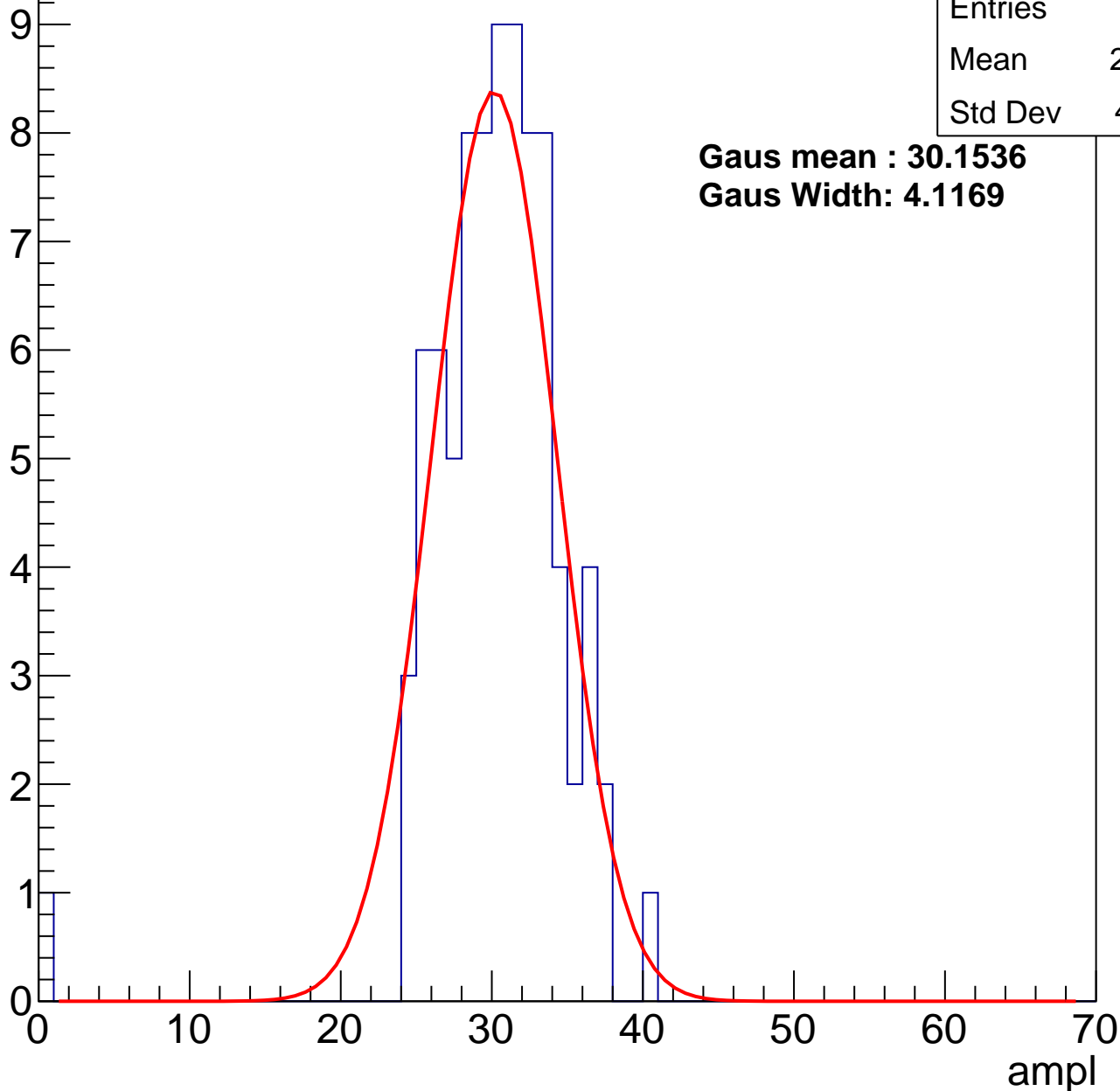
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	29.79
Std Dev	4.761

Gaus mean : 30.1536

Gaus Width: 4.1169



B1L103S, U12-ch59, adc1

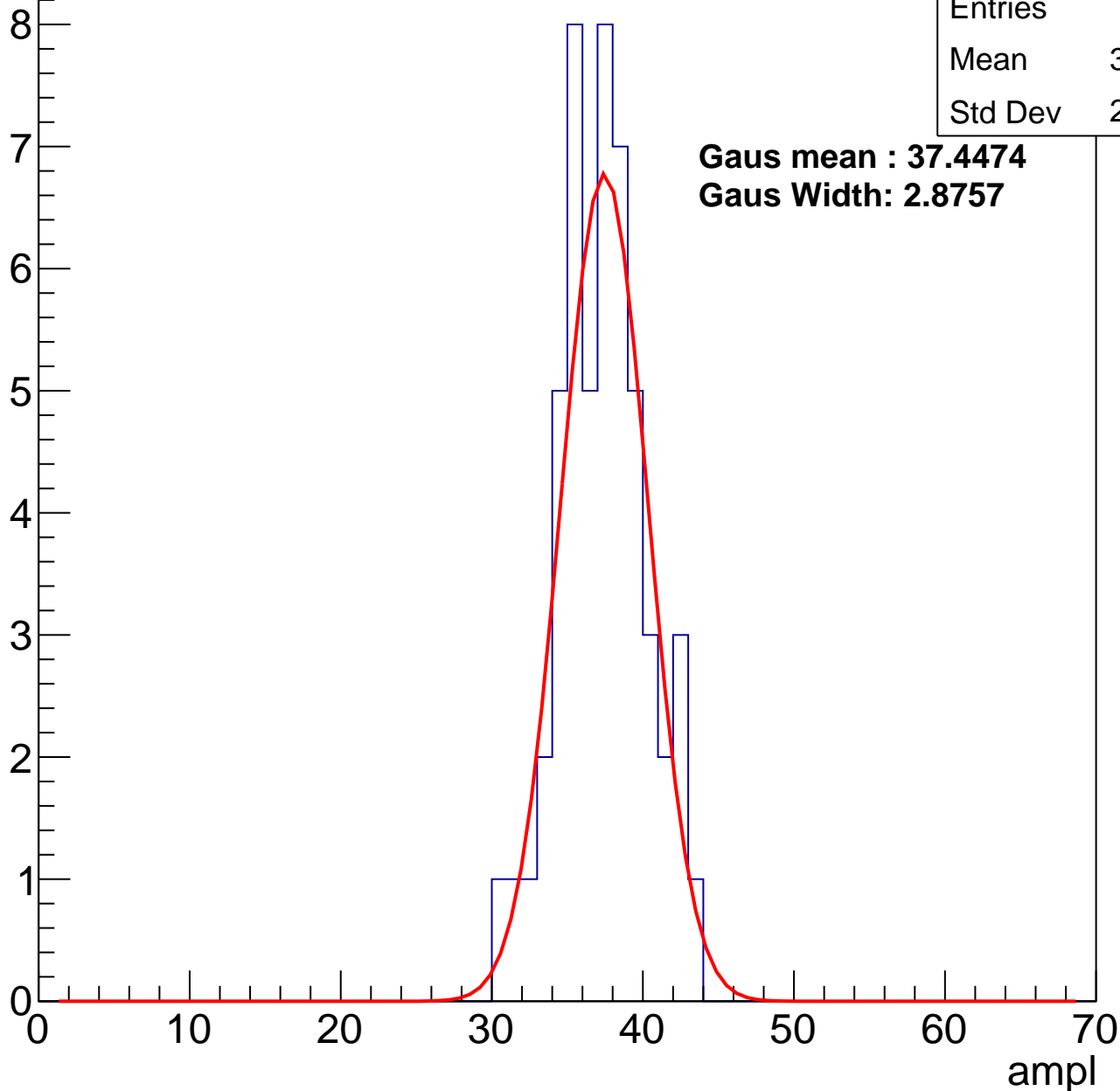
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	36.87
Std Dev	2.842

Gaus mean : 37.4474

Gaus Width: 2.8757



B1L103S, U12-ch59, adc2

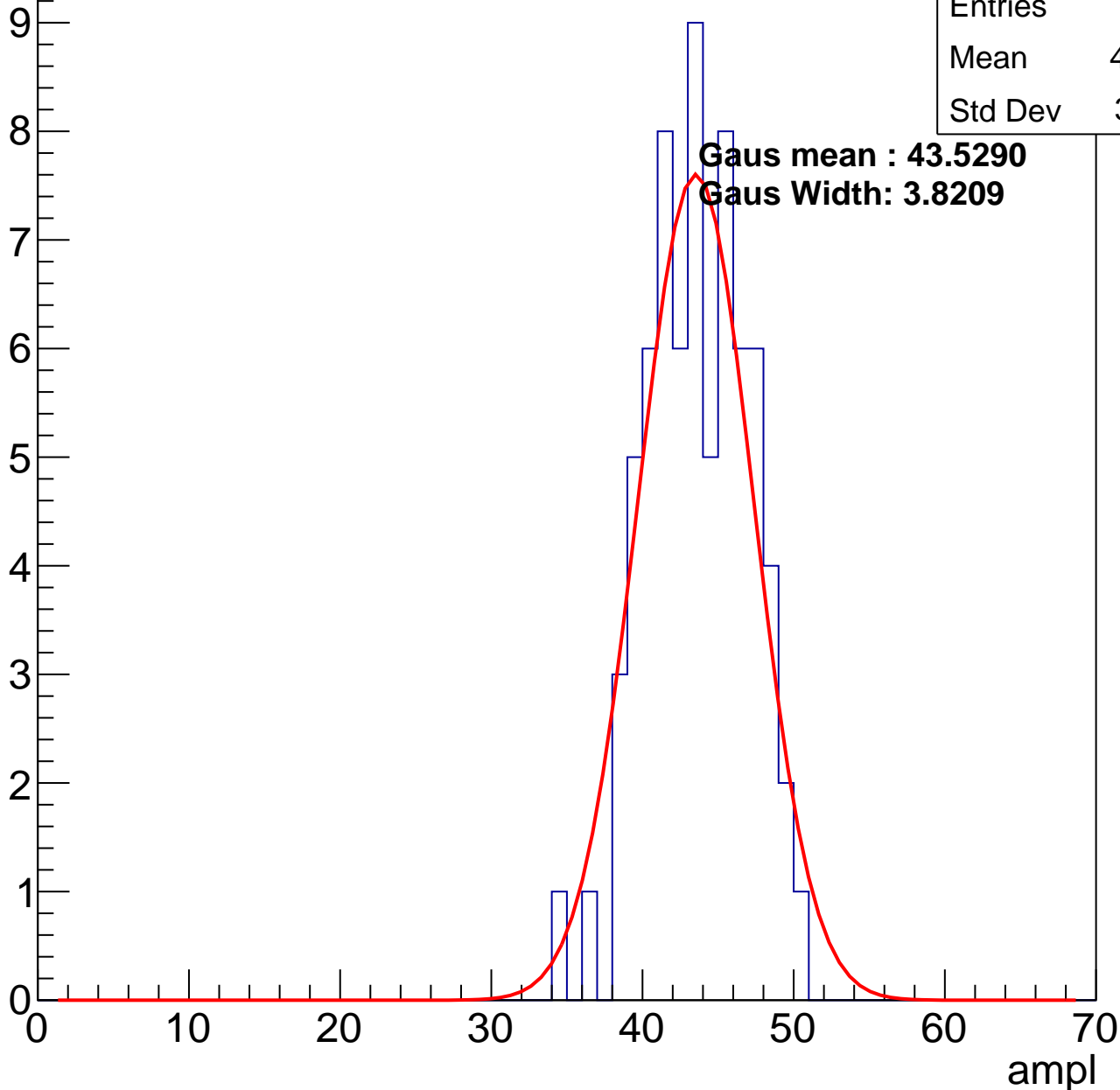
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	43.15
Std Dev	3.321

Gaus mean : 43.5290

Gaus Width: 3.8209



B1L103S, U12-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	49.72
Std Dev	3.133

Entry

10

8

6

4

2

0

0

10

20

30

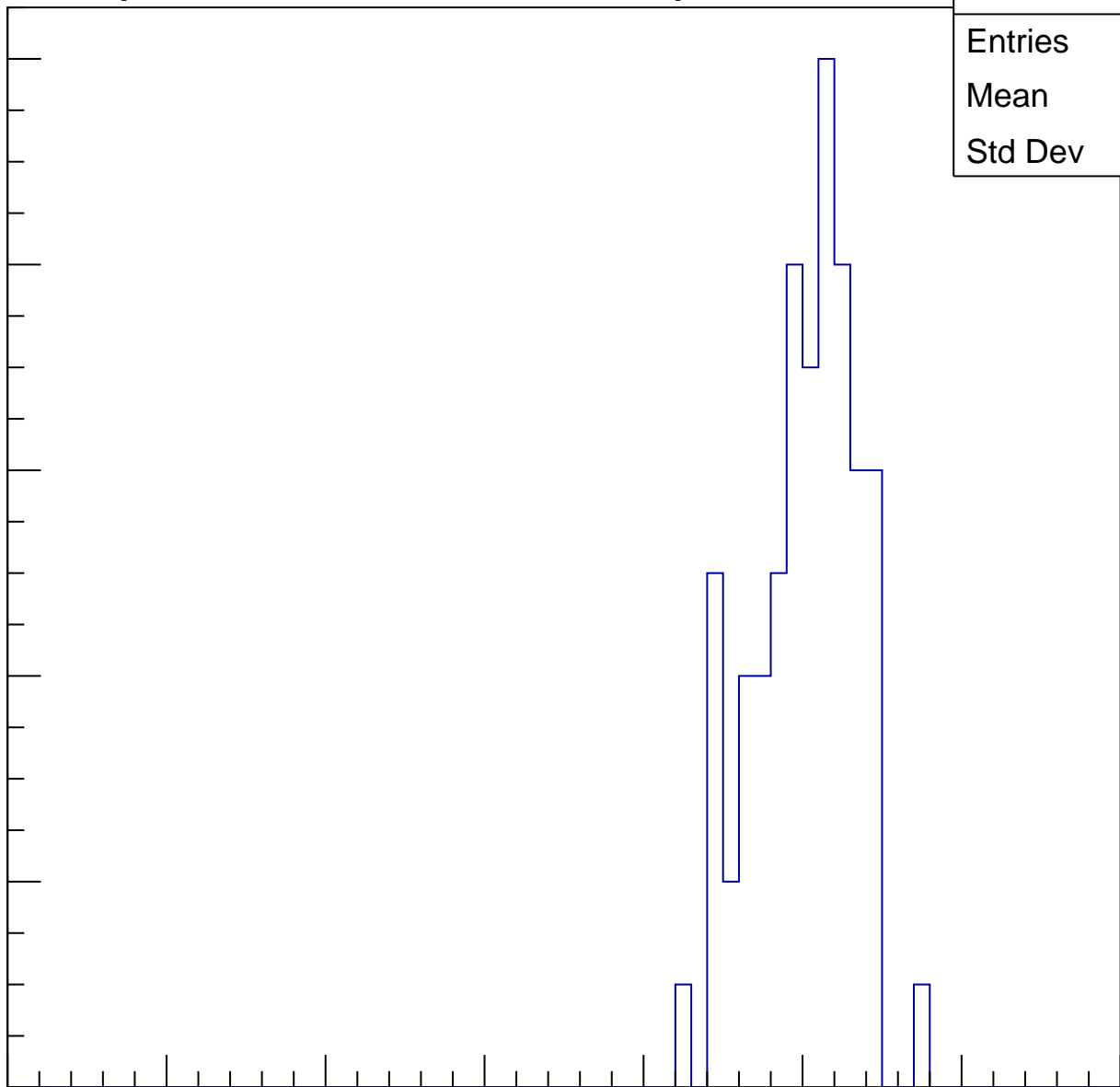
40

50

60

70

ampl



B1L103S, U12-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

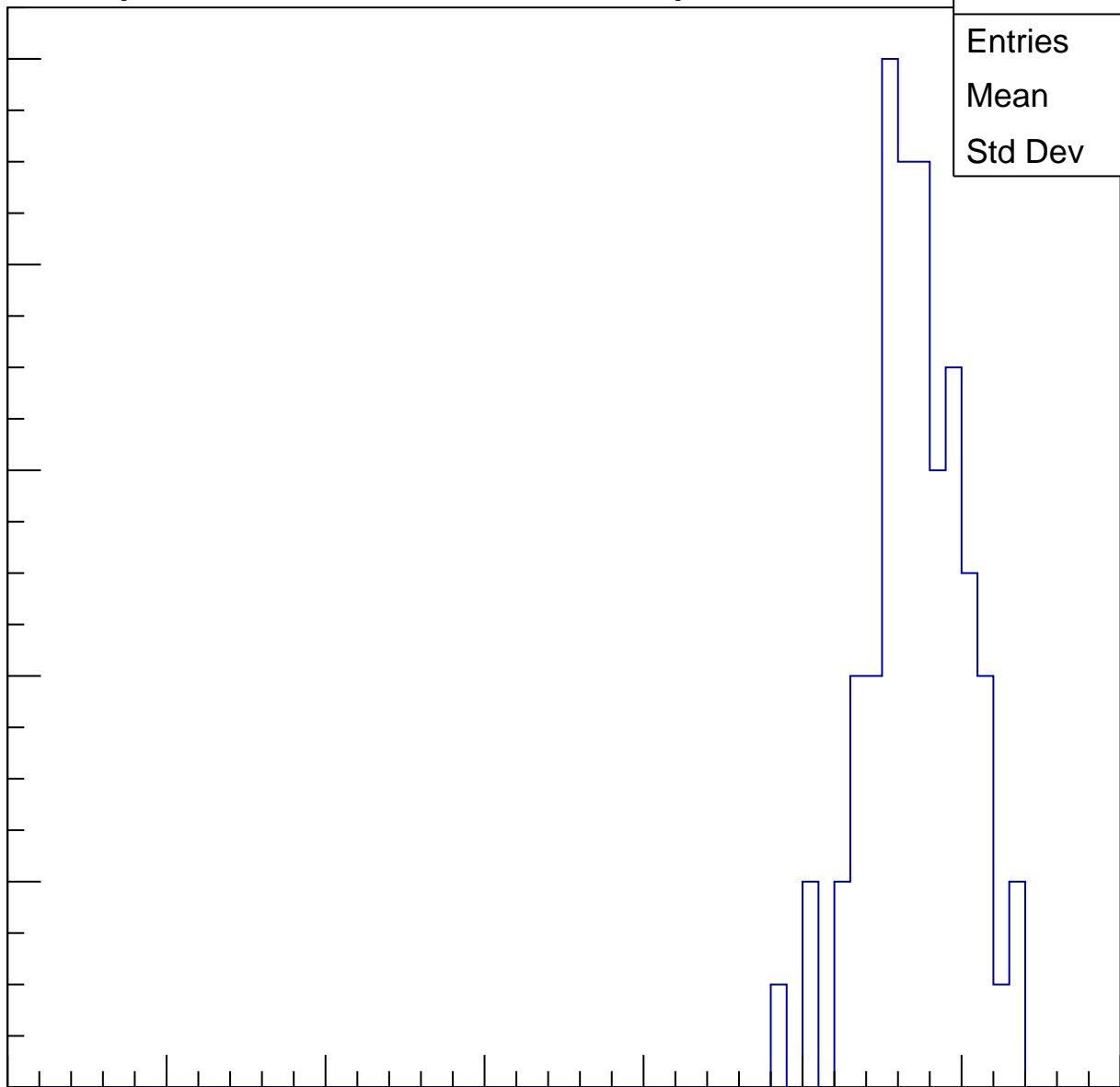
Entries	66
Mean	56.67
Std Dev	3.042

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

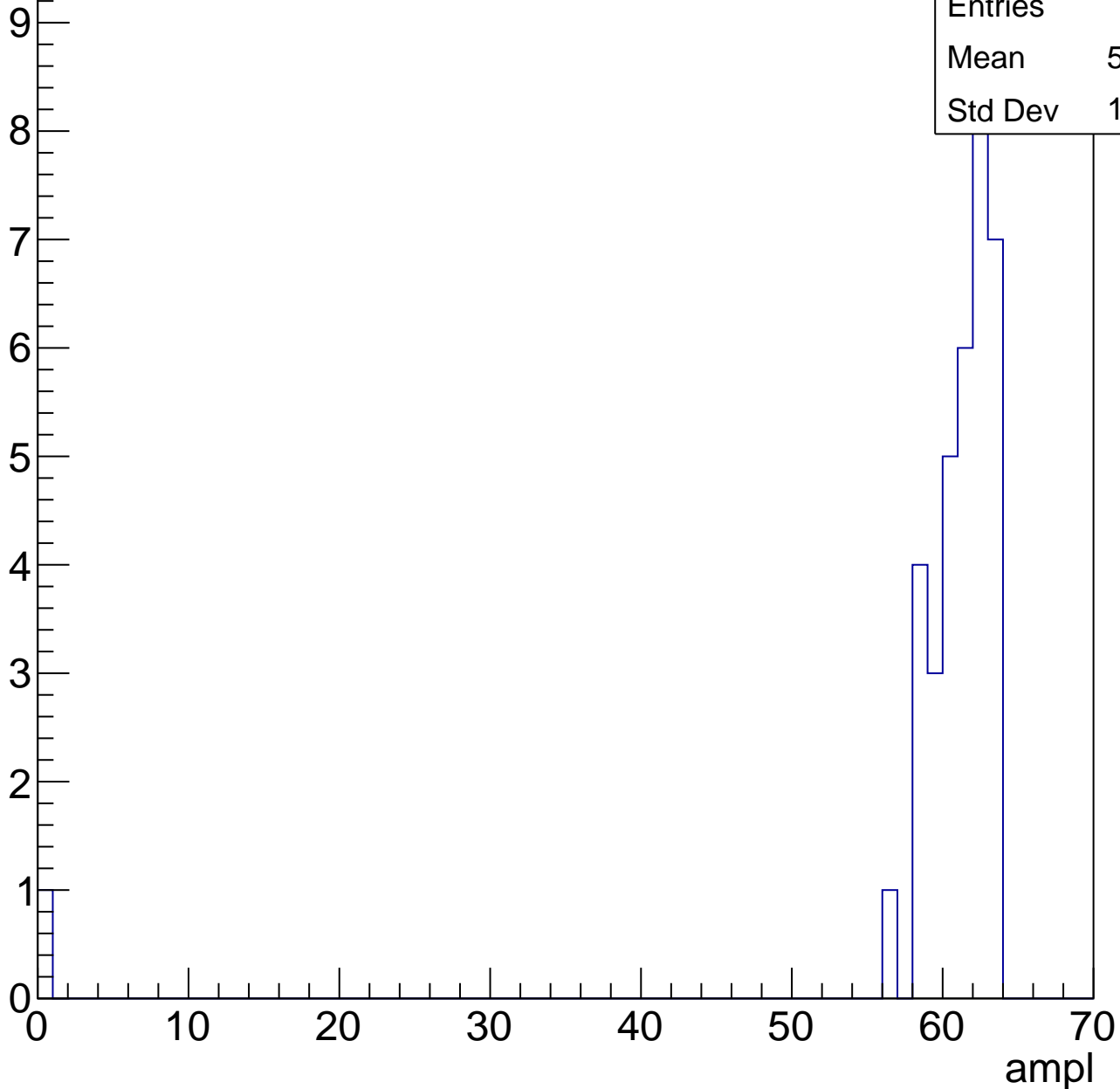


B1L103S, U12-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	59.17
Std Dev	10.16



B1L103S, U12-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

63

Std Dev

0

B1L103S, U12-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch60, adc0

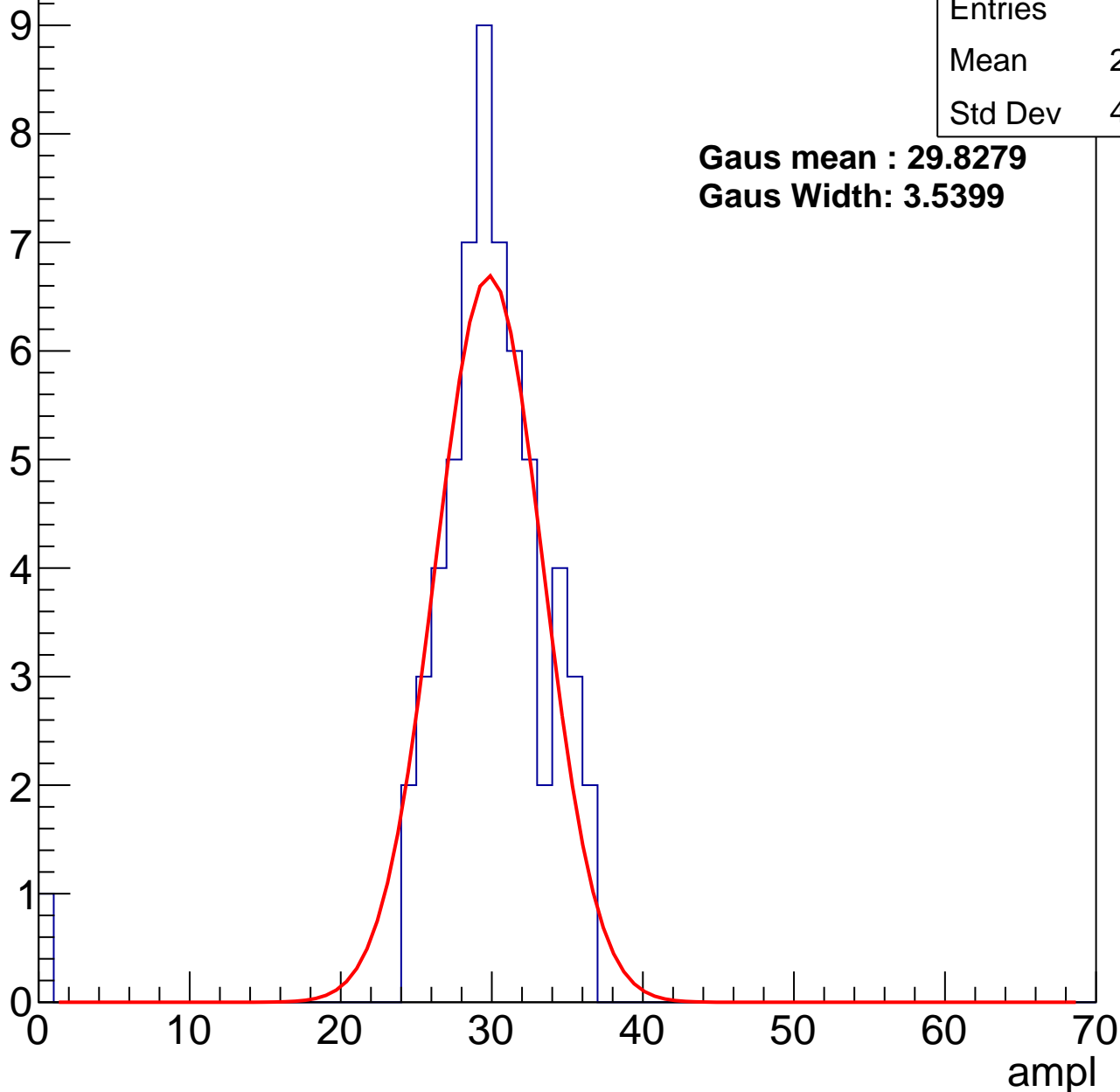
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	29.23
Std Dev	4.852

Gaus mean : 29.8279

Gaus Width: 3.5399



B1L103S, U12-ch60, adc1

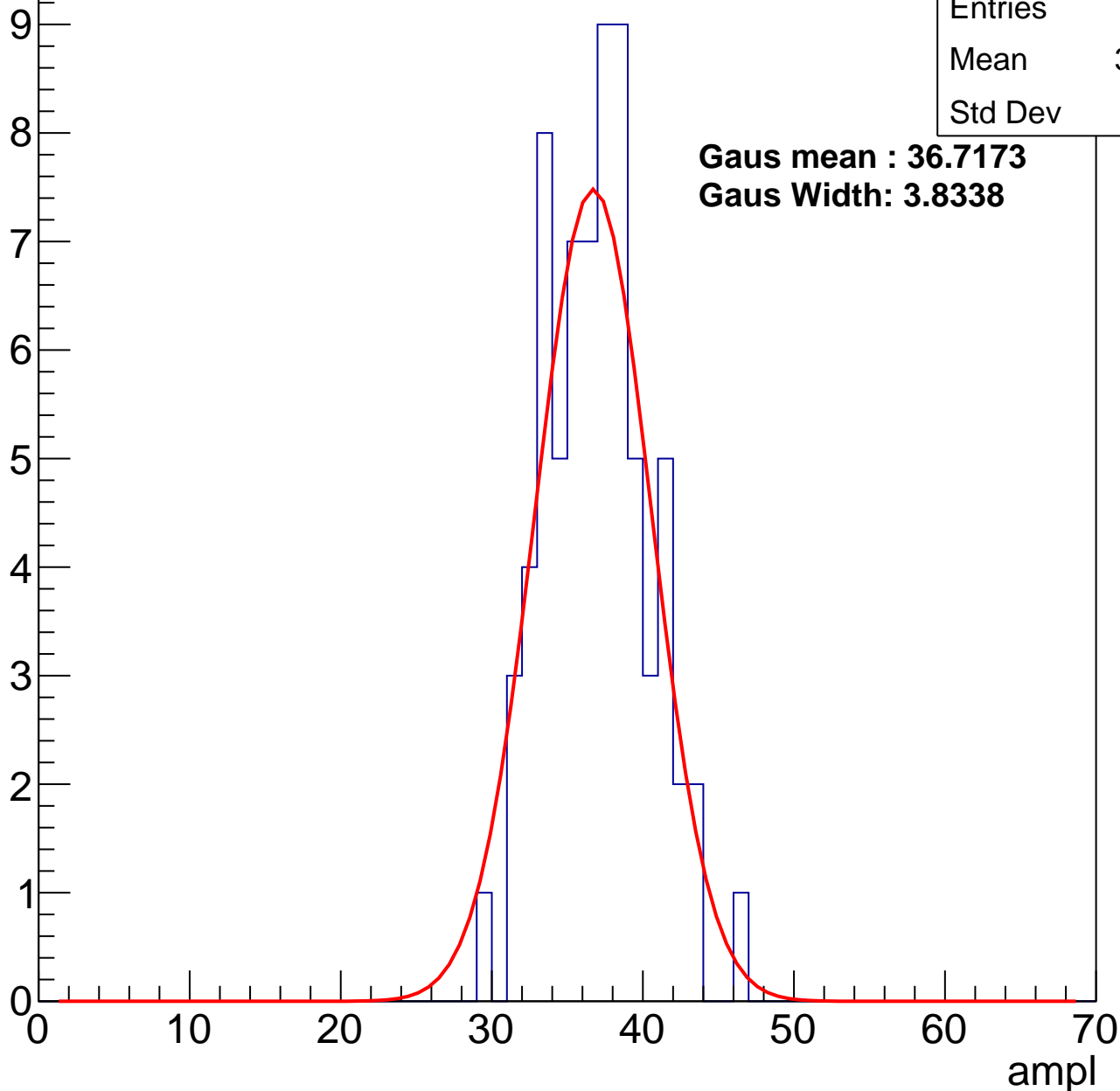
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.51
Std Dev	3.36

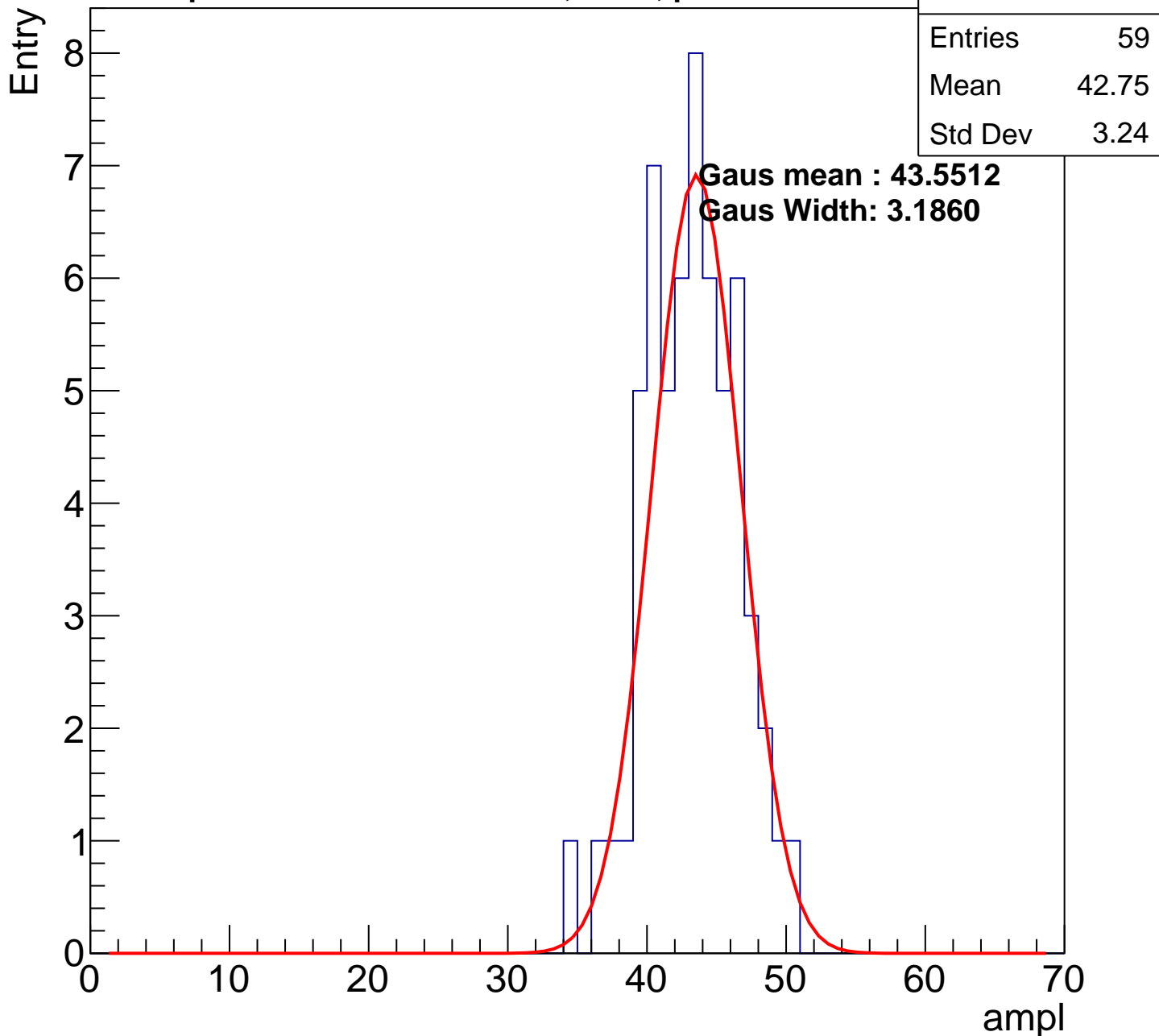
Gaus mean : 36.7173

Gaus Width: 3.8338



B1L103S, U12-ch60, adc2

calib_packv5_042523_0143.root, FC#7, port C2

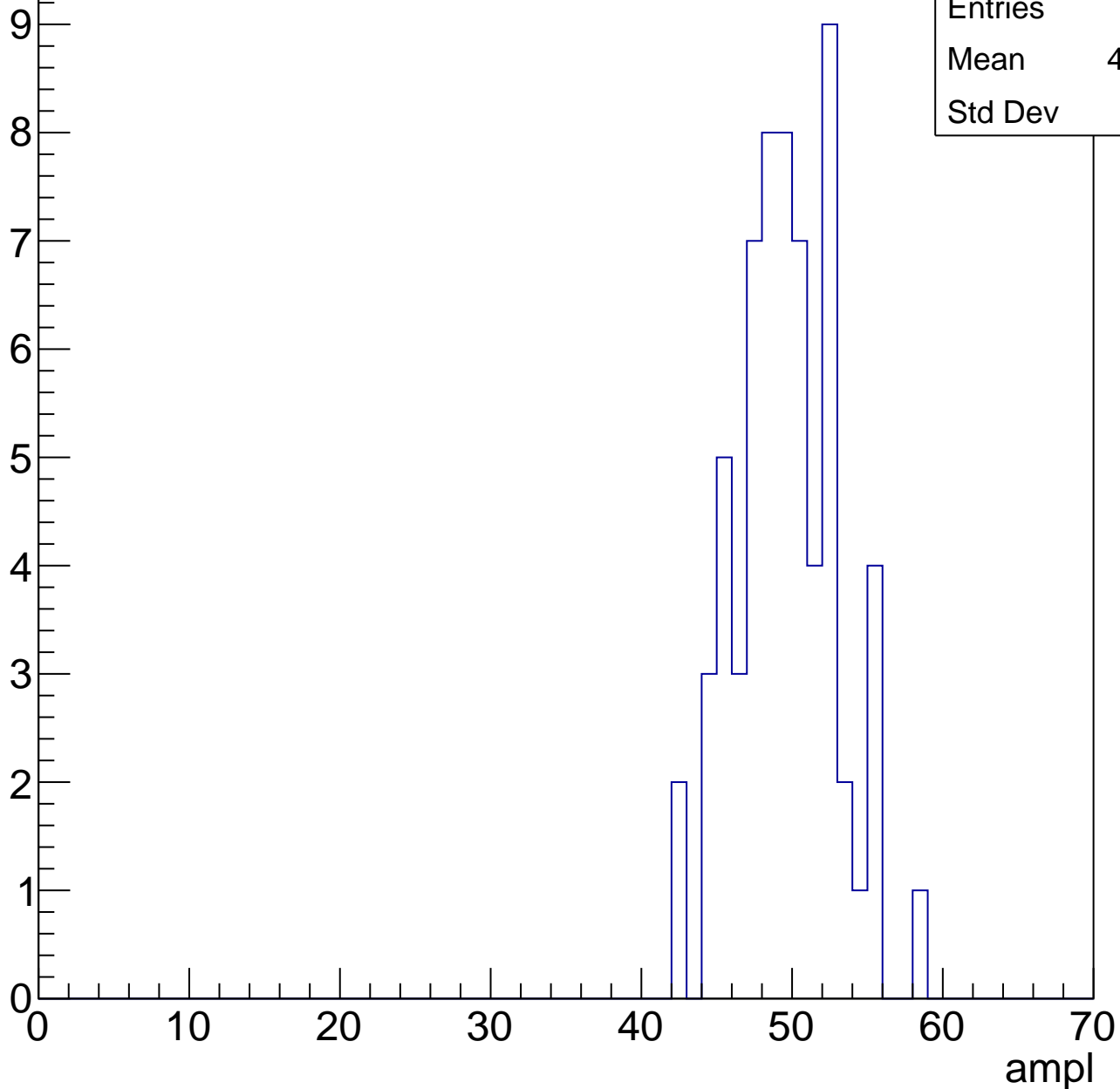


B1L103S, U12-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.12
Std Dev	3.31

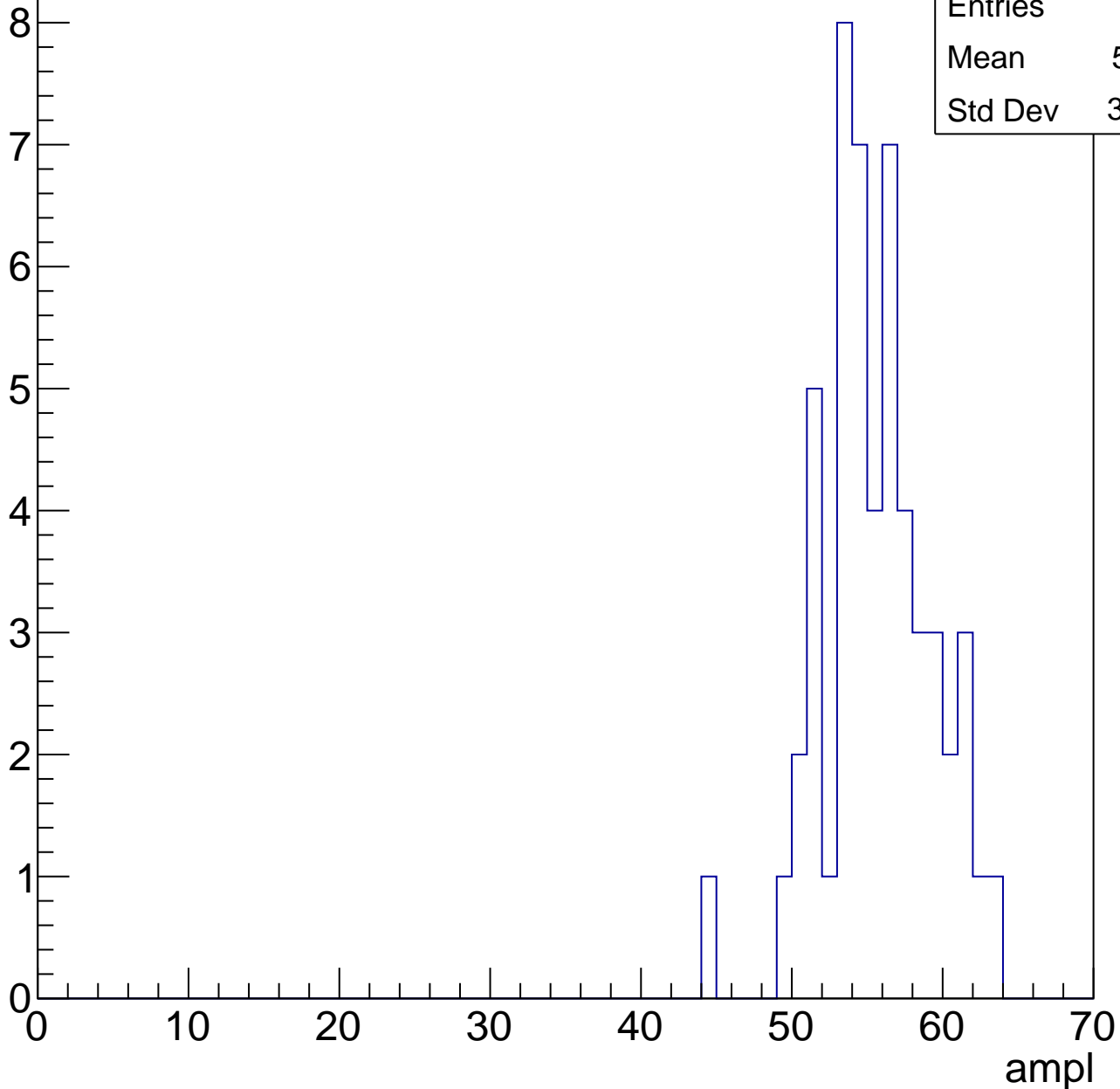


B1L103S, U12-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.11
Std Dev	3.643

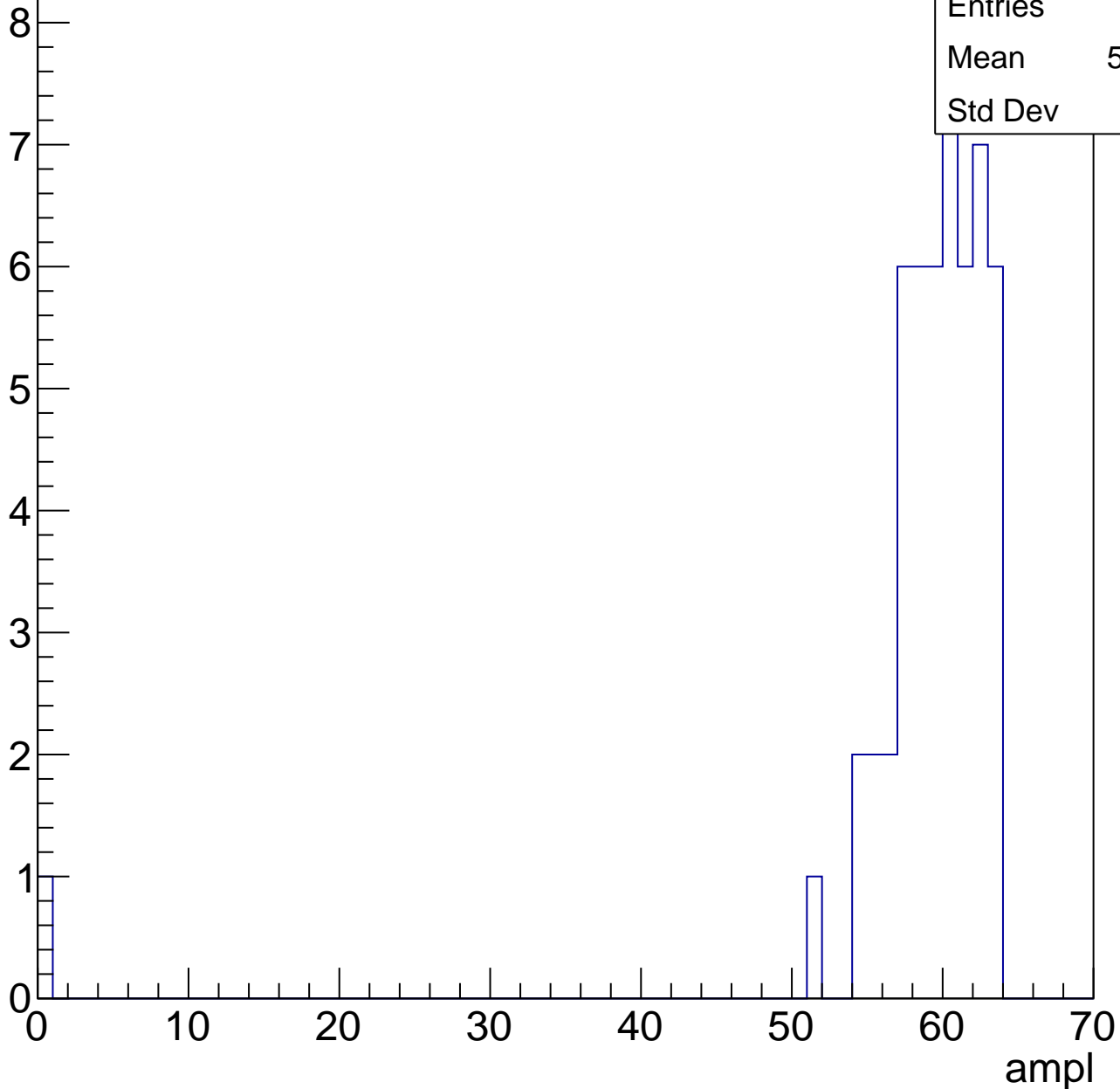


B1L103S, U12-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	58.17
Std Dev	8.5



B1L103S, U12-ch60, adc6

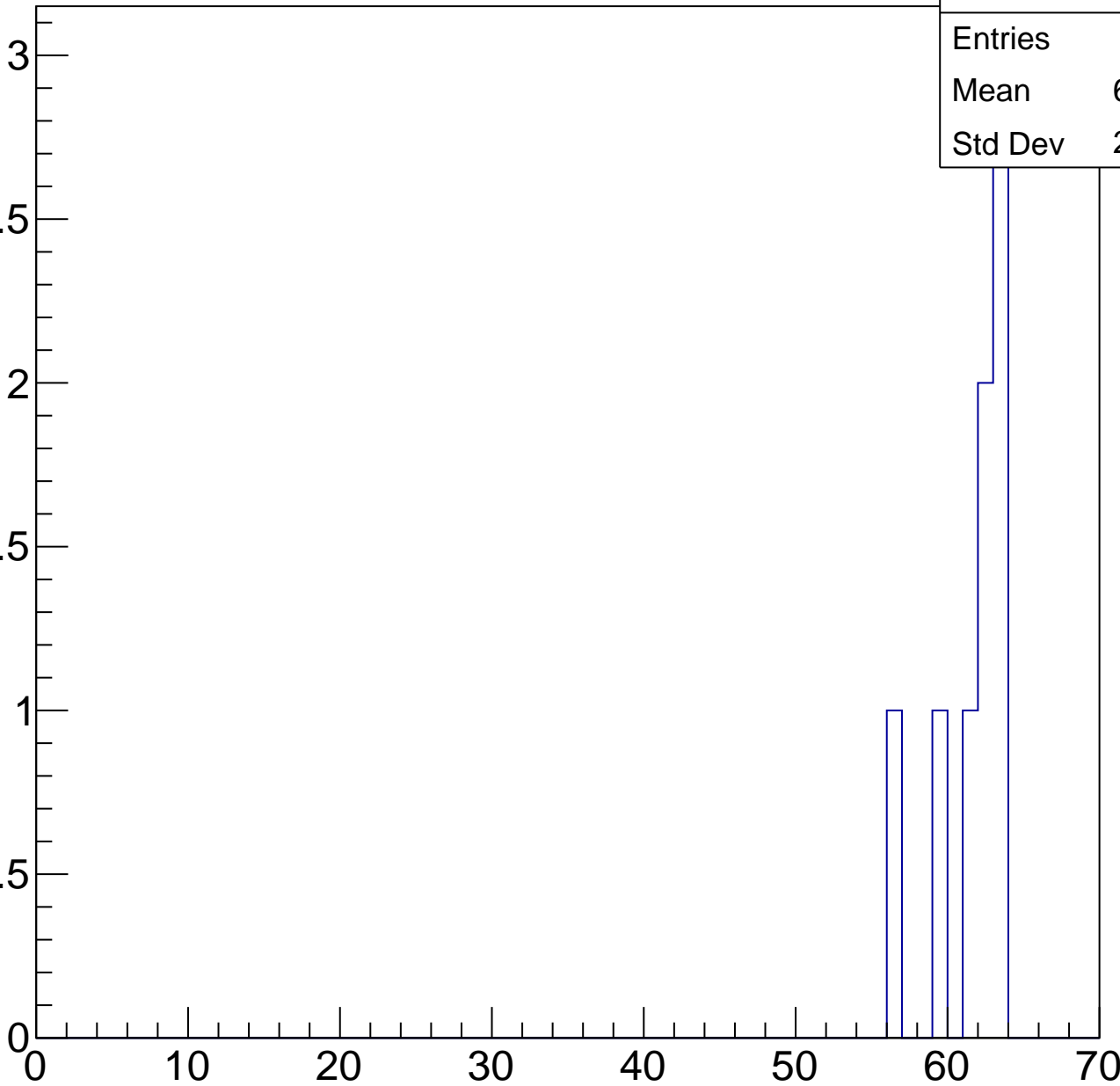
calib_packv5_042523_0143.root, FC#7, port C2

Entry

3
2.5
2
1.5
1
0.5
0

Entries	8
Mean	61.12
Std Dev	2.315

ampl



B1L103S, U12-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch61, adc0

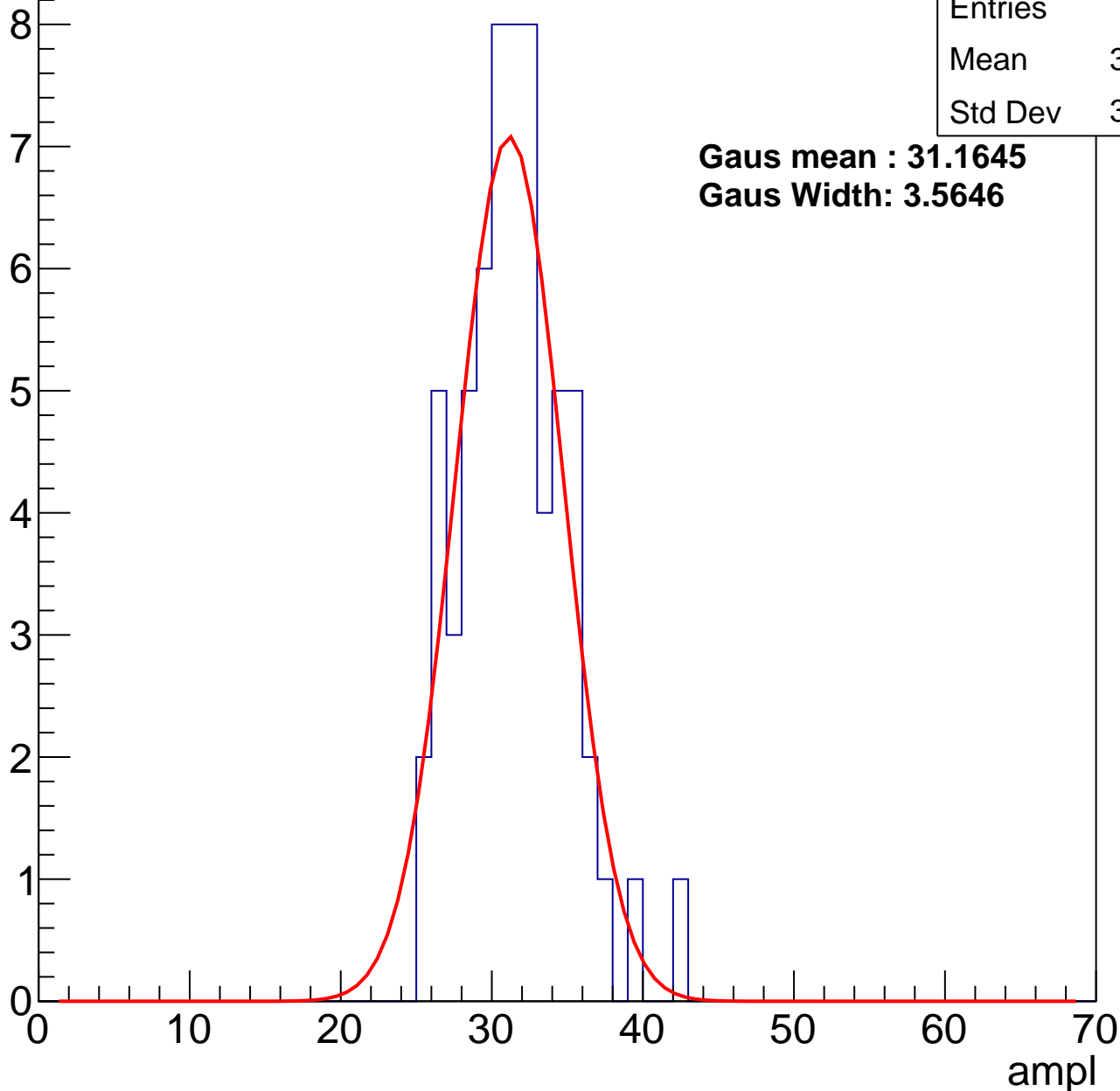
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	31.03
Std Dev	3.405

Gaus mean : 31.1645

Gaus Width: 3.5646



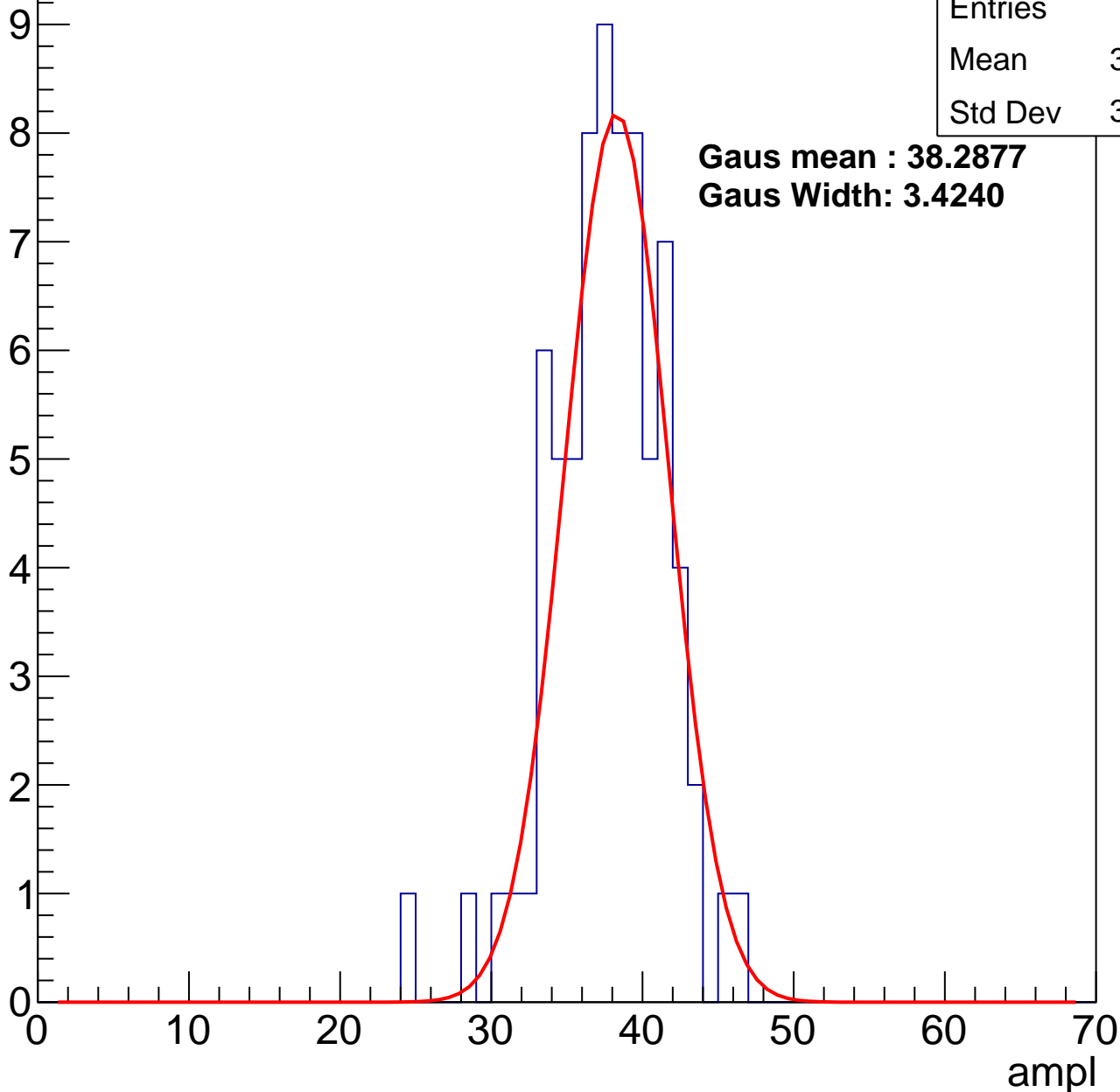
B1L103S, U12-ch61, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	37.26
Std Dev	3.745

Gaus mean : 38.2877
Gaus Width: 3.4240



B1L103S, U12-ch61, adc2

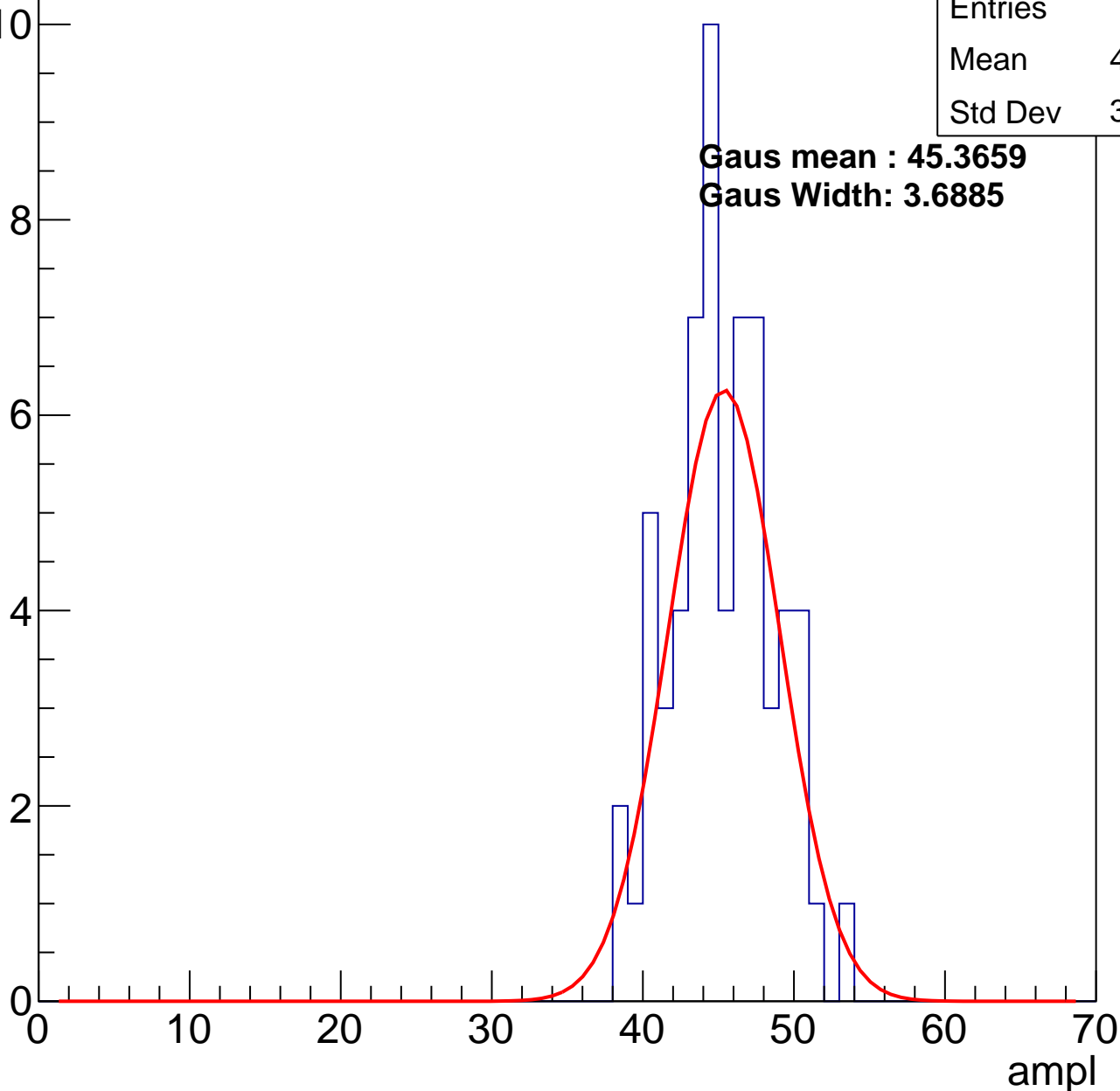
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.79
Std Dev	3.344

Gaus mean : 45.3659

Gaus Width: 3.6885

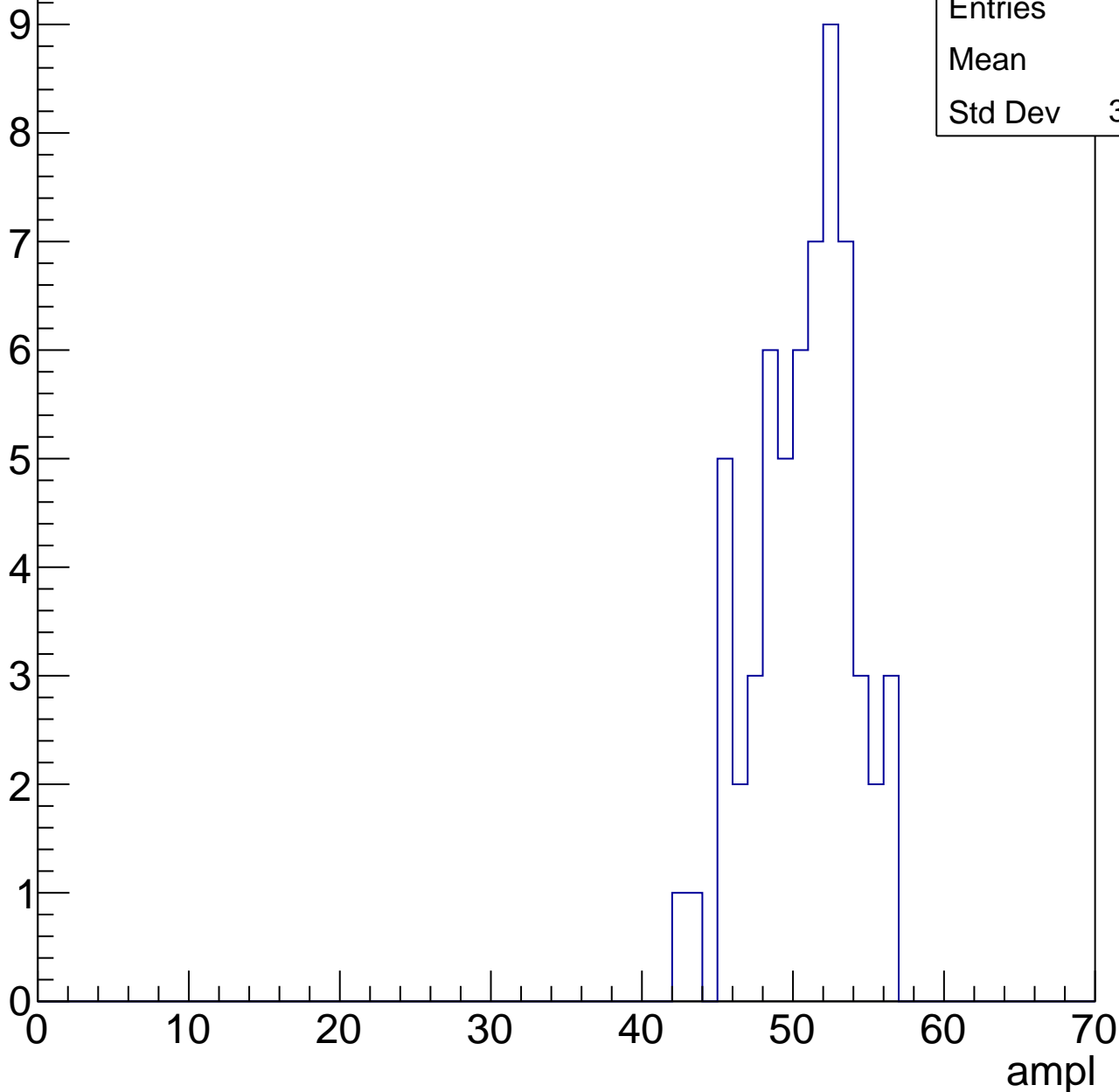


B1L103S, U12-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	50.2
Std Dev	3.255

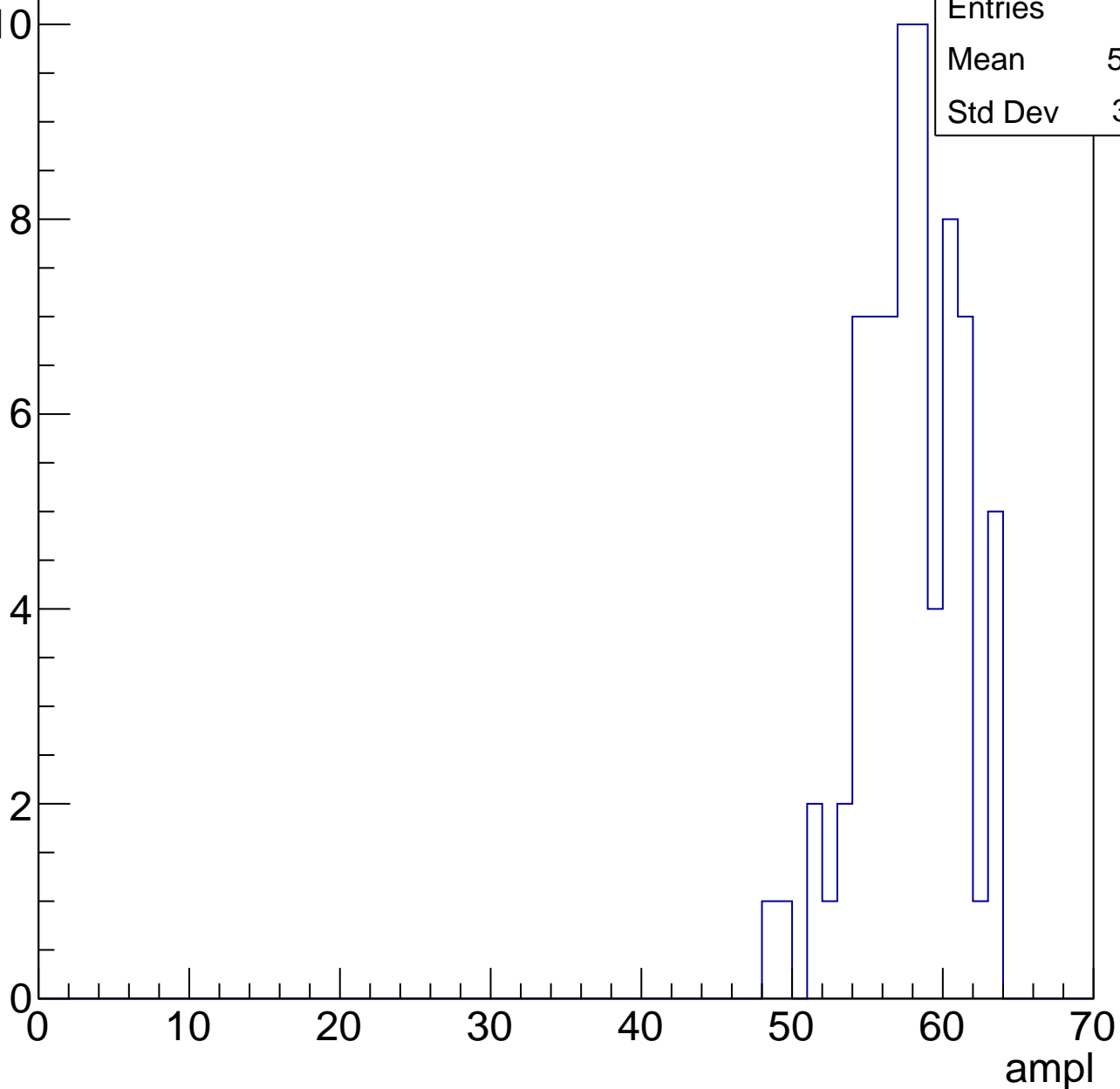


B1L103S, U12-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	57.29
Std Dev	3.271

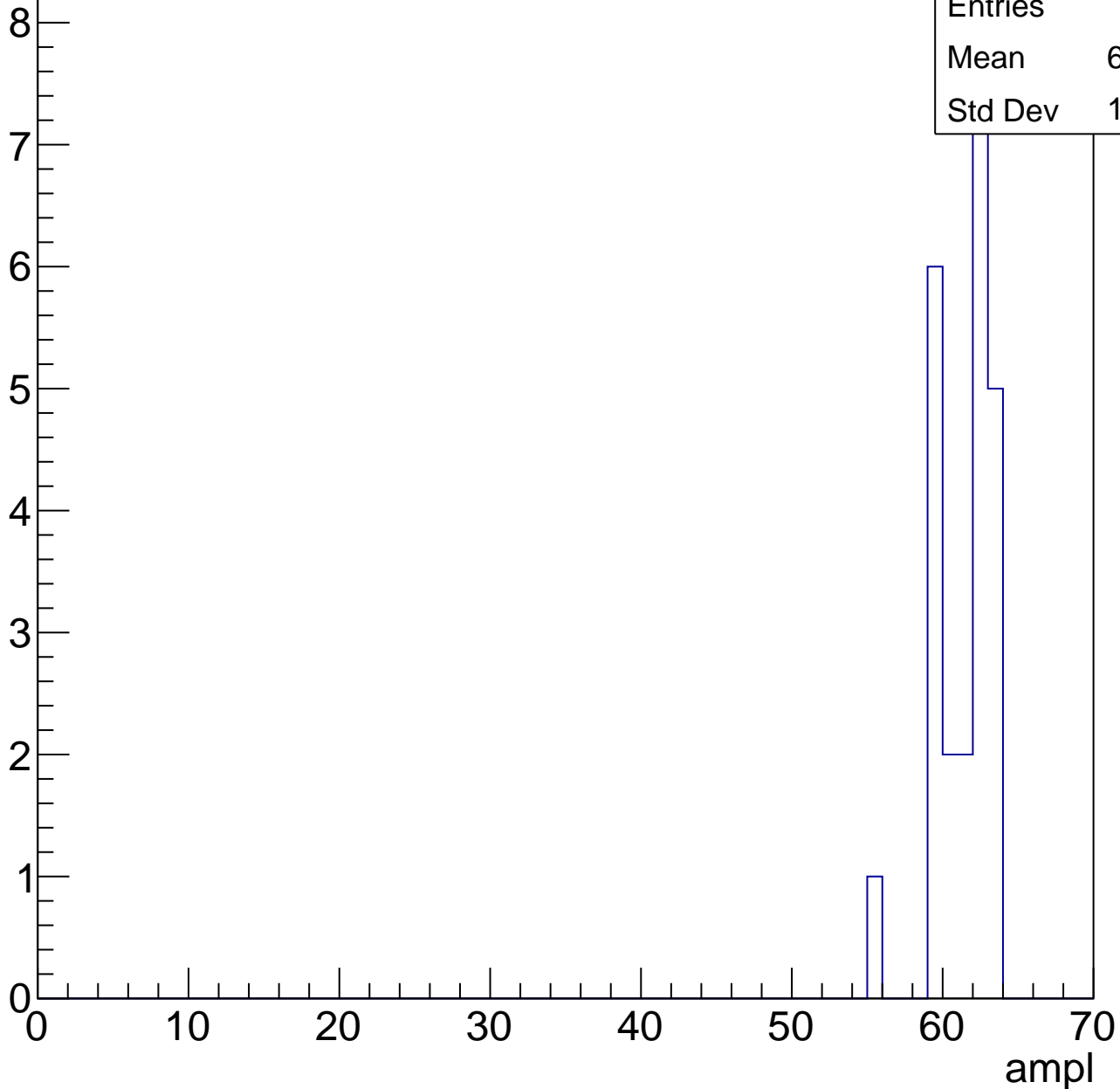


B1L103S, U12-ch61, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	24
Mean	60.92
Std Dev	1.935



B1L103S, U12-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

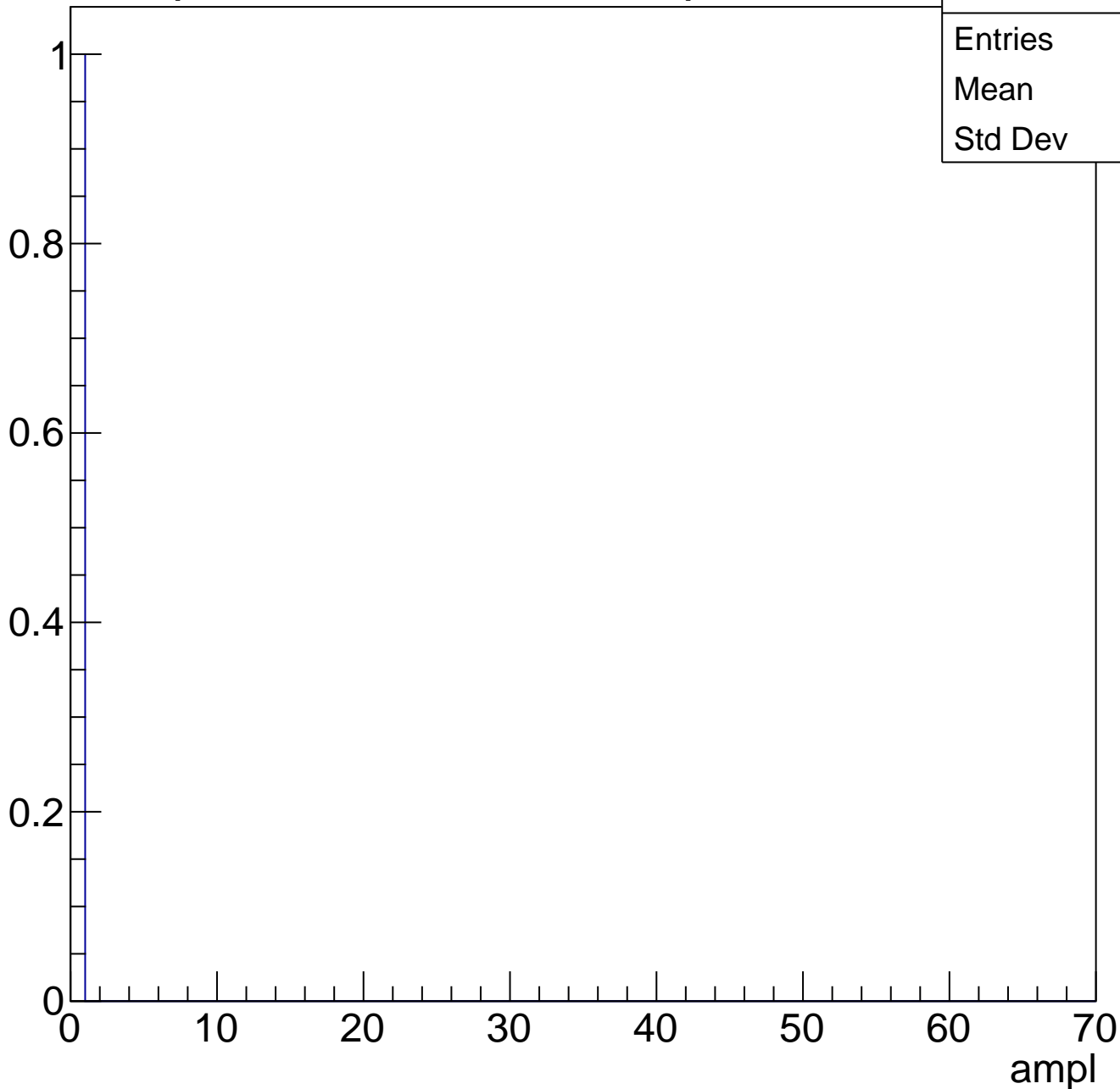
Entry



B1L103S, U12-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch62, adc0

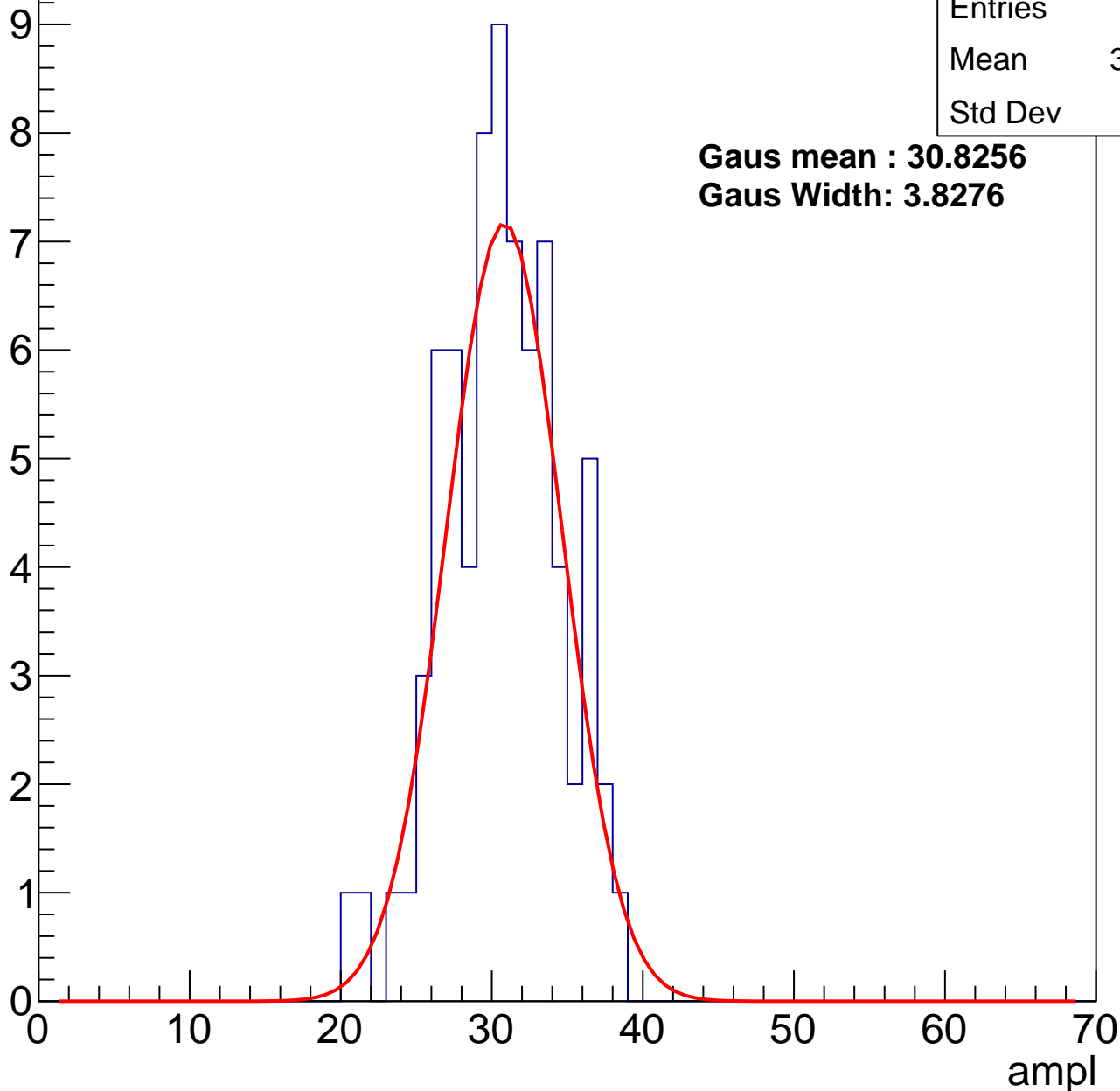
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.18
Std Dev	3.79

Gaus mean : 30.8256

Gaus Width: 3.8276



B1L103S, U12-ch62, adc1

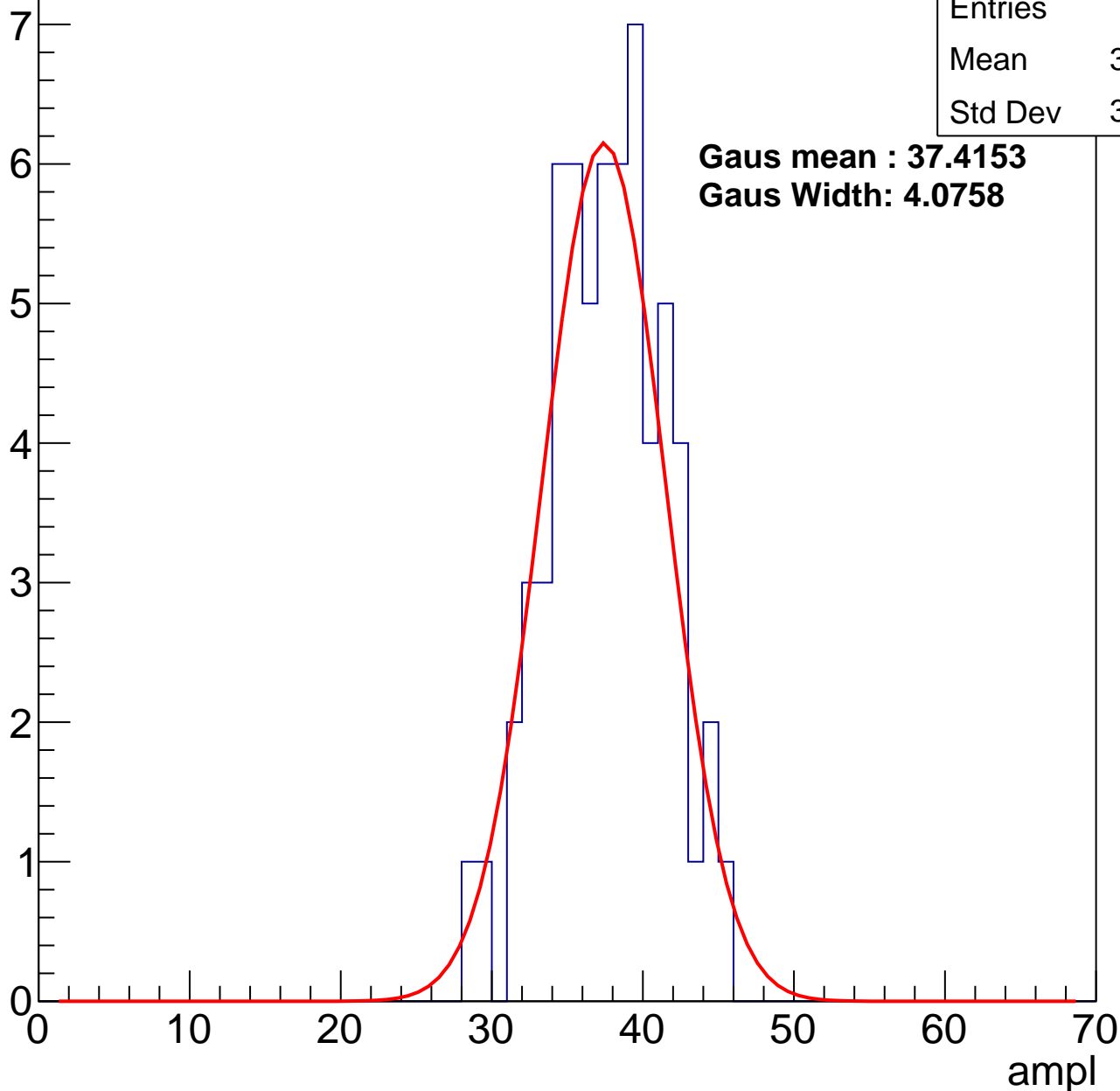
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.14
Std Dev	3.716

Gaus mean : 37.4153

Gaus Width: 4.0758



B1L103S, U12-ch62, adc2

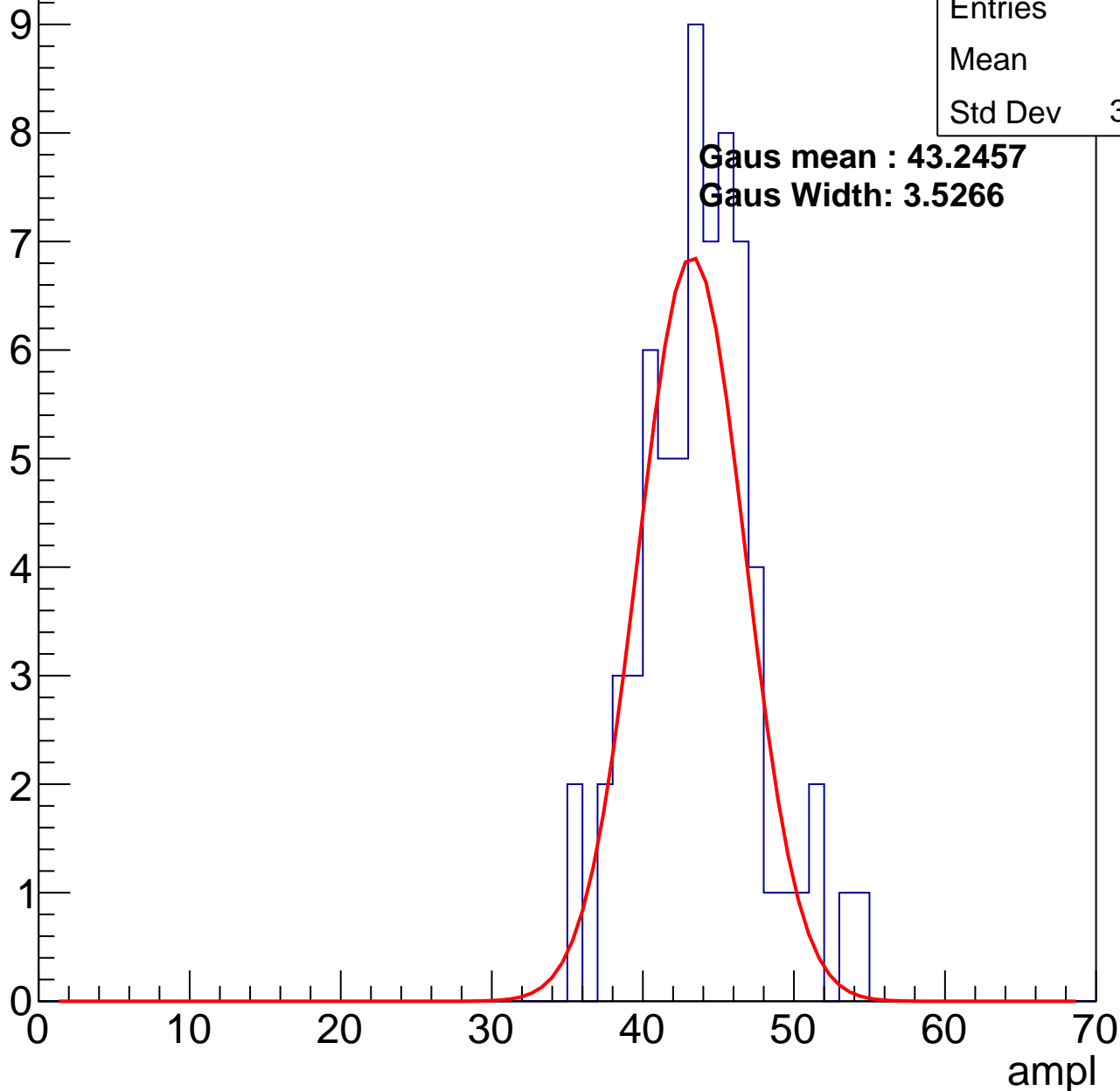
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.4
Std Dev	3.866

Gaus mean : 43.2457

Gaus Width: 3.5266

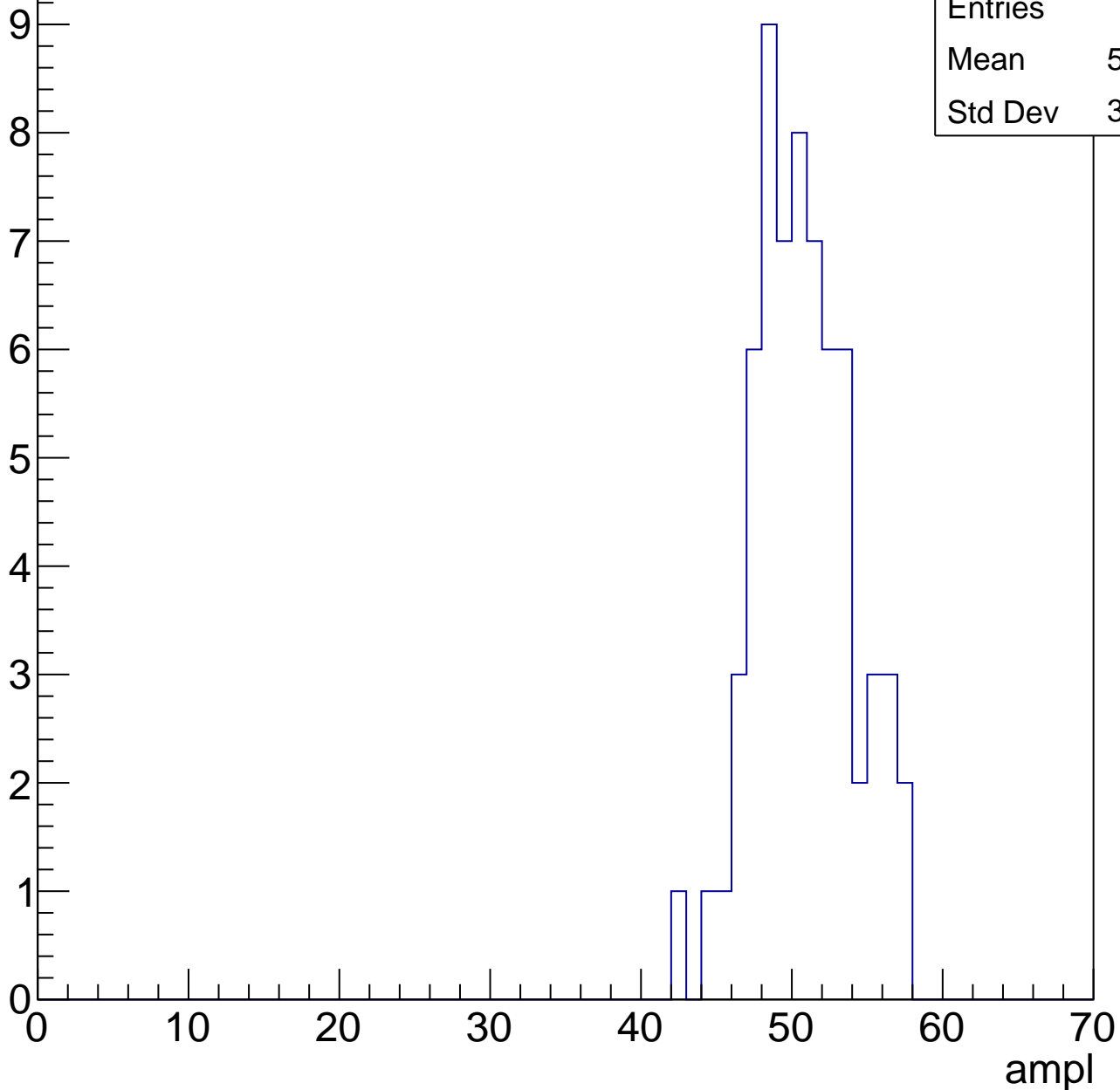


B1L103S, U12-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	50.28
Std Dev	3.194

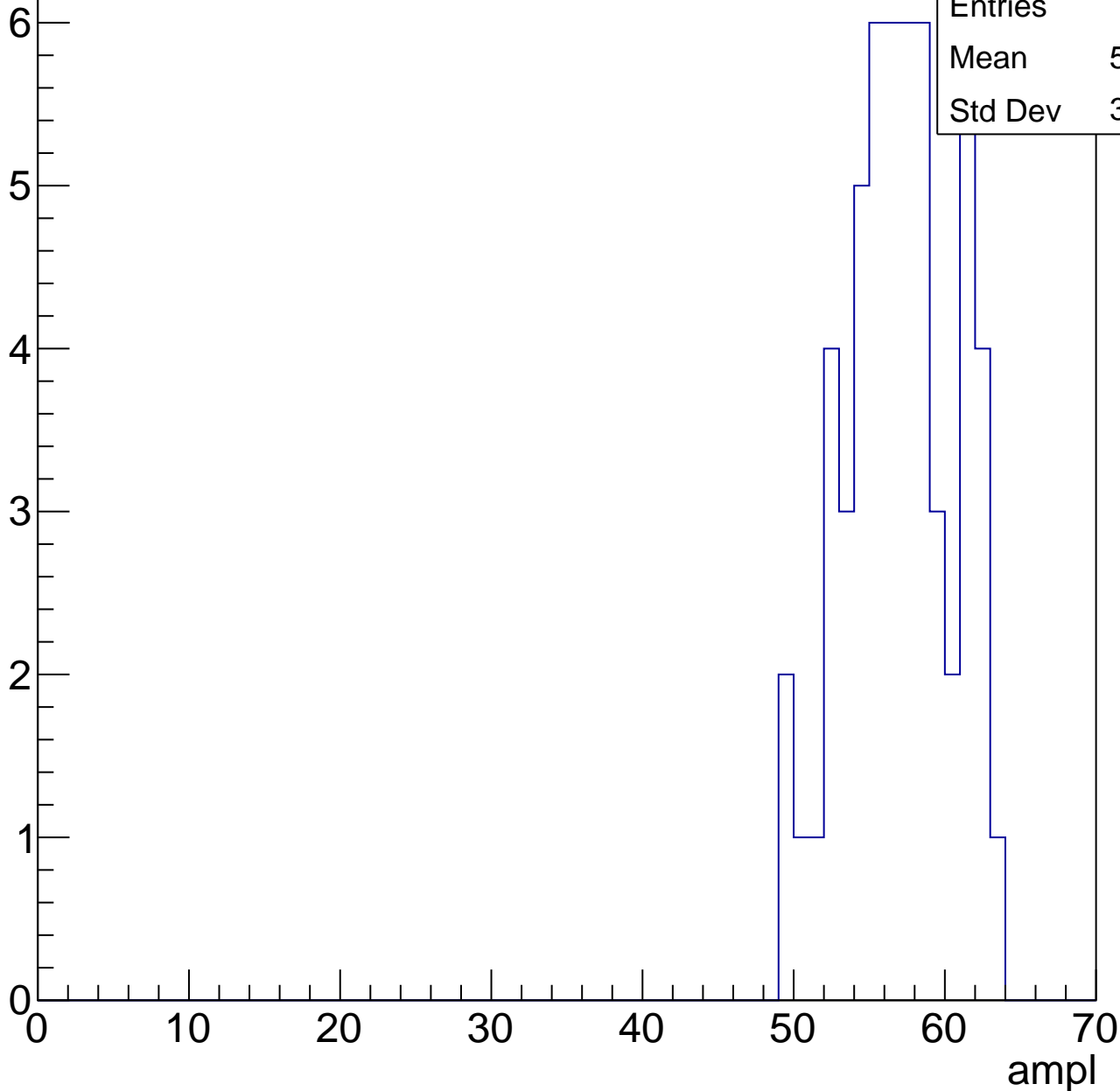


B1L103S, U12-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	56.54
Std Dev	3.515

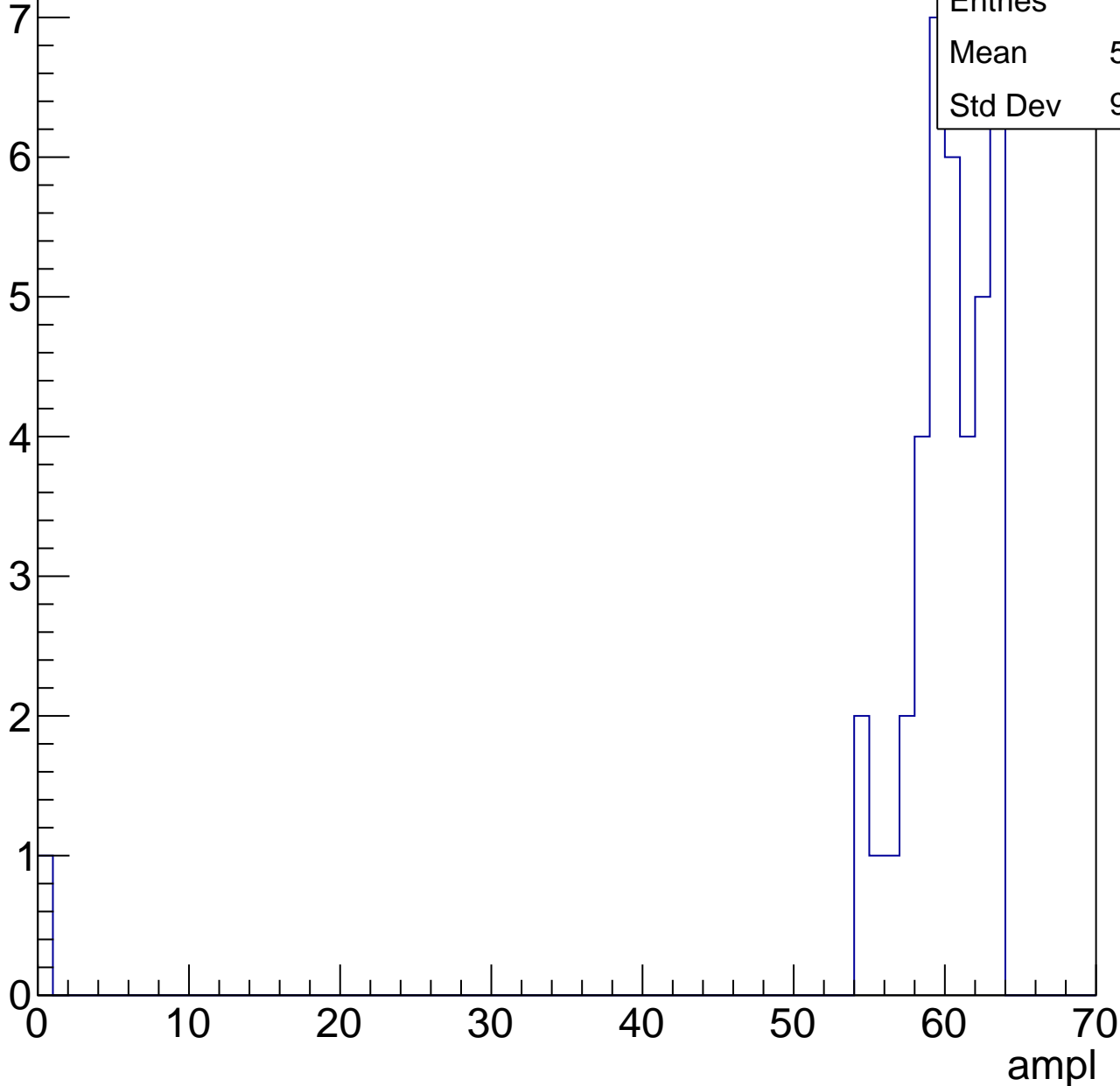


B1L103S, U12-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.33
Std Dev	9.655



B1L103S, U12-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.6
Std Dev	1.356

ampl

0 10 20 30 40 50 60 70

B1L103S, U12-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch63, adc0

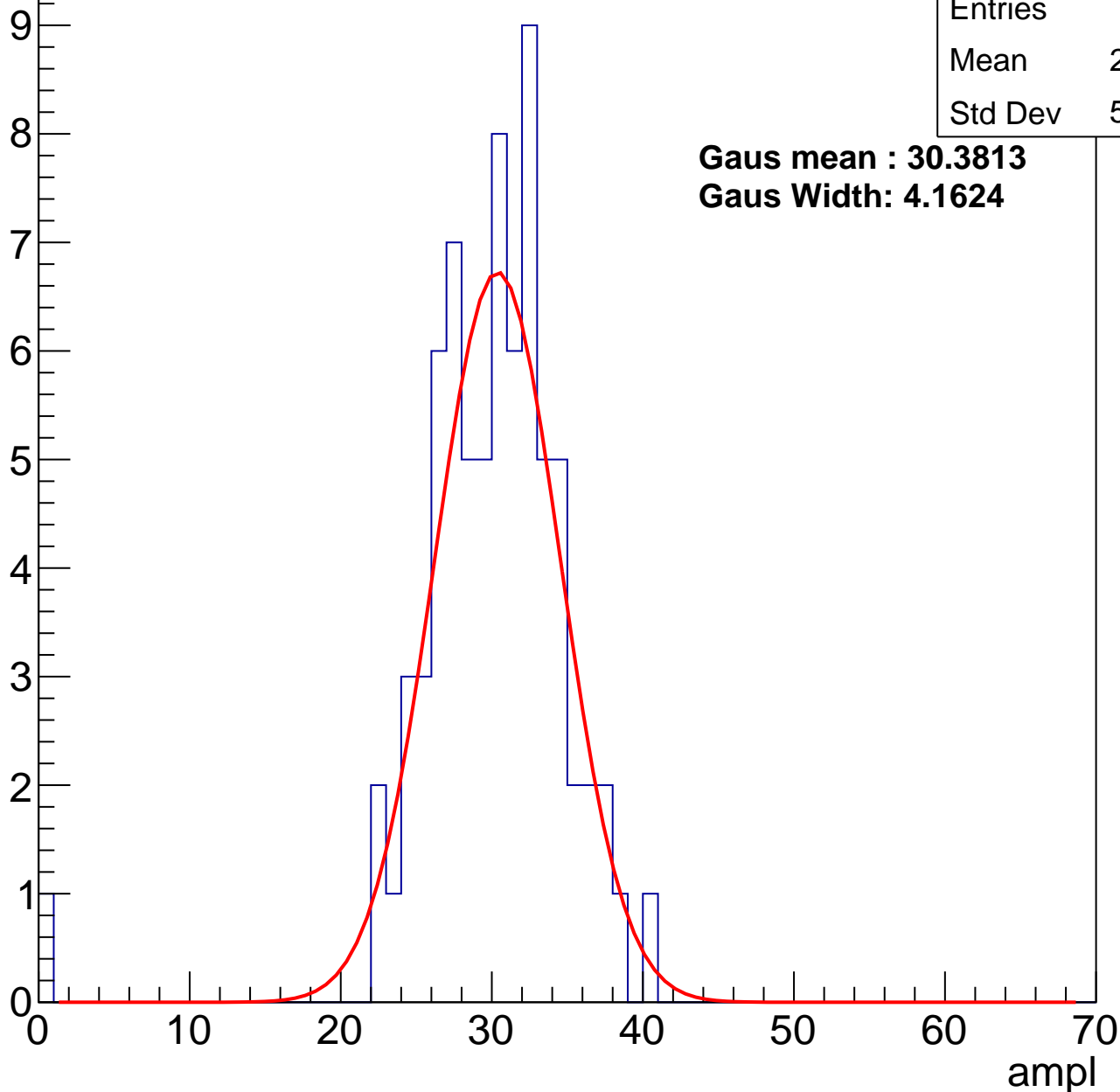
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	29.55
Std Dev	5.173

Gaus mean : 30.3813

Gaus Width: 4.1624



B1L103S, U12-ch63, adc1

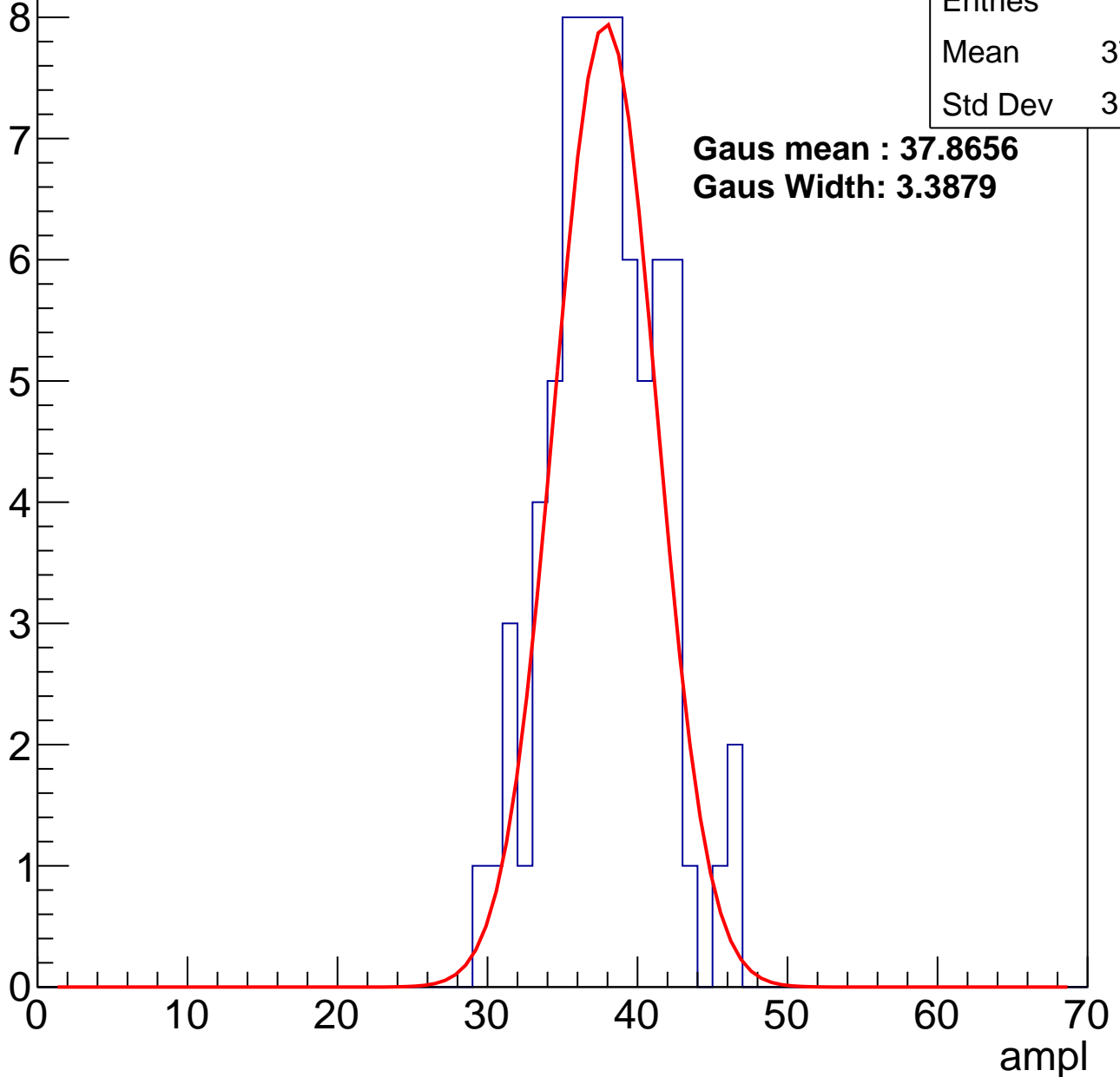
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	37.38
Std Dev	3.619

Gaus mean : 37.8656

Gaus Width: 3.3879



B1L103S, U12-ch63, adc2

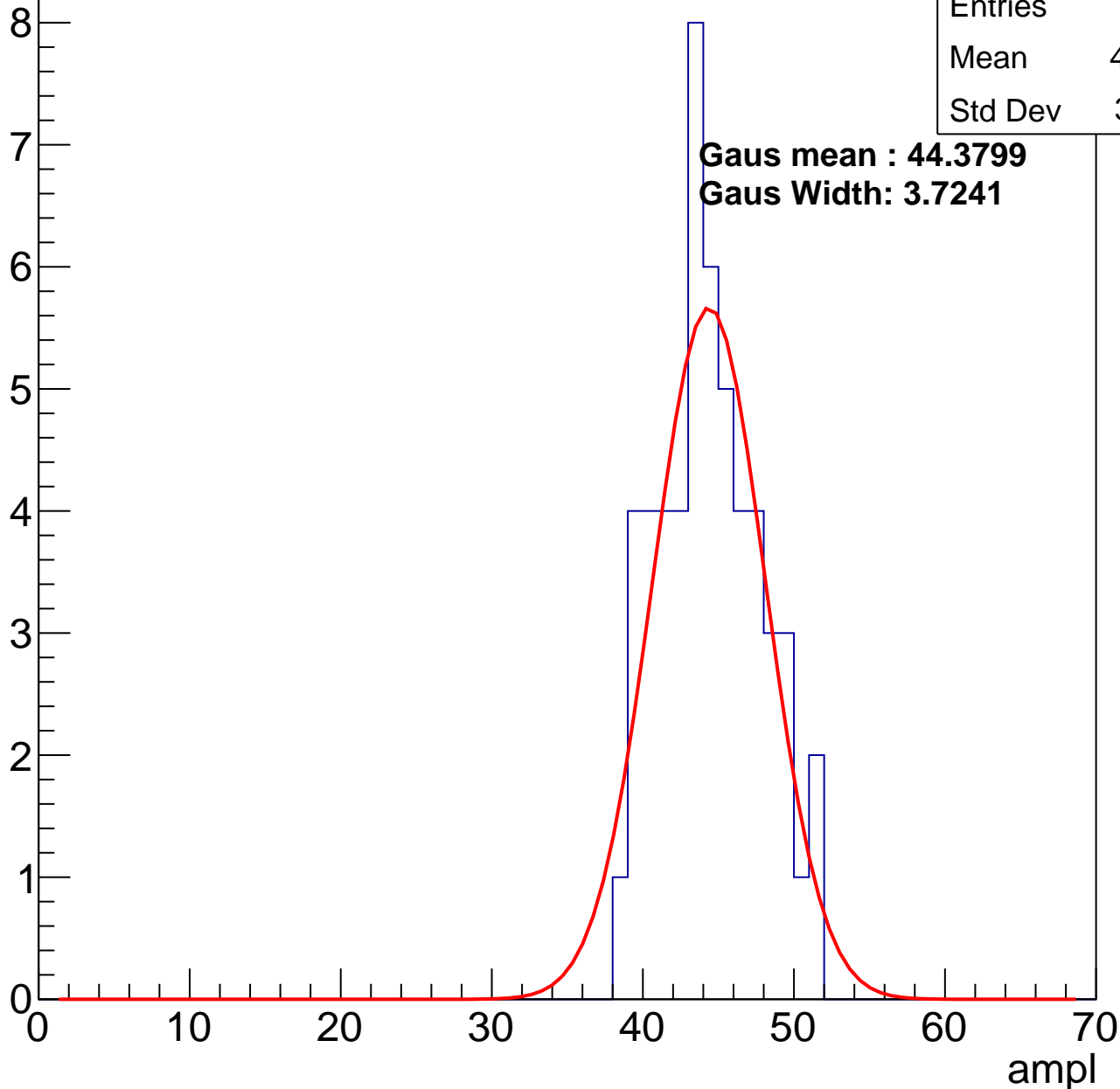
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	44.04
Std Dev	3.291

Gaus mean : 44.3799

Gaus Width: 3.7241

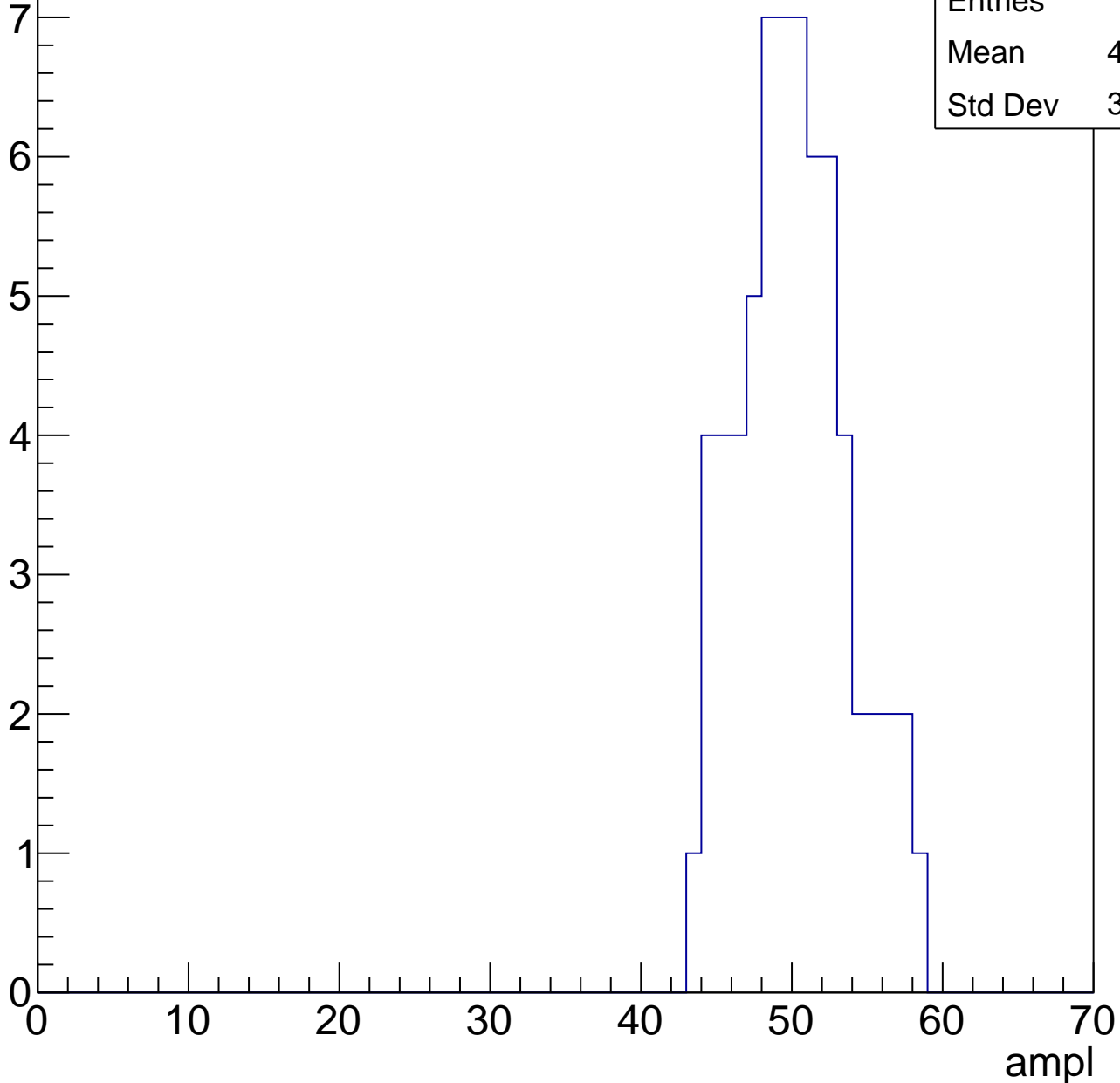


B1L103S, U12-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	49.67
Std Dev	3.562

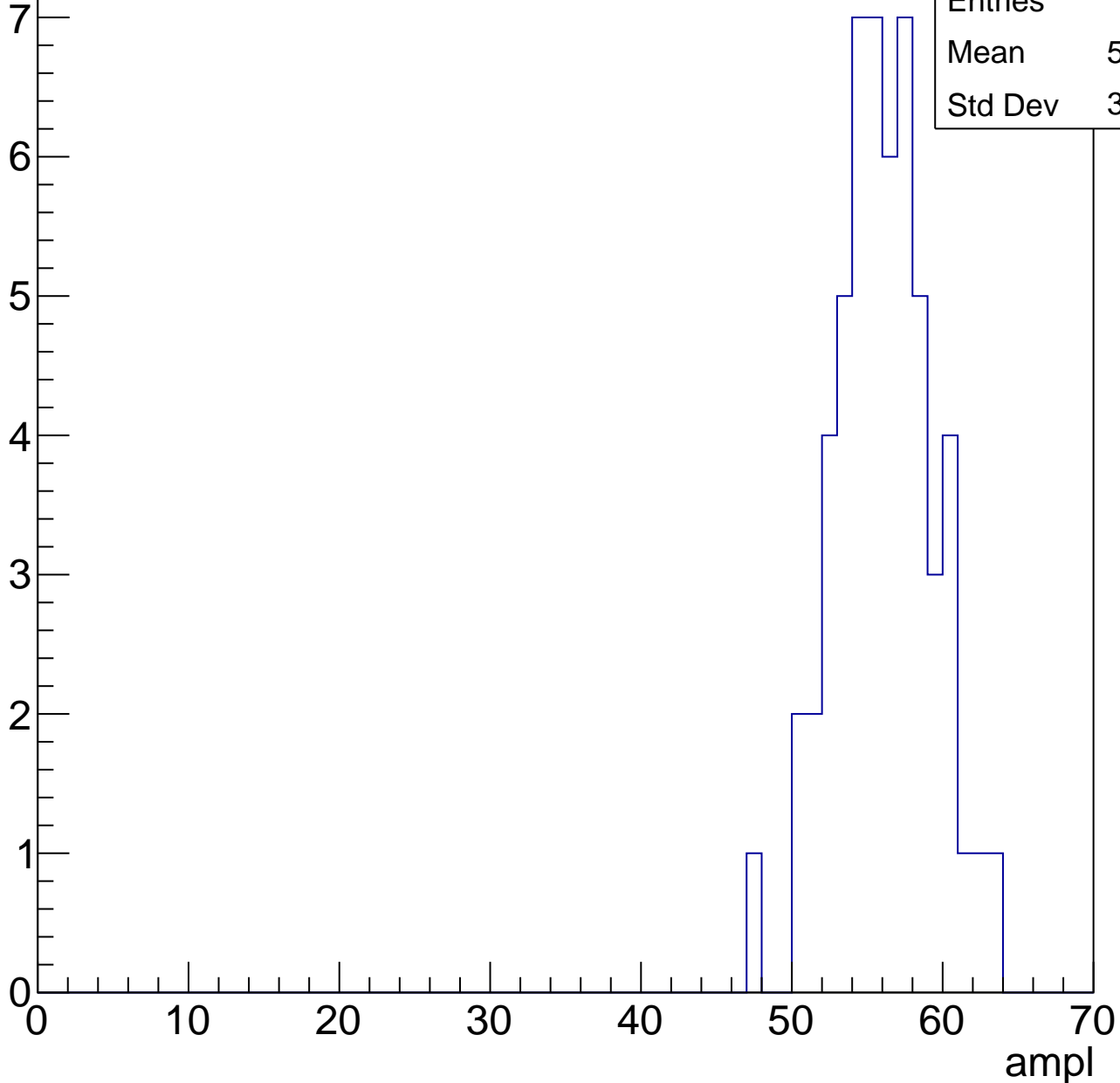


B1L103S, U12-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.59
Std Dev	3.178

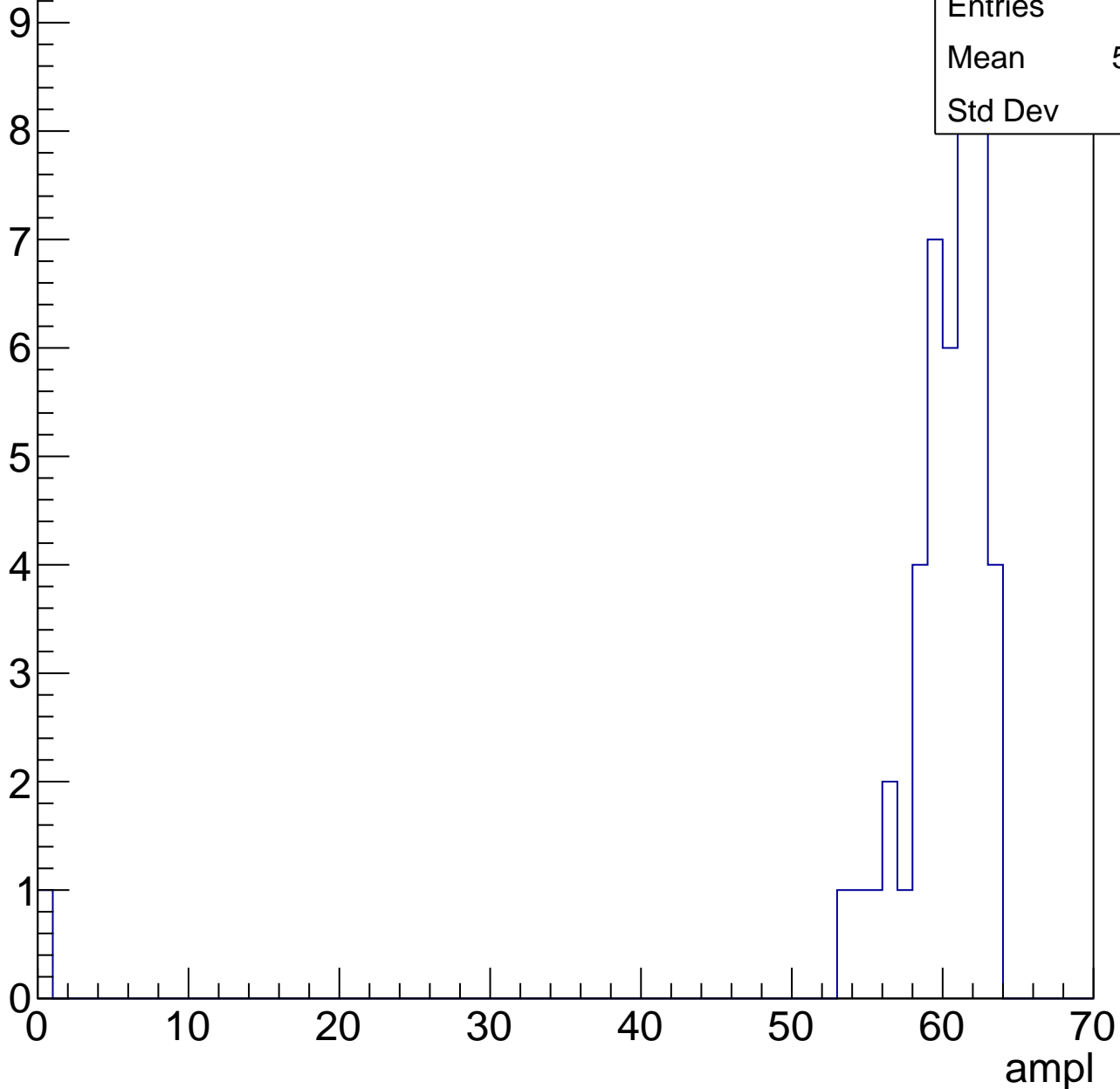


B1L103S, U12-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.51
Std Dev	9.13



B1L103S, U12-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	63
Std Dev	0

B1L103S, U12-ch64, adc0

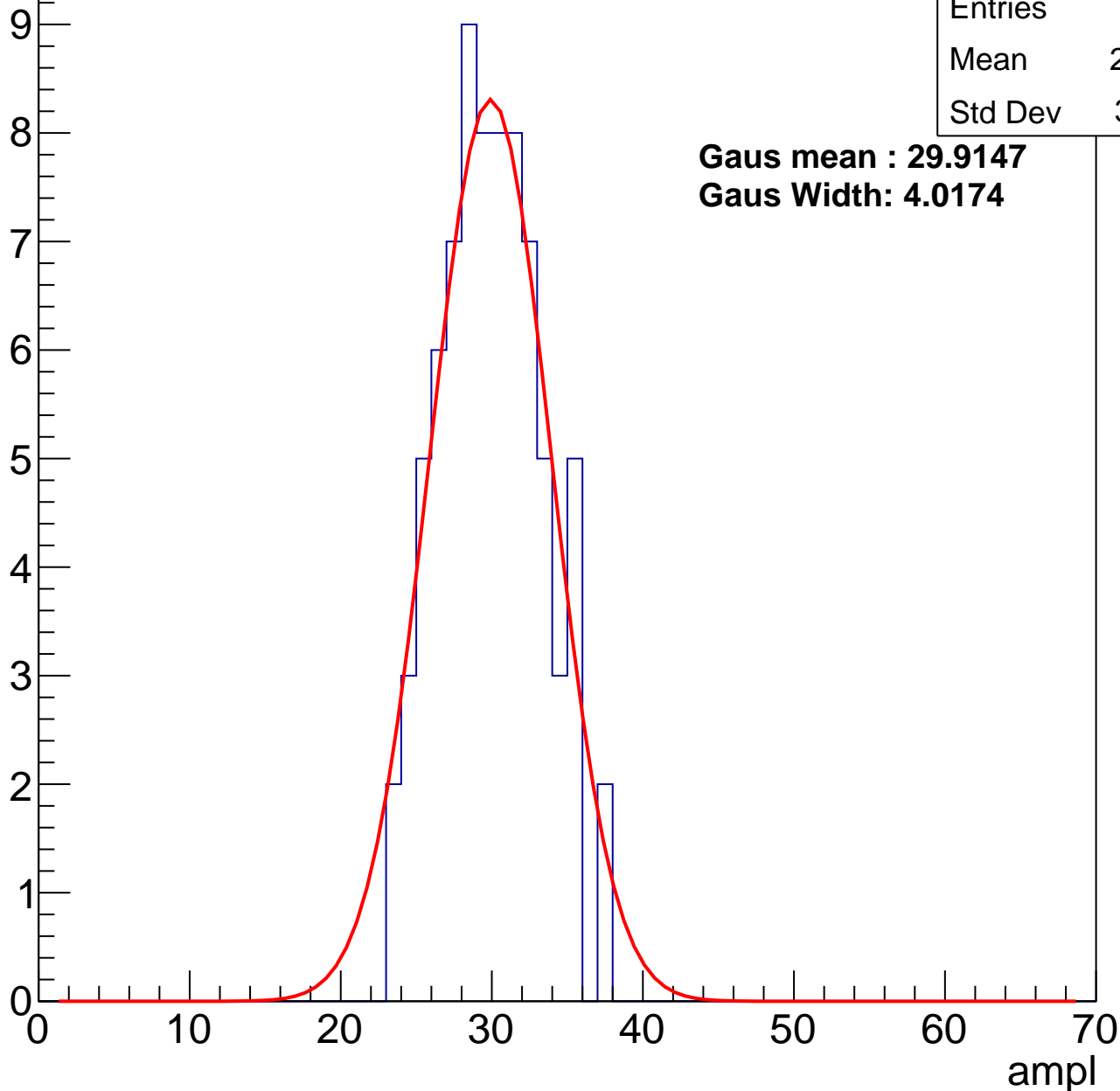
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.49
Std Dev	3.331

Gaus mean : 29.9147

Gaus Width: 4.0174



B1L103S, U12-ch64, adc1

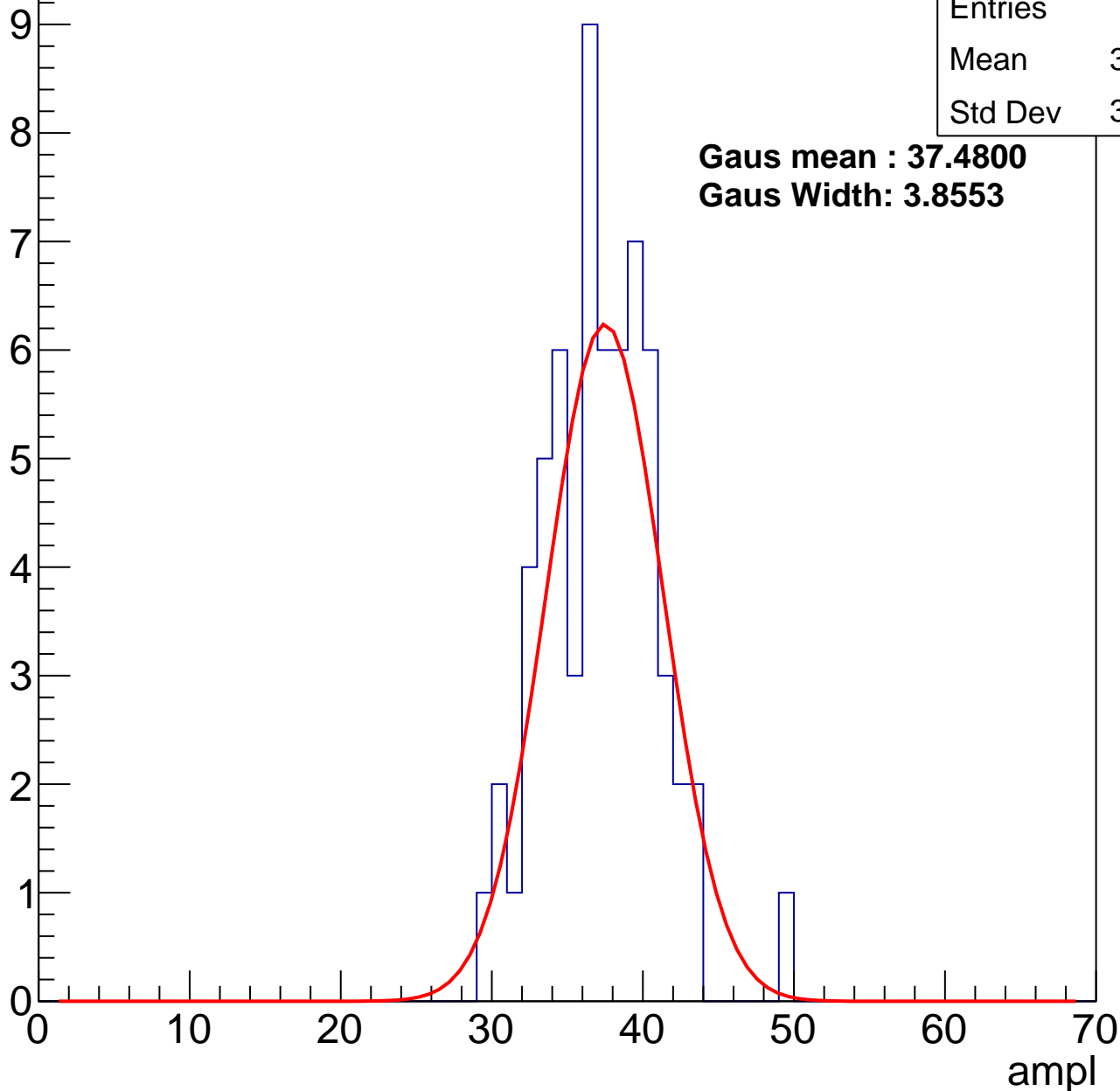
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.73
Std Dev	3.645

Gaus mean : 37.4800

Gaus Width: 3.8553



B1L103S, U12-ch64, adc2

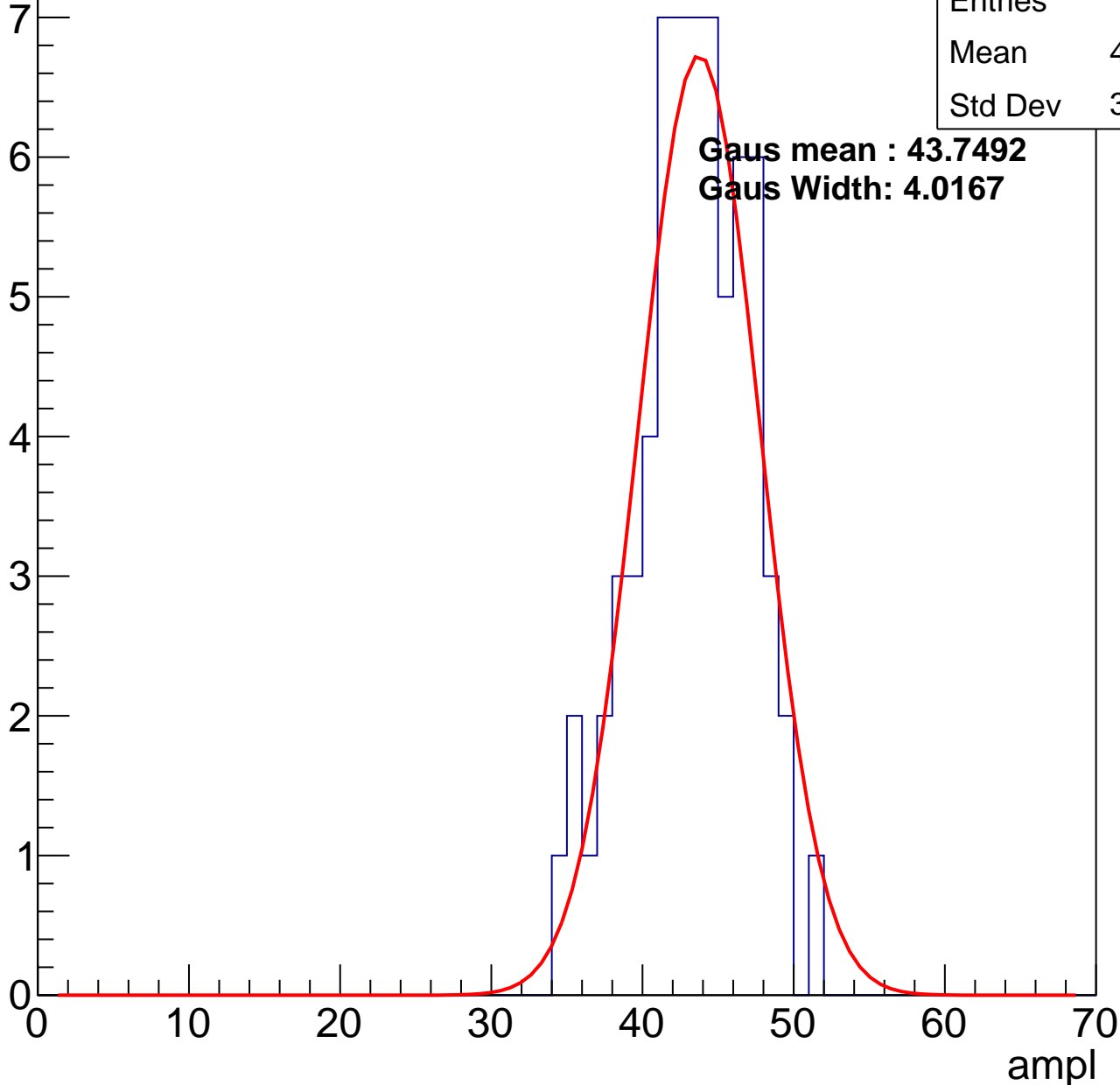
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.85
Std Dev	3.686

Gaus mean : 43.7492

Gaus Width: 4.0167

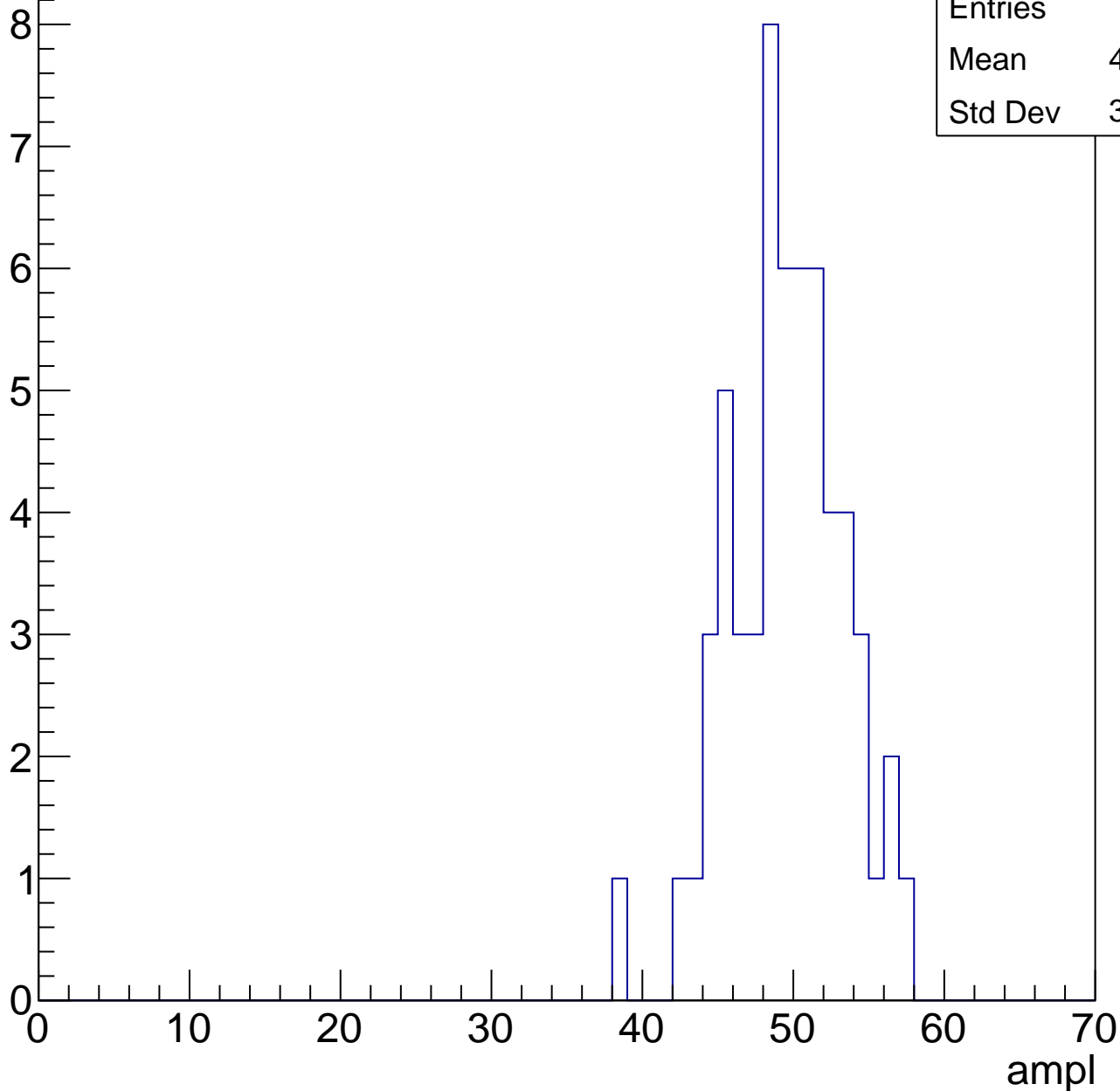


B1L103S, U12-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.12
Std Dev	3.737

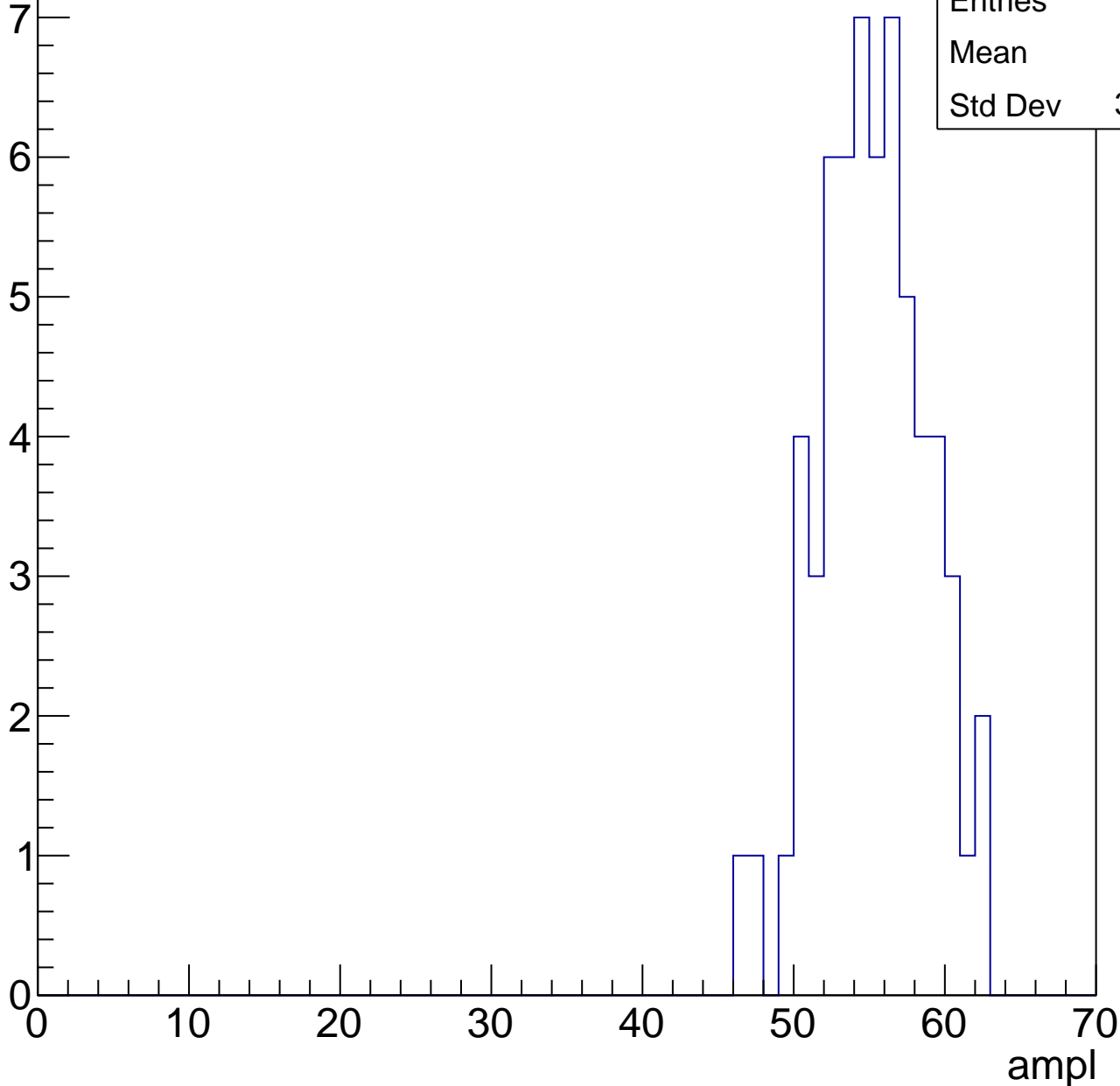


B1L103S, U12-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	54.8
Std Dev	3.501

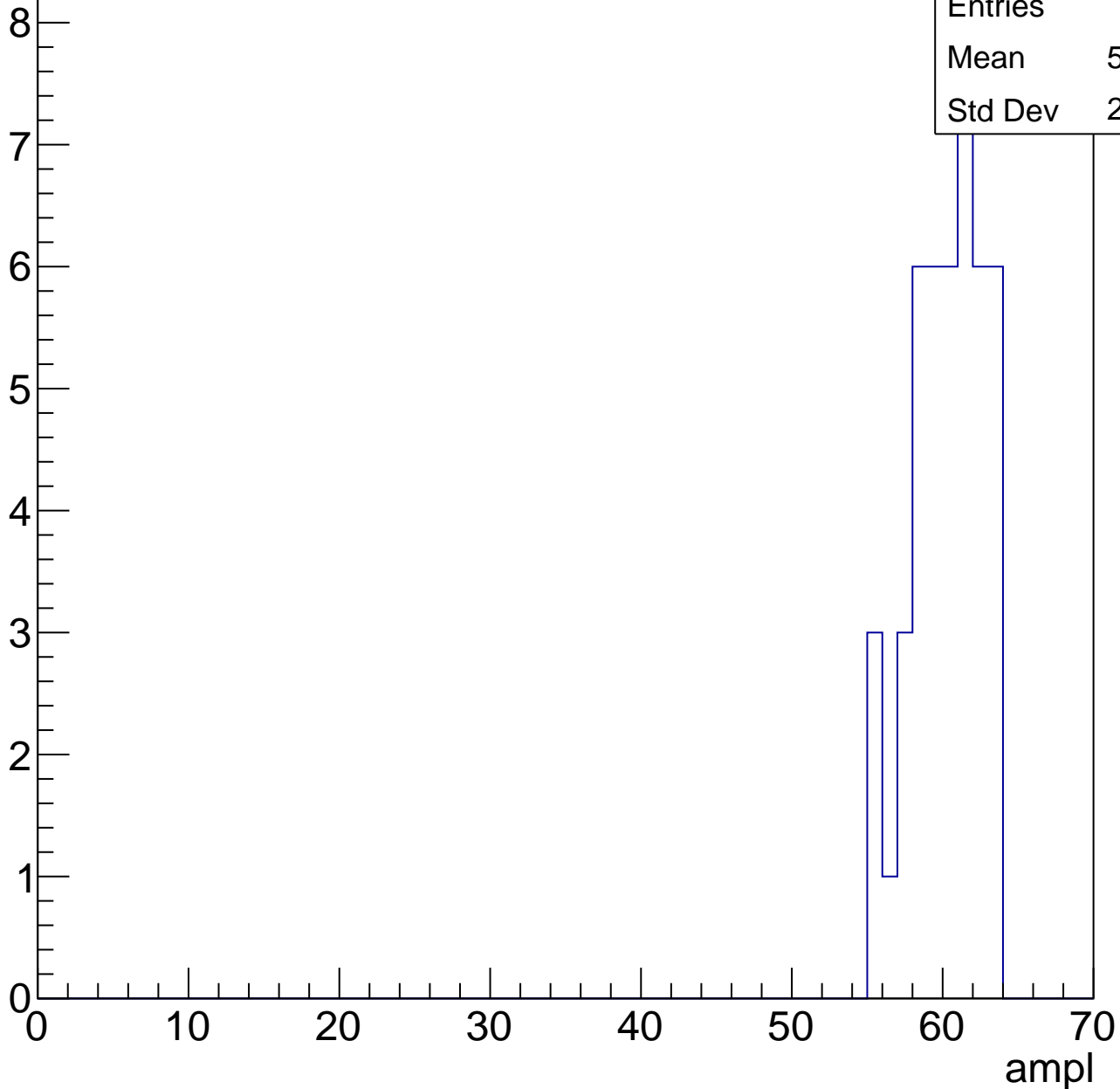


B1L103S, U12-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

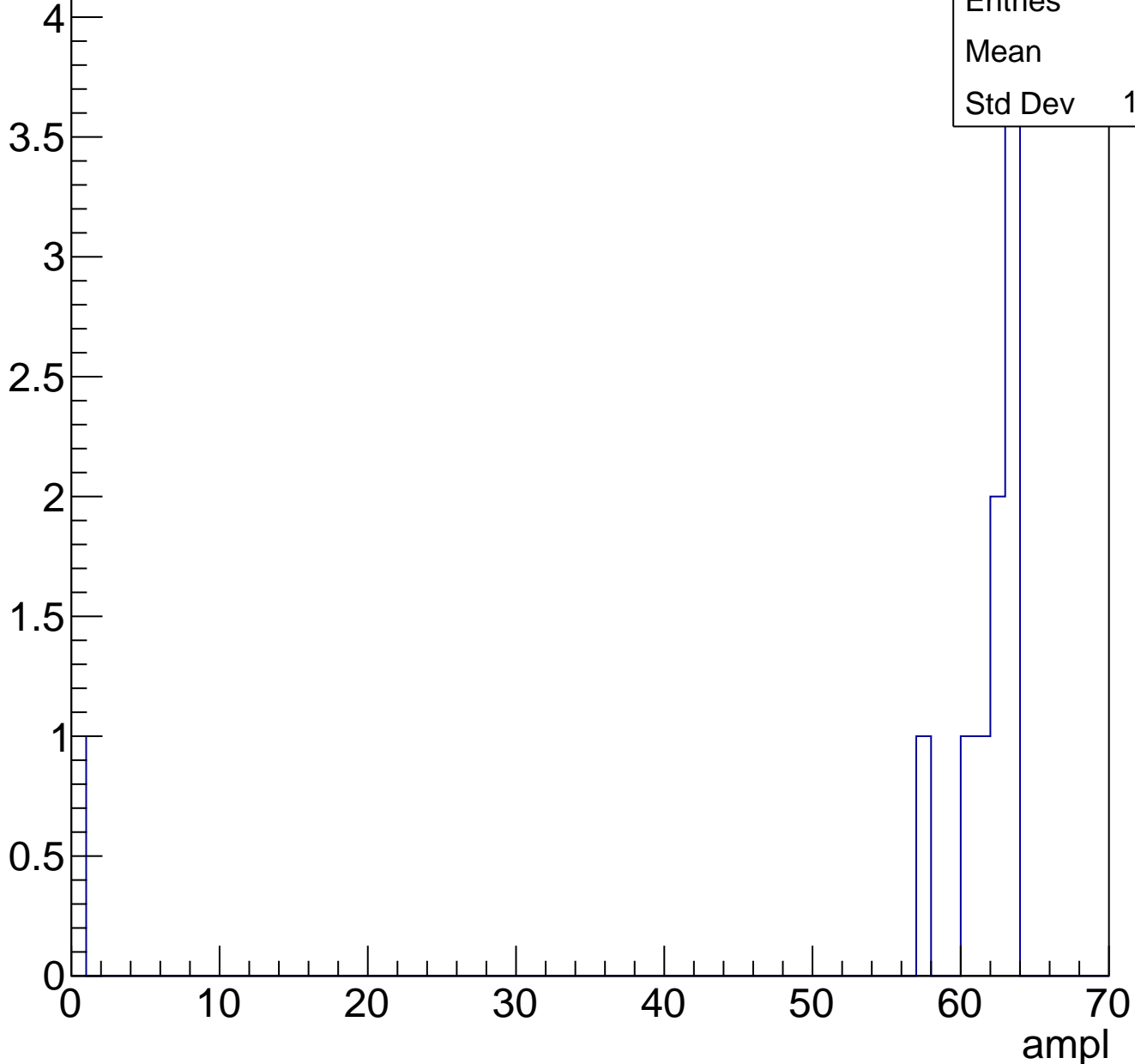
Entries	45
Mean	59.82
Std Dev	2.273



B1L103S, U12-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	10
Mean	55.4
Std Dev	18.55

B1L103S, U12-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U12-ch65, adc0

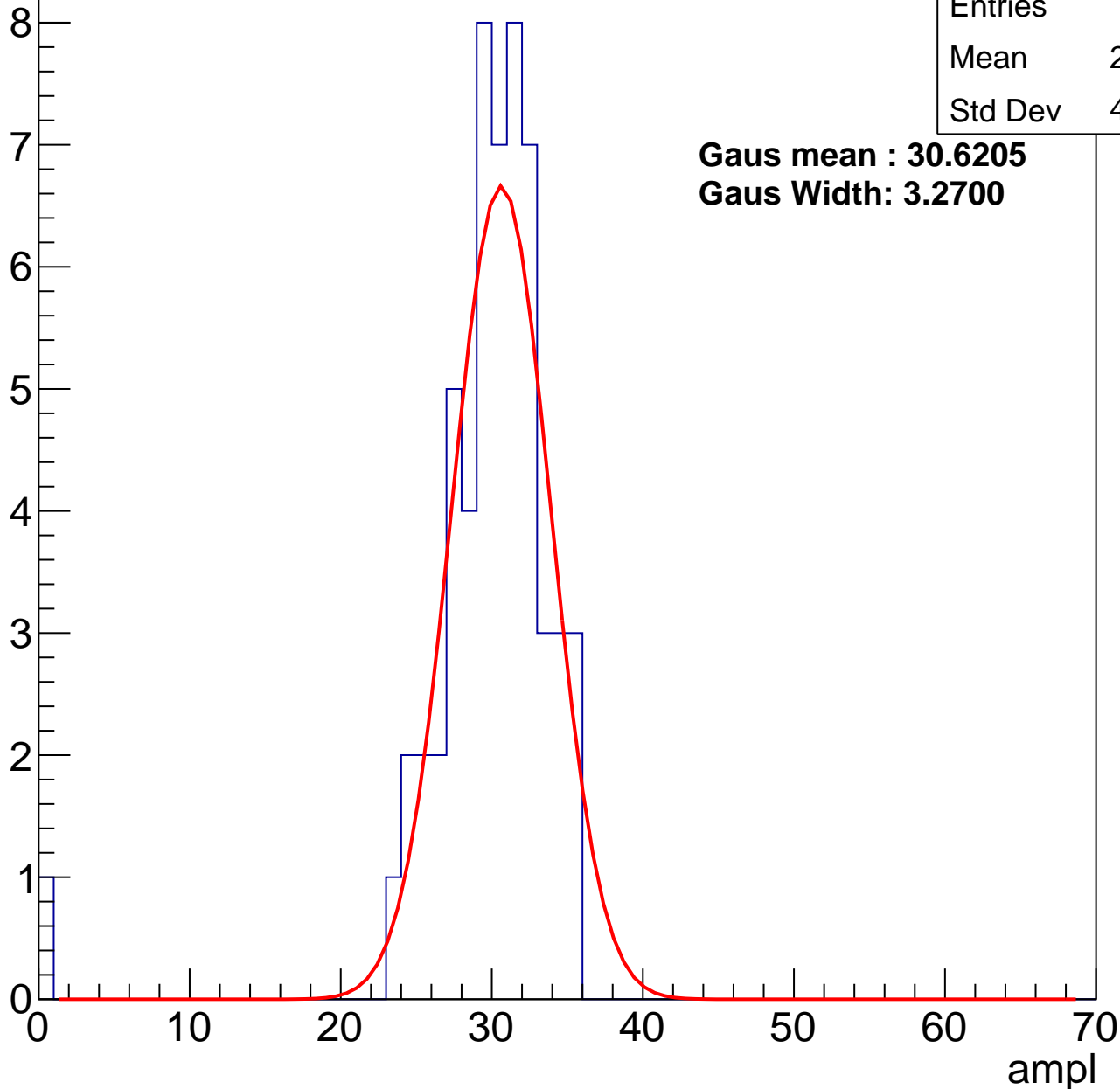
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	29.29
Std Dev	4.872

Gaus mean : 30.6205

Gaus Width: 3.2700



B1L103S, U12-ch65, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	37.11
Std Dev	3.44

Gaus mean : 37.8748

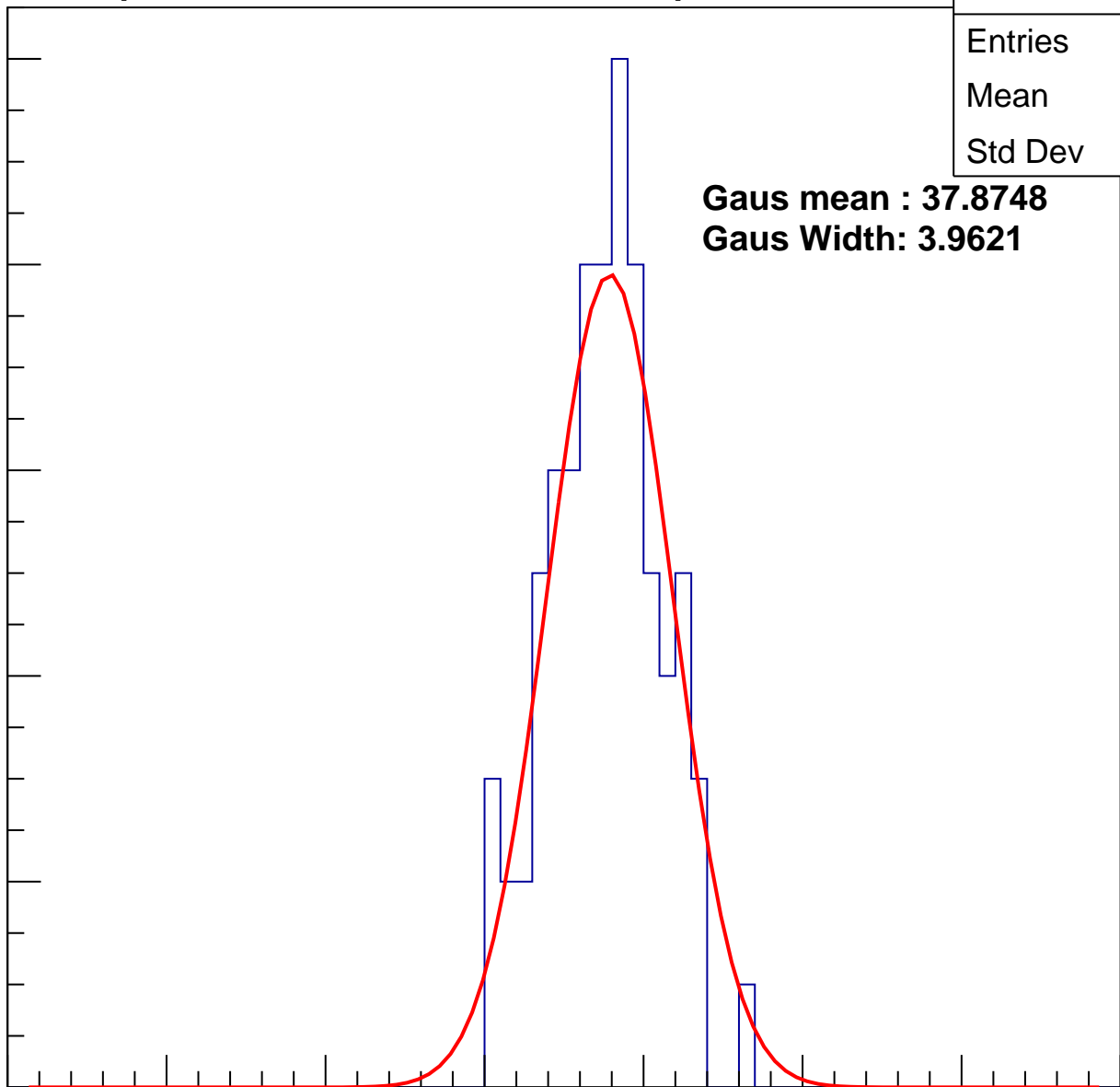
Gaus Width: 3.9621

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch65, adc2

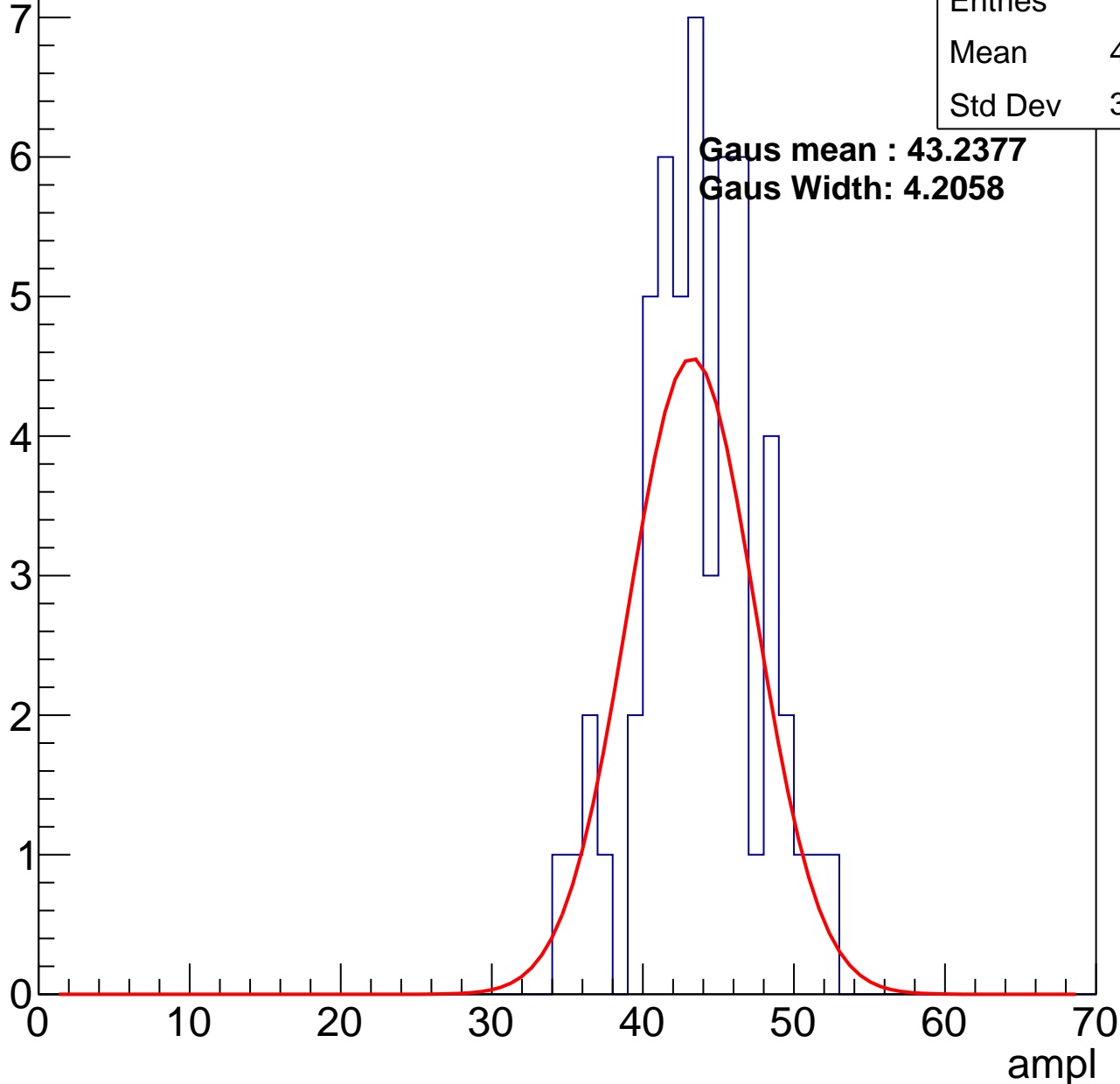
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	43.29
Std Dev	3.916

Gaus mean : 43.2377

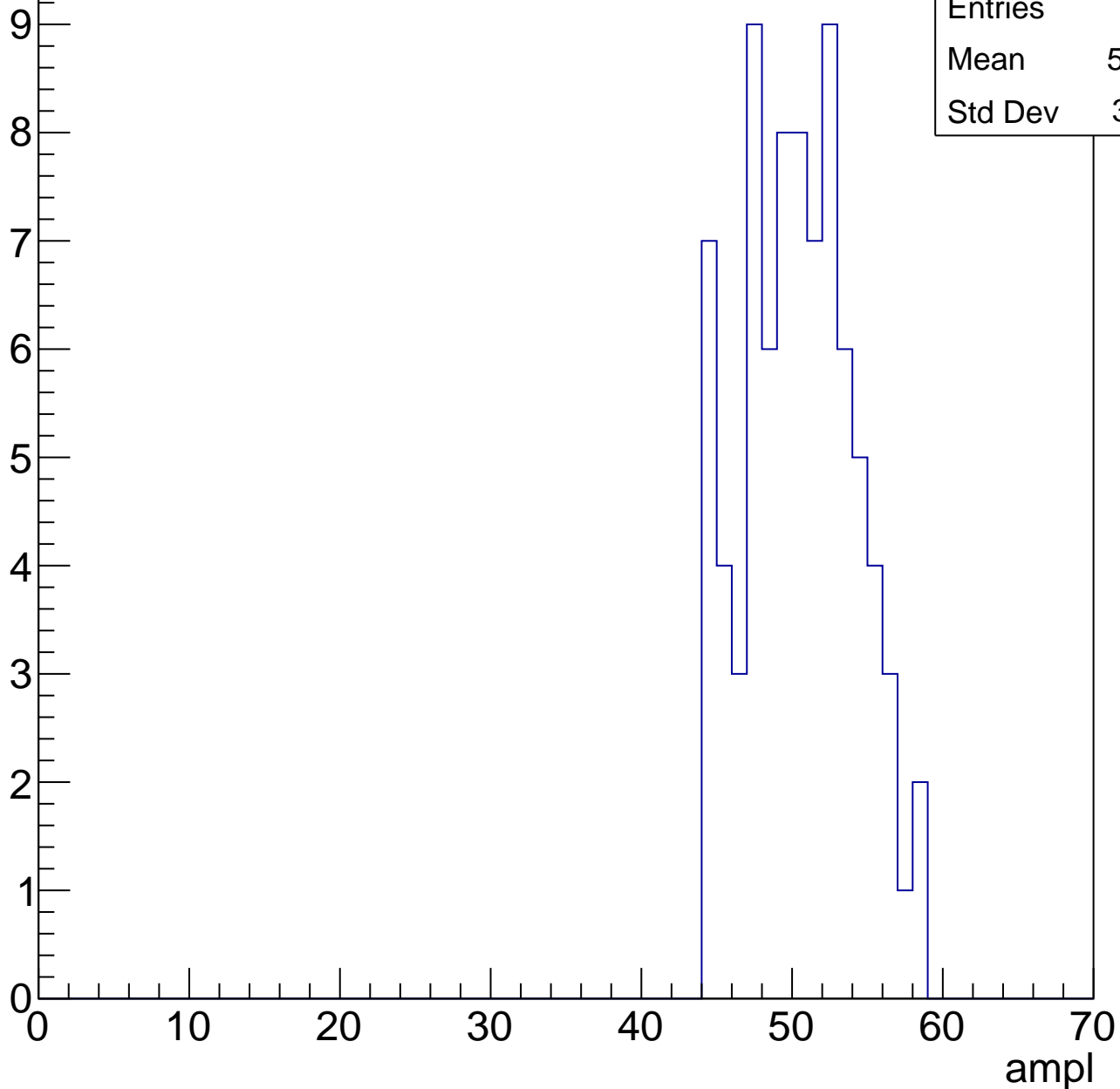
Gaus Width: 4.2058



B1L103S, U12-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

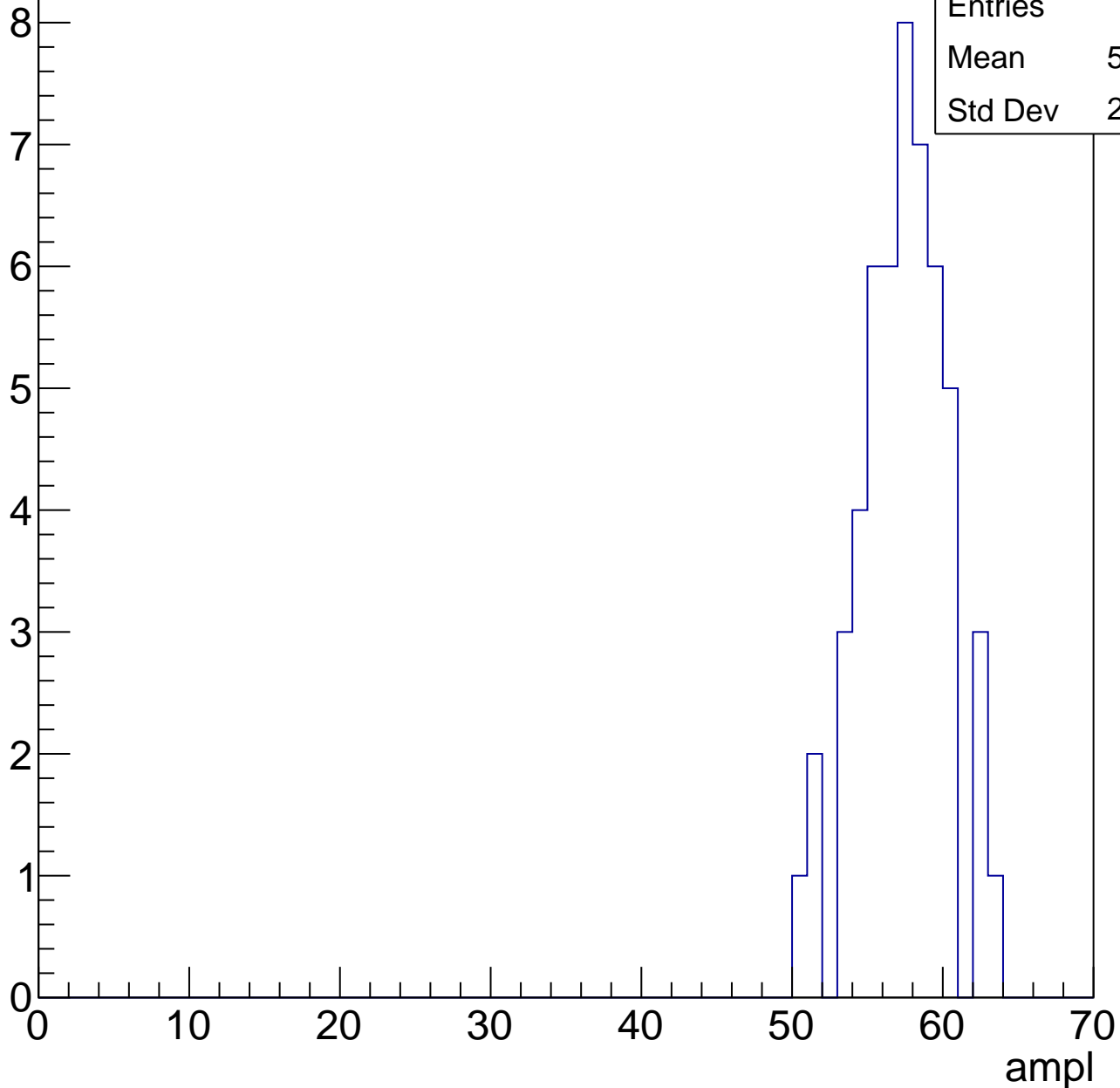


Entries	82
Mean	50.04
Std Dev	3.621

B1L103S, U12-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



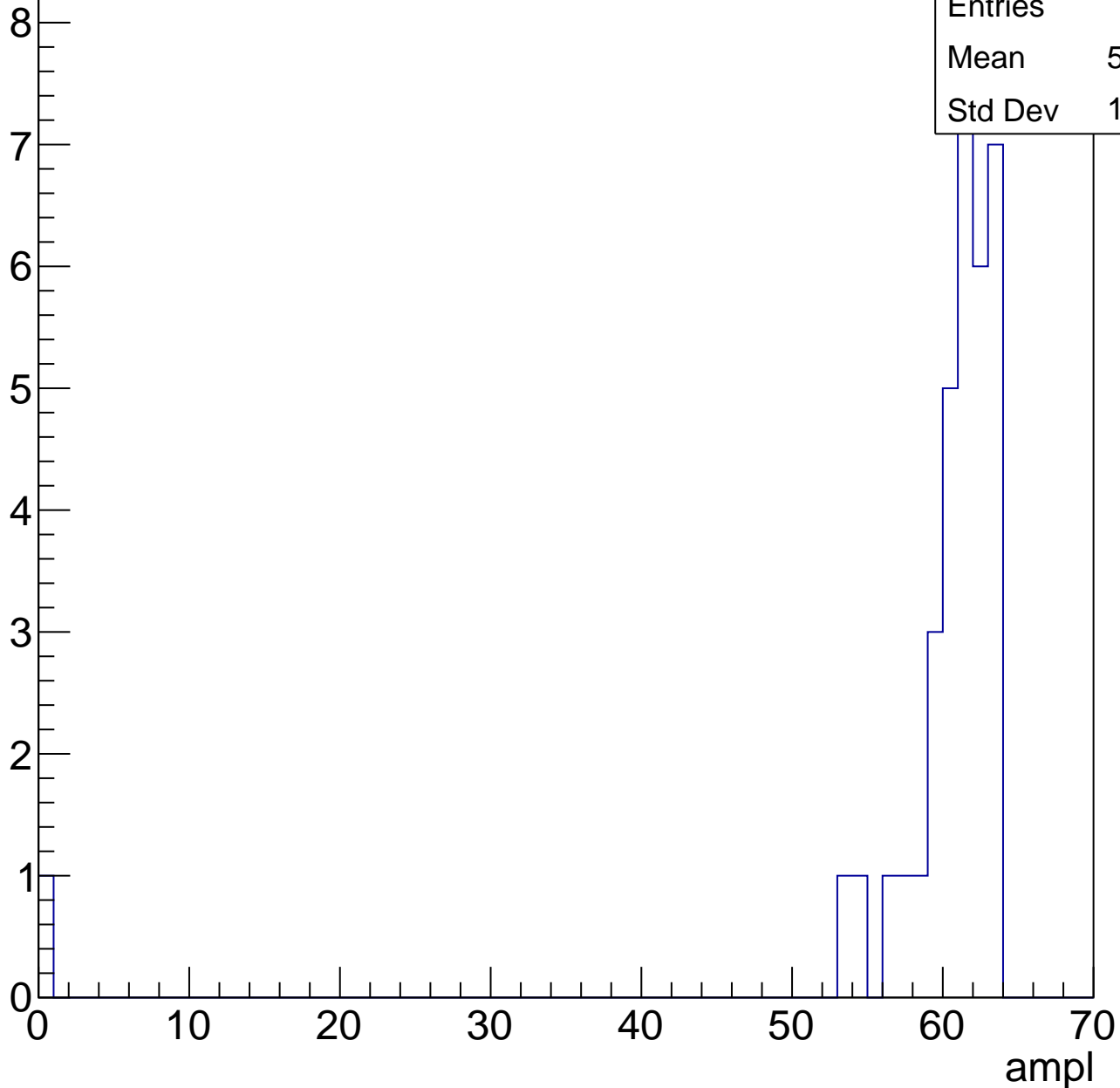
Entries	52
Mean	56.88
Std Dev	2.846

B1L103S, U12-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

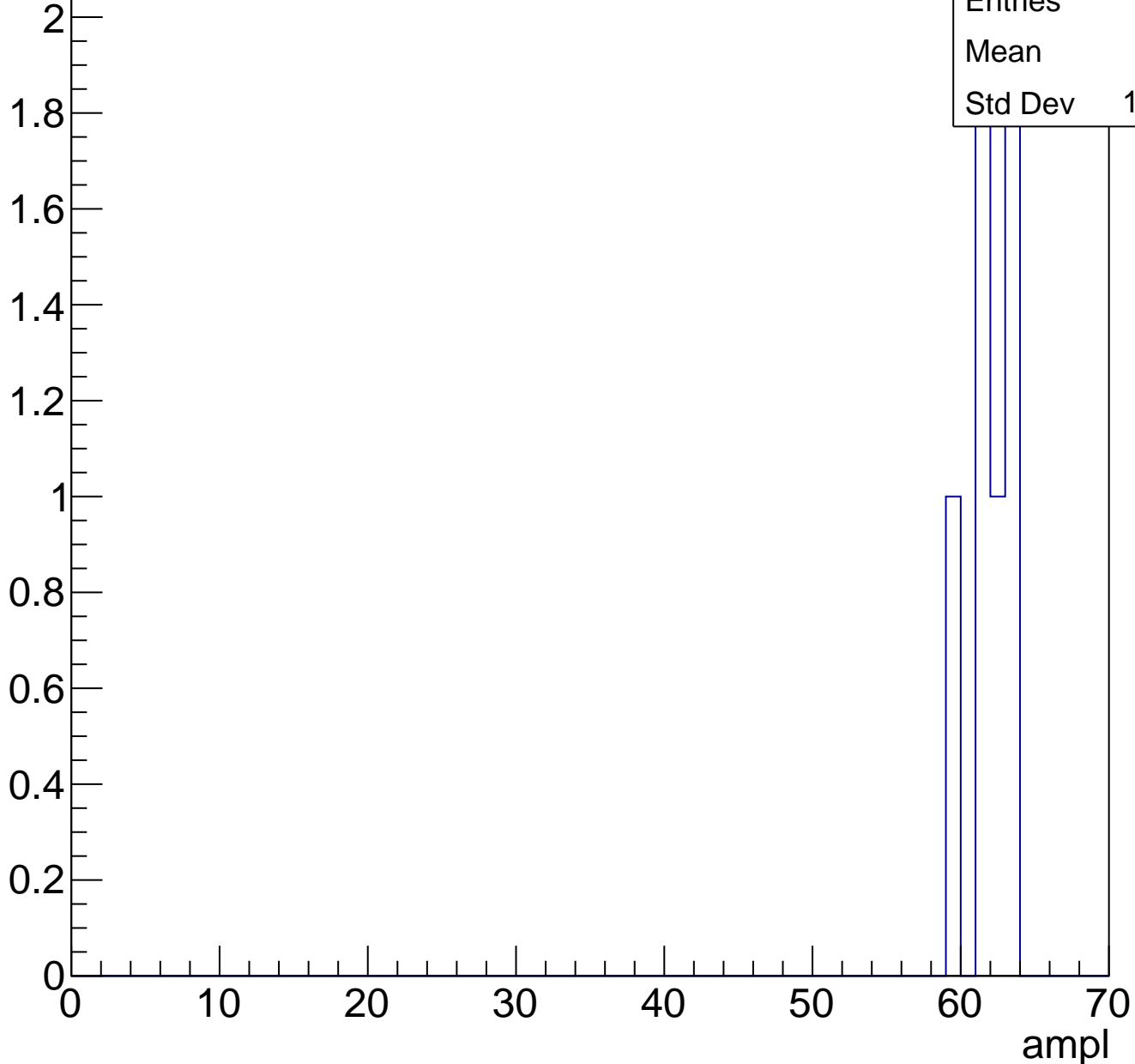
Entries	35
Mean	58.74
Std Dev	10.36



B1L103S, U12-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch66, adc0

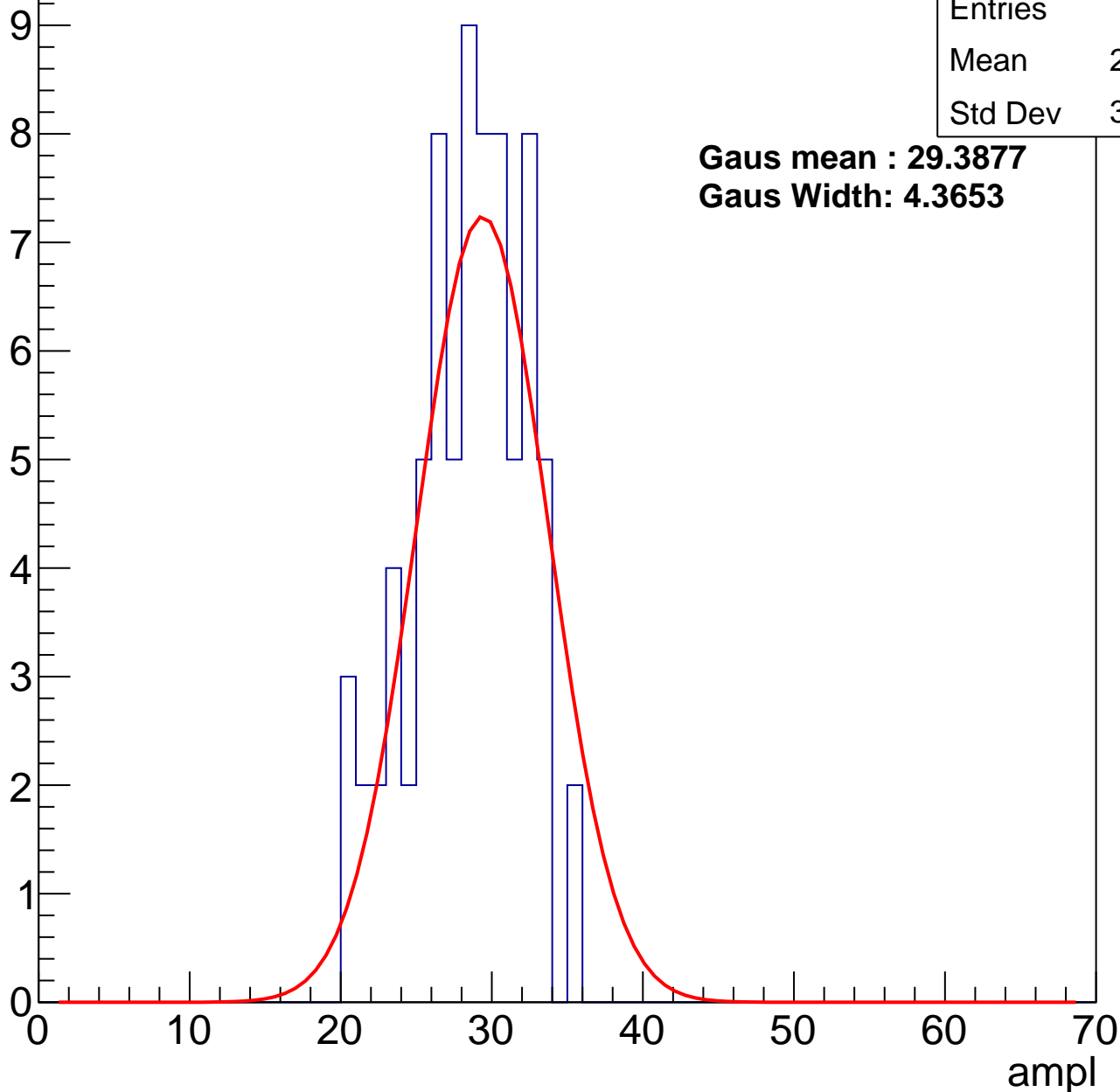
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	27.95
Std Dev	3.649

Gaus mean : 29.3877

Gaus Width: 4.3653



B1L103S, U12-ch66, adc1

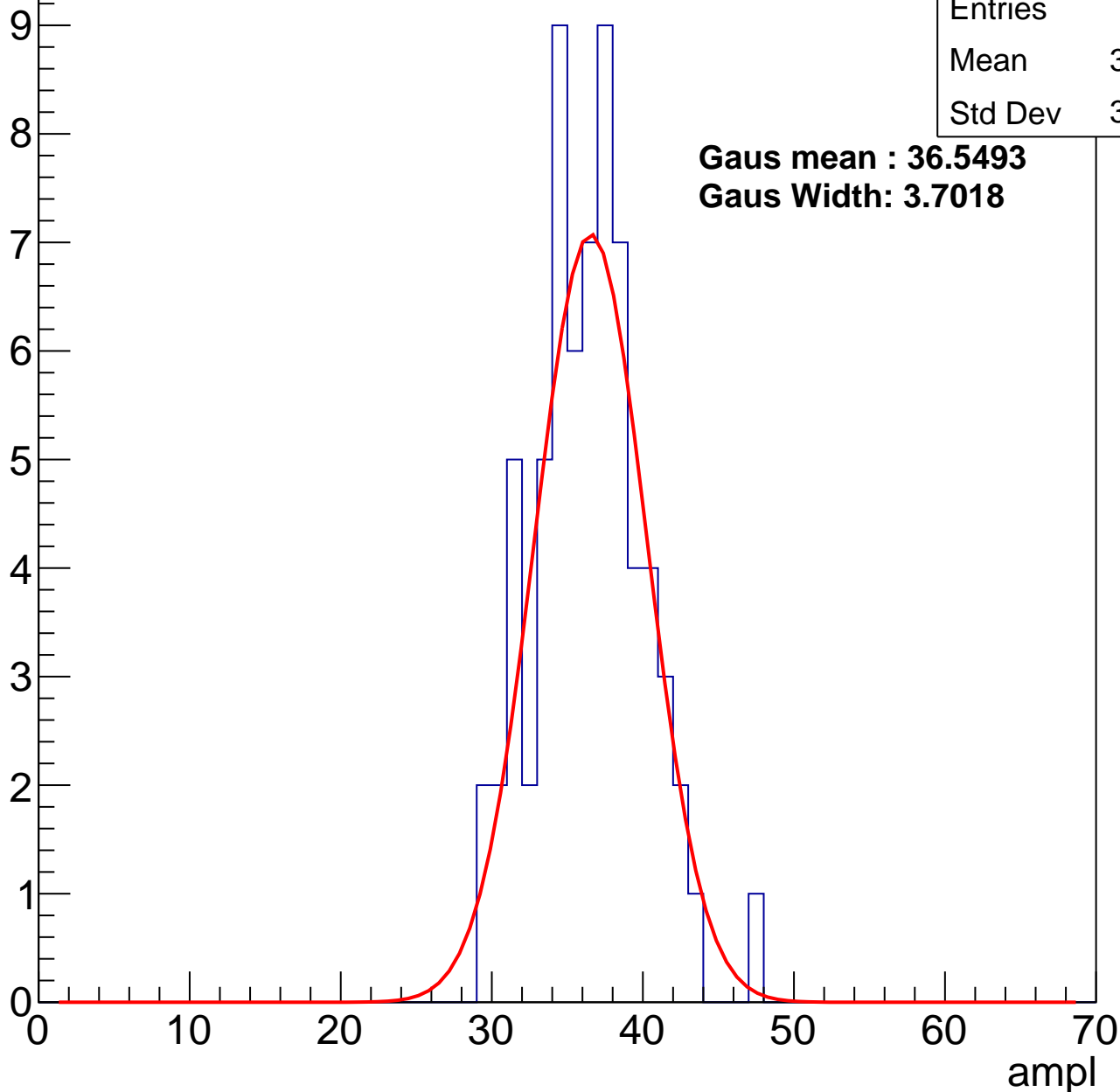
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	35.97
Std Dev	3.547

Gaus mean : 36.5493

Gaus Width: 3.7018



B1L103S, U12-ch66, adc2

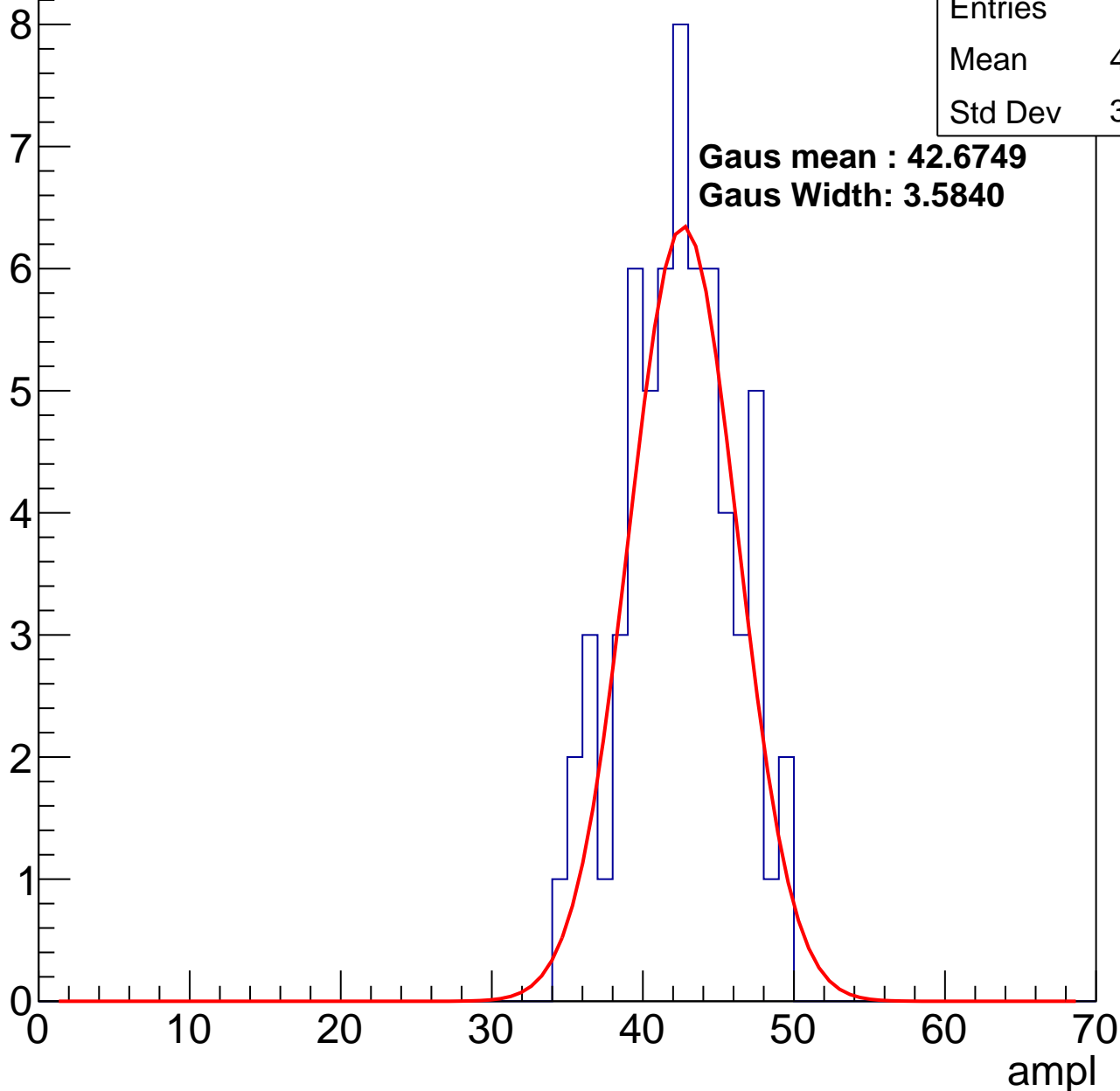
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	41.94
Std Dev	3.583

Gaus mean : 42.6749

Gaus Width: 3.5840

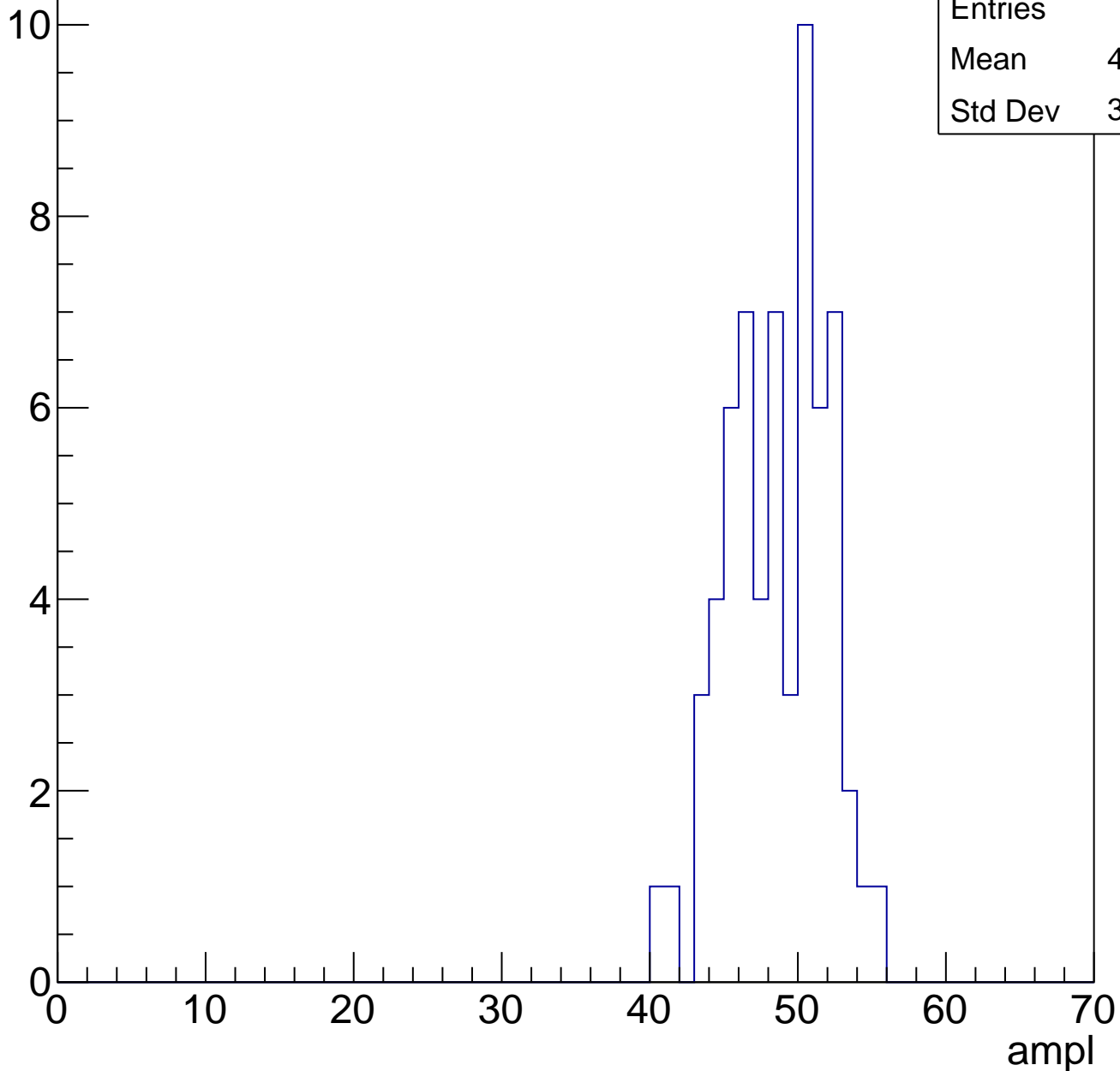


B1L103S, U12-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.16
Std Dev	3.286

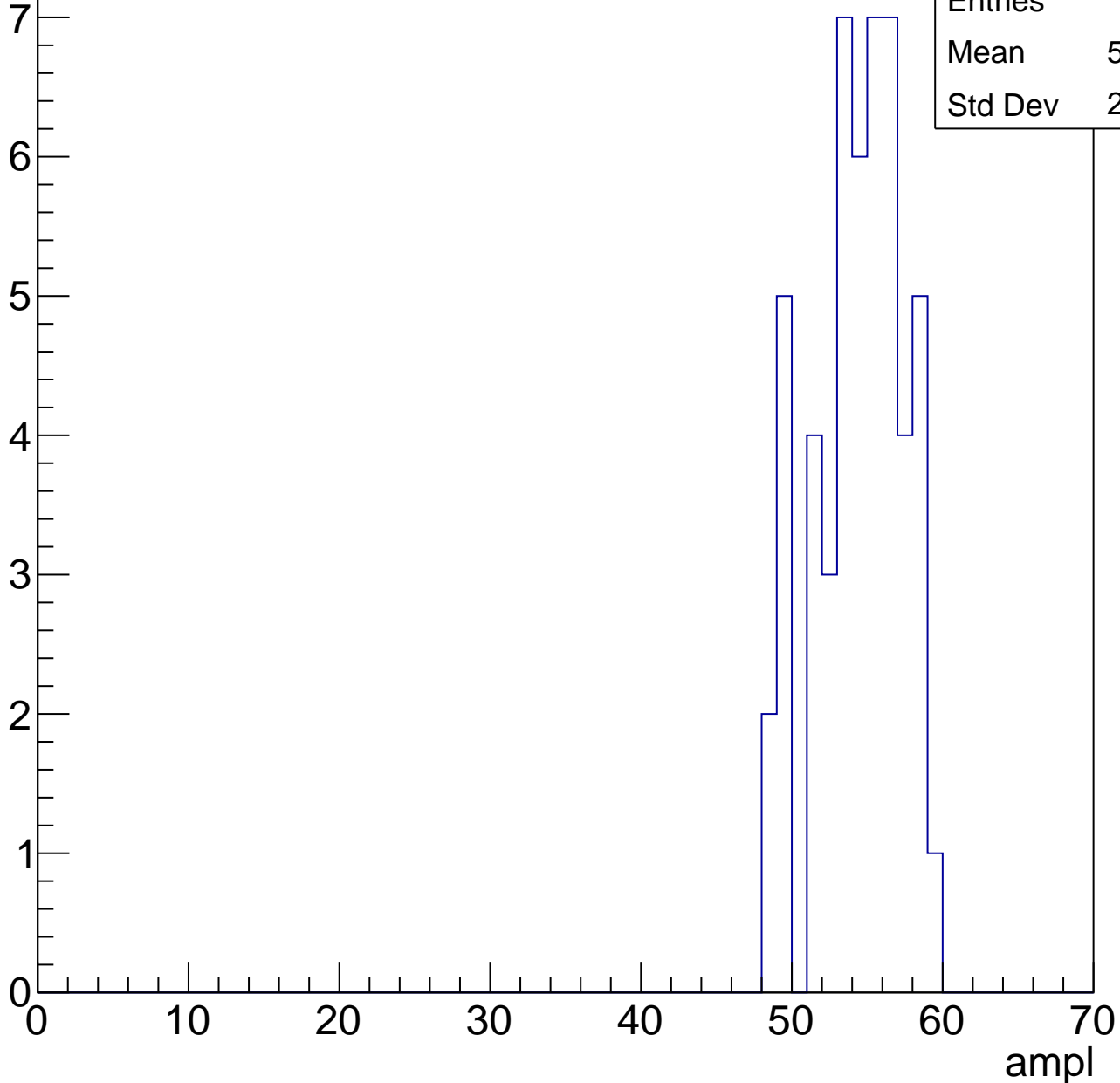


B1L103S, U12-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

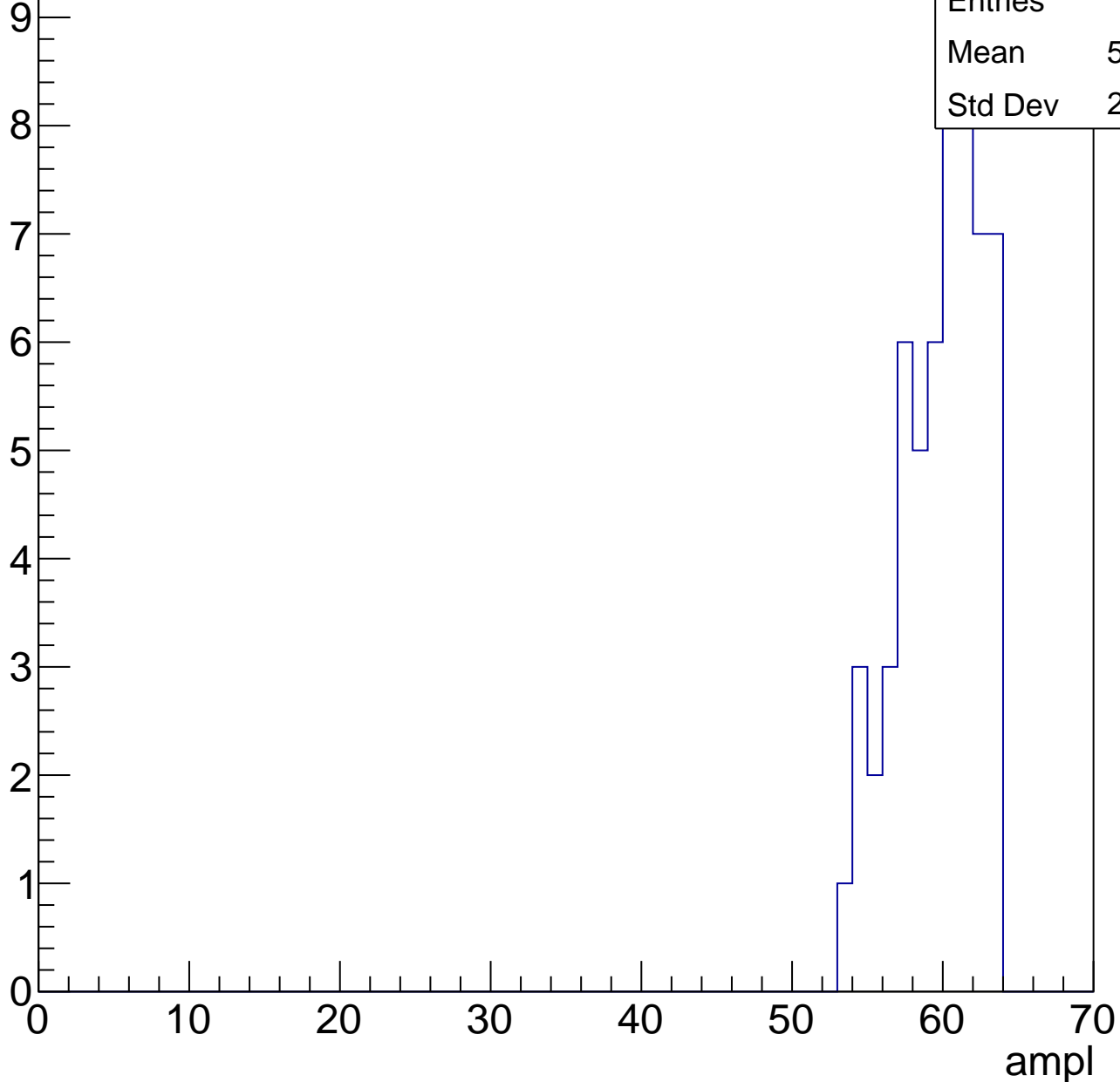
Entries	51
Mean	53.92
Std Dev	2.896



B1L103S, U12-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	58
Mean	59.36
Std Dev	2.663

B1L103S, U12-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

12

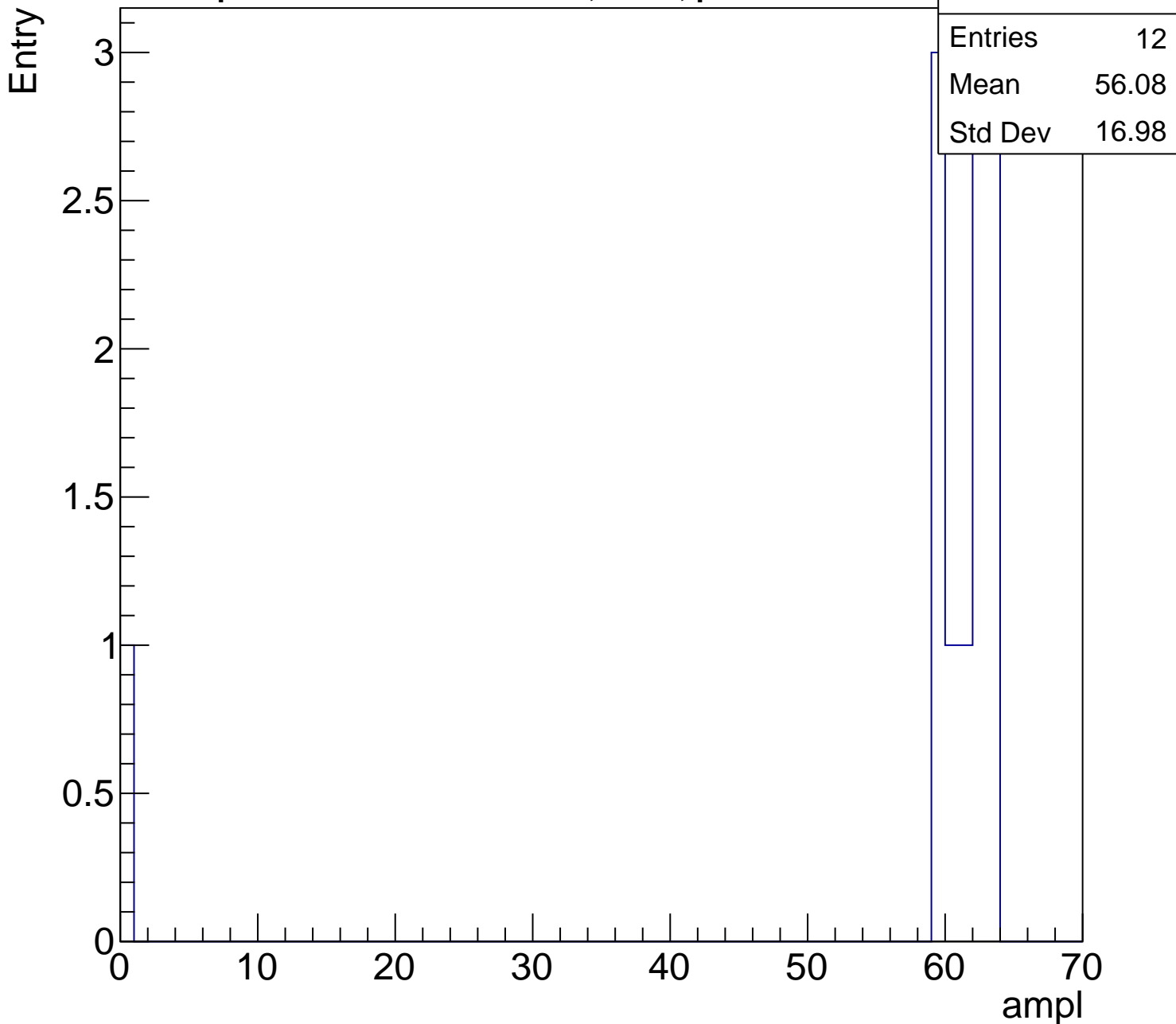
Mean

56.08

Std Dev

16.98

ampl



B1L103S, U12-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch67, adc0

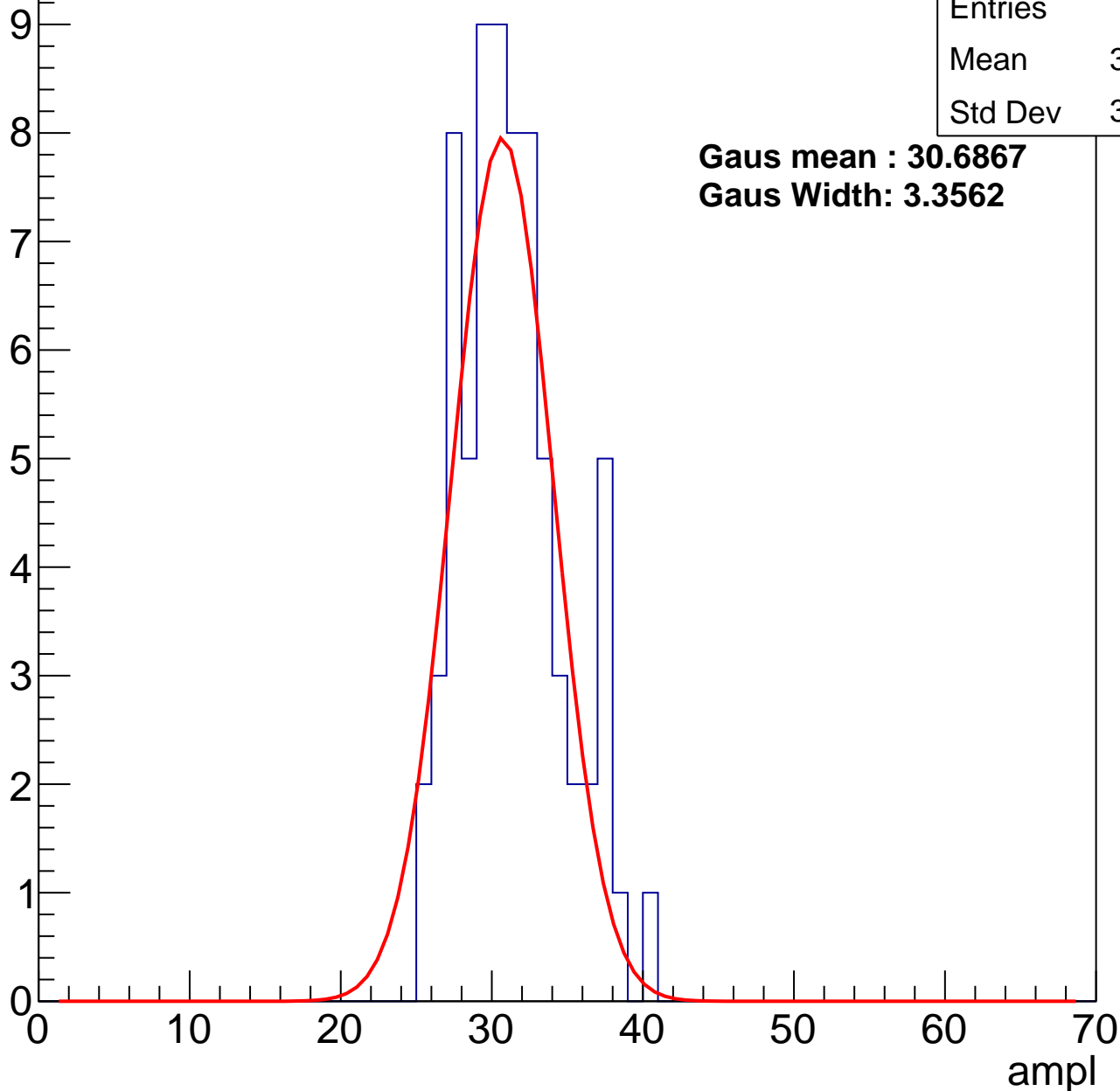
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.86
Std Dev	3.379

Gaus mean : 30.6867

Gaus Width: 3.3562



B1L103S, U12-ch67, adc1

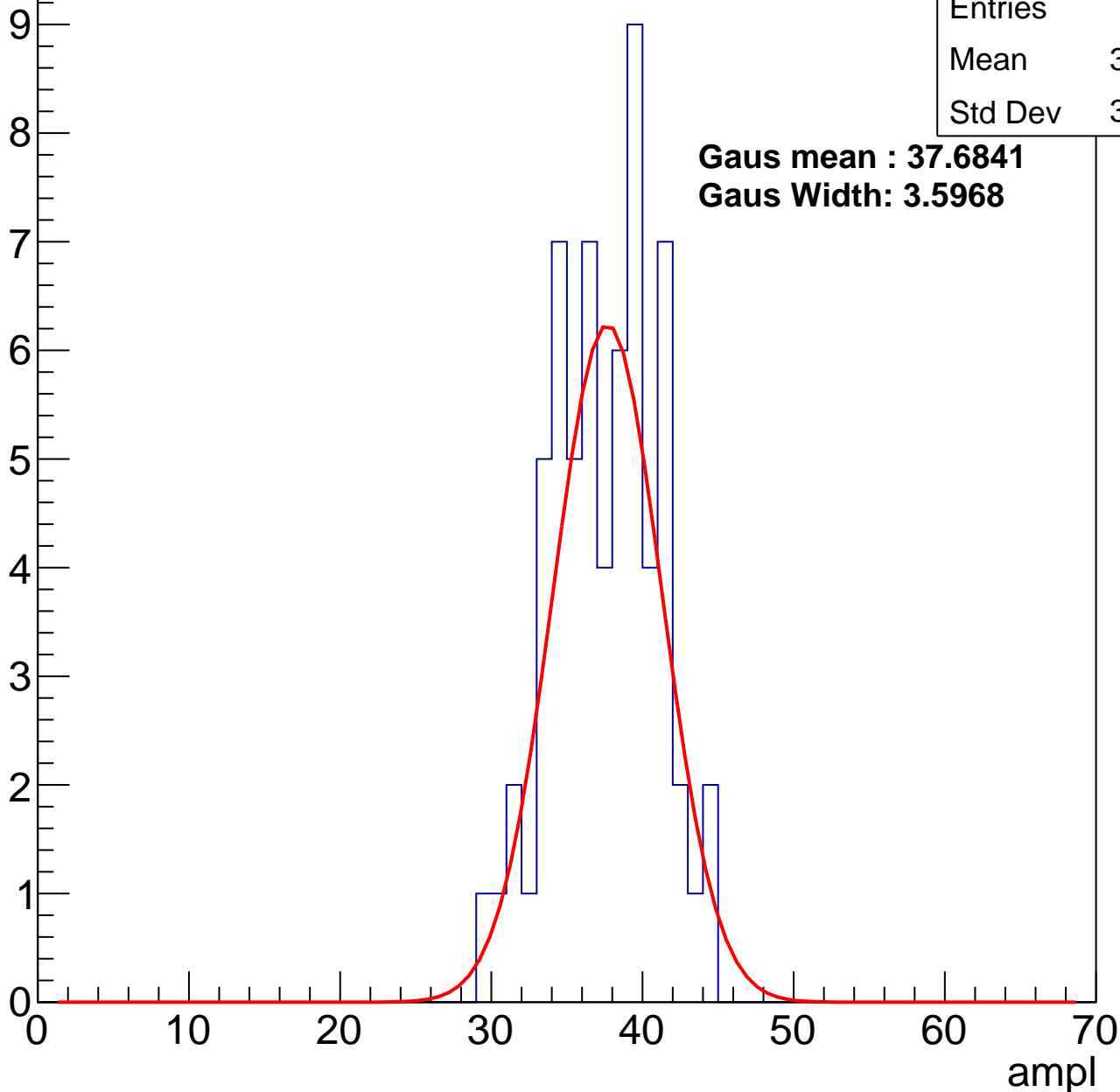
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.06
Std Dev	3.432

Gaus mean : 37.6841

Gaus Width: 3.5968



B1L103S, U12-ch67, adc2

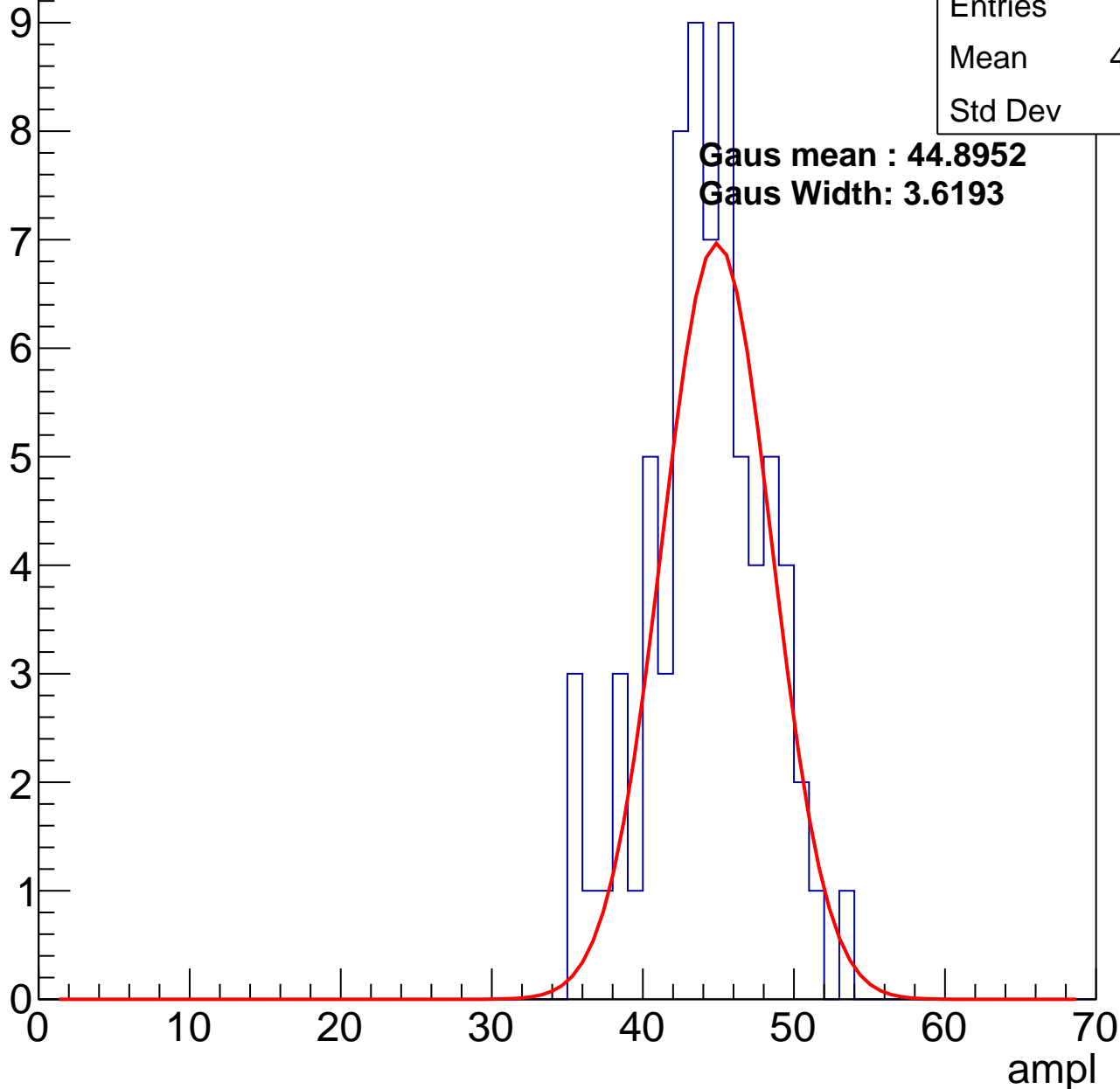
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.72
Std Dev	3.87

Gaus mean : 44.8952

Gaus Width: 3.6193

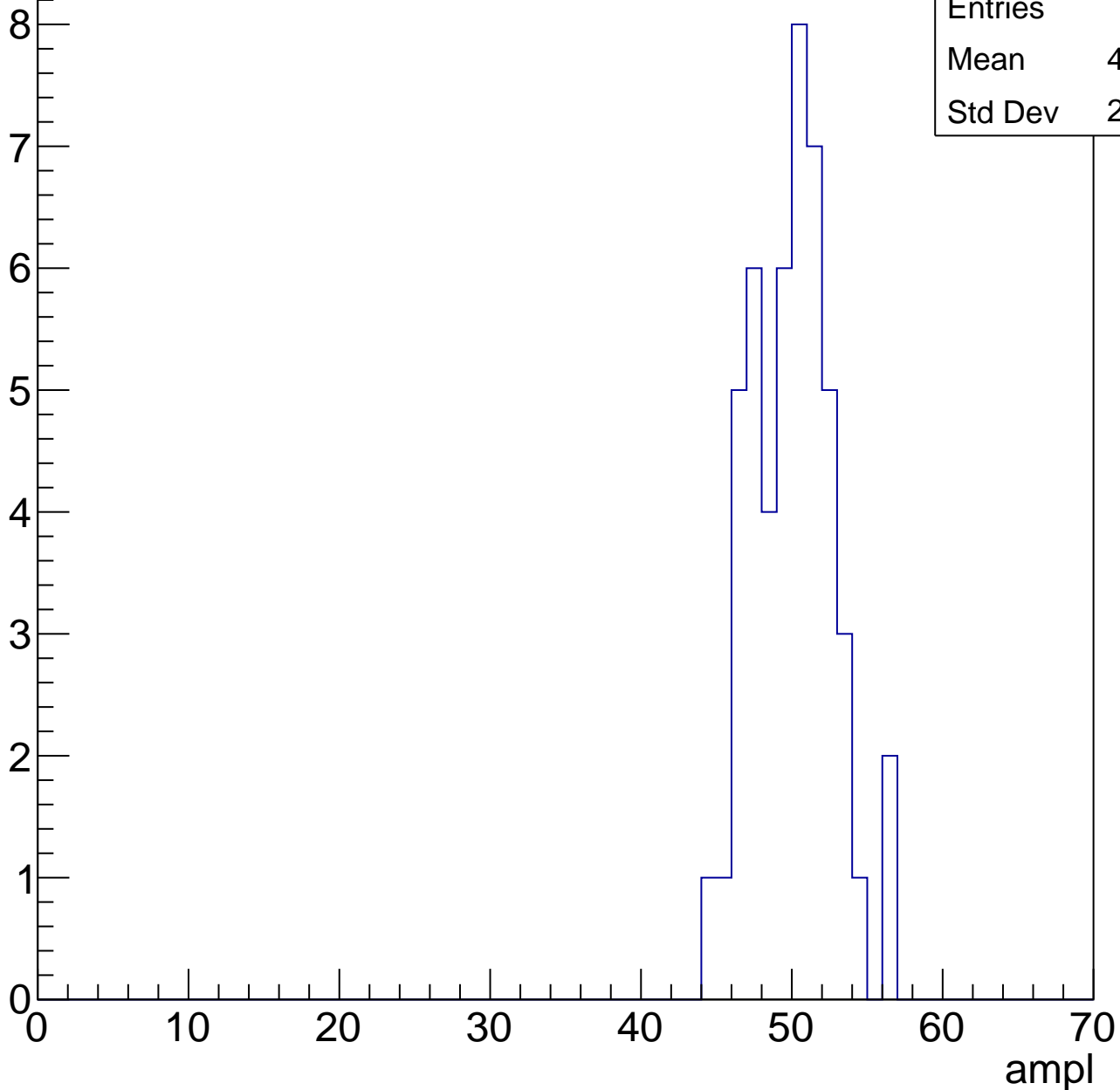


B1L103S, U12-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	49.57
Std Dev	2.673

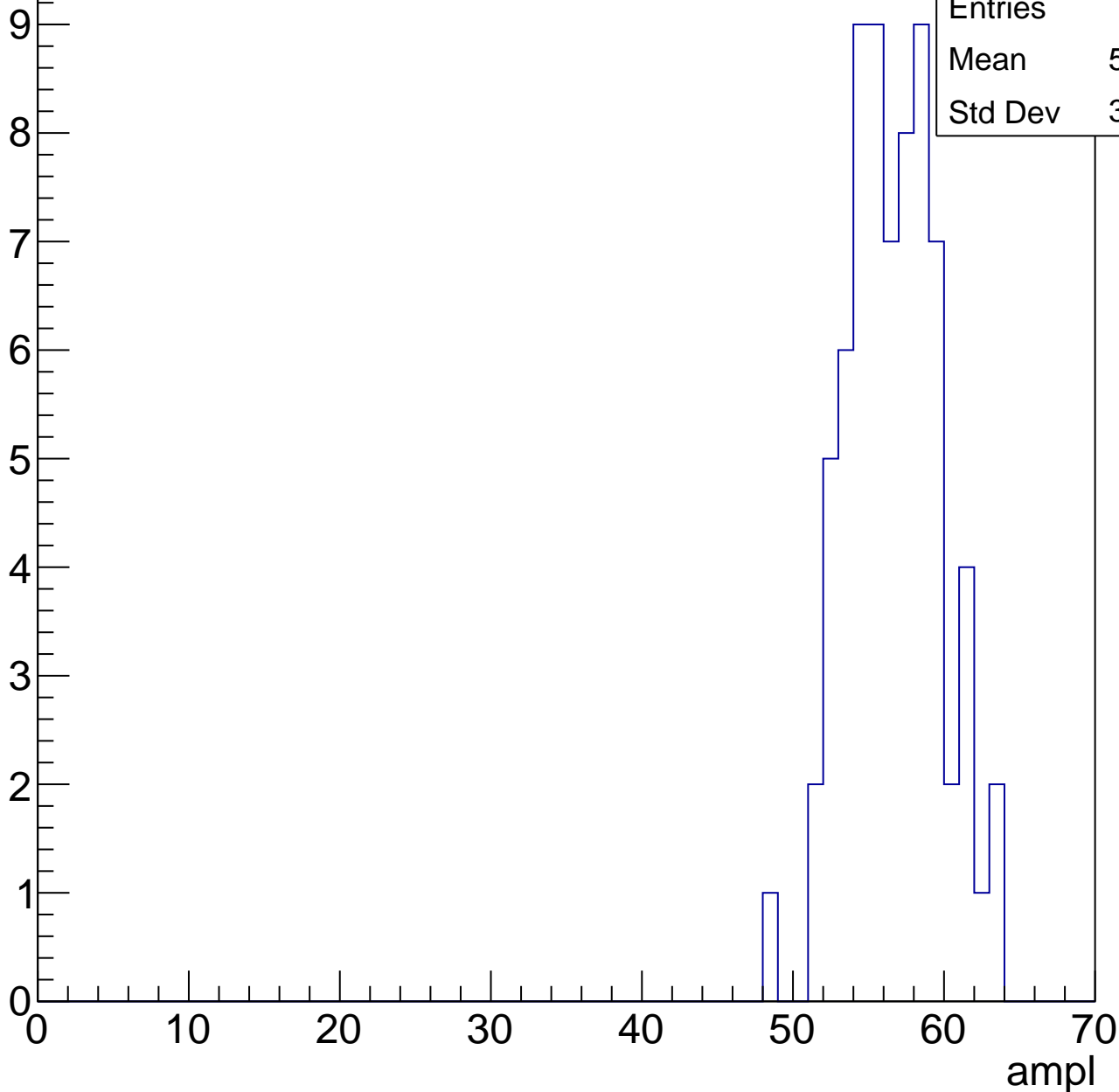


B1L103S, U12-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	56.17
Std Dev	3.037

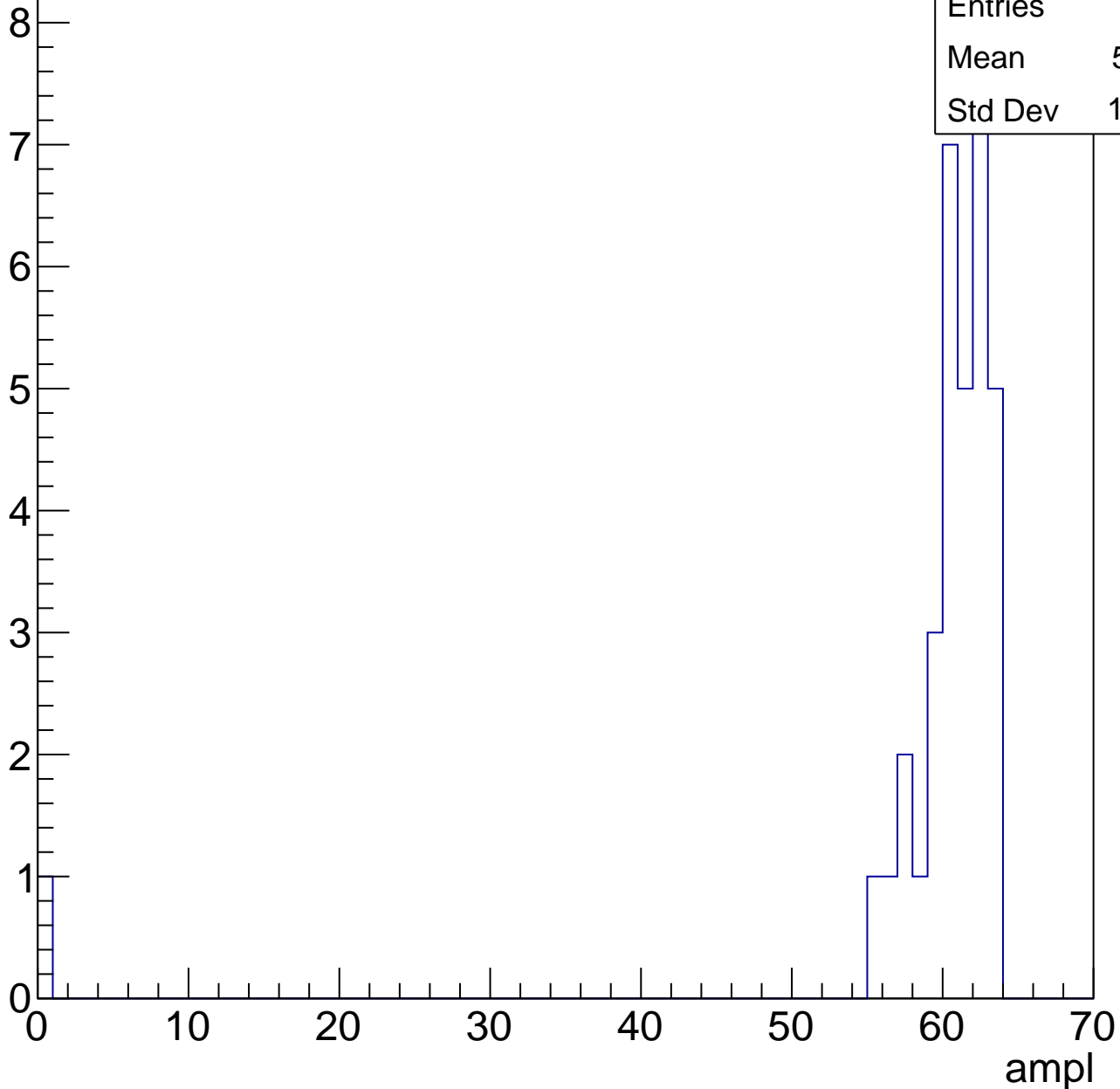


B1L103S, U12-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.71
Std Dev	10.42



B1L103S, U12-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch68, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	29.8
Std Dev	5.129

Gaus mean : 31.4653

Gaus Width: 3.7219

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L103S, U12-ch68, adc1

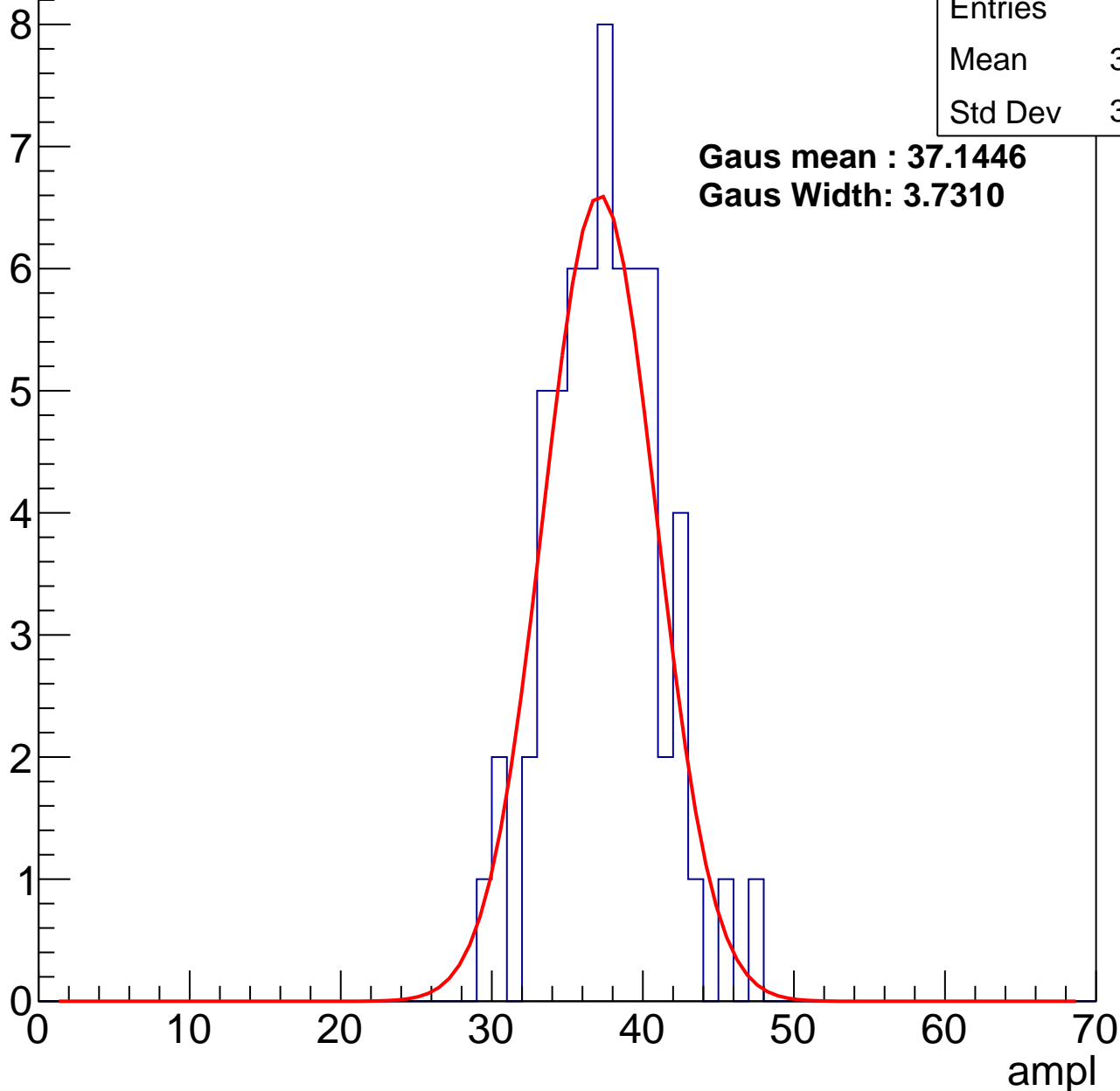
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	37.05
Std Dev	3.553

Gaus mean : 37.1446

Gaus Width: 3.7310



B1L103S, U12-ch68, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	43.75
Std Dev	2.937

Gaus mean : 44.4533

Gaus Width: 3.1684

10

8

6

4

2

0

0

10

20

30

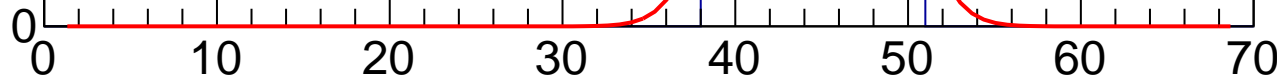
40

50

60

70

ampl

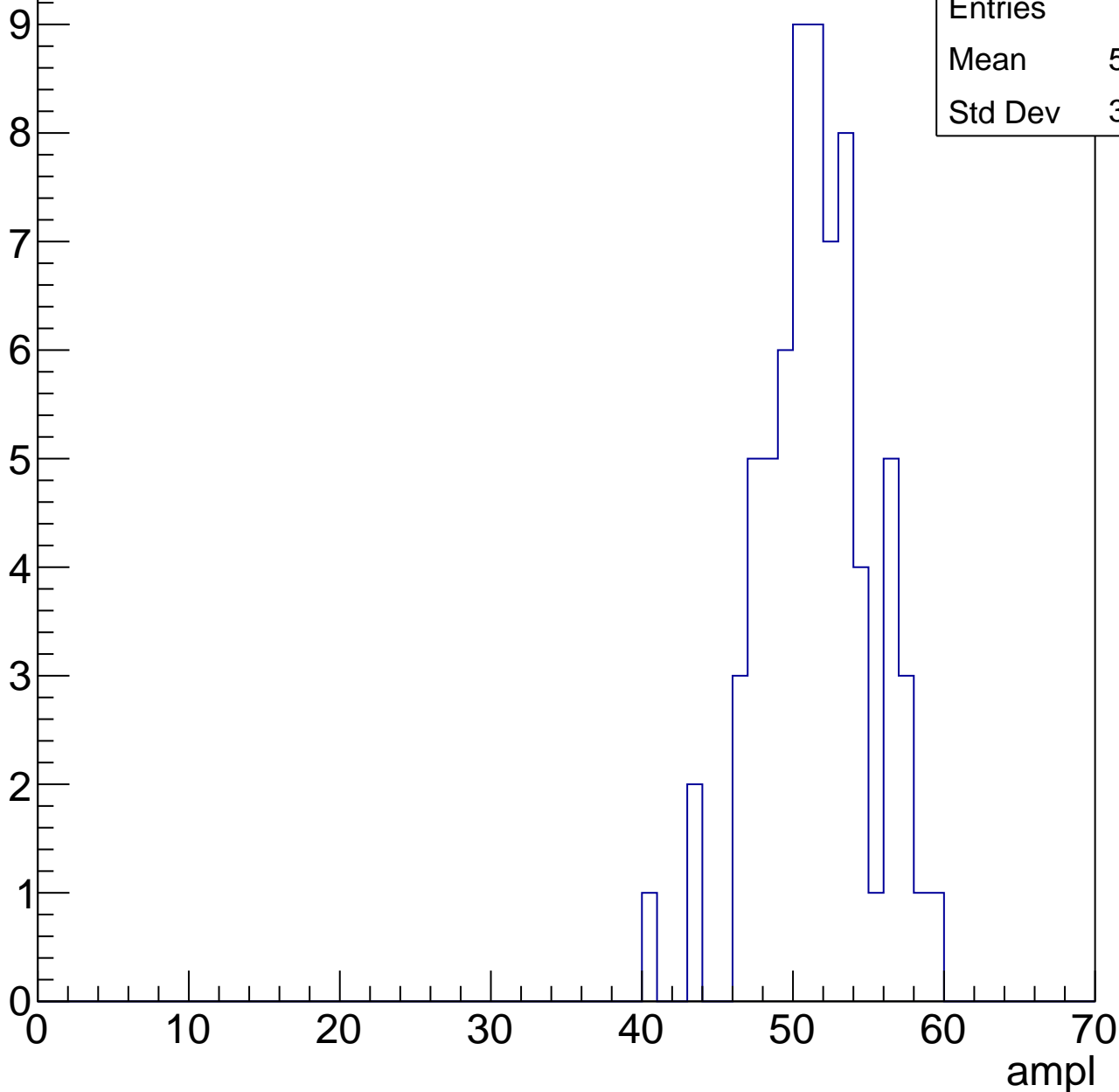


B1L103S, U12-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	50.99
Std Dev	3.623

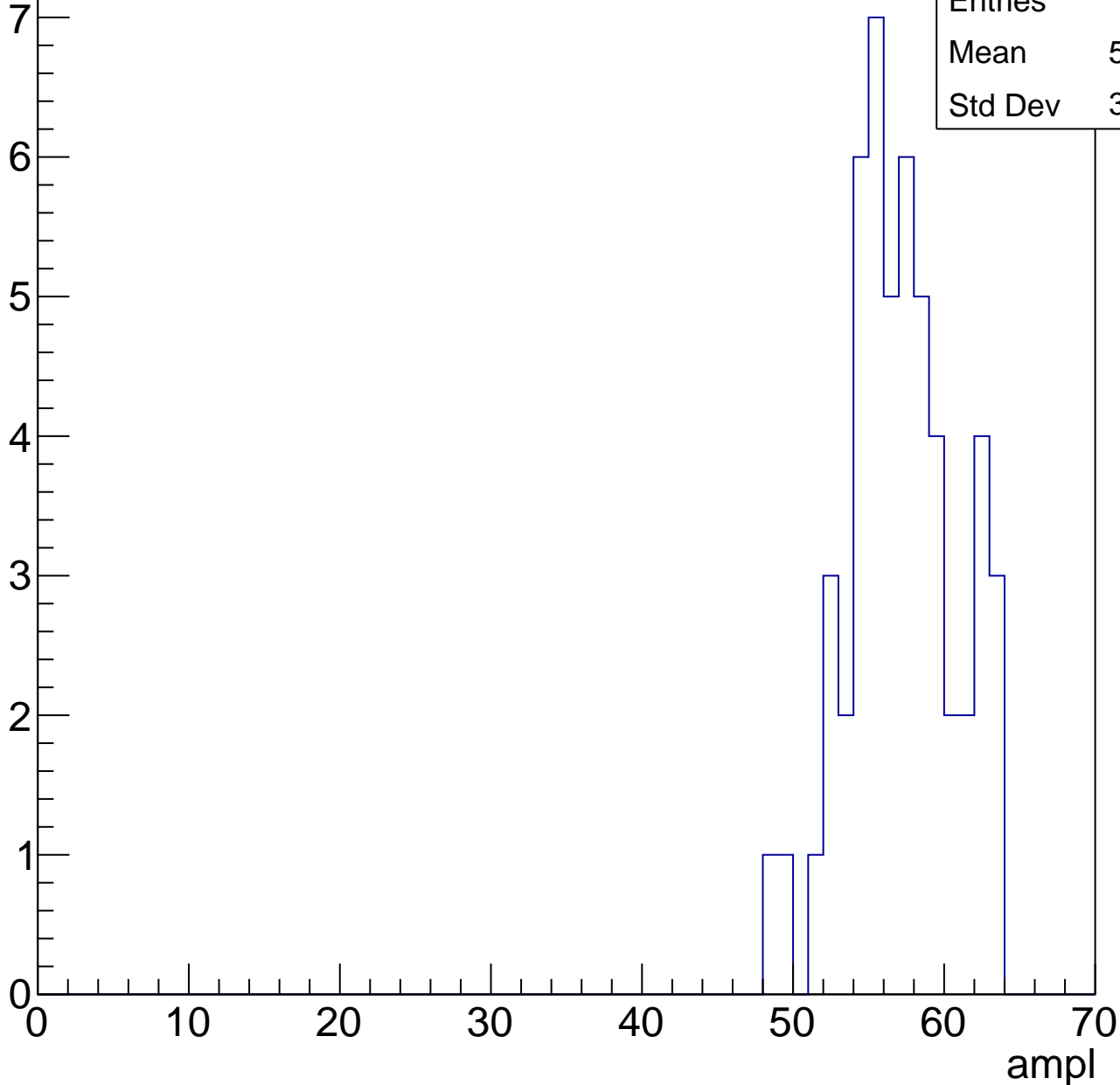


B1L103S, U12-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	56.65
Std Dev	3.529



B1L103S, U12-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

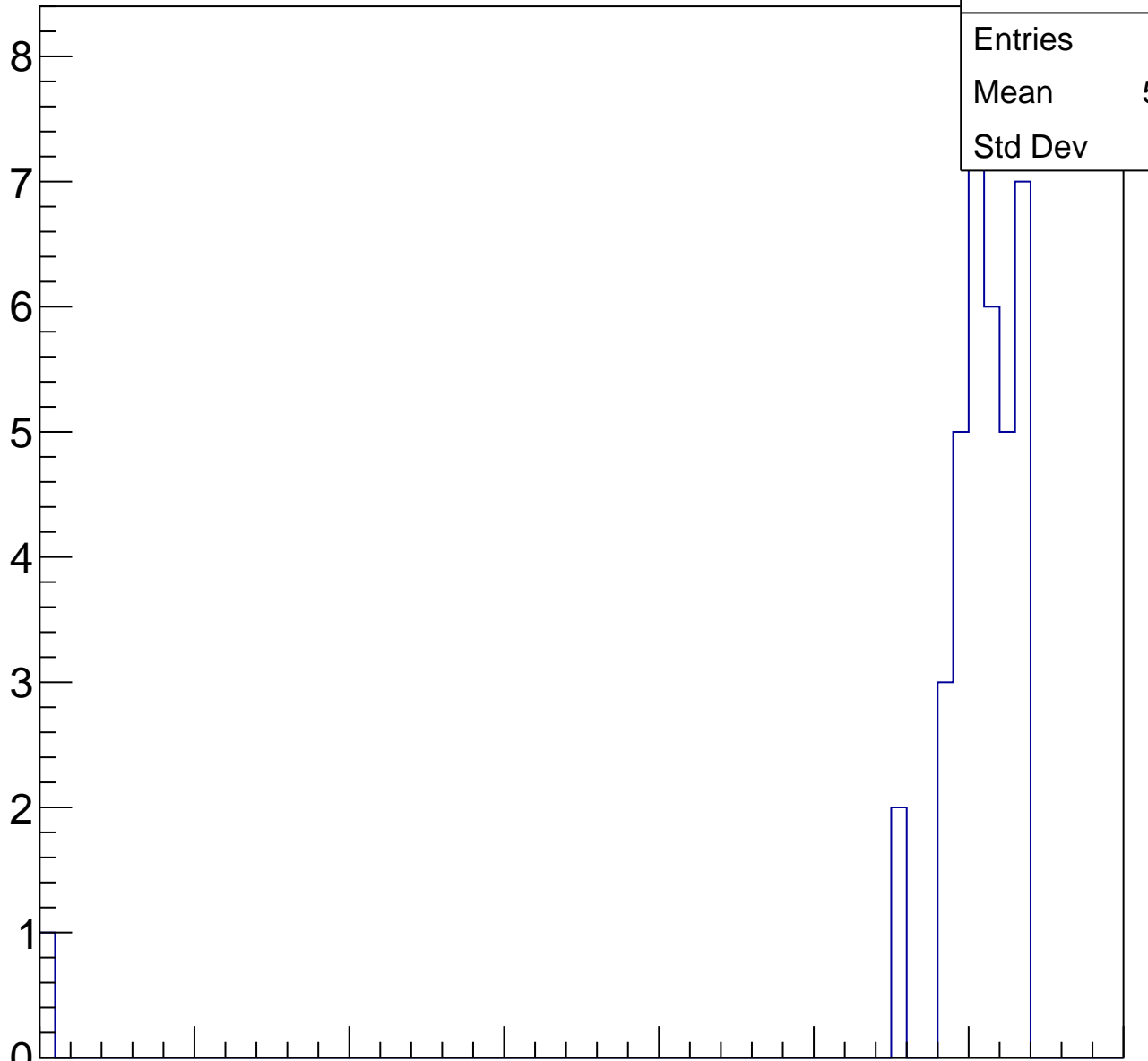
Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	58.81
Std Dev	10

ampl

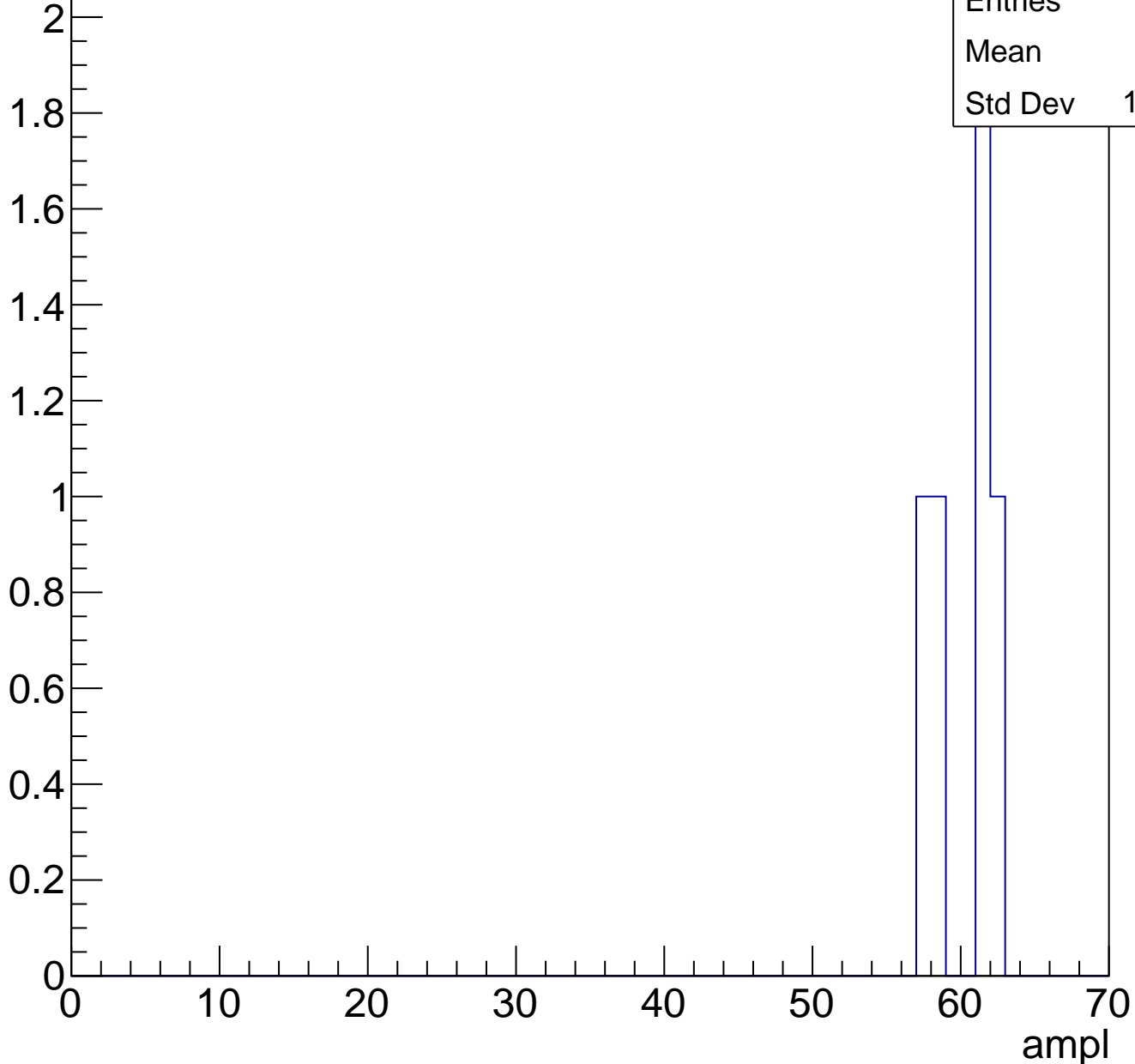
0 10 20 30 40 50 60 70



B1L103S, U12-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch69, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	58
Mean	30.55
Std Dev	3.27

Gaus mean : 31.4987

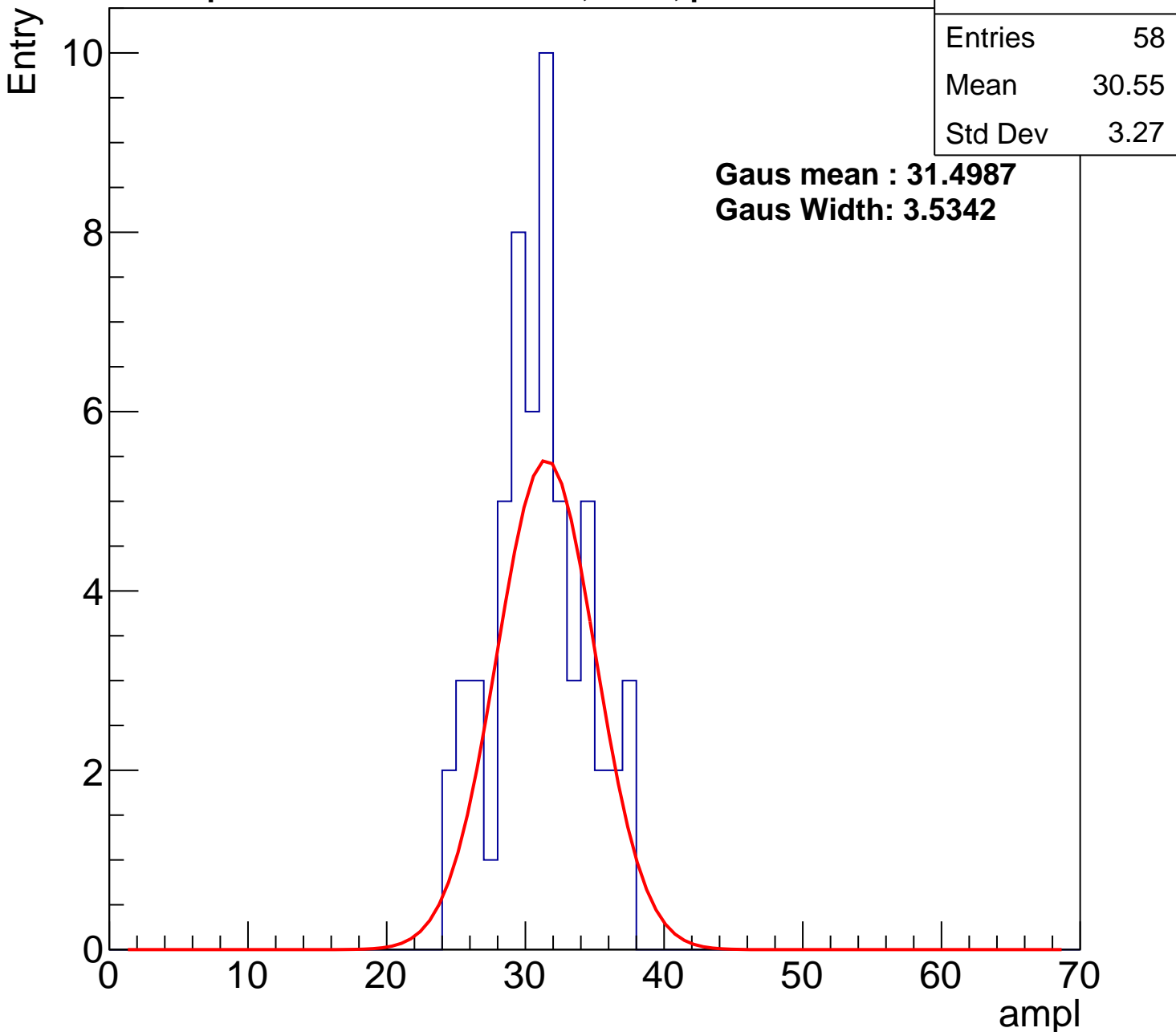
Gaus Width: 3.5342

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch69, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	37.56
Std Dev	3.752

Gaus mean : 37.7549

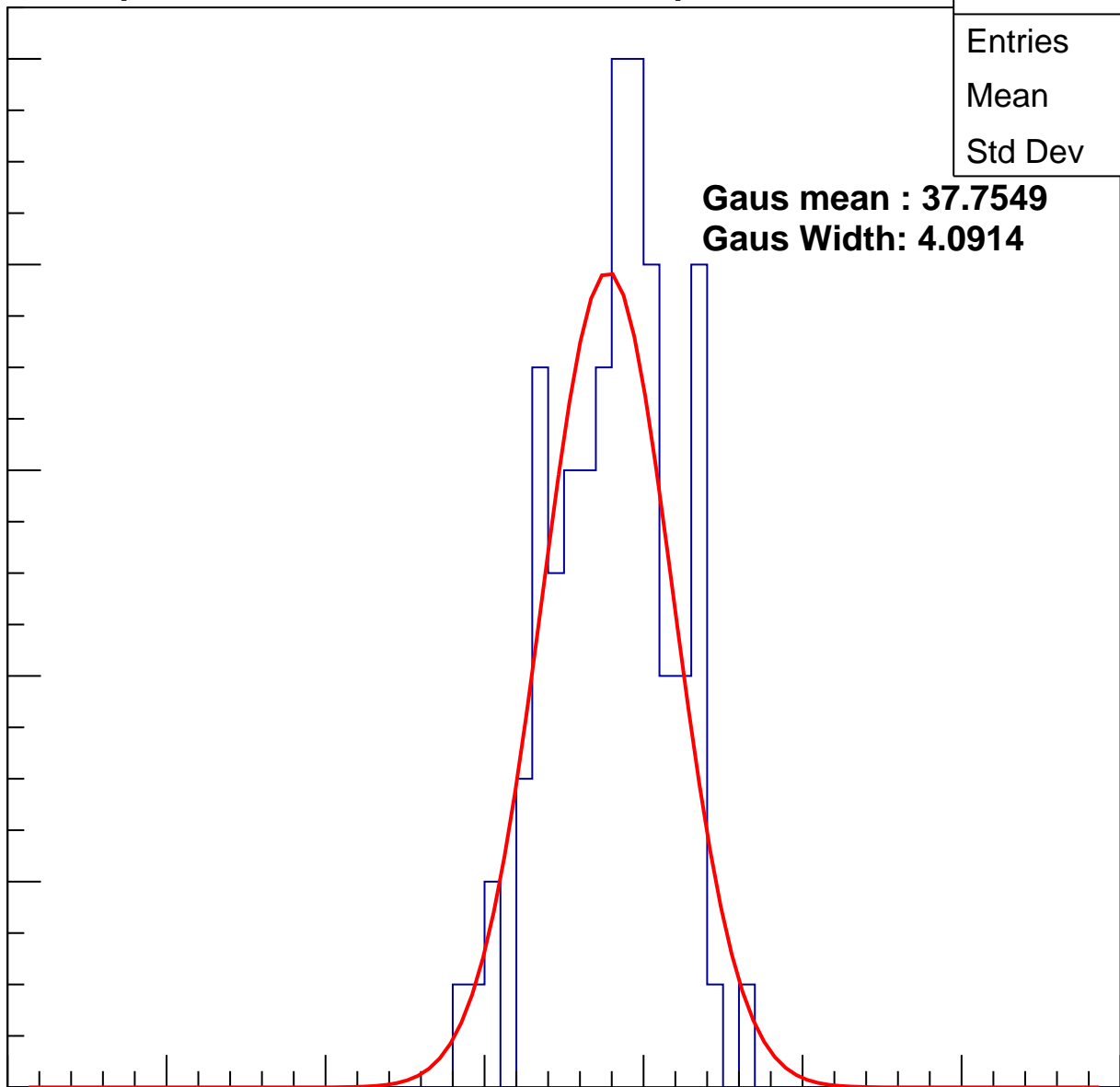
Gaus Width: 4.0914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch69, adc2

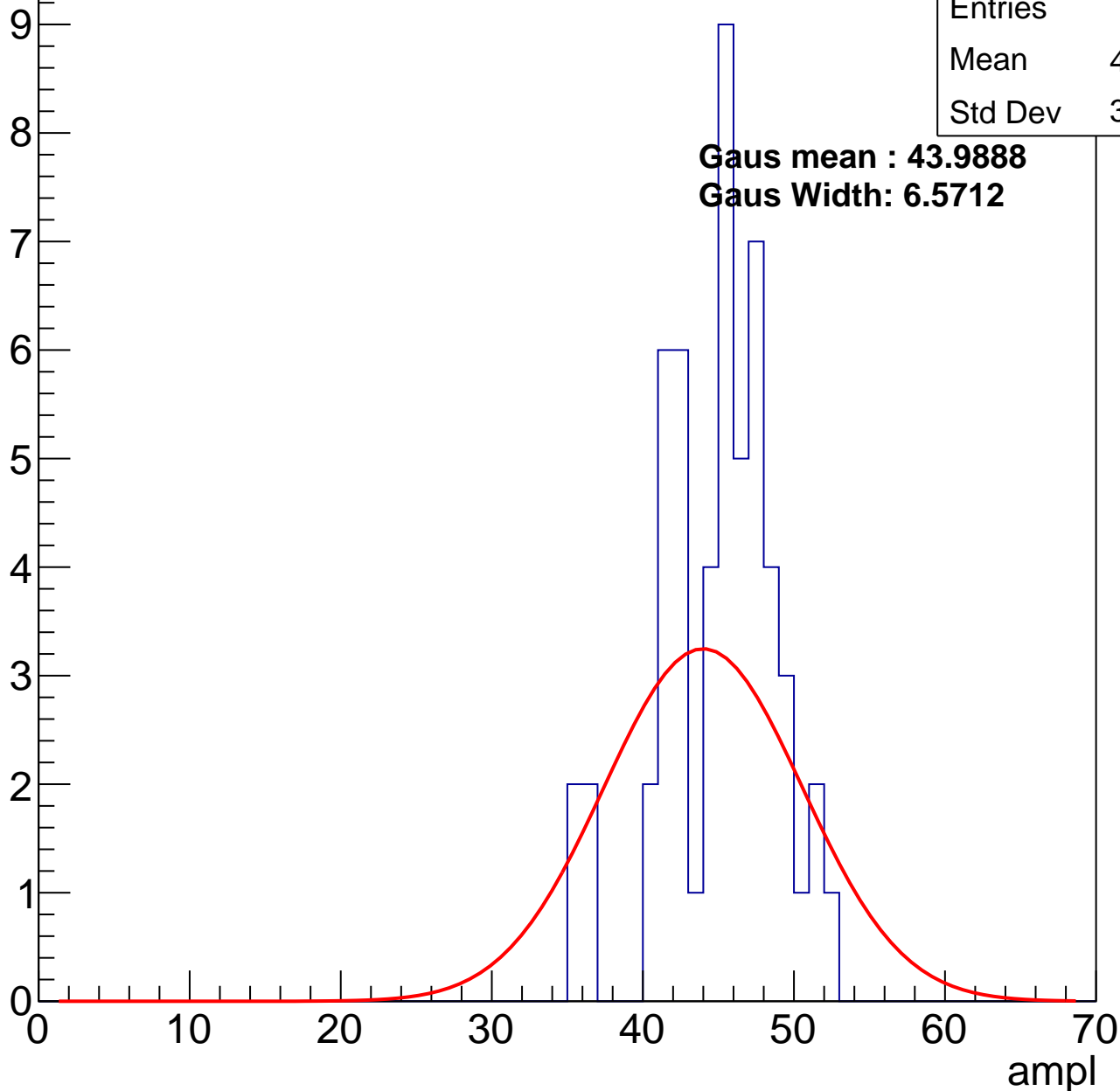
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	44.47
Std Dev	3.856

Gaus mean : 43.9888

Gaus Width: 6.5712

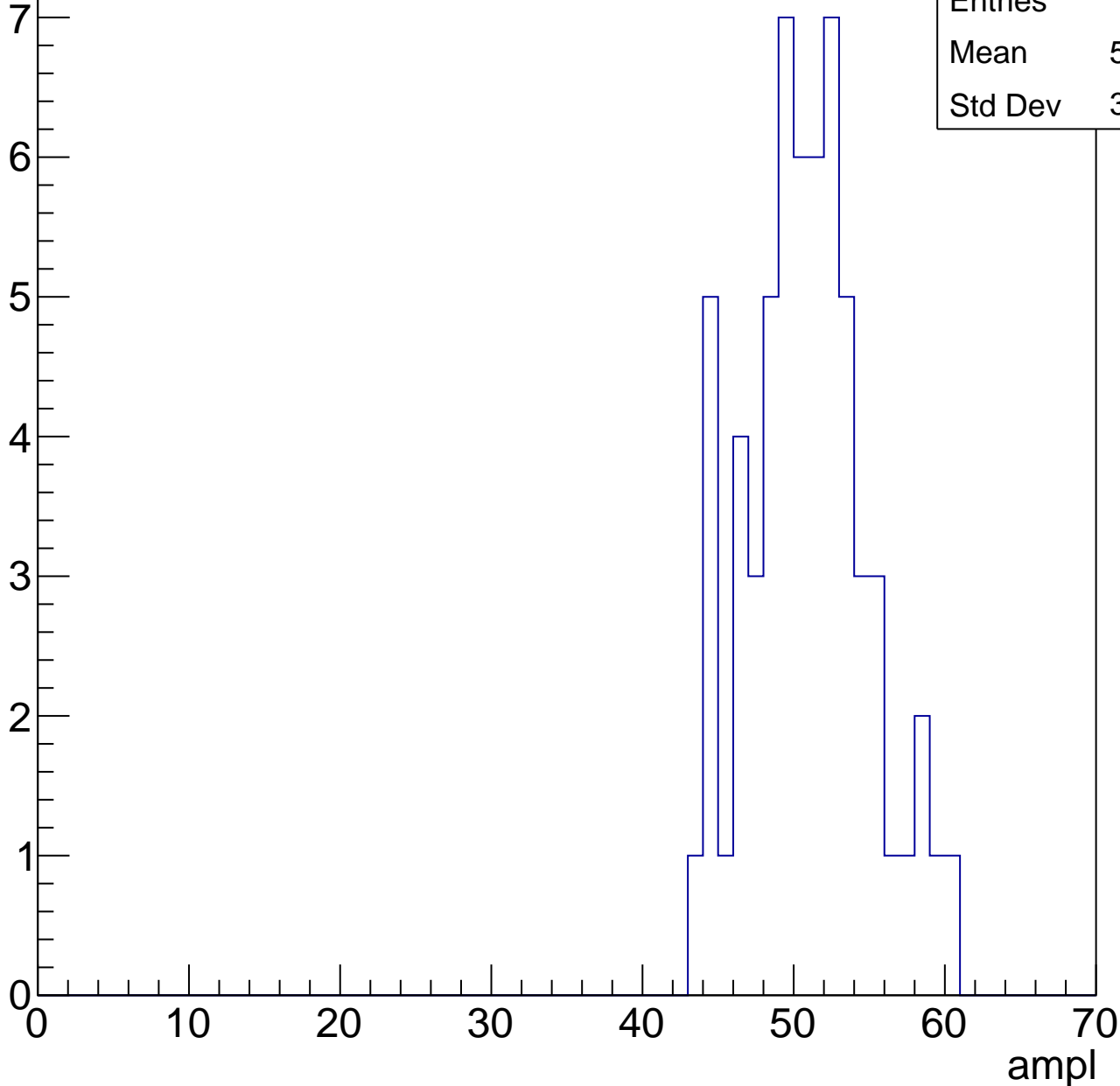


B1L103S, U12-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	50.42
Std Dev	3.929

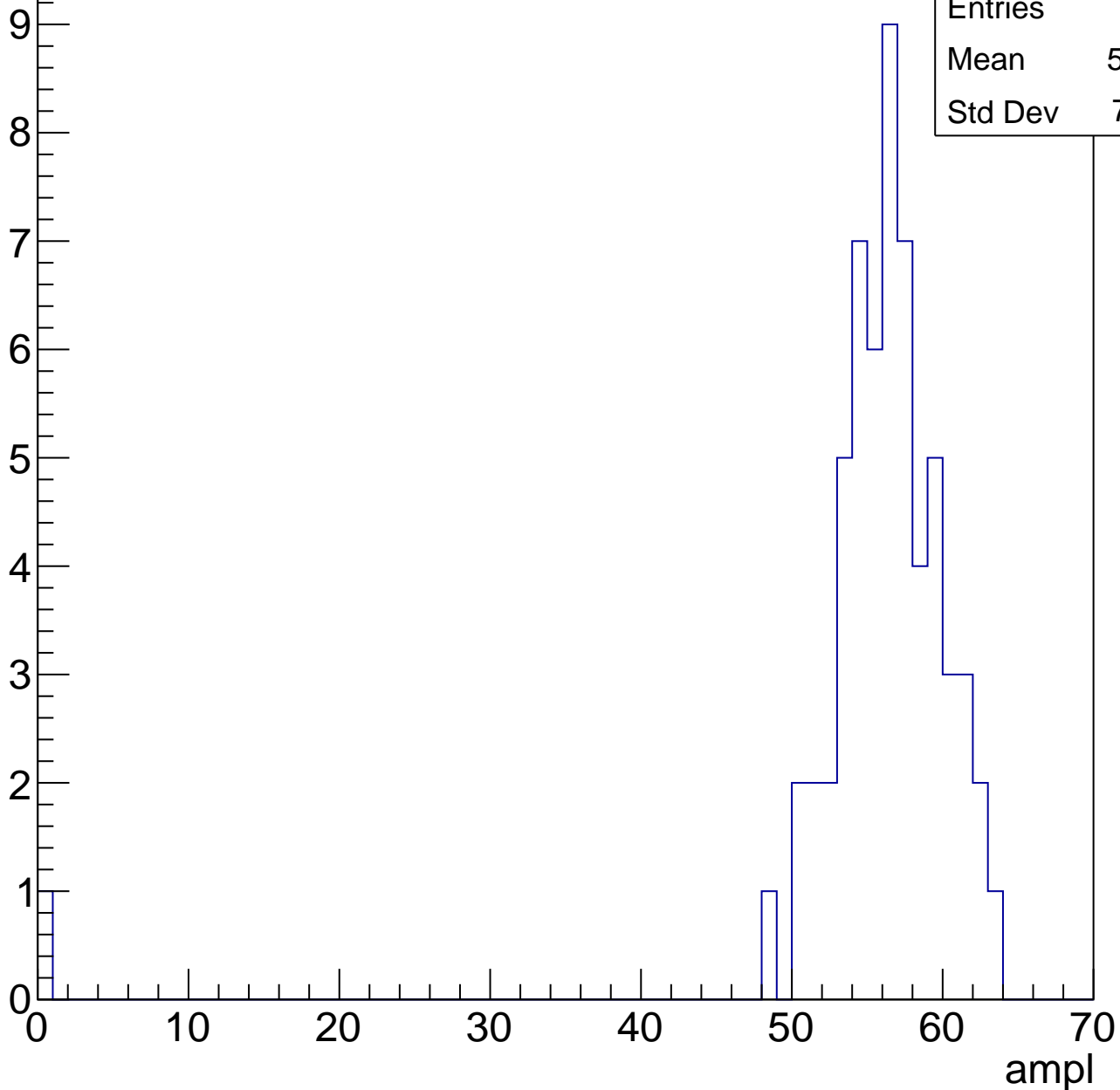


B1L103S, U12-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.12
Std Dev	7.851

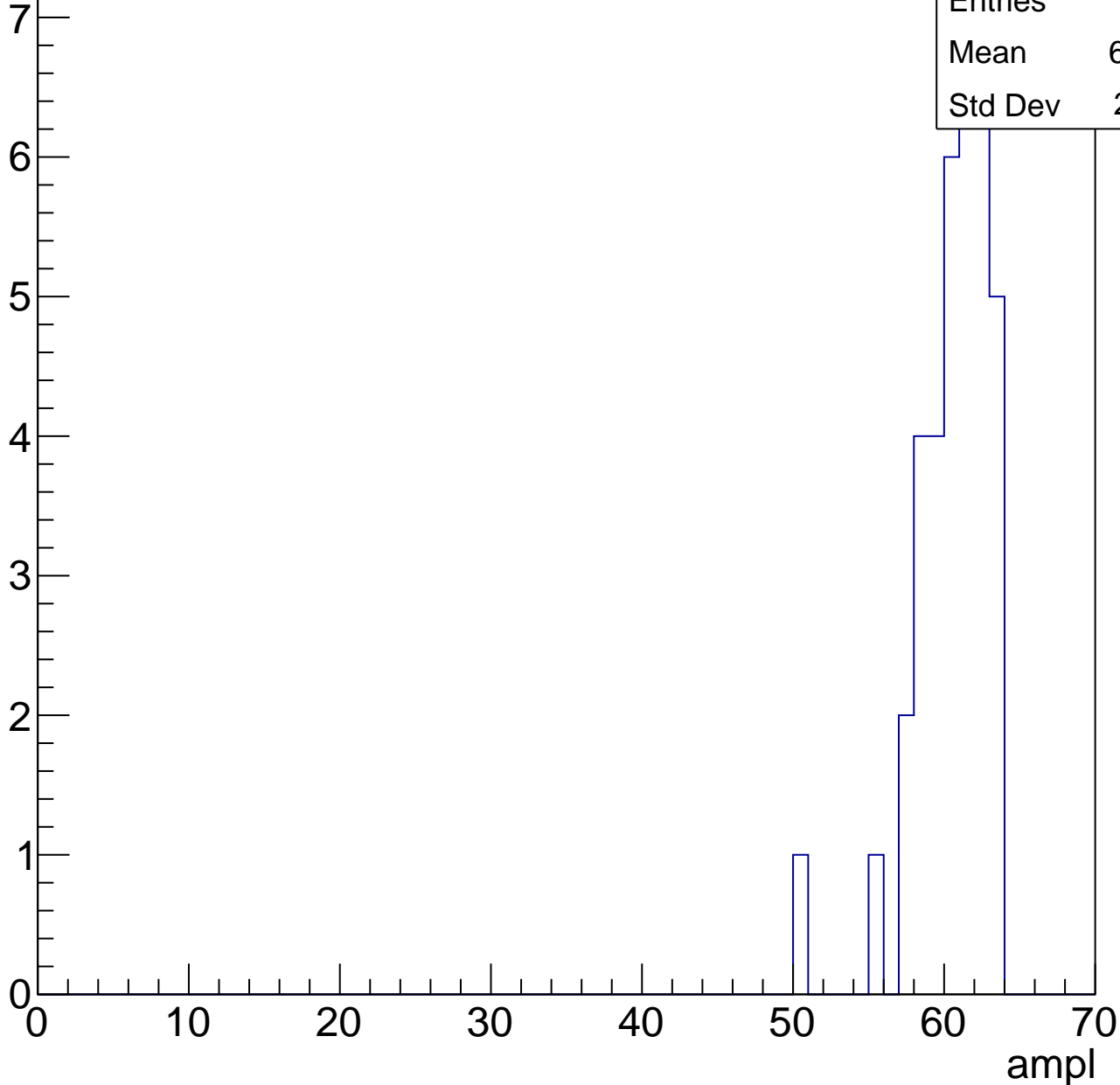


B1L103S, U12-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

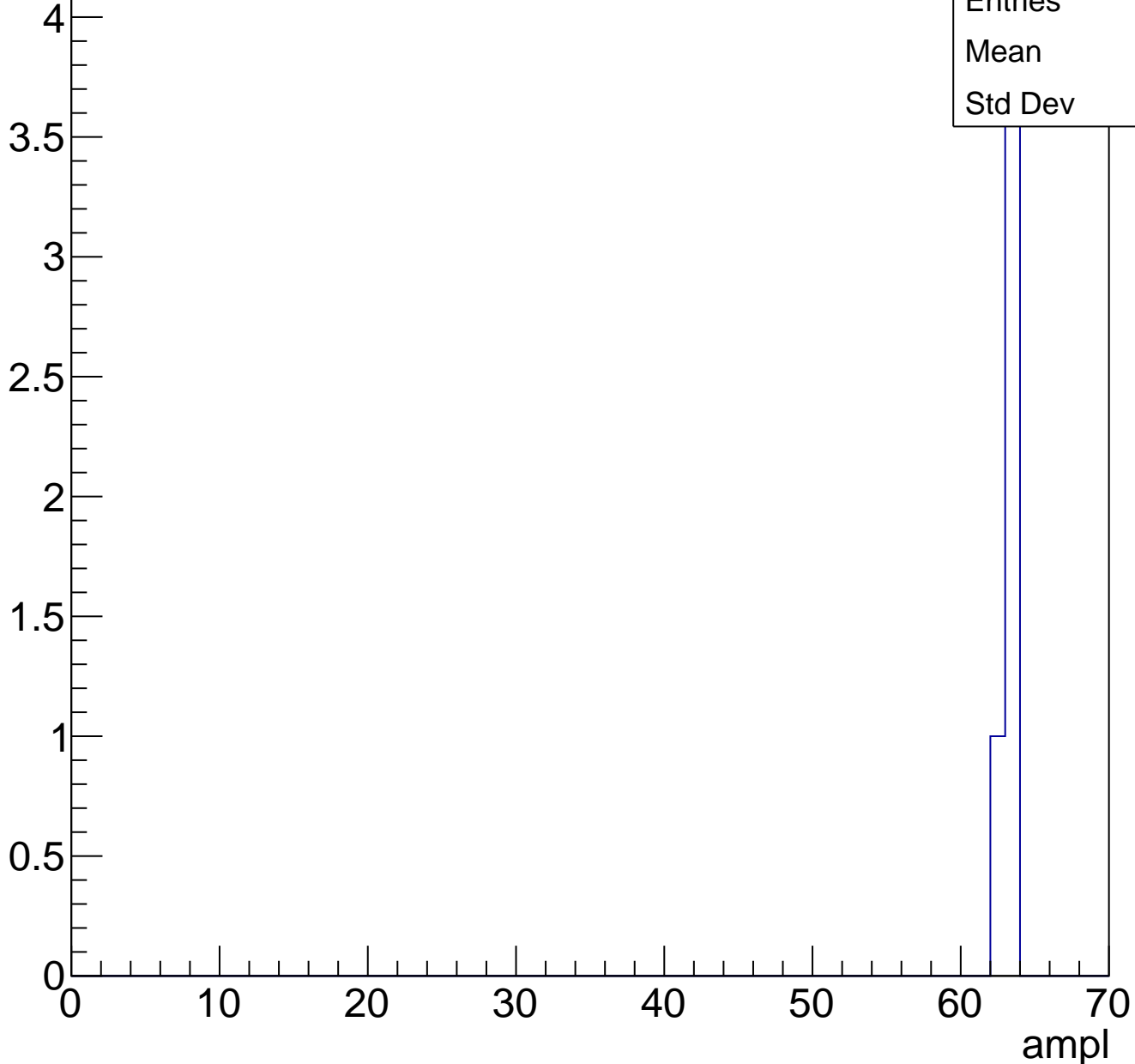
Entries	37
Mean	60.08
Std Dev	2.561



B1L103S, U12-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

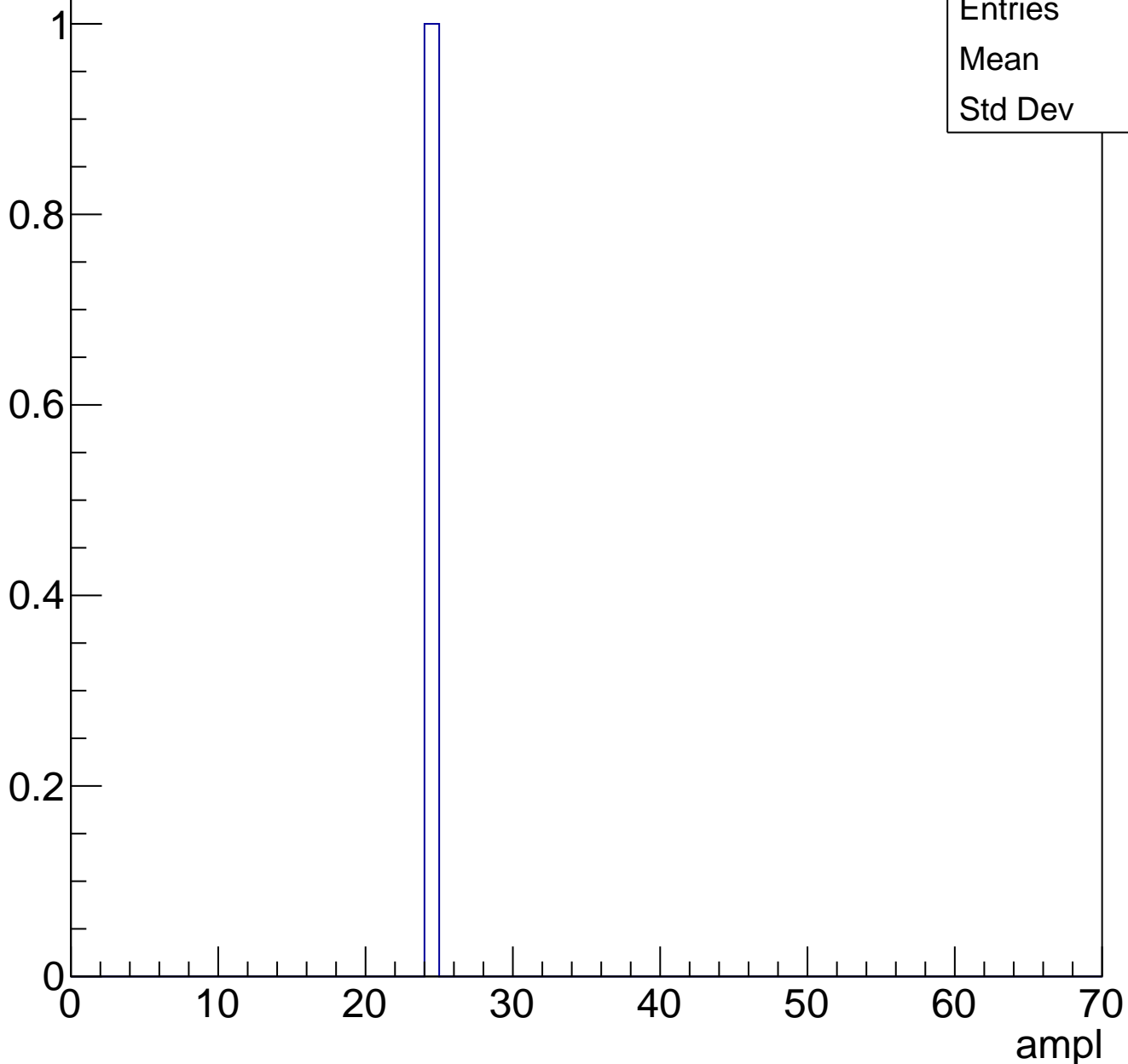


Entries	5
Mean	62.8
Std Dev	0.4

B1L103S, U12-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	24
Std Dev	0

B1L103S, U12-ch70, adc0

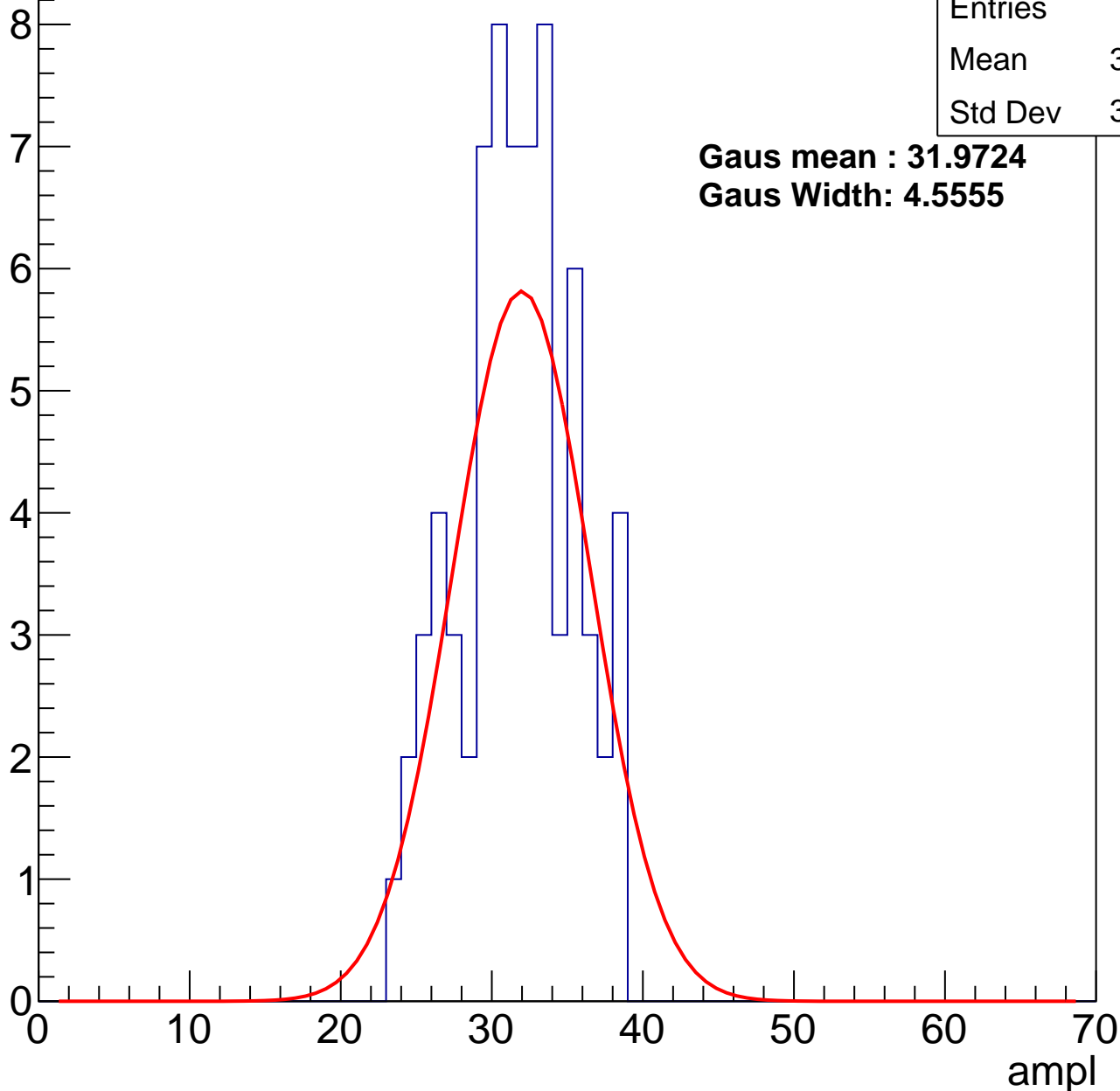
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	31.16
Std Dev	3.733

Gaus mean : 31.9724

Gaus Width: 4.5555



B1L103S, U12-ch70, adc1

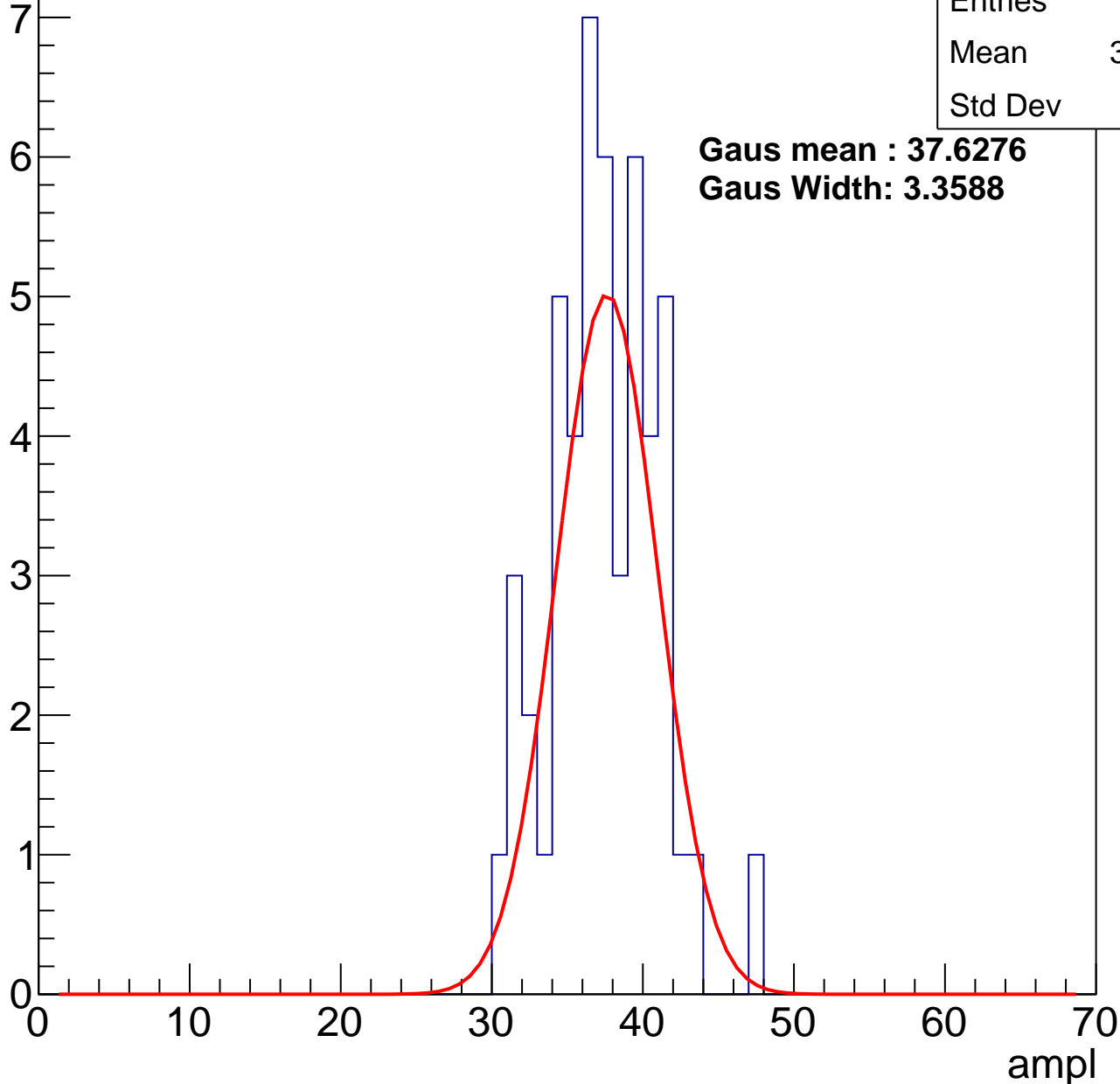
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	36.98
Std Dev	3.45

Gaus mean : 37.6276

Gaus Width: 3.3588



B1L103S, U12-ch70, adc2

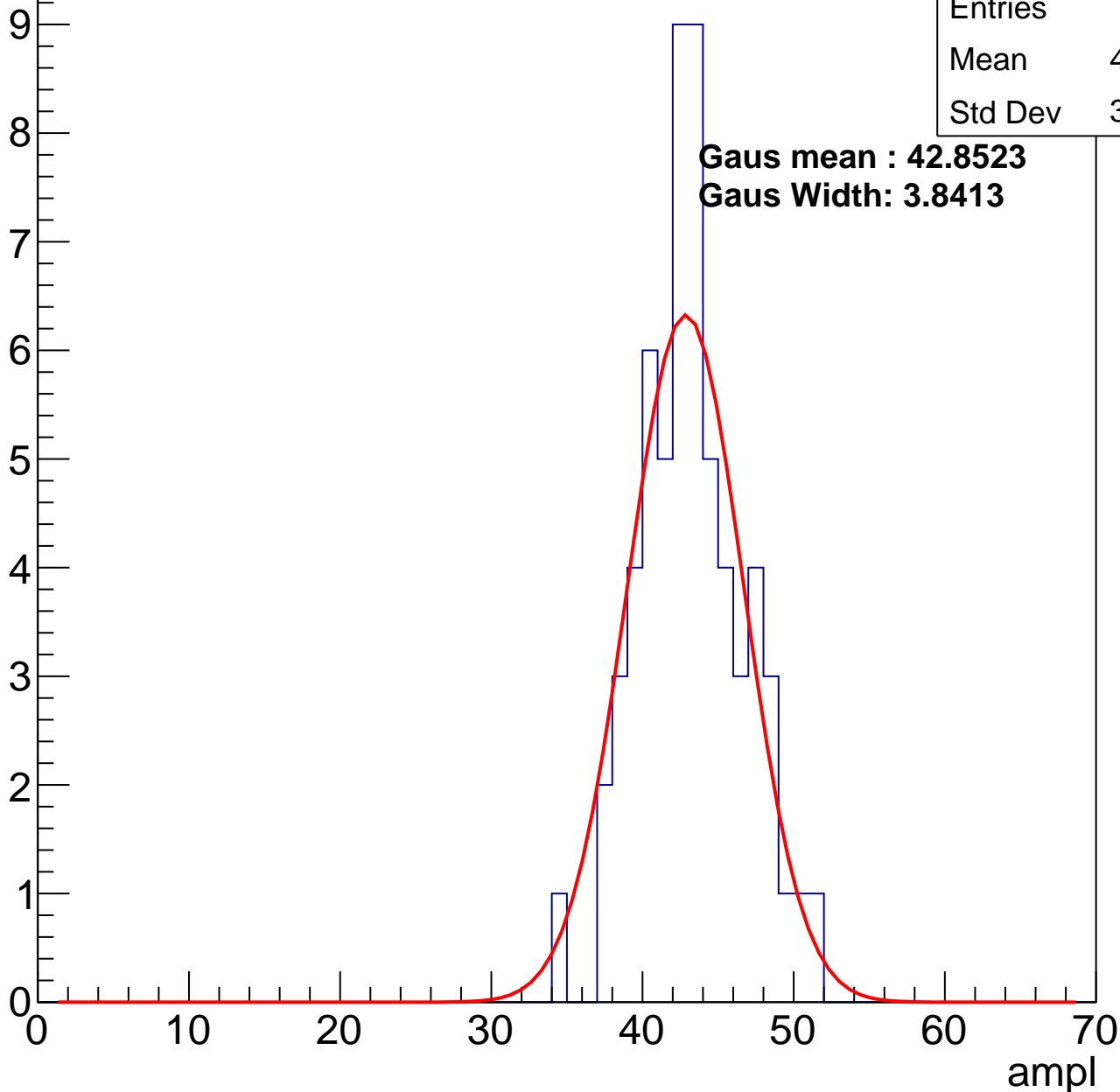
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.75
Std Dev	3.405

Gaus mean : 42.8523

Gaus Width: 3.8413

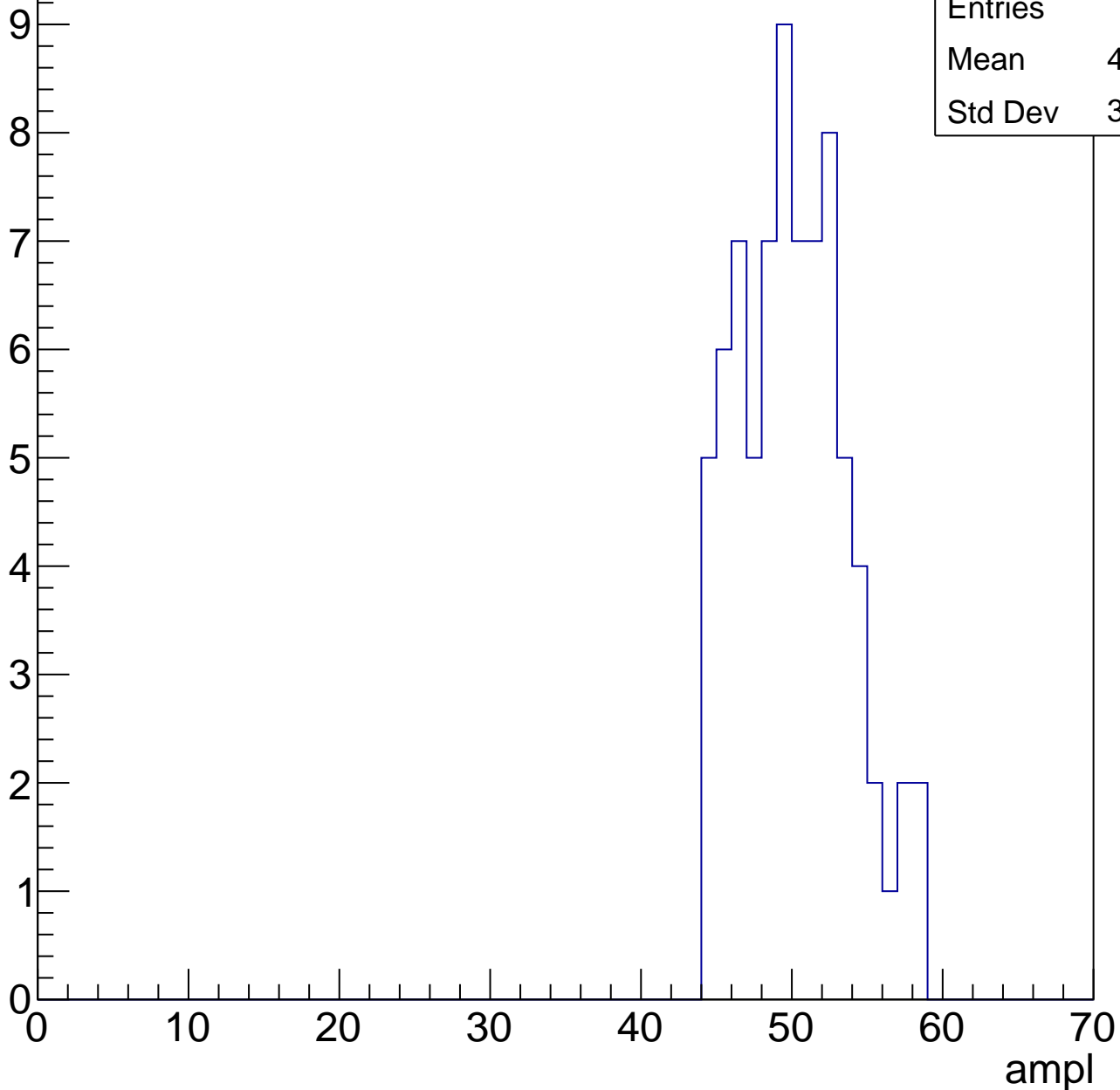


B1L103S, U12-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

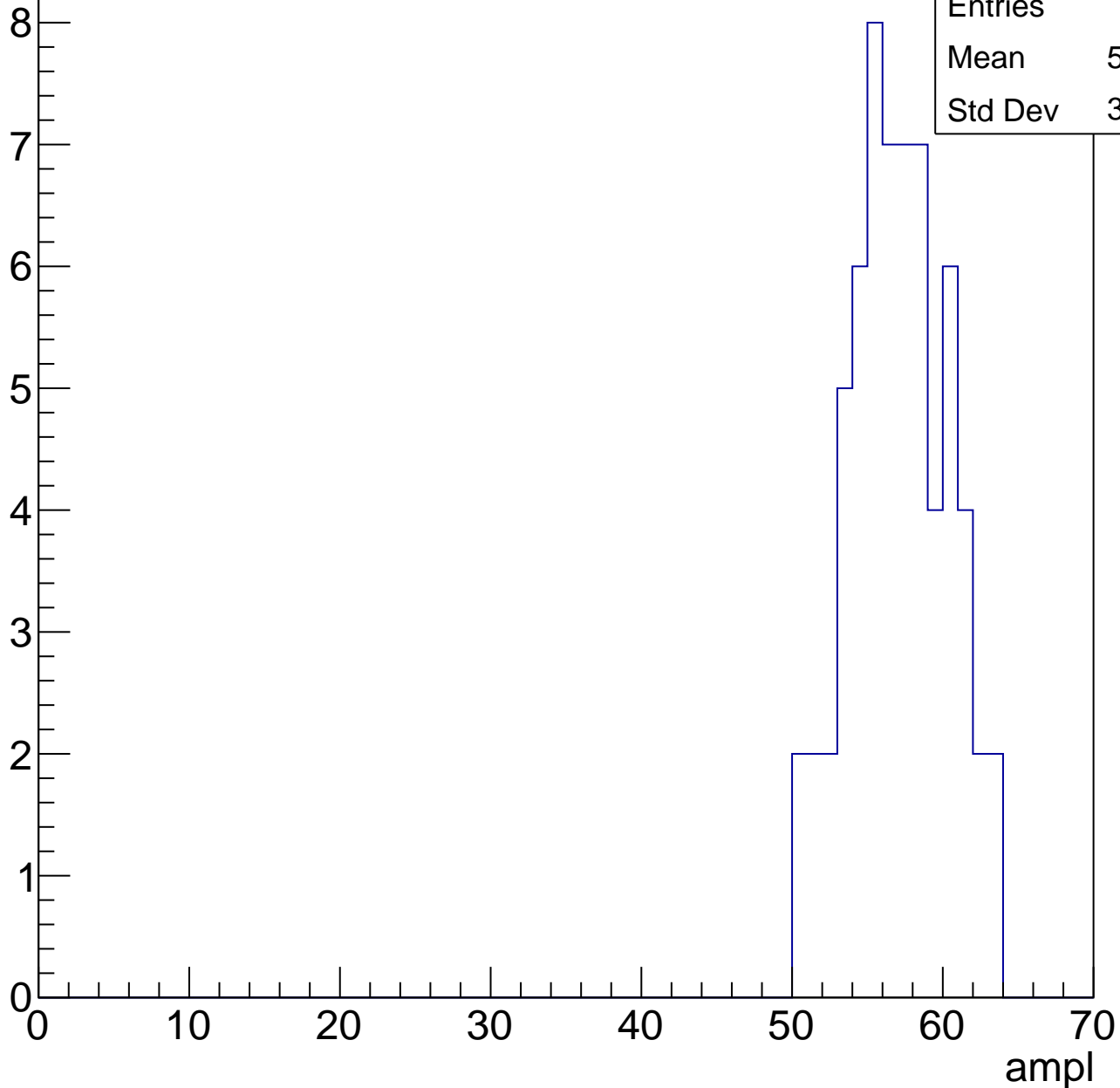
Entries	77
Mean	49.66
Std Dev	3.559



B1L103S, U12-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



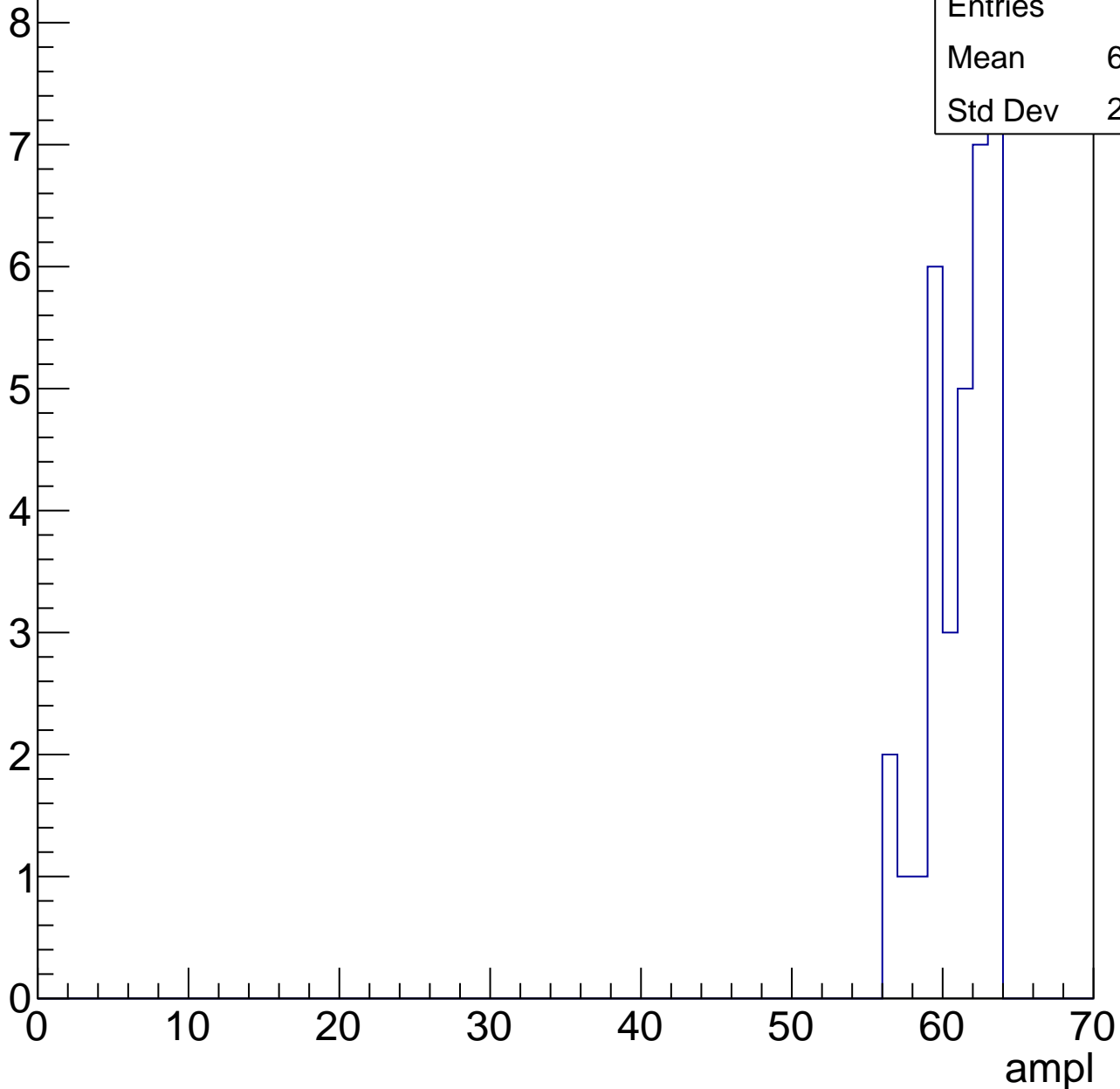
Entries	64
Mean	56.59
Std Dev	3.176

B1L103S, U12-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

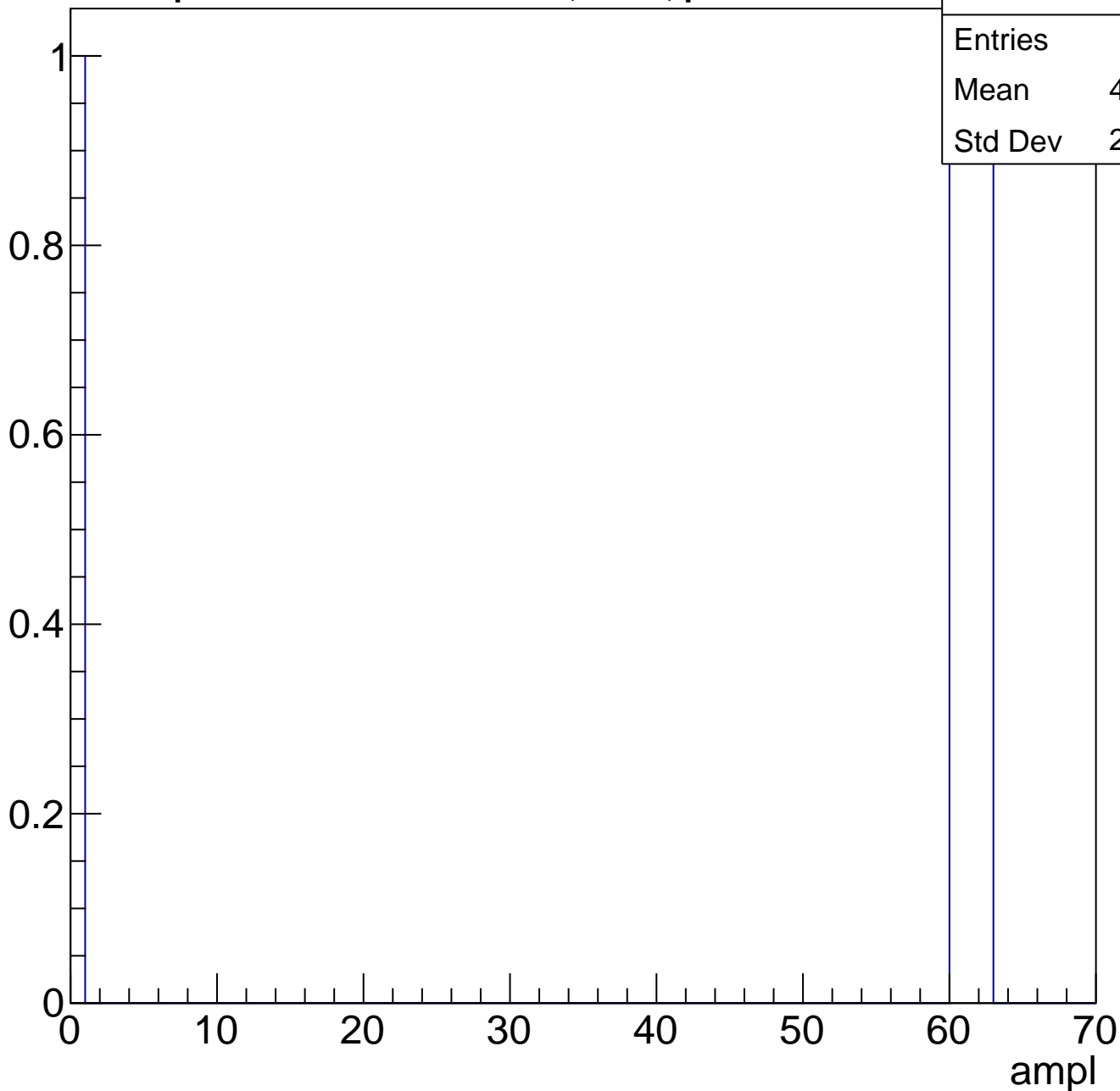
Entries	33
Mean	60.73
Std Dev	2.049



B1L103S, U12-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch71, adc0

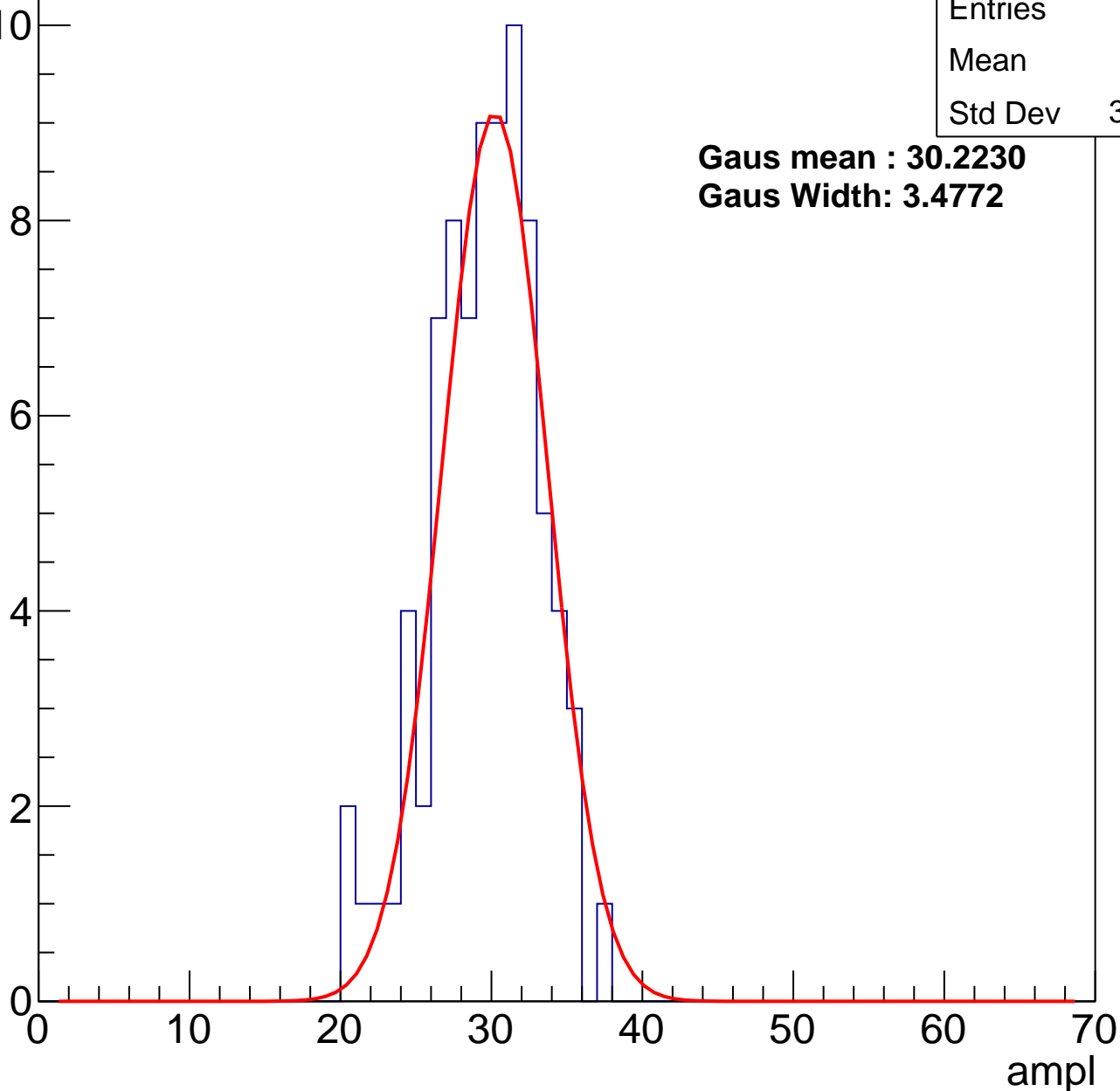
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	29.1
Std Dev	3.522

Gaus mean : 30.2230

Gaus Width: 3.4772



B1L103S, U12-ch71, adc1

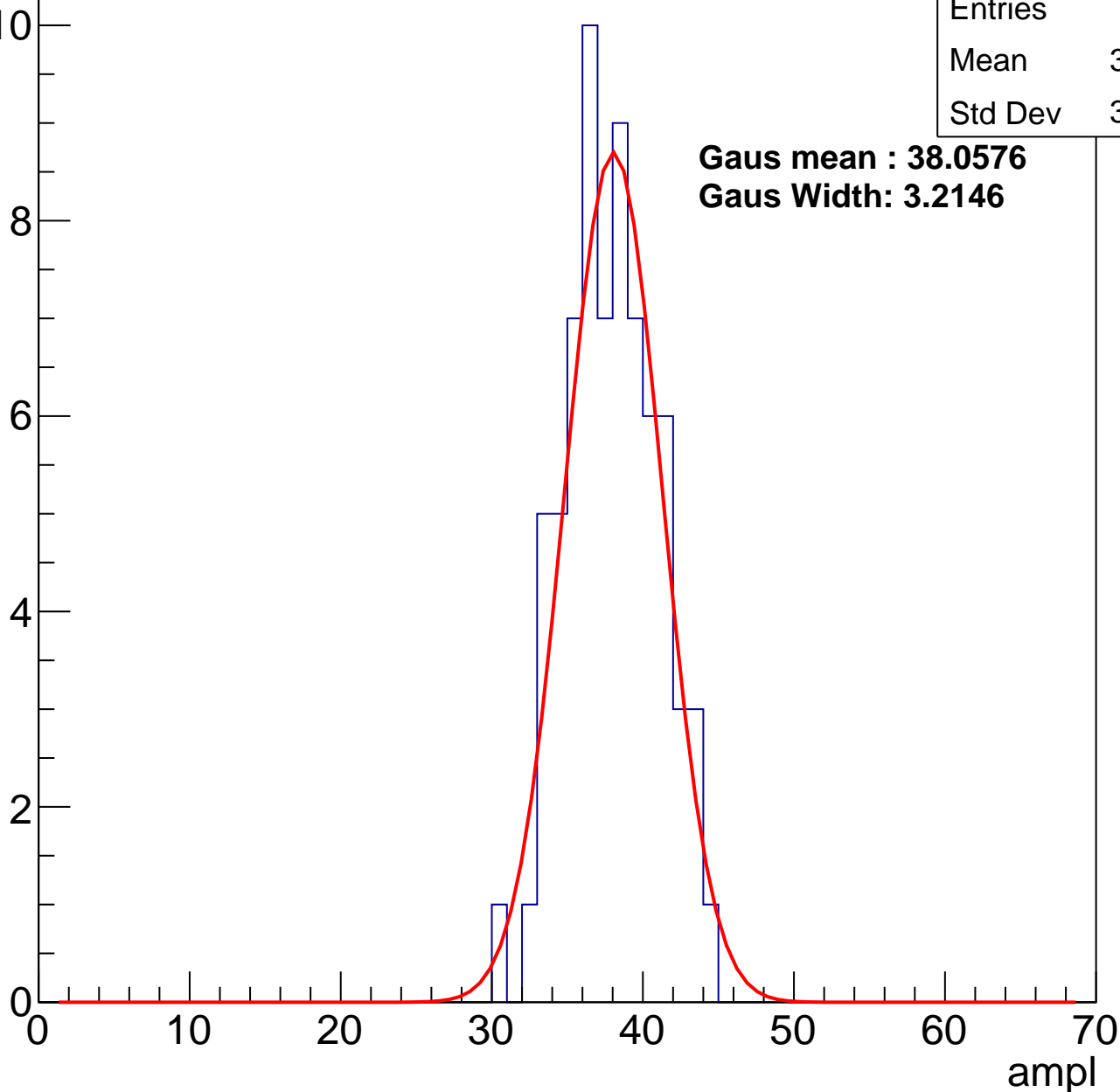
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	37.48
Std Dev	3.002

Gaus mean : 38.0576

Gaus Width: 3.2146



B1L103S, U12-ch71, adc2

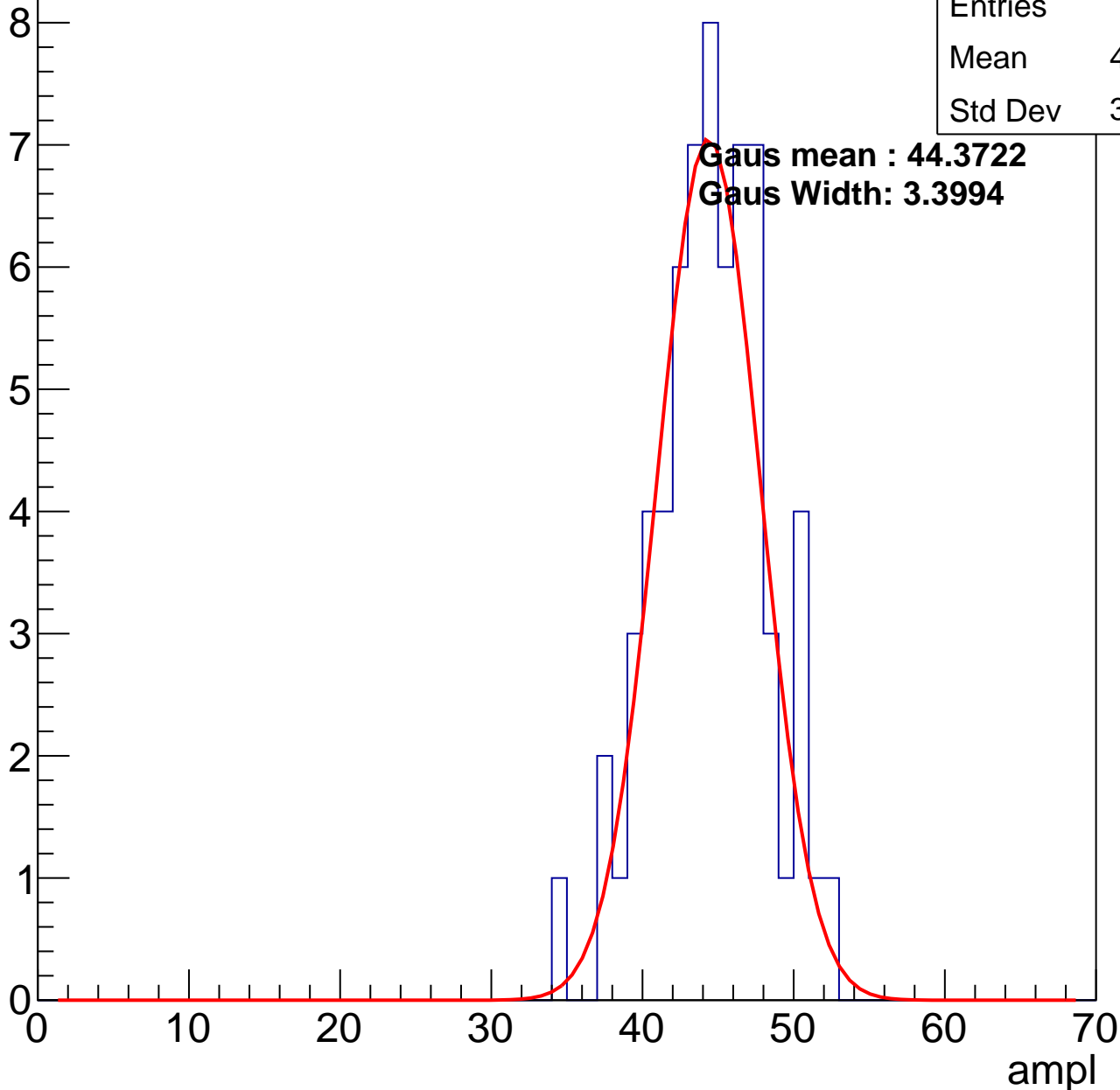
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	44.08
Std Dev	3.624

Gaus mean : 44.3722

Gaus Width: 3.3994

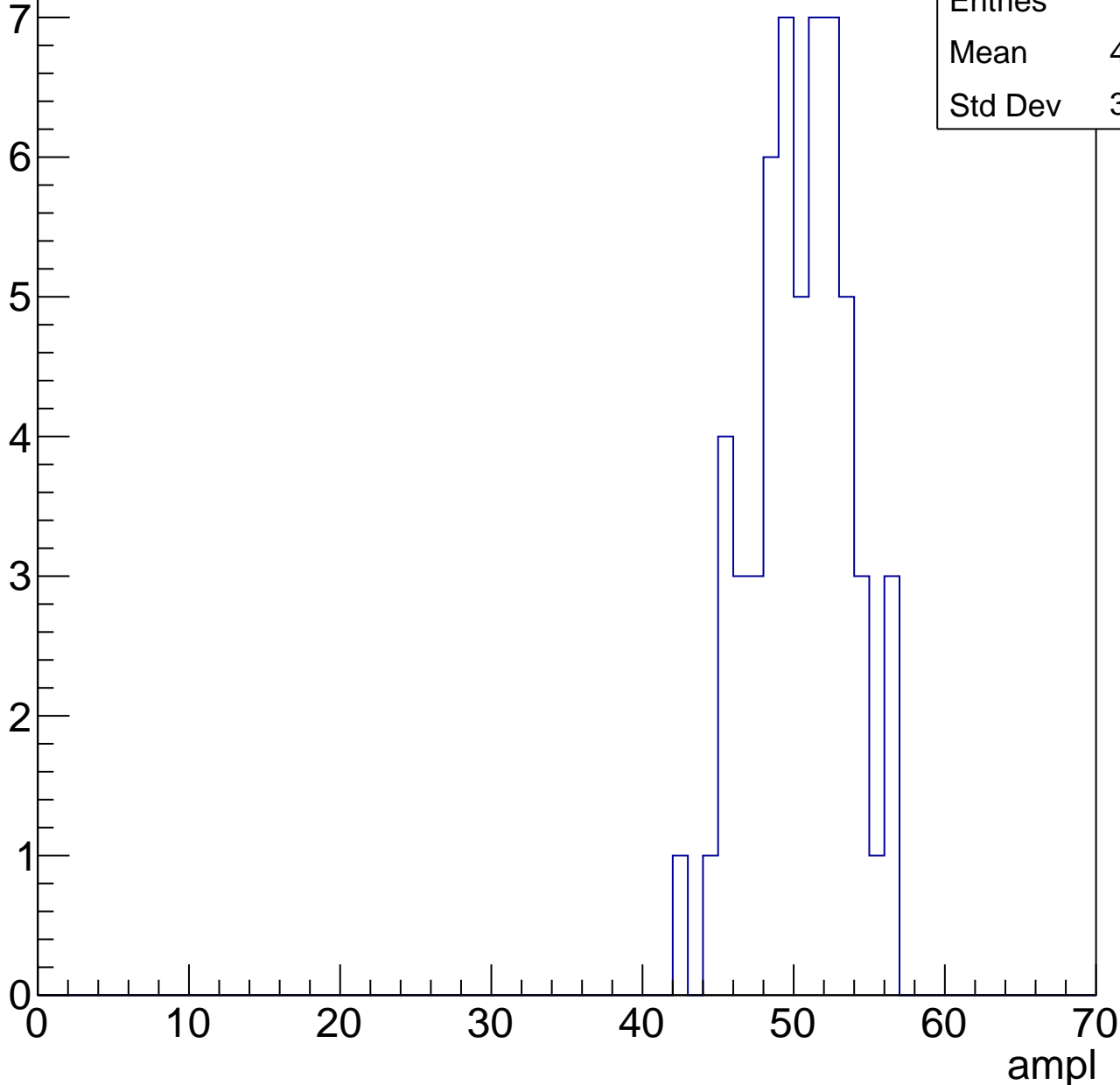


B1L103S, U12-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

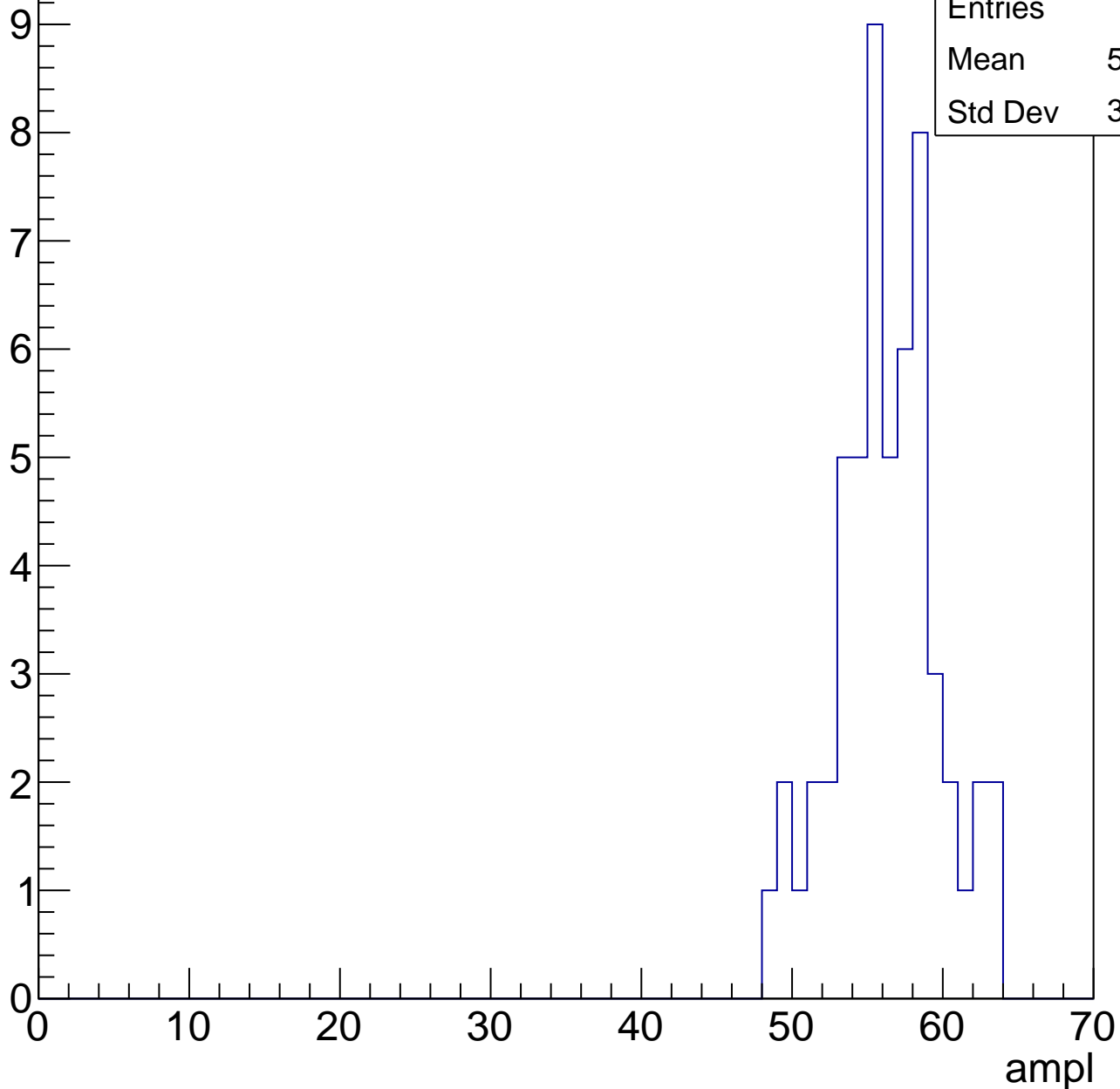
Entries	56
Mean	49.95
Std Dev	3.182



B1L103S, U12-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch71, adc5

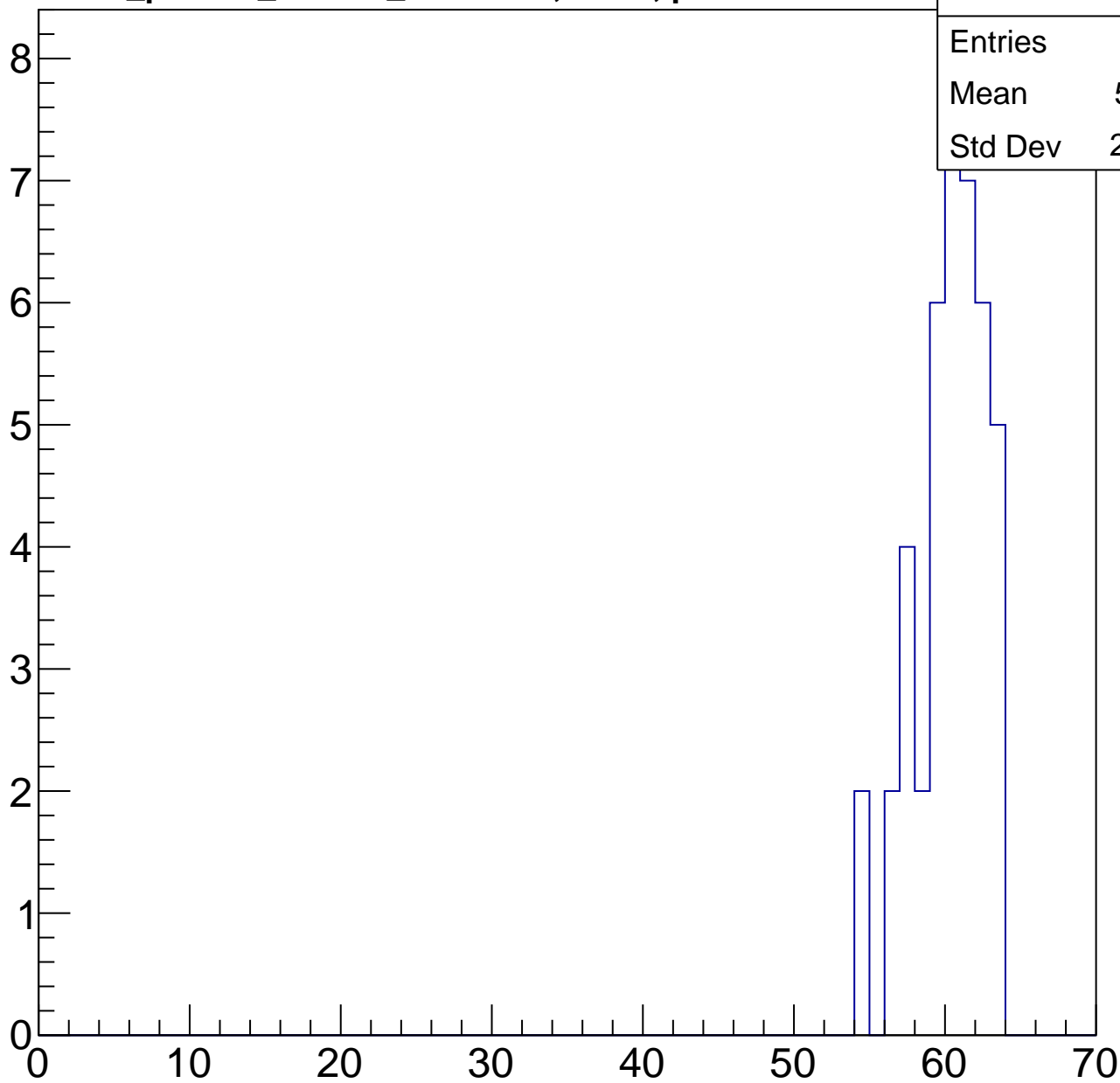
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	59.81
Std Dev	2.332

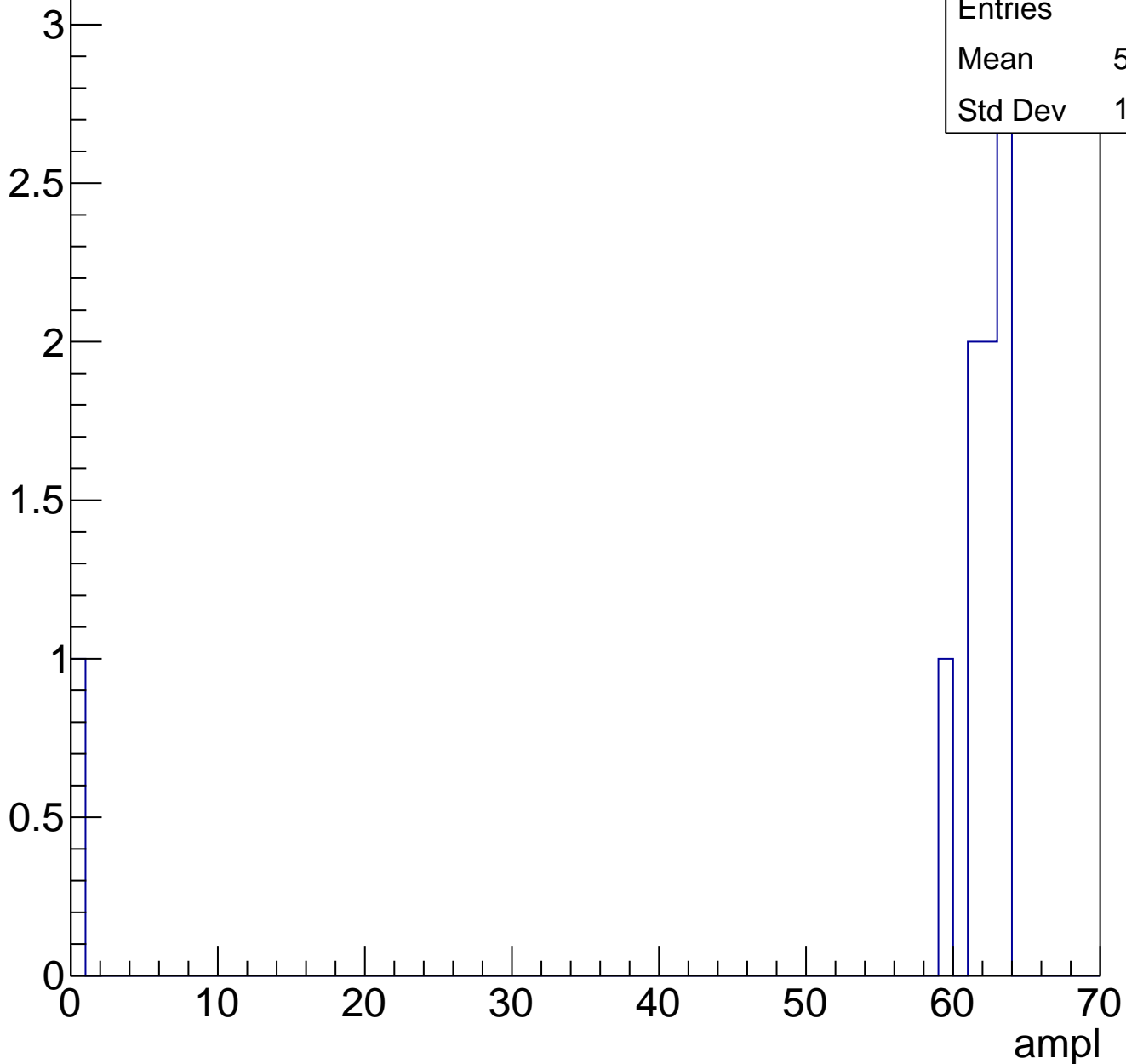
ampl



B1L103S, U12-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	9
Mean	54.89
Std Dev	19.44

B1L103S, U12-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch72, adc0

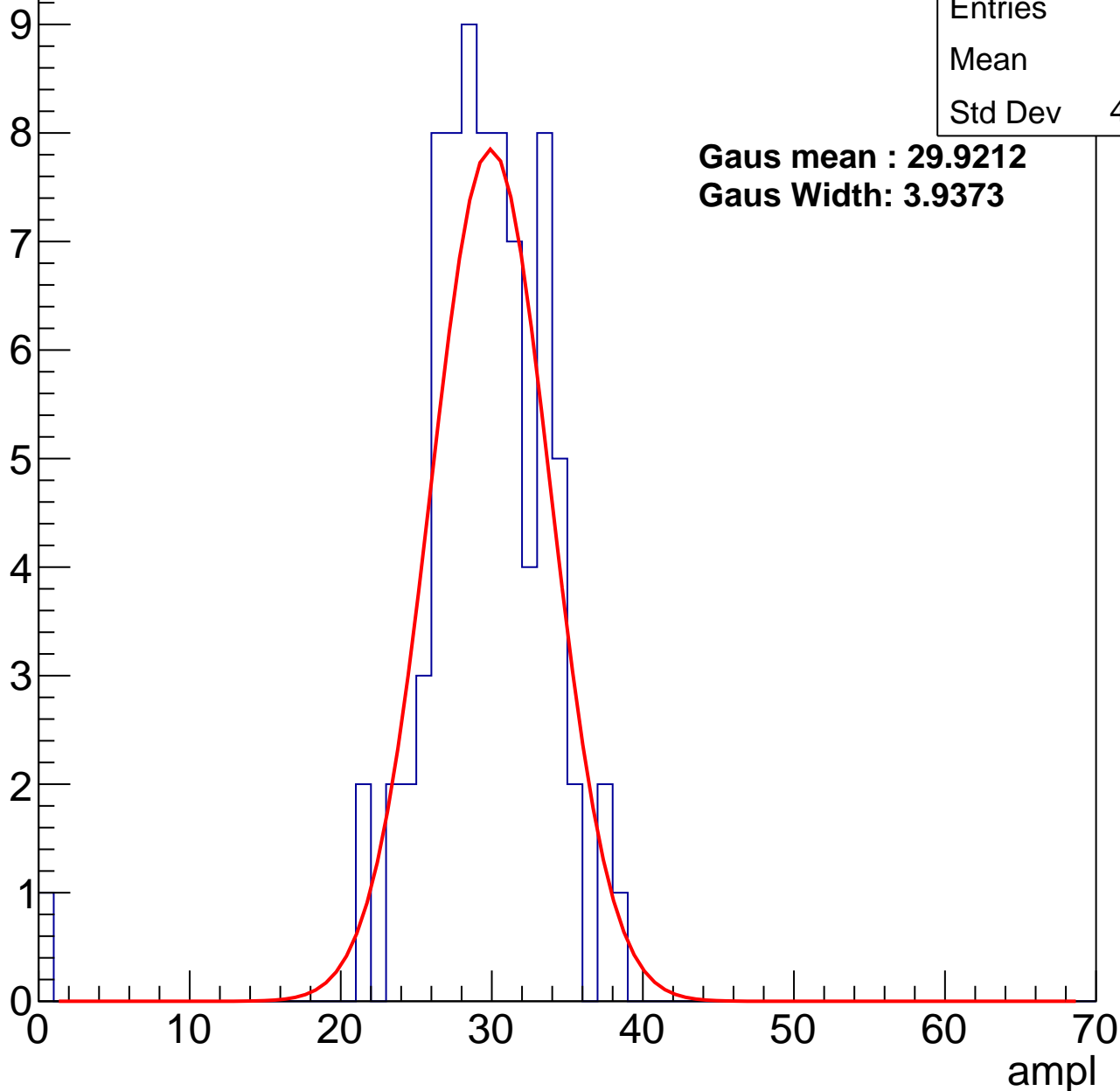
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	29
Std Dev	4.819

Gaus mean : 29.9212

Gaus Width: 3.9373



B1L103S, U12-ch72, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	37.19
Std Dev	3.842

Gaus mean : 37.4237

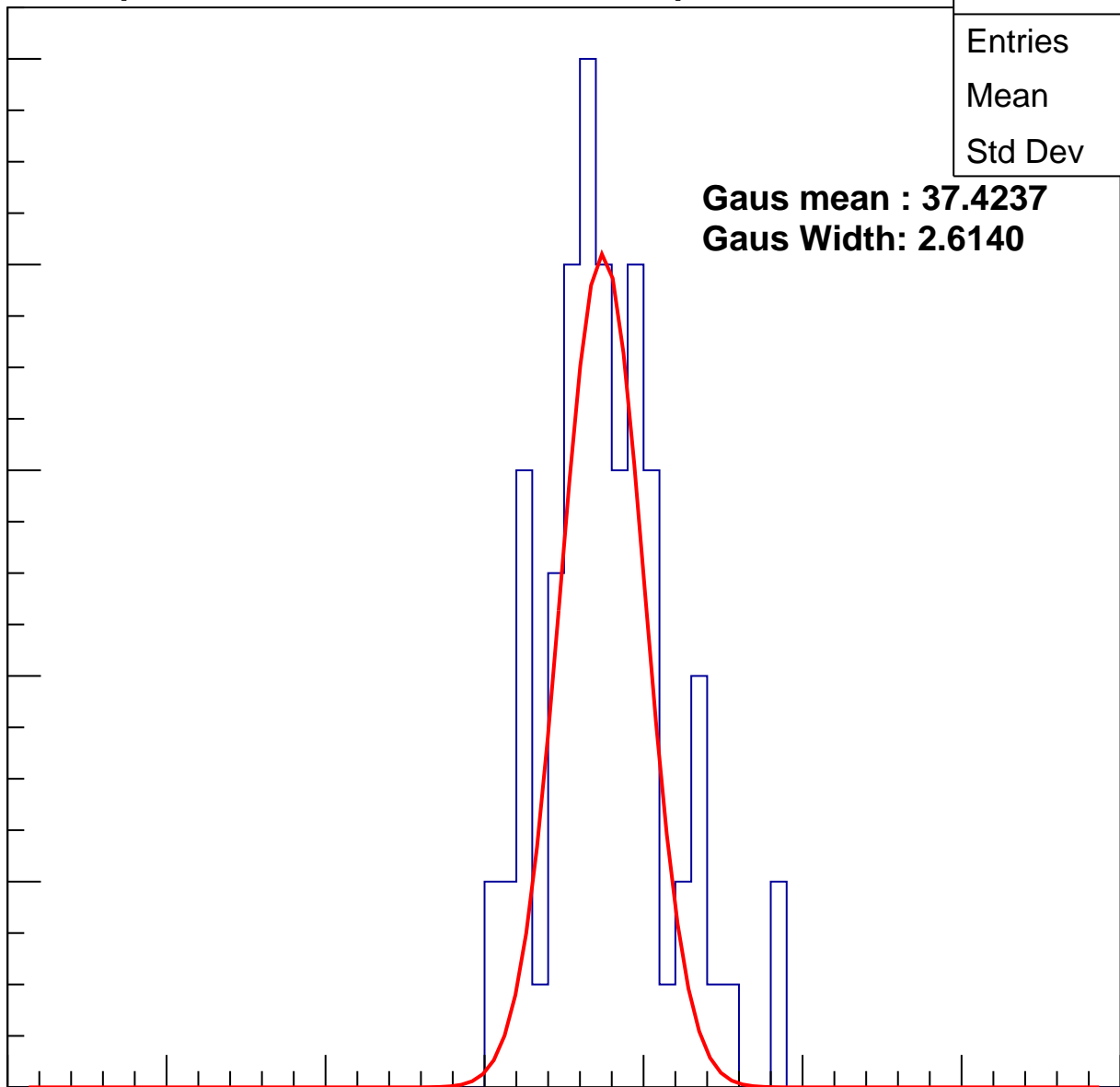
Gaus Width: 2.6140

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch72, adc2

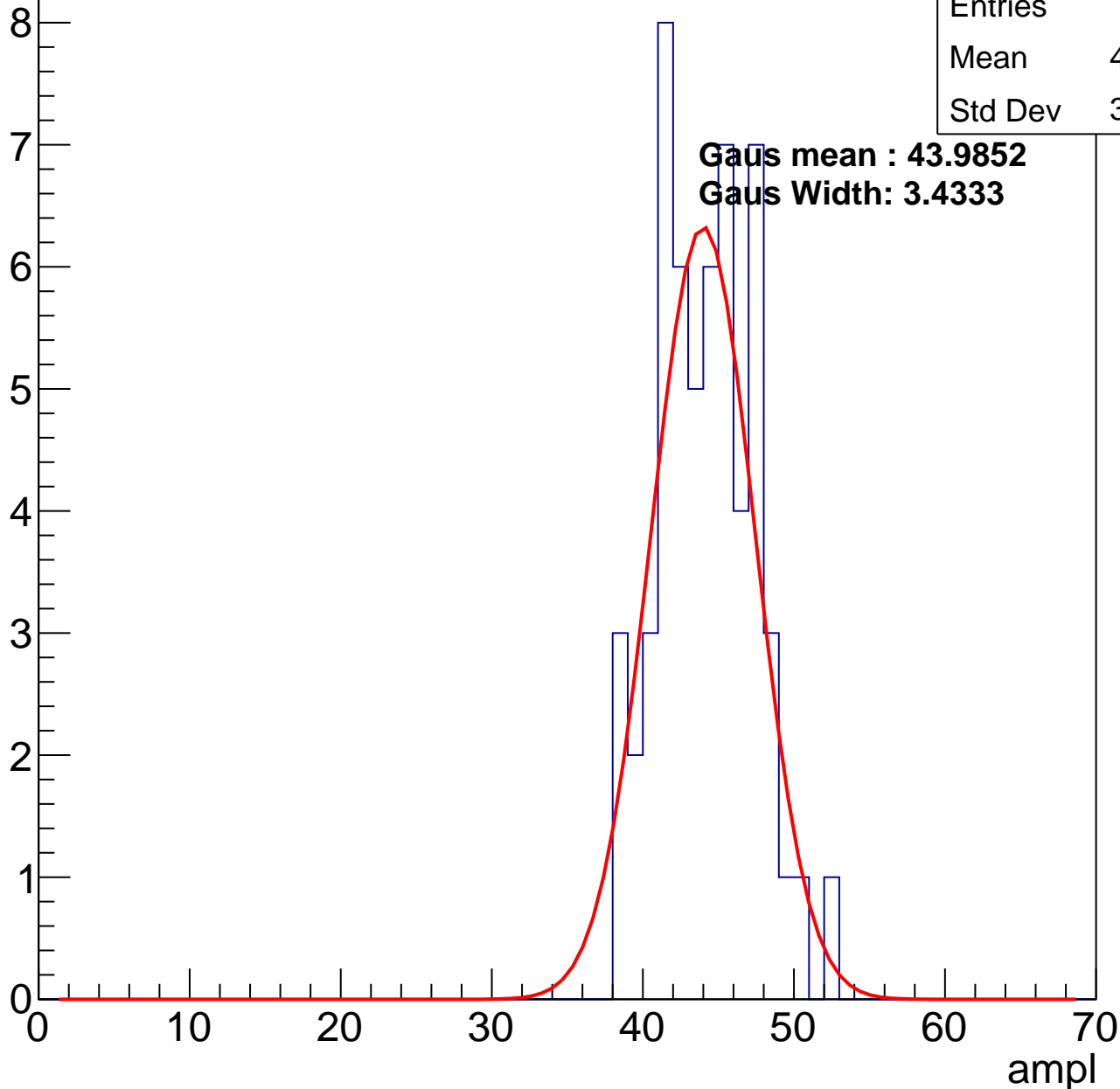
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.75
Std Dev	3.142

Gaus mean : 43.9852

Gaus Width: 3.4333

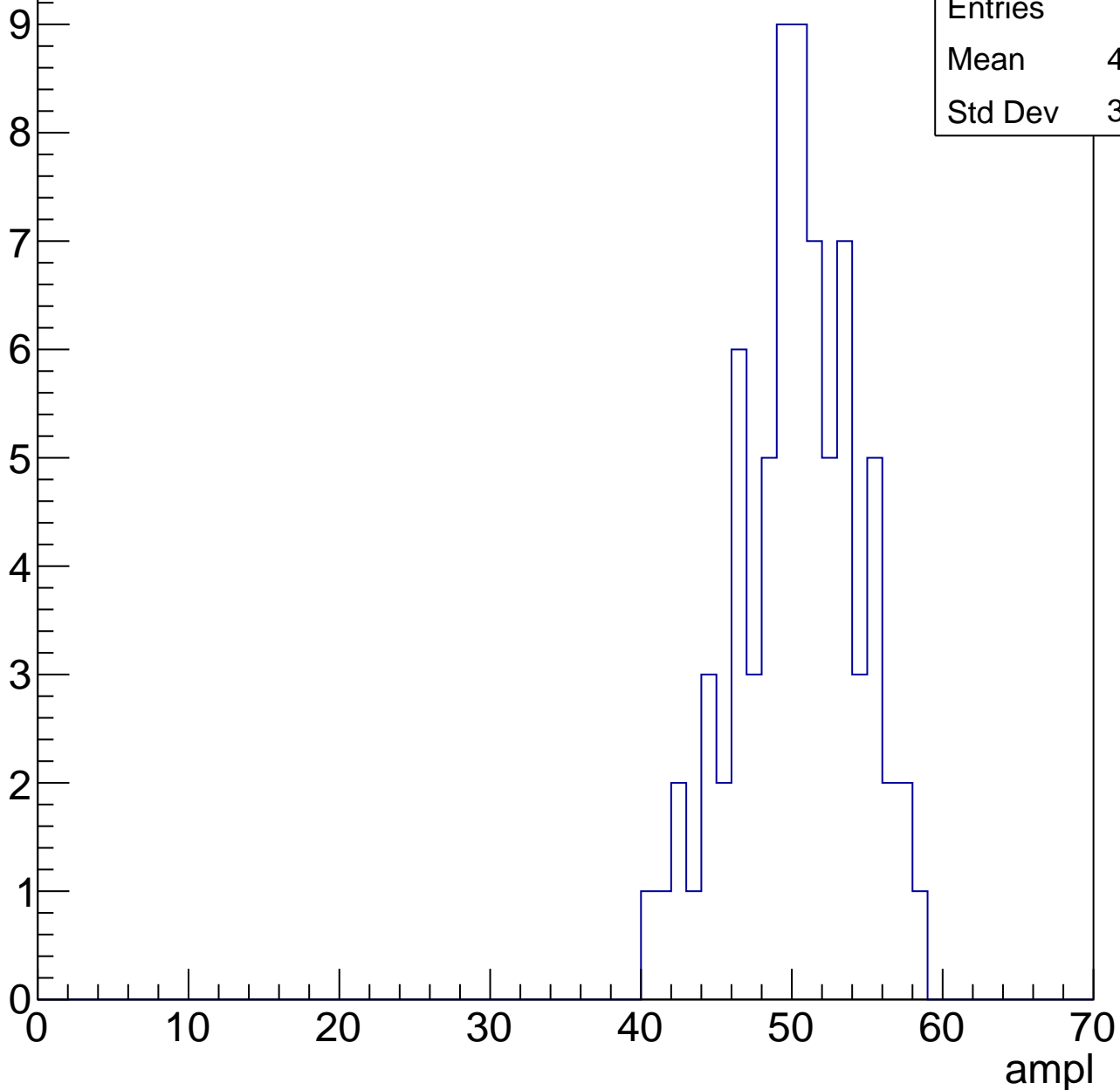


B1L103S, U12-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.82
Std Dev	3.964

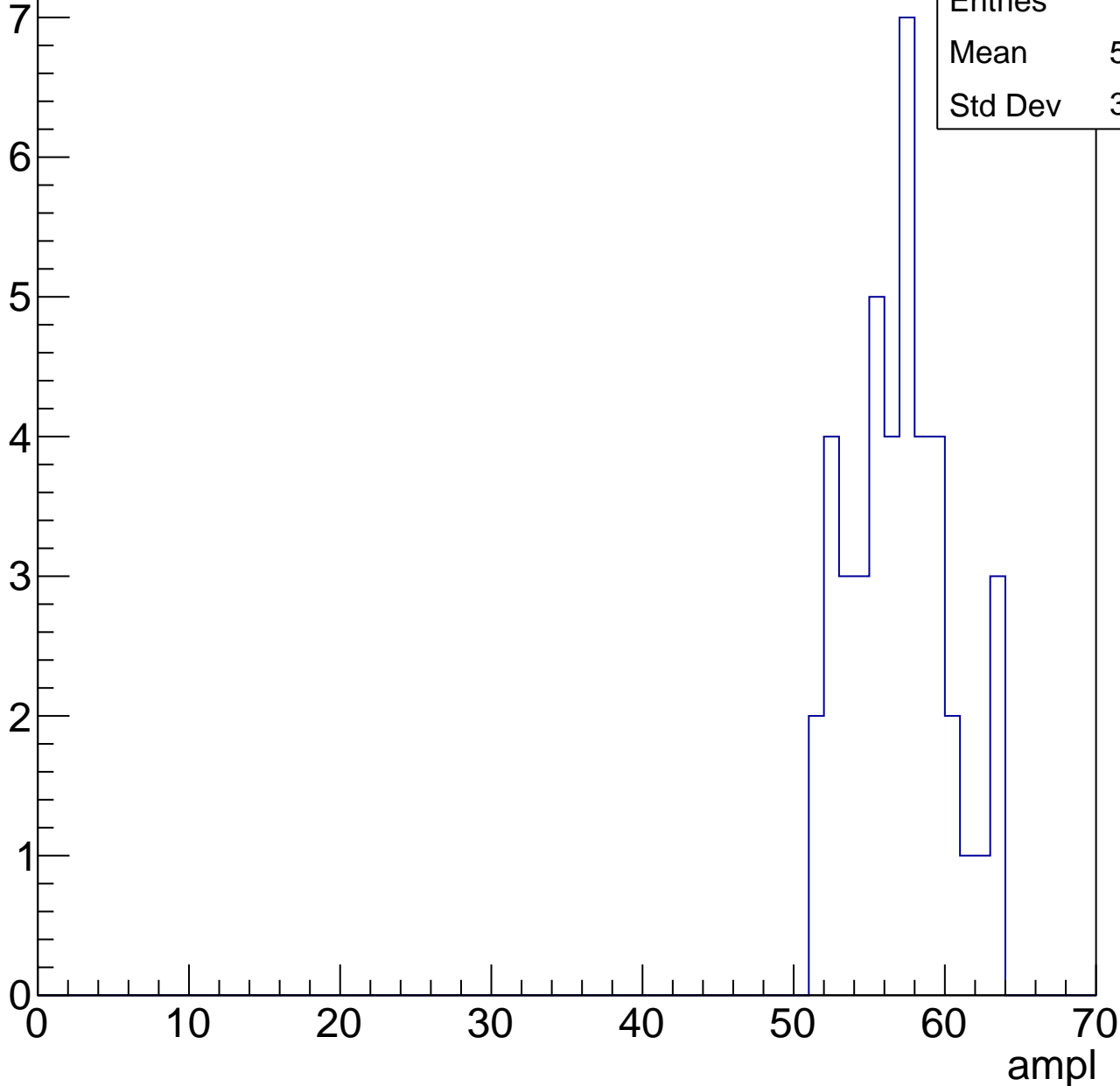


B1L103S, U12-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	56.49
Std Dev	3.223

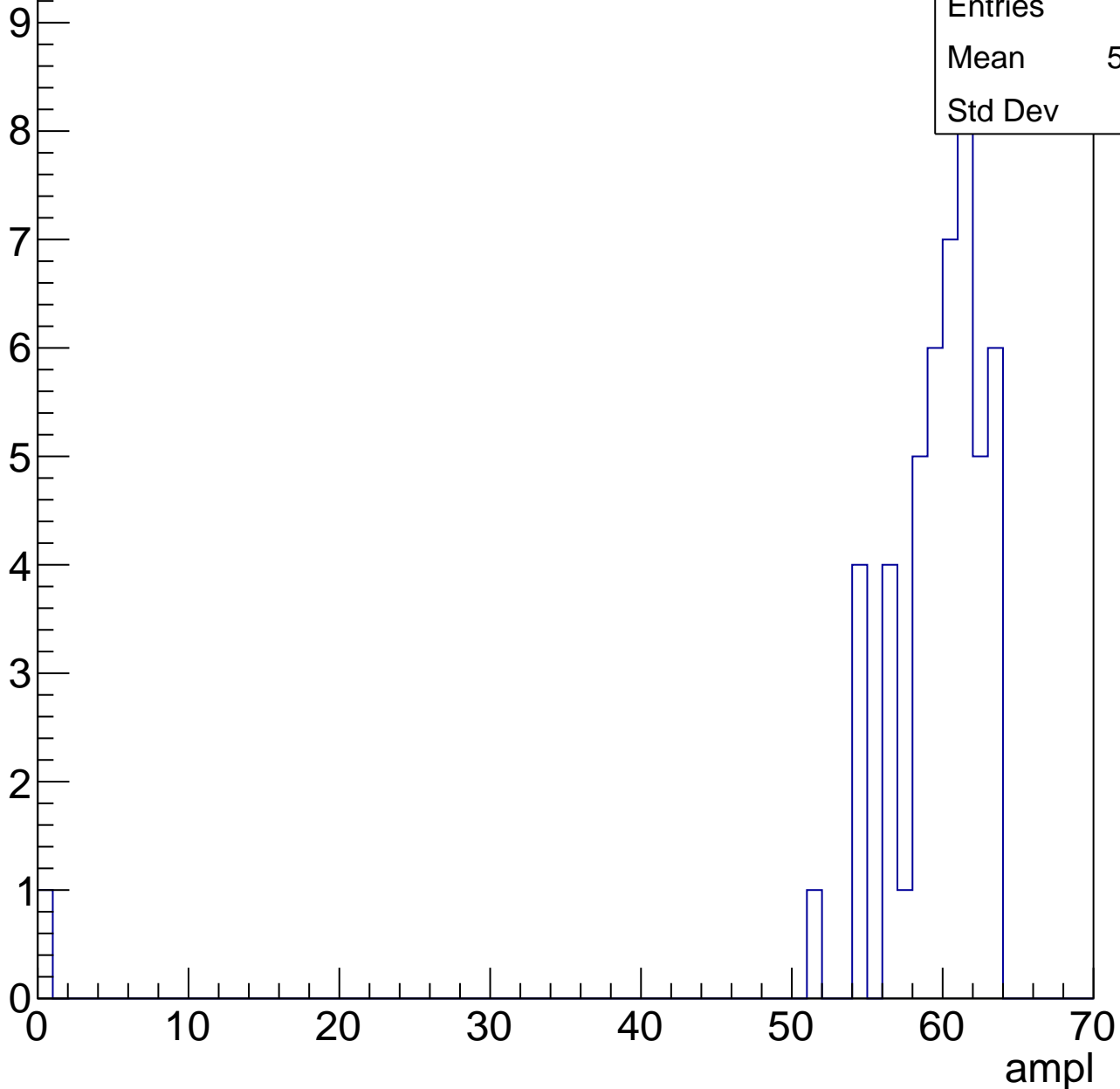


B1L103S, U12-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

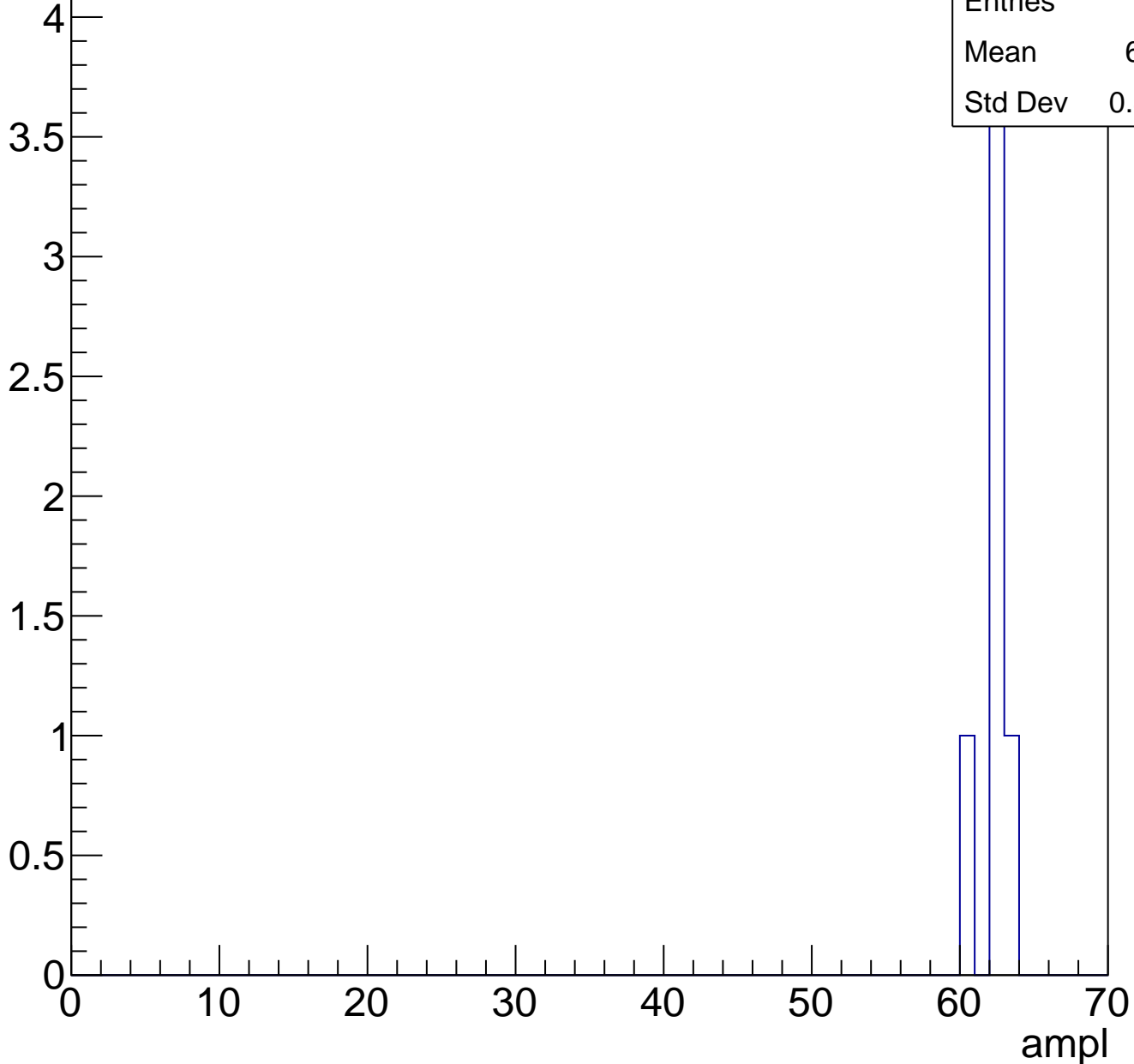
Entries	49
Mean	58.14
Std Dev	8.85



B1L103S, U12-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch73, adc0

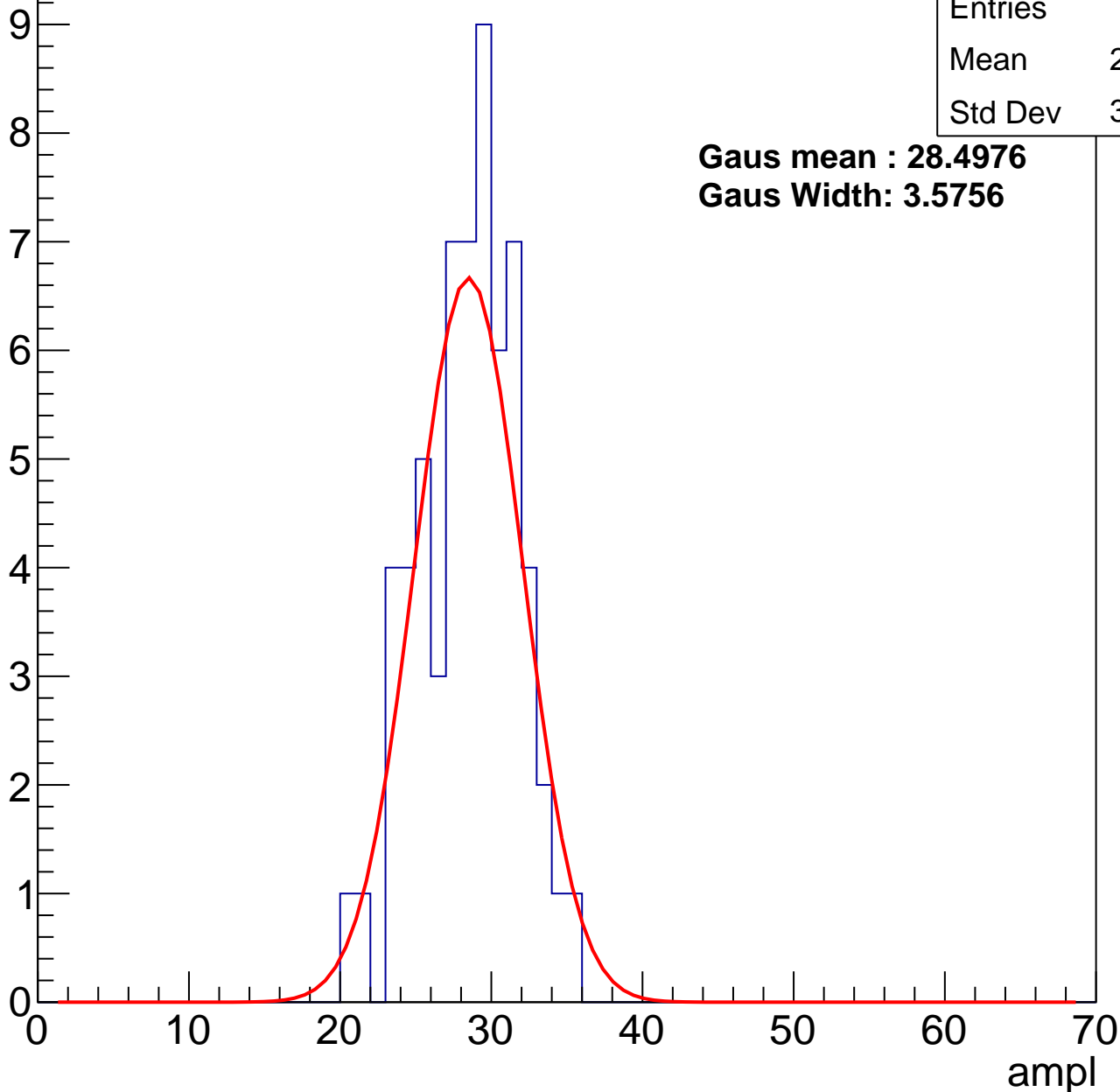
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.03
Std Dev	3.203

Gaus mean : 28.4976

Gaus Width: 3.5756



B1L103S, U12-ch73, adc1

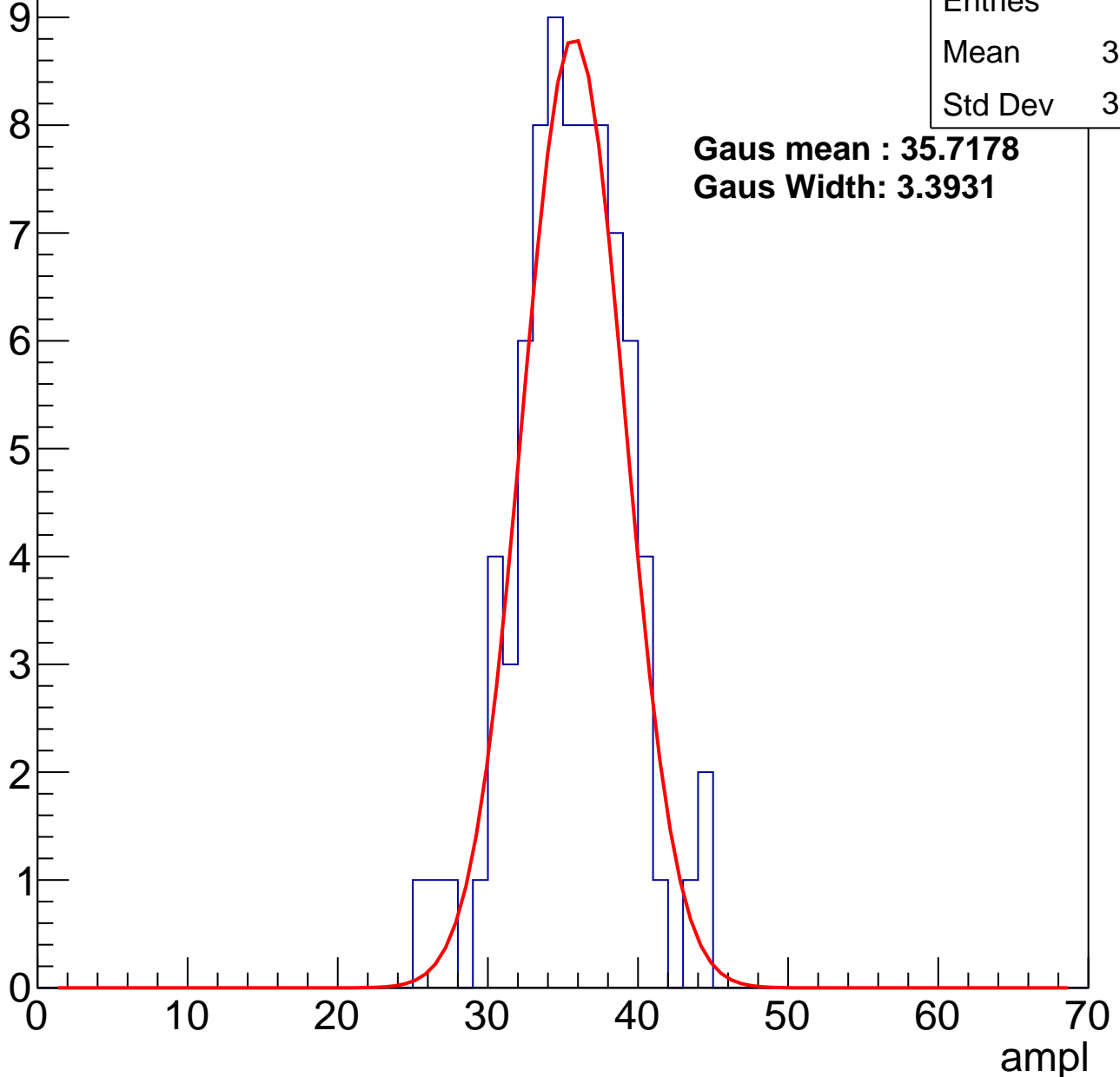
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.16
Std Dev	3.702

Gaus mean : 35.7178

Gaus Width: 3.3931



B1L103S, U12-ch73, adc2

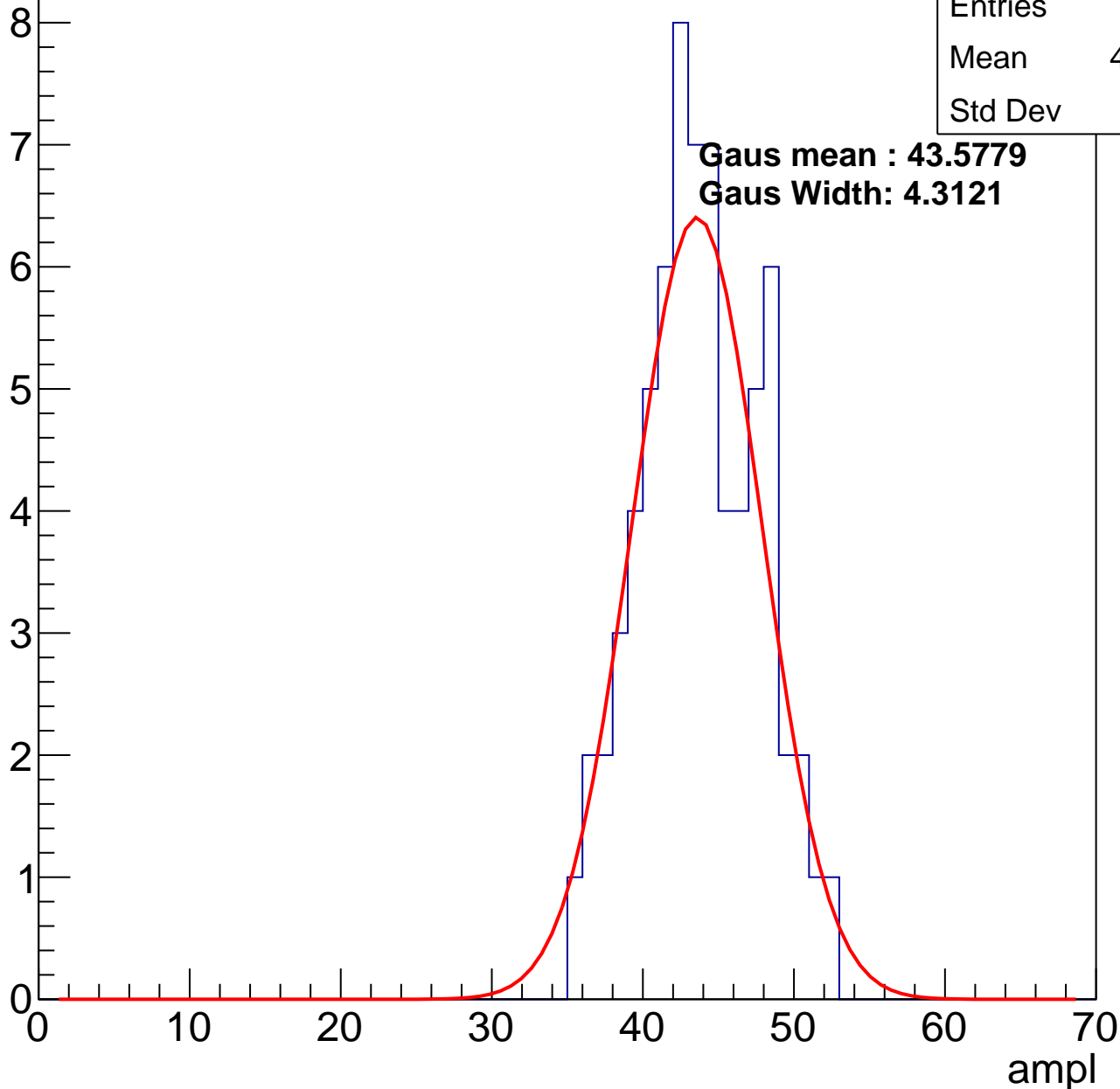
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.29
Std Dev	3.87

Gaus mean : 43.5779

Gaus Width: 4.3121

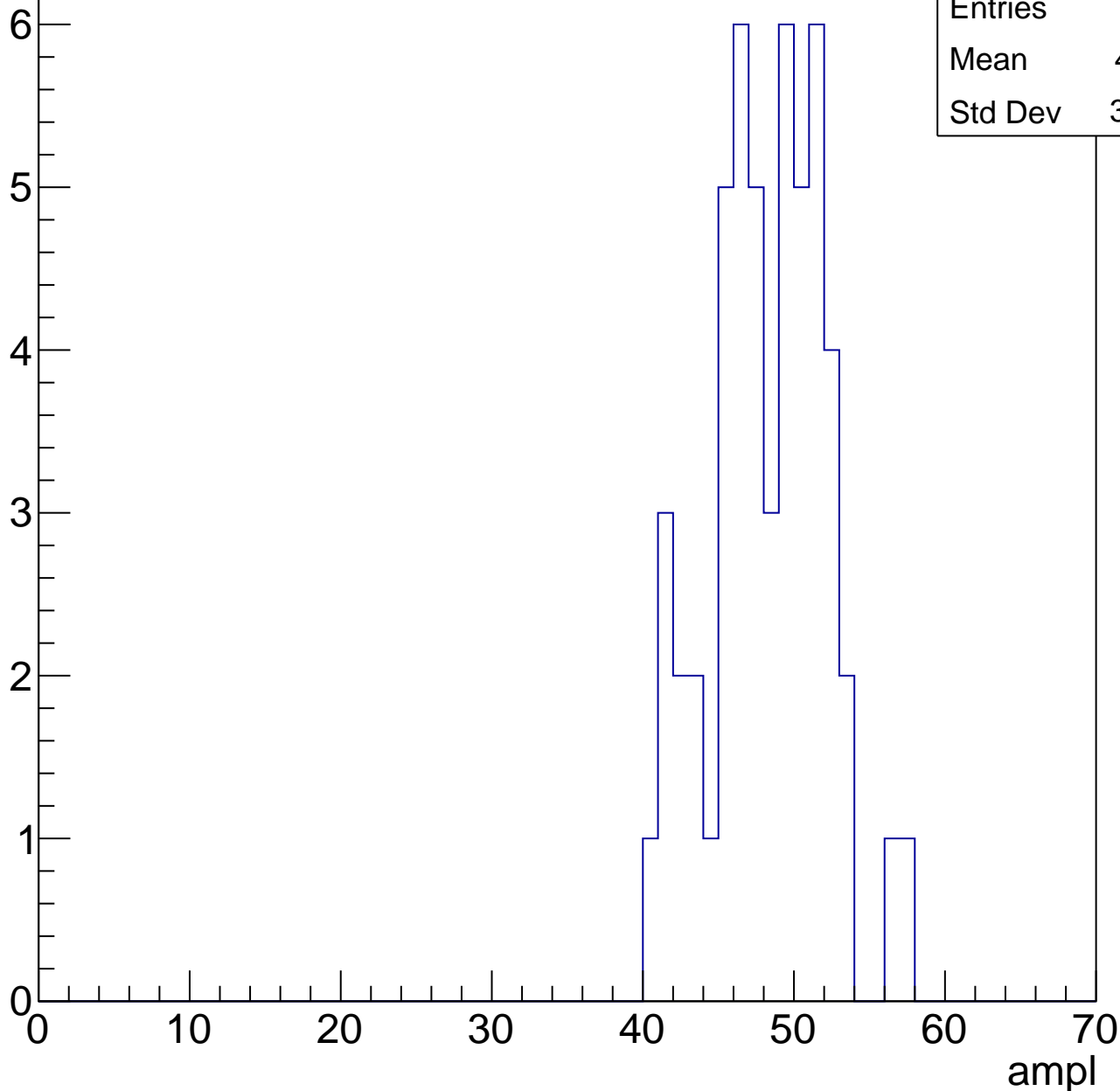


B1L103S, U12-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

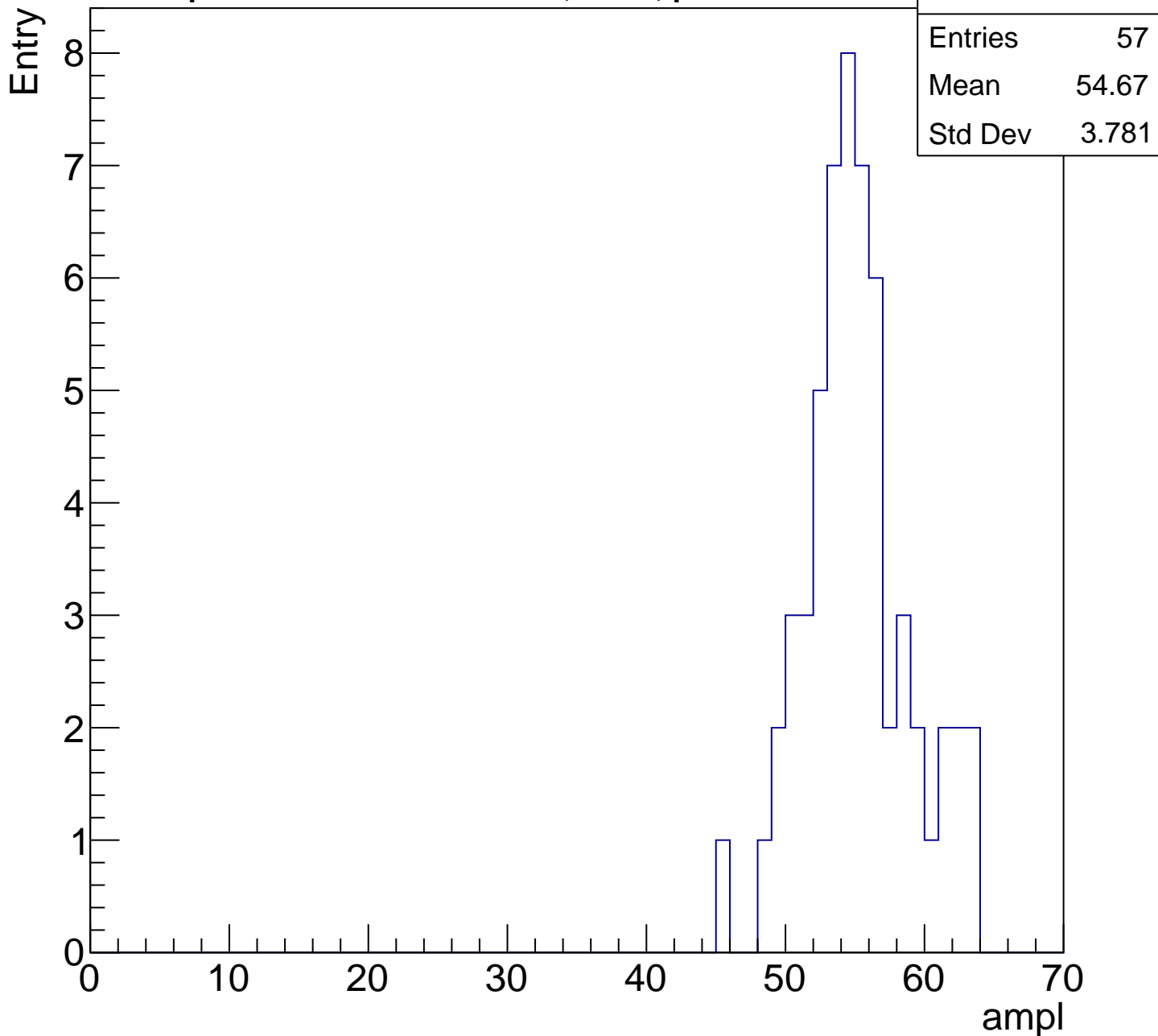
Entry

Entries	53
Mean	47.81
Std Dev	3.787



B1L103S, U12-ch73, adc4

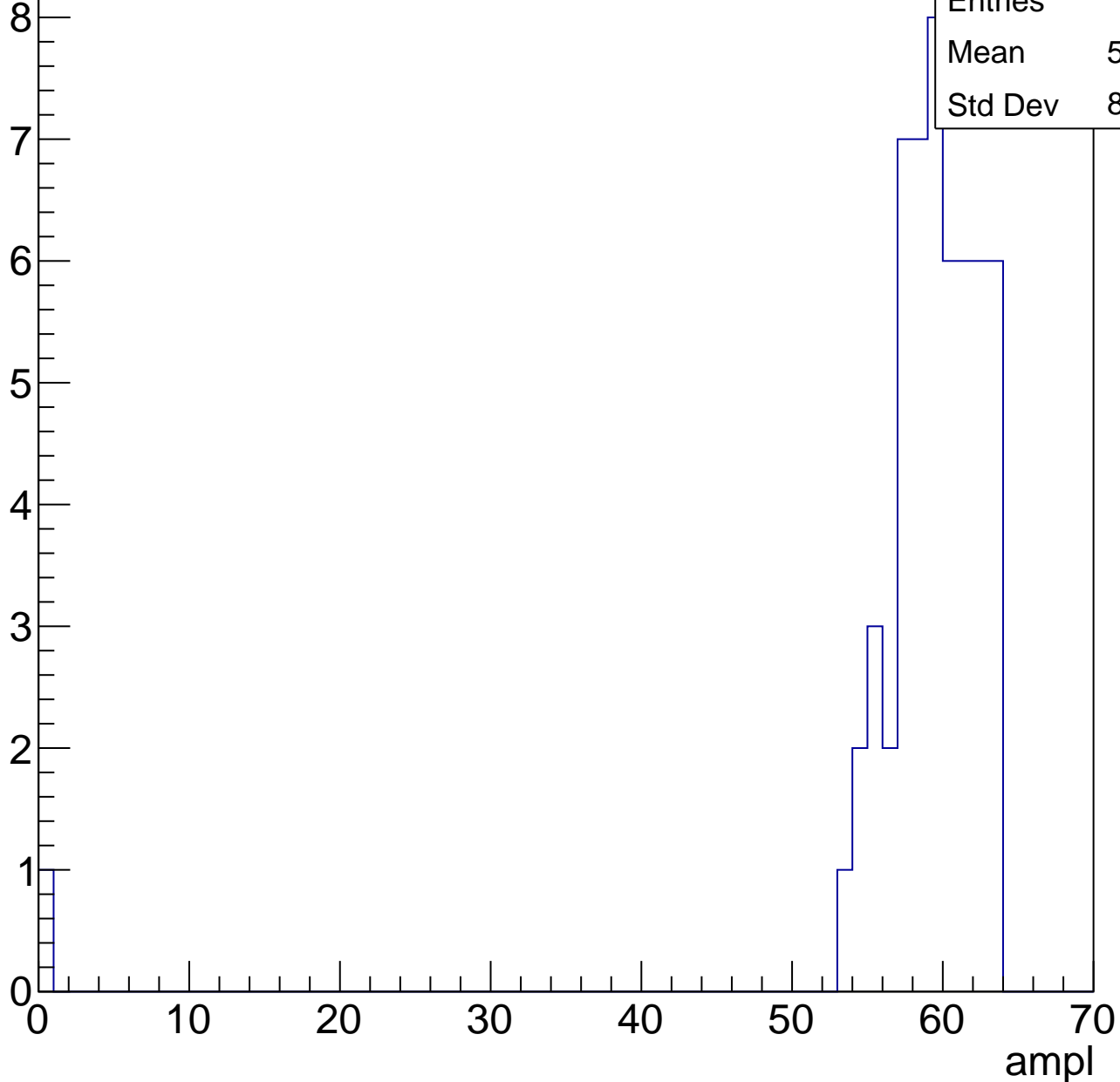
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U12-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.89
Std Dev	2.025

0 10 20 30 40 50 60 70

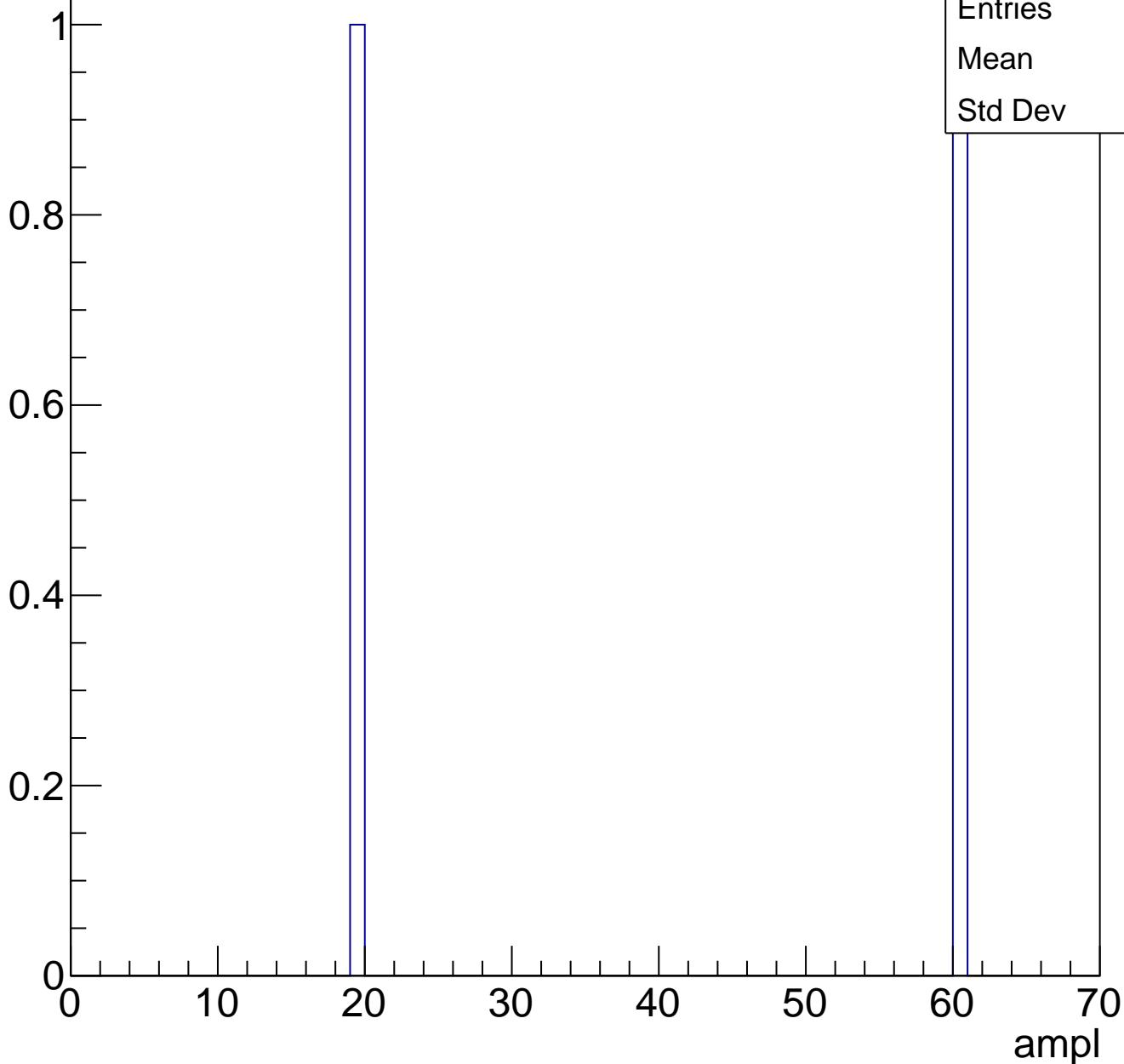
ampl

1

B1L103S, U12-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch74, adc0

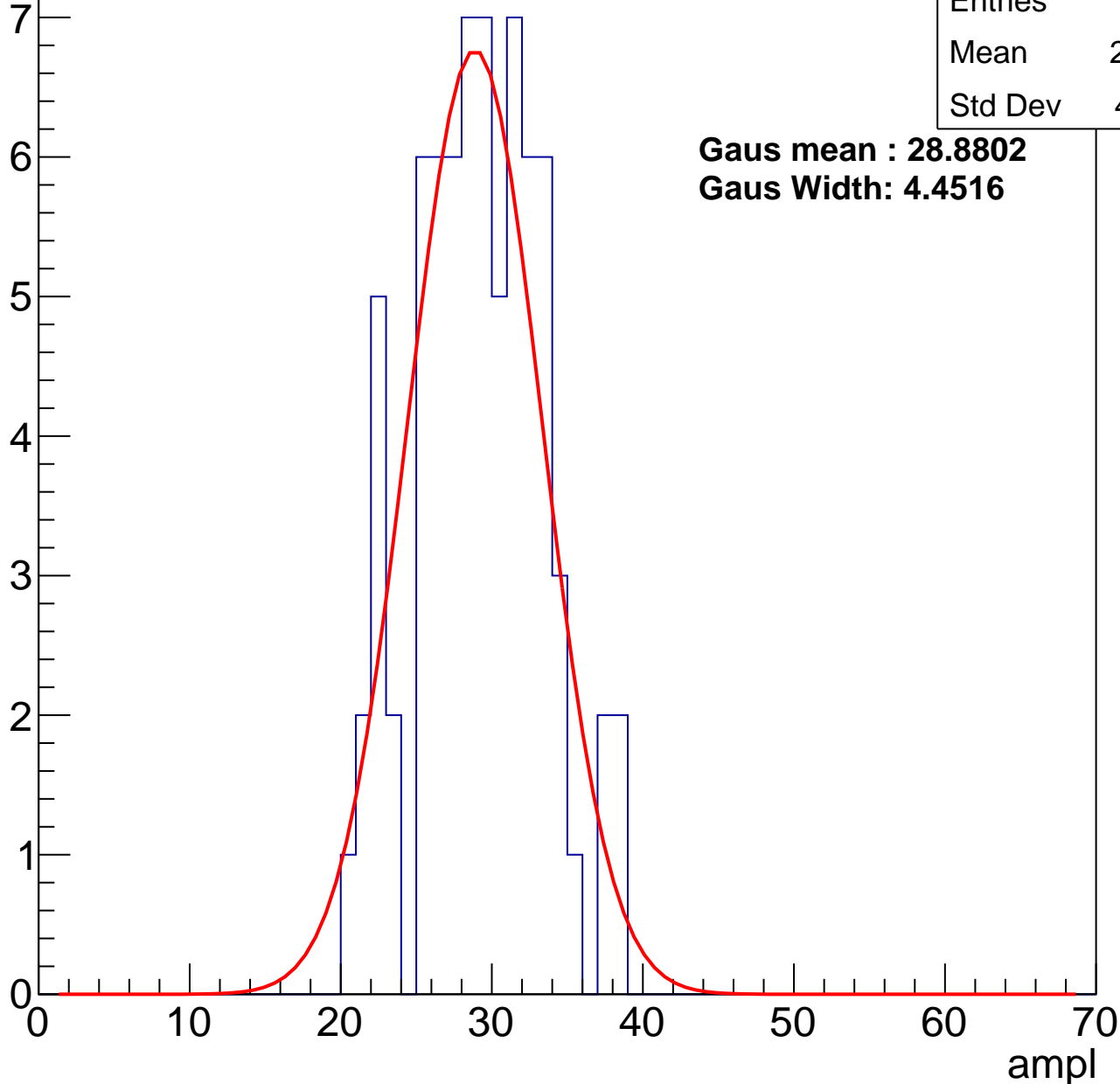
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	28.77
Std Dev	4.171

Gaus mean : 28.8802

Gaus Width: 4.4516



B1L103S, U12-ch74, adc1

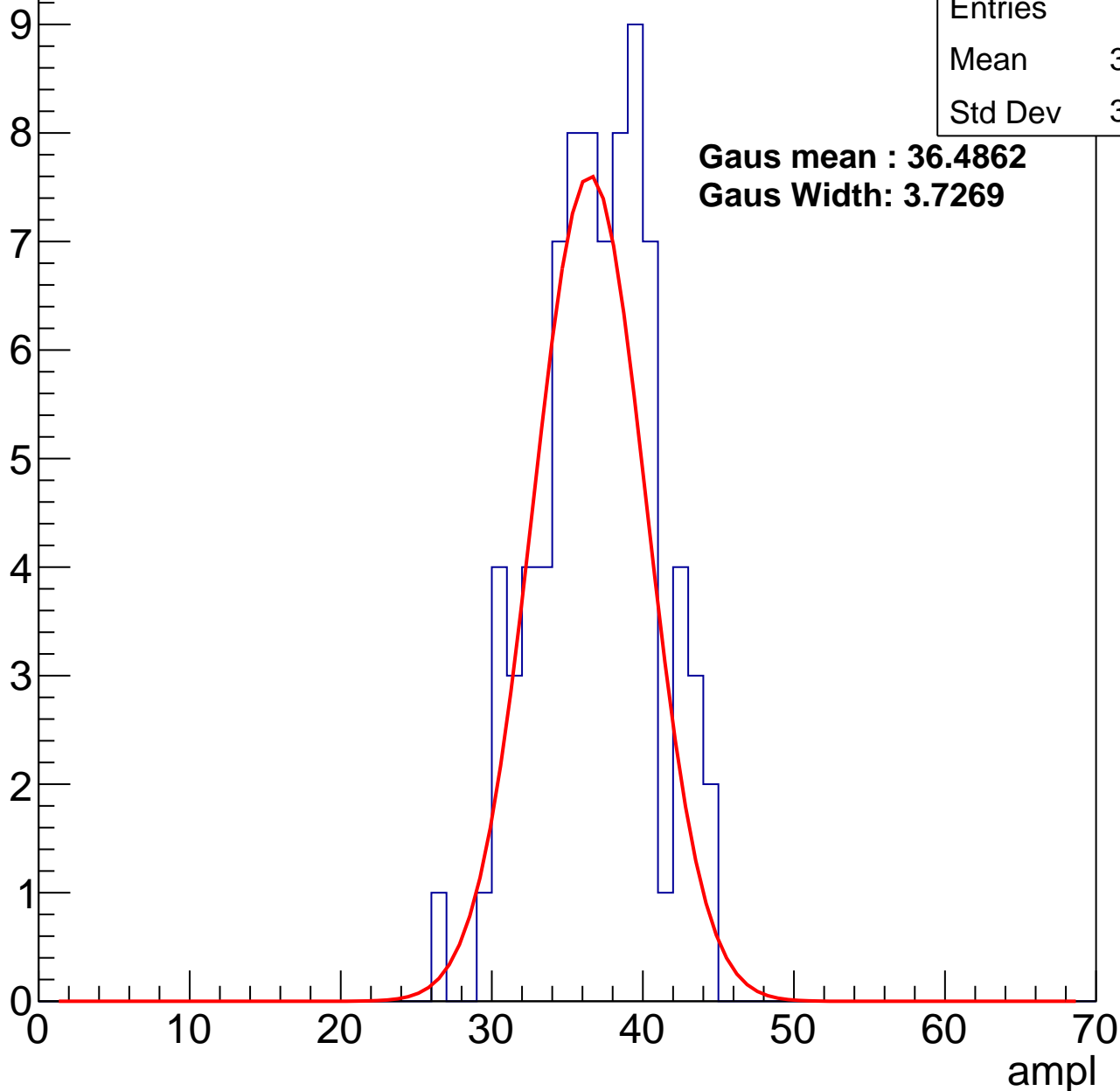
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	36.47
Std Dev	3.788

Gaus mean : 36.4862

Gaus Width: 3.7269



B1L103S, U12-ch74, adc2

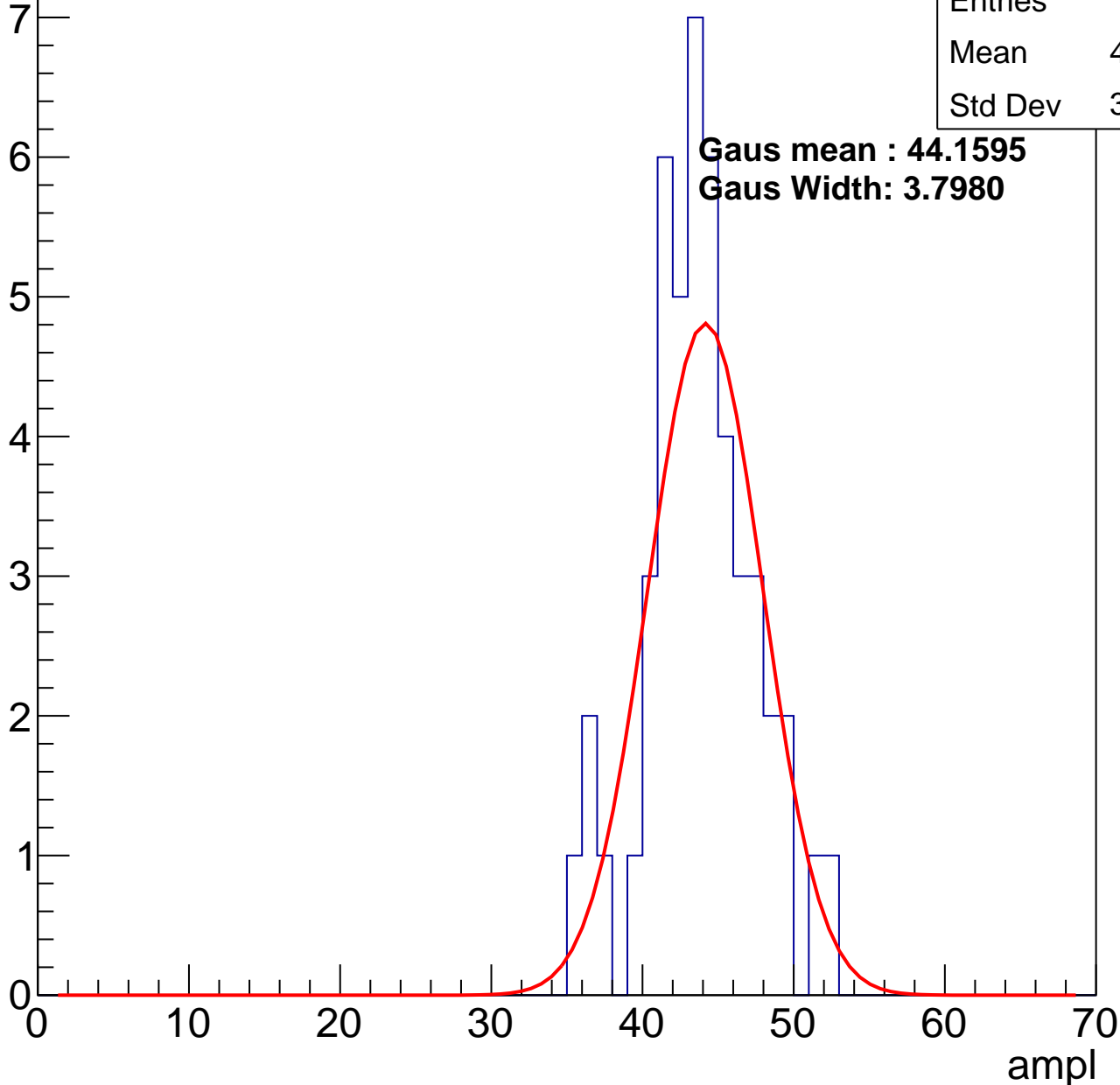
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	43.33
Std Dev	3.619

Gaus mean : 44.1595

Gaus Width: 3.7980

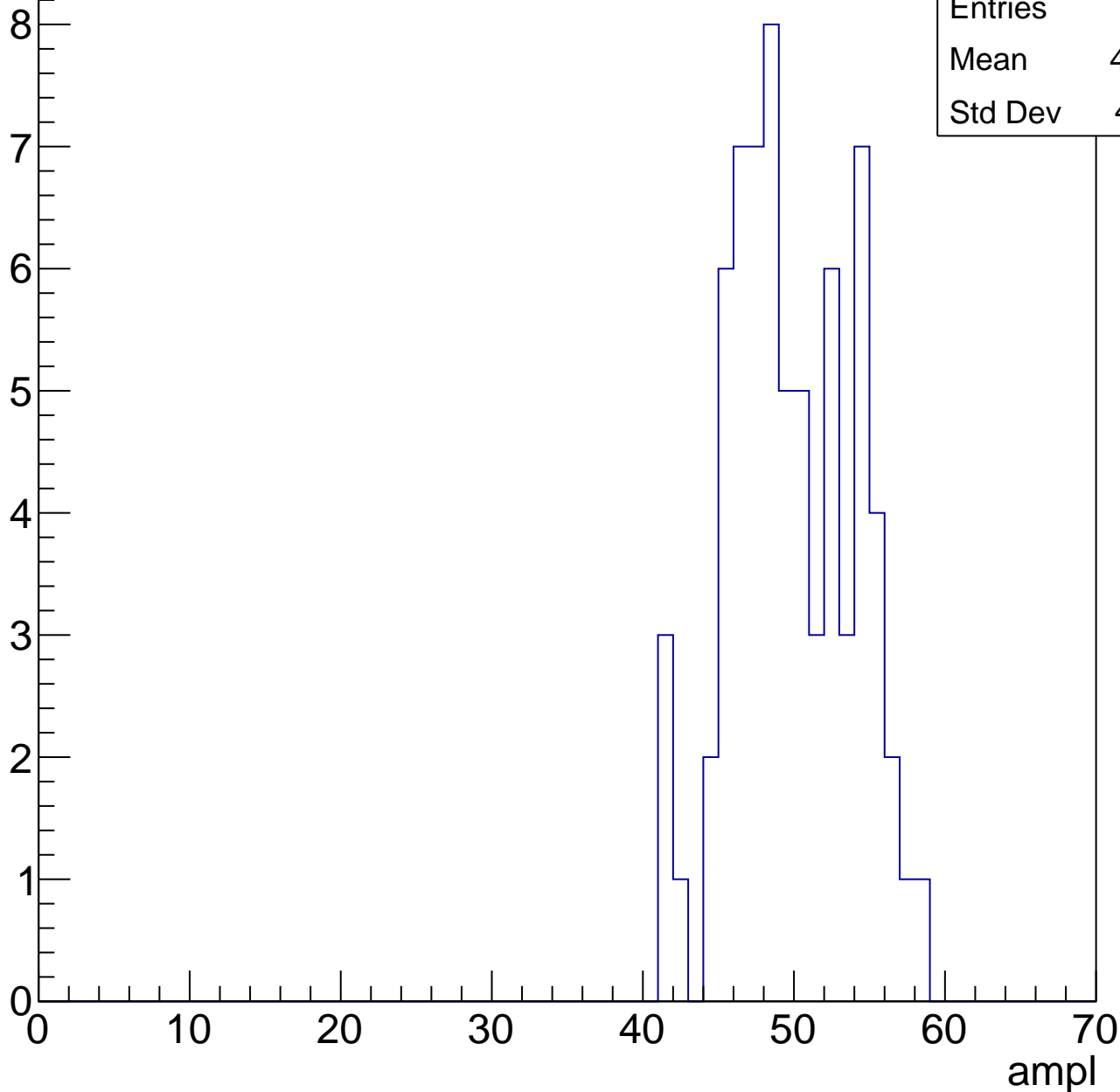


B1L103S, U12-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	49.32
Std Dev	4.041

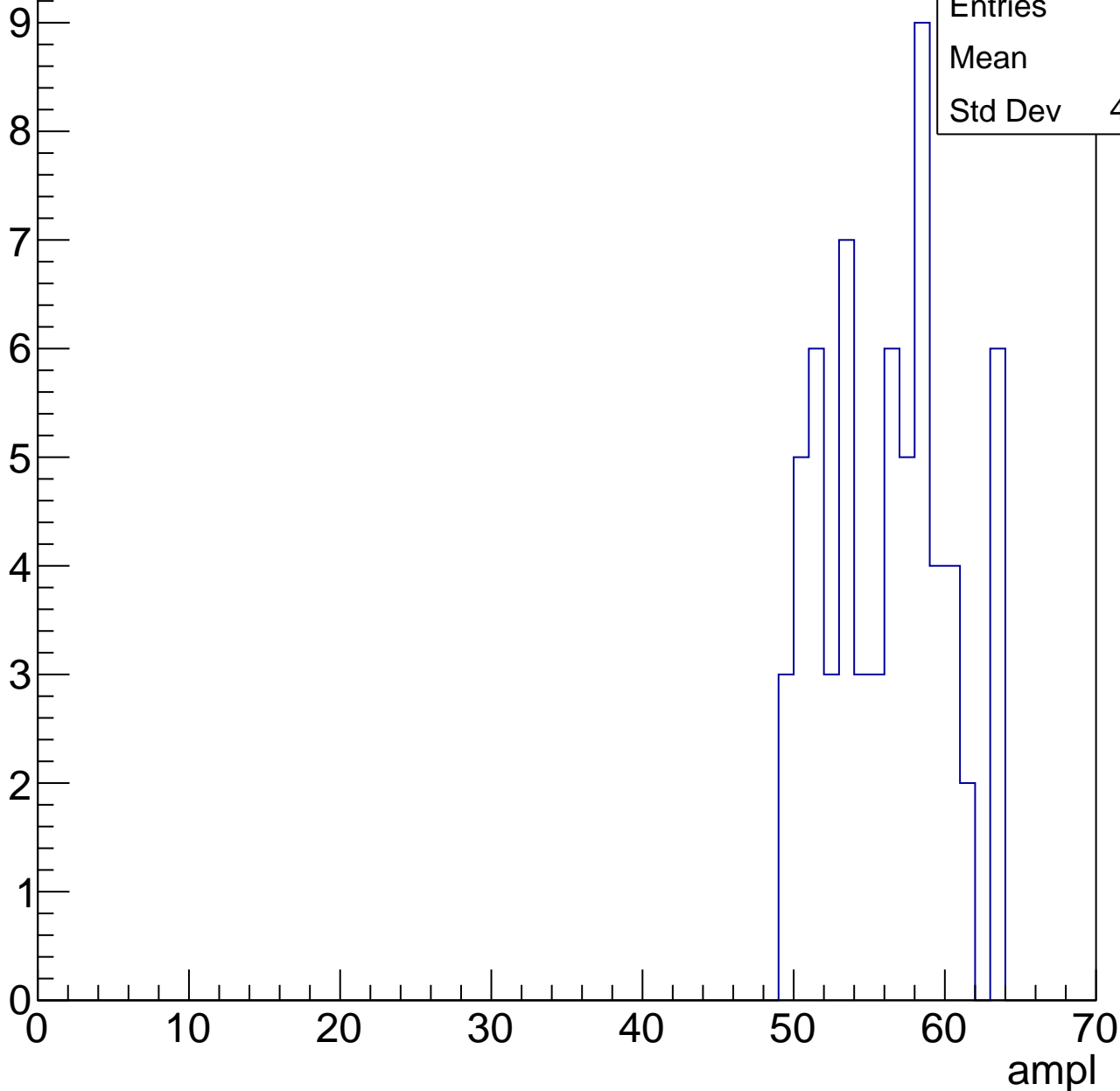


B1L103S, U12-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.7
Std Dev	4.049

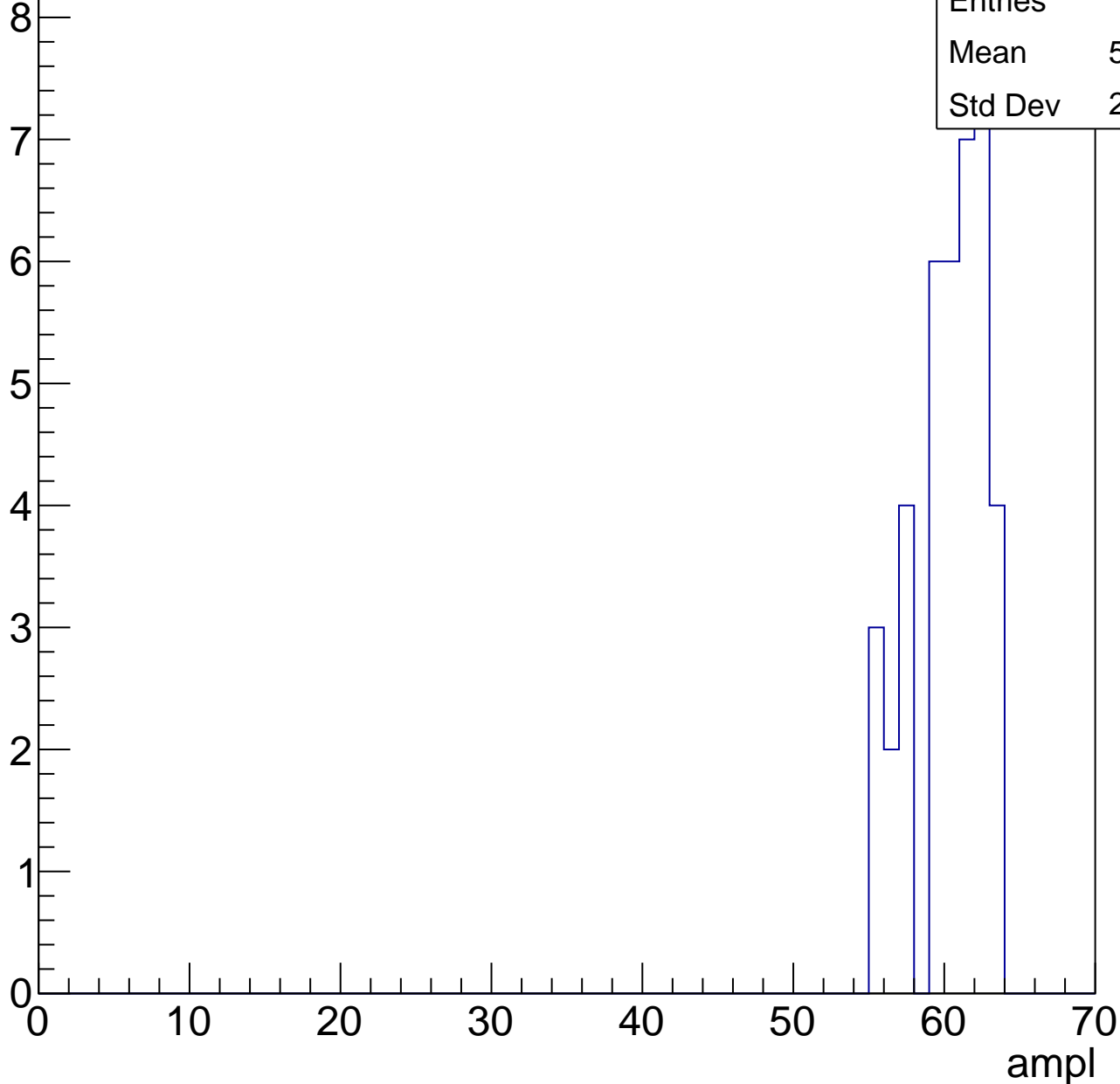


B1L103S, U12-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

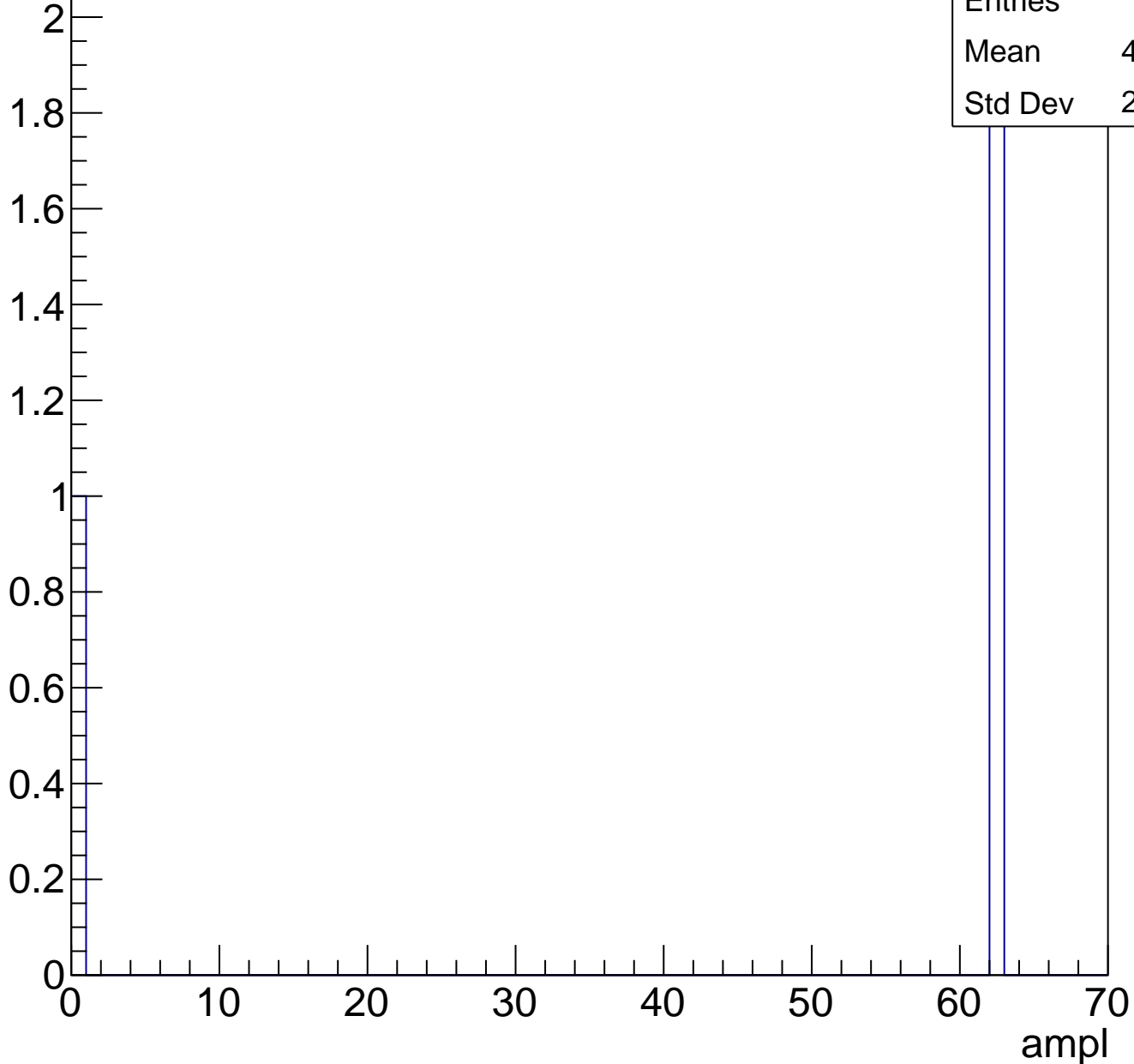
Entries	40
Mean	59.85
Std Dev	2.362



B1L103S, U12-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

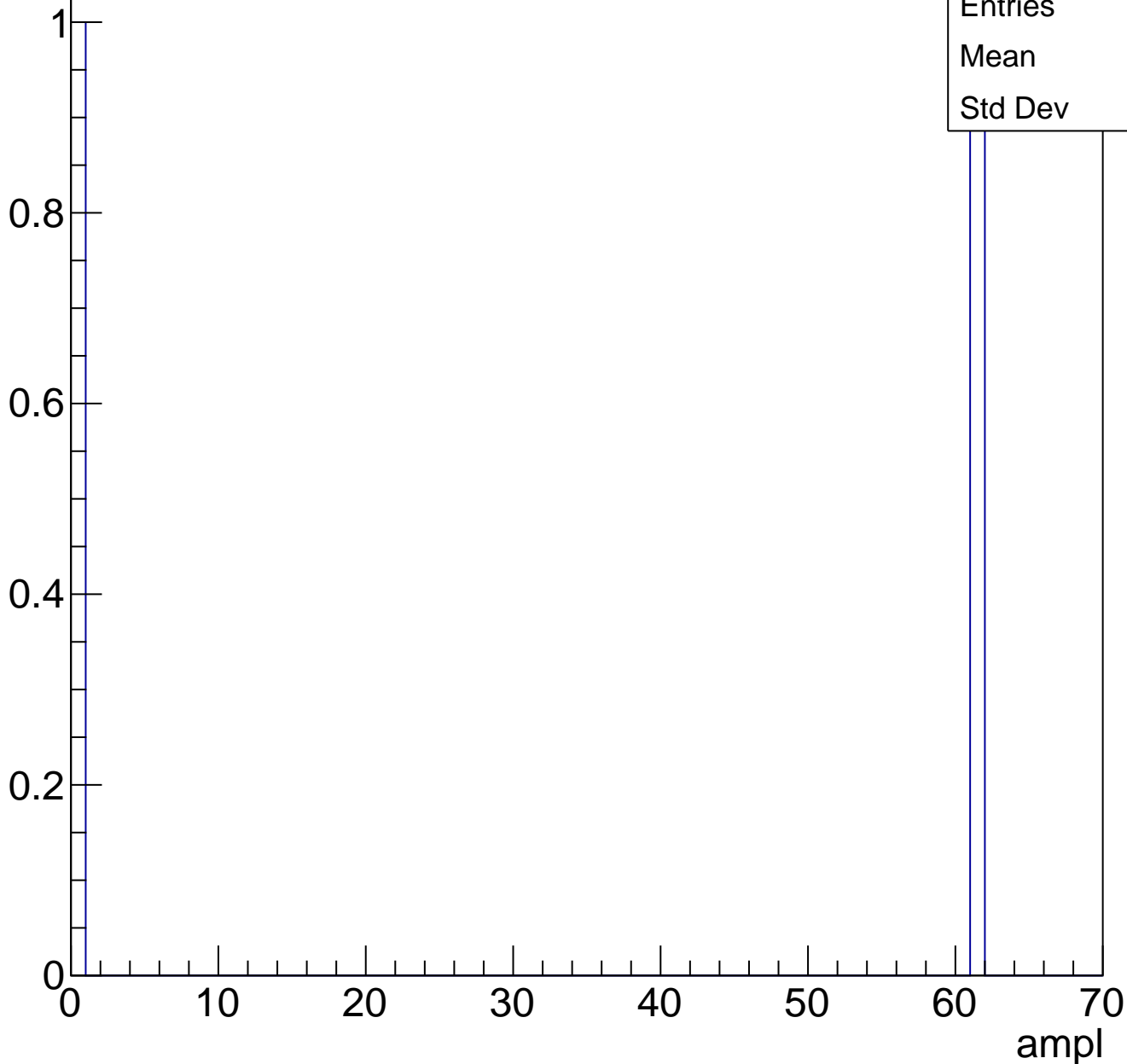
Entry



B1L103S, U12-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch75, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	31.43
Std Dev	4.098

Gaus mean : 31.7140

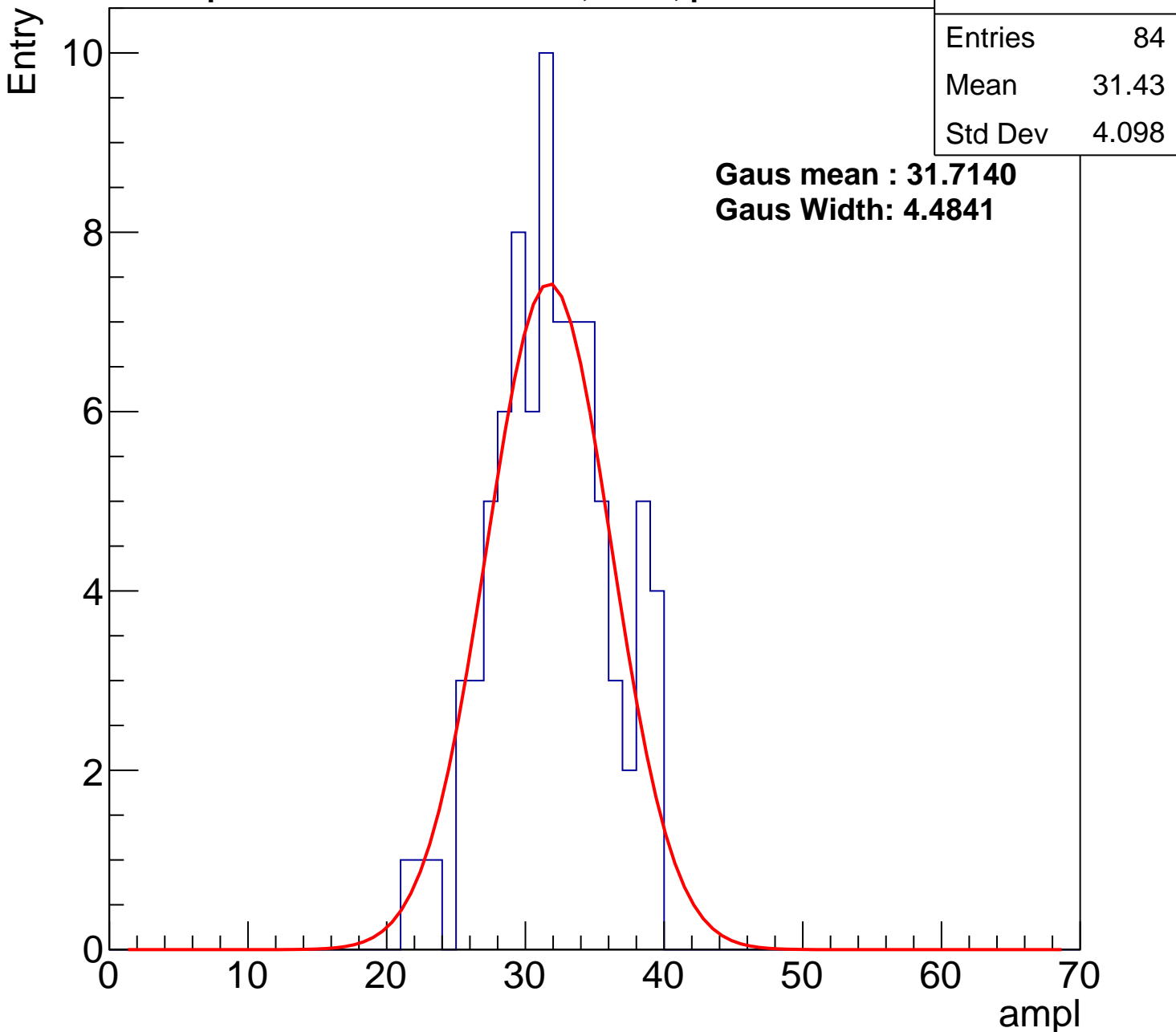
Gaus Width: 4.4841

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch75, adc1

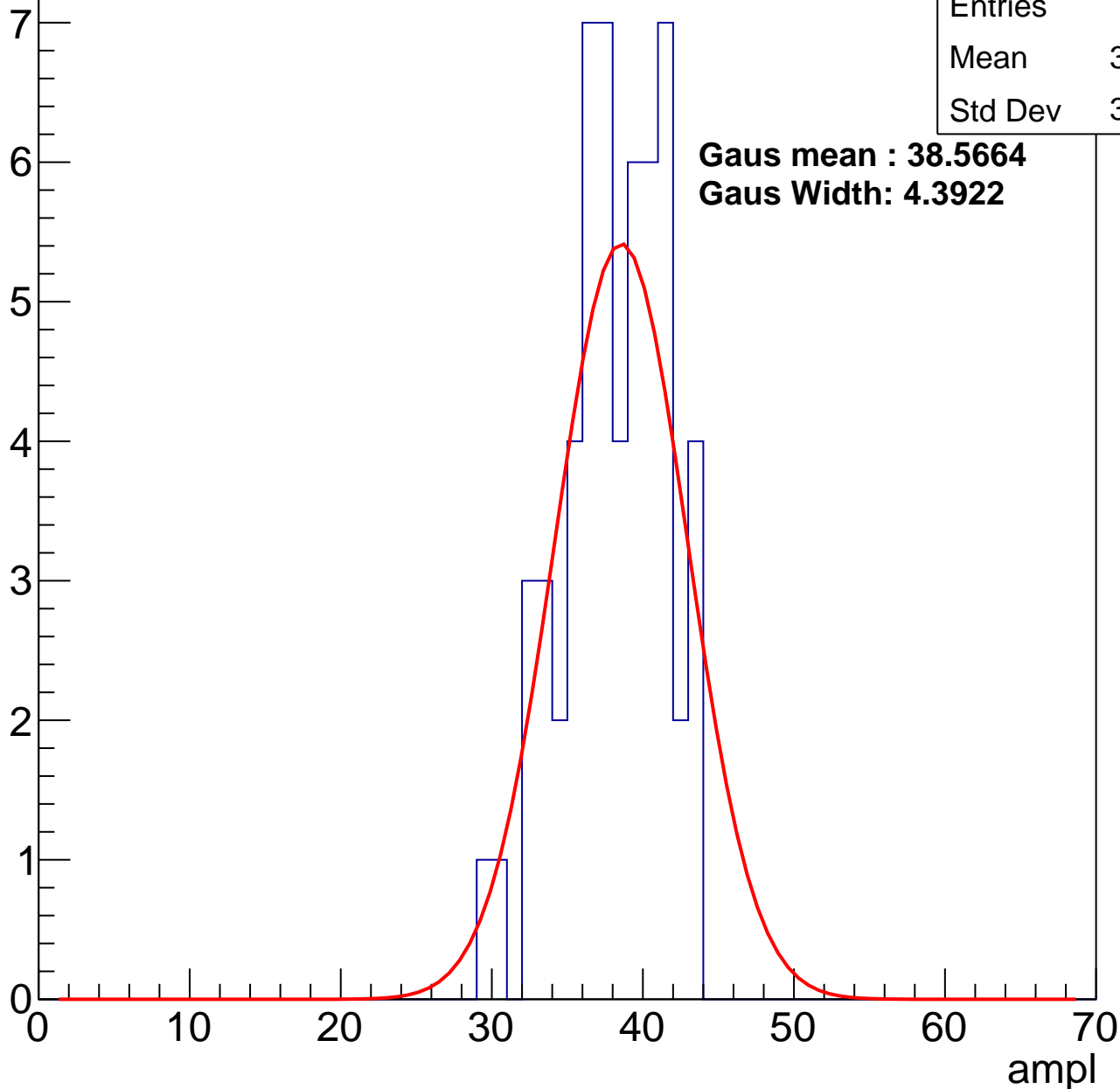
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	37.58
Std Dev	3.366

Gaus mean : 38.5664

Gaus Width: 4.3922



B1L103S, U12-ch75, adc2

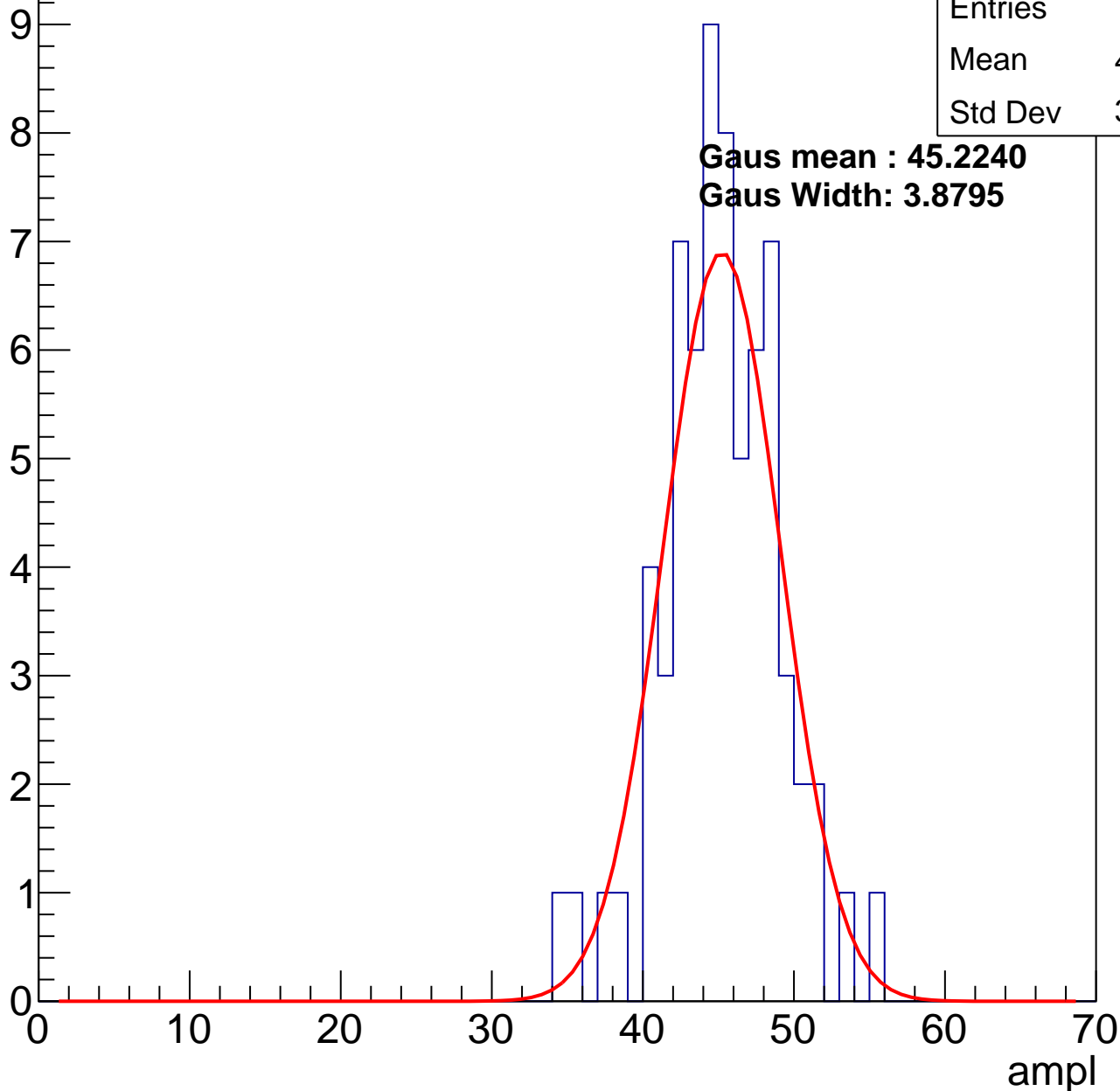
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	44.71
Std Dev	3.831

Gaus mean : 45.2240

Gaus Width: 3.8795

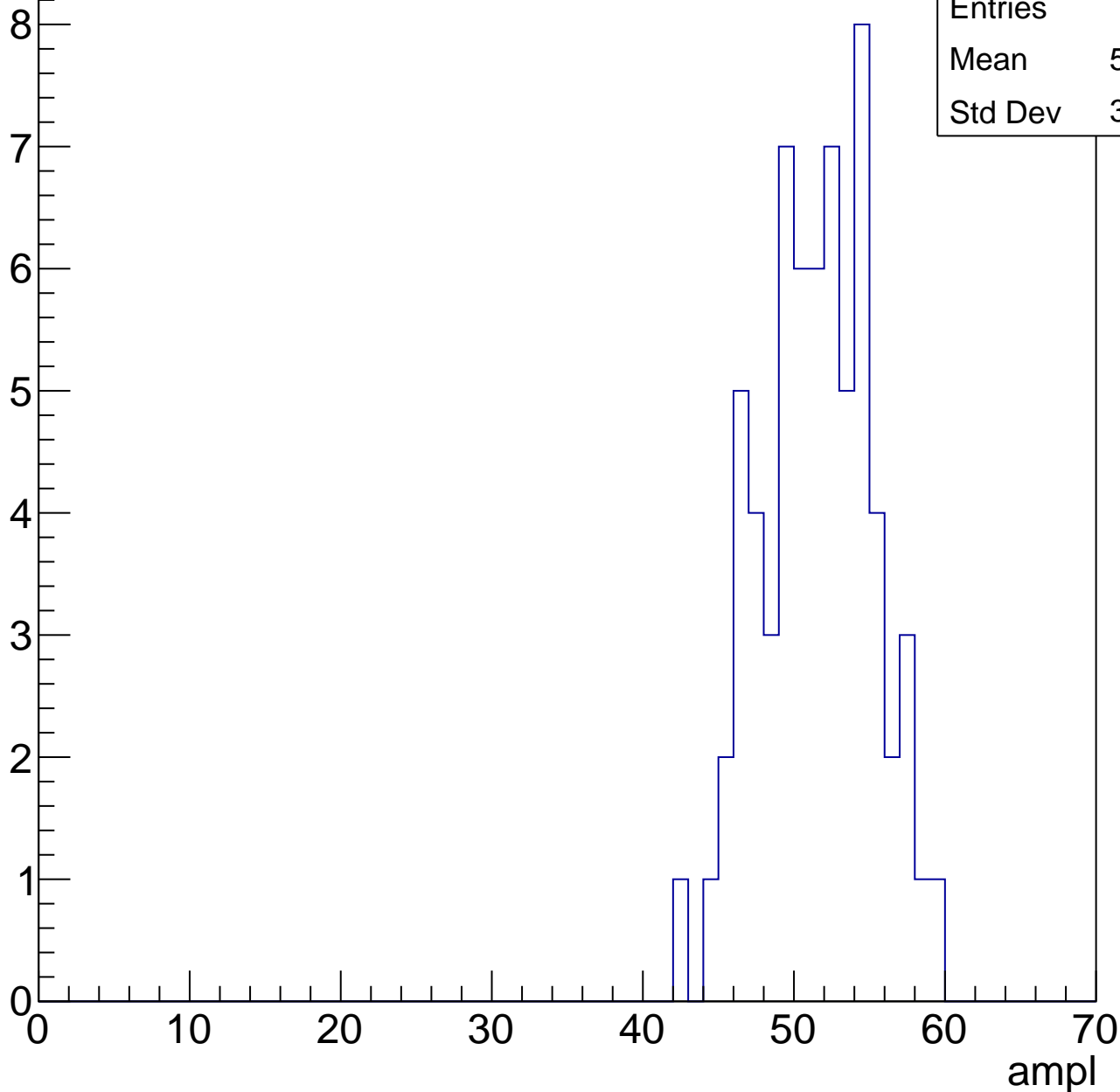


B1L103S, U12-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	51.03
Std Dev	3.664

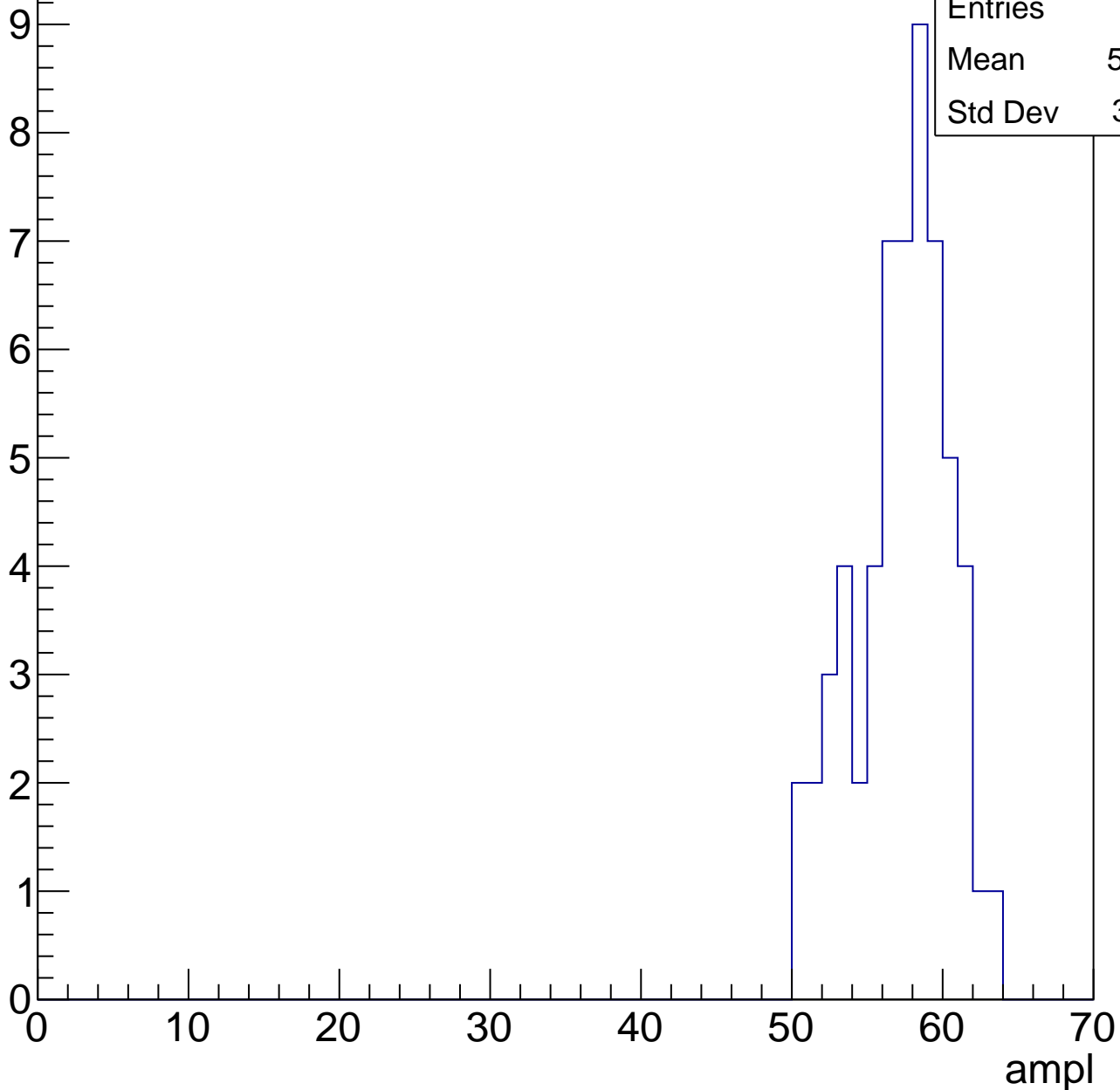


B1L103S, U12-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.78
Std Dev	3.091

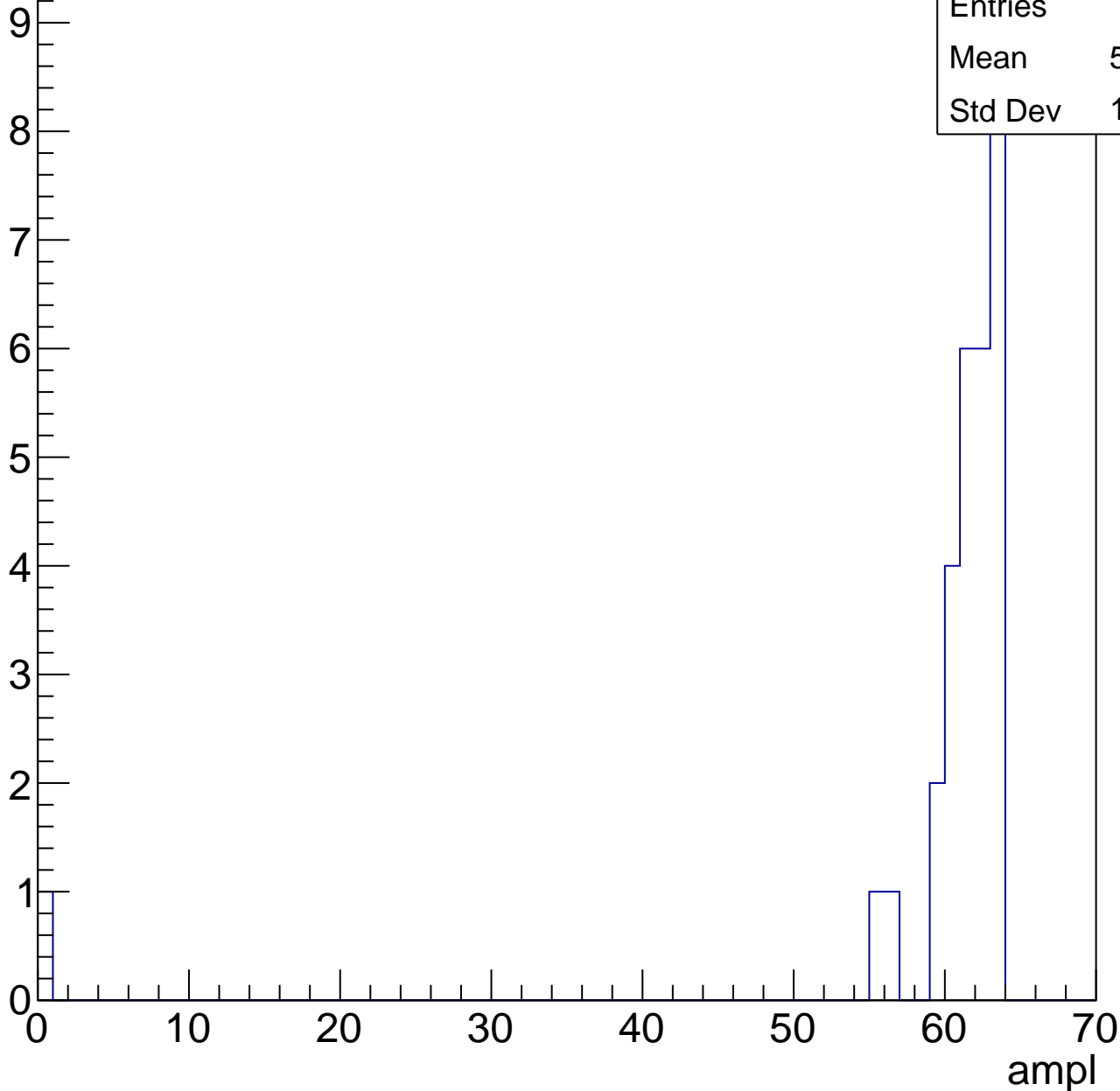


B1L103S, U12-ch75, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

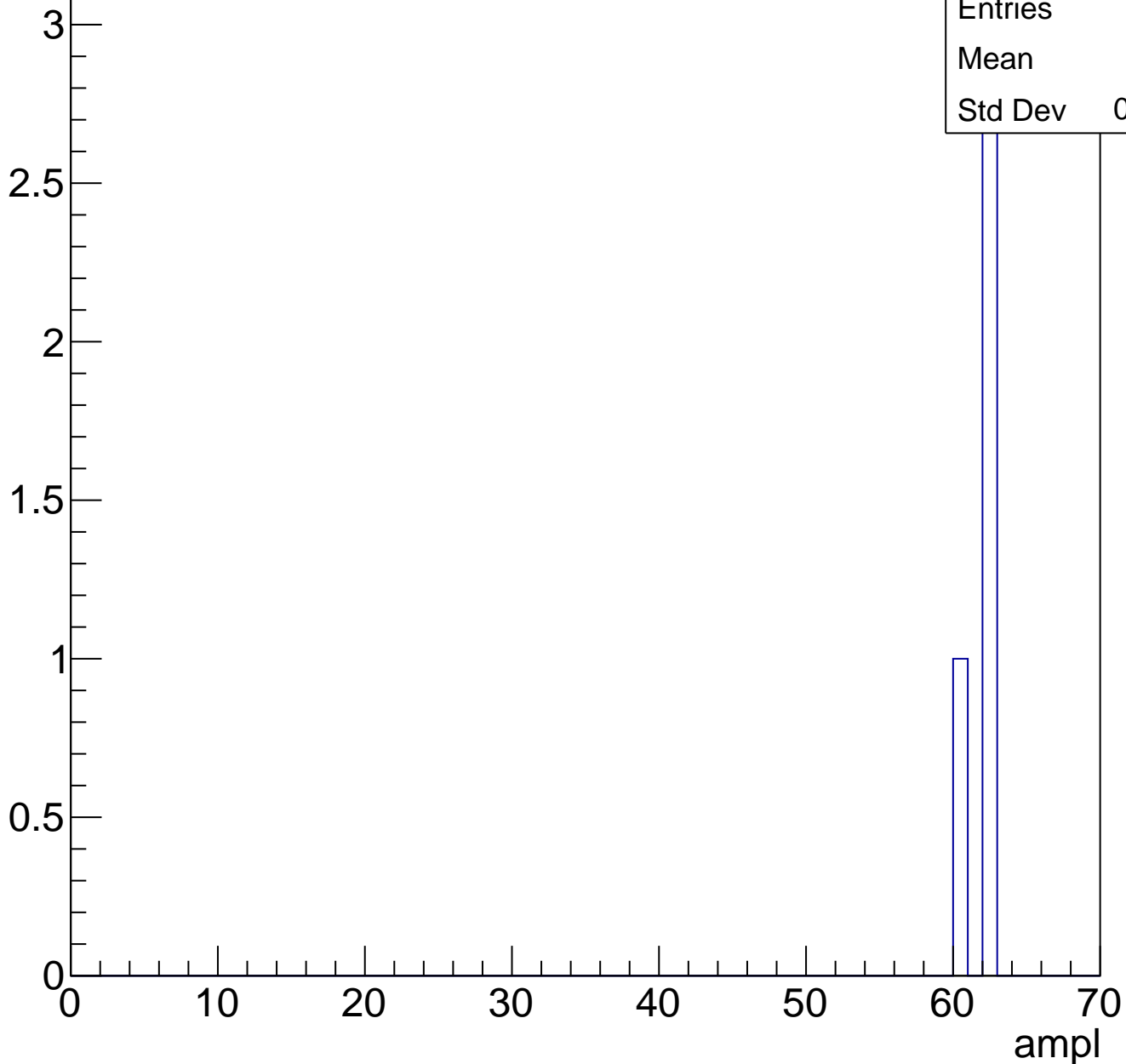
Entries	30
Mean	59.13
Std Dev	11.15



B1L103S, U12-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U12-ch76, adc0

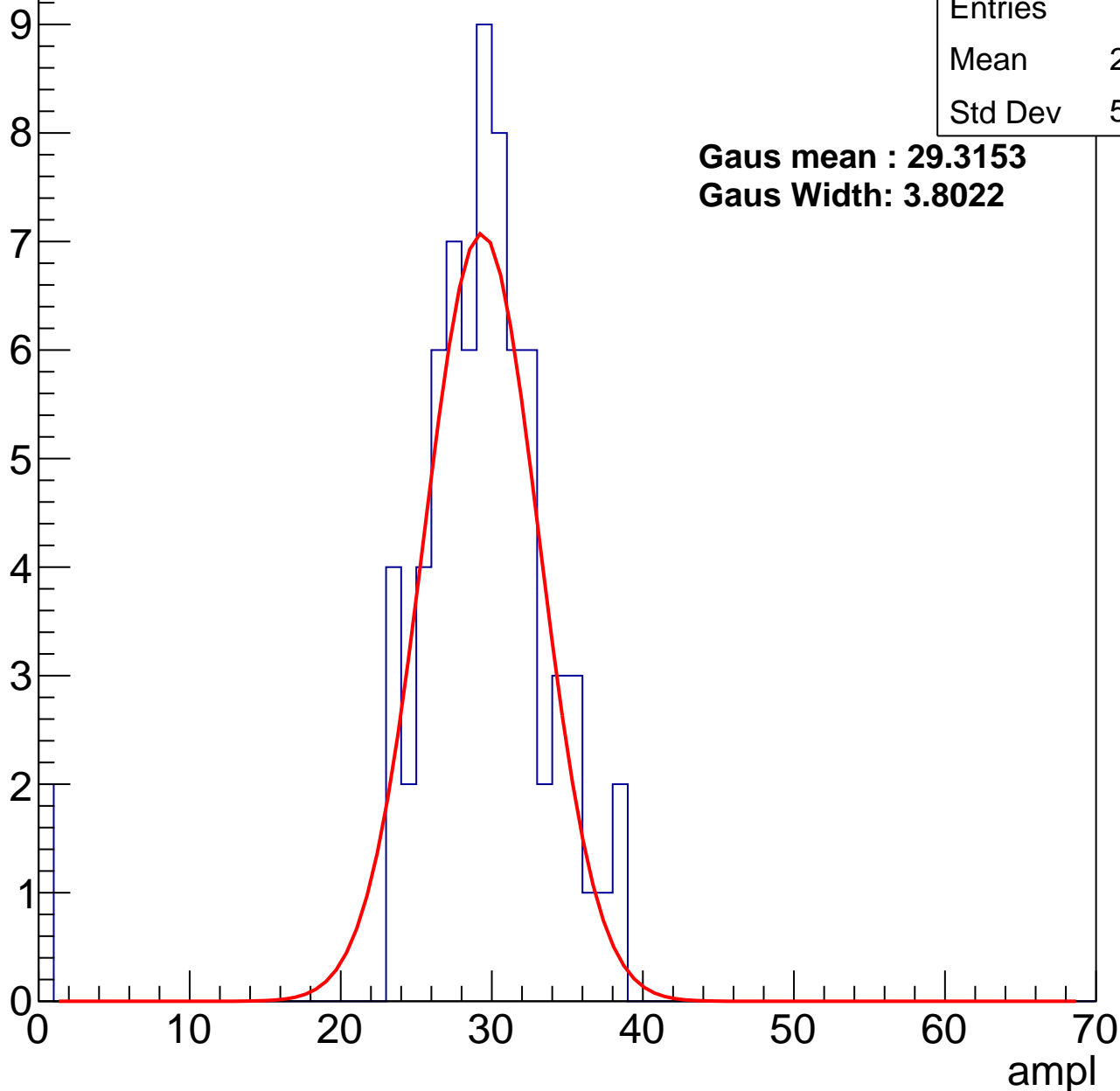
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.53
Std Dev	5.995

Gaus mean : 29.3153

Gaus Width: 3.8022



B1L103S, U12-ch76, adc1

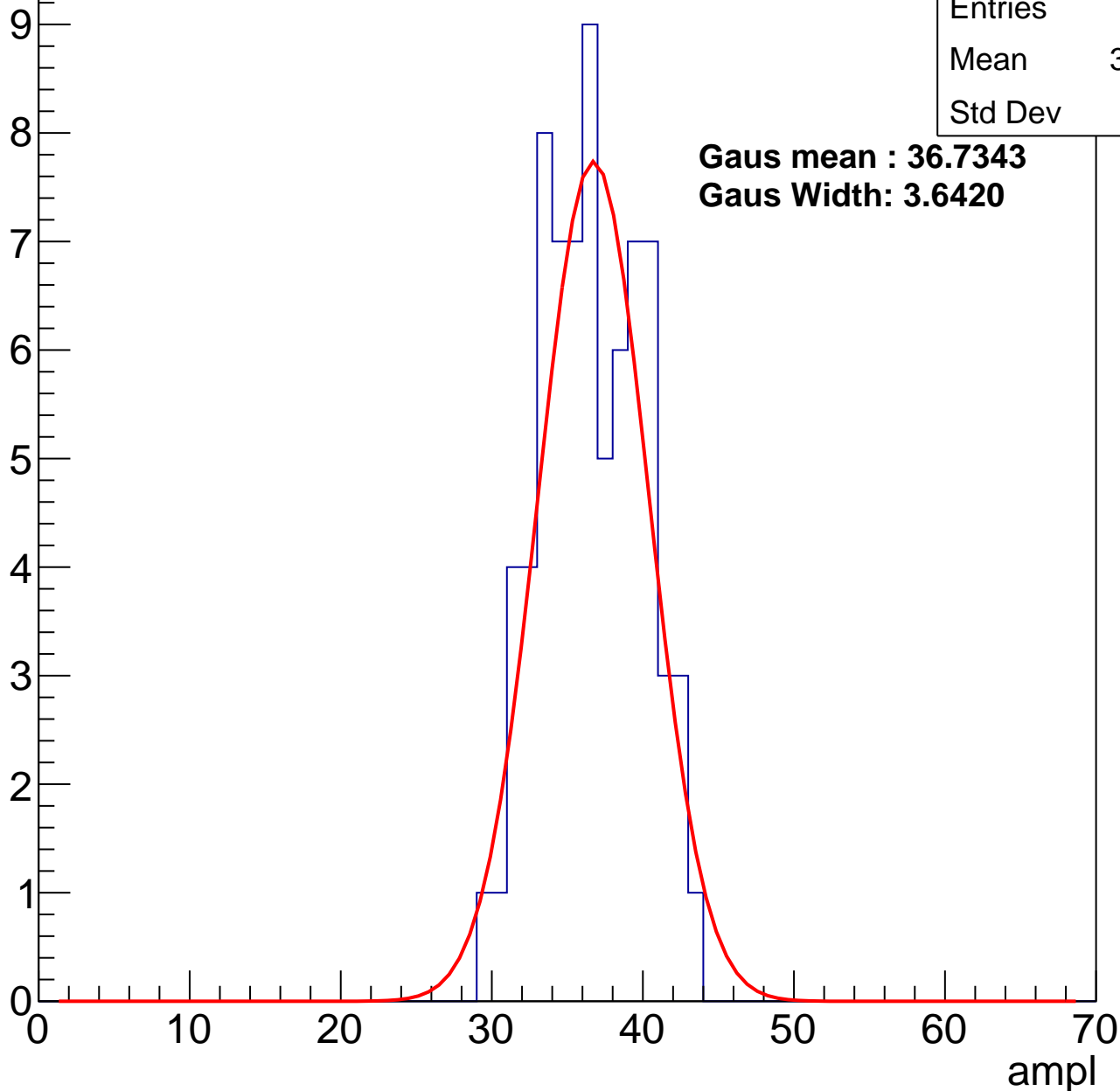
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	36.16
Std Dev	3.29

Gaus mean : 36.7343

Gaus Width: 3.6420

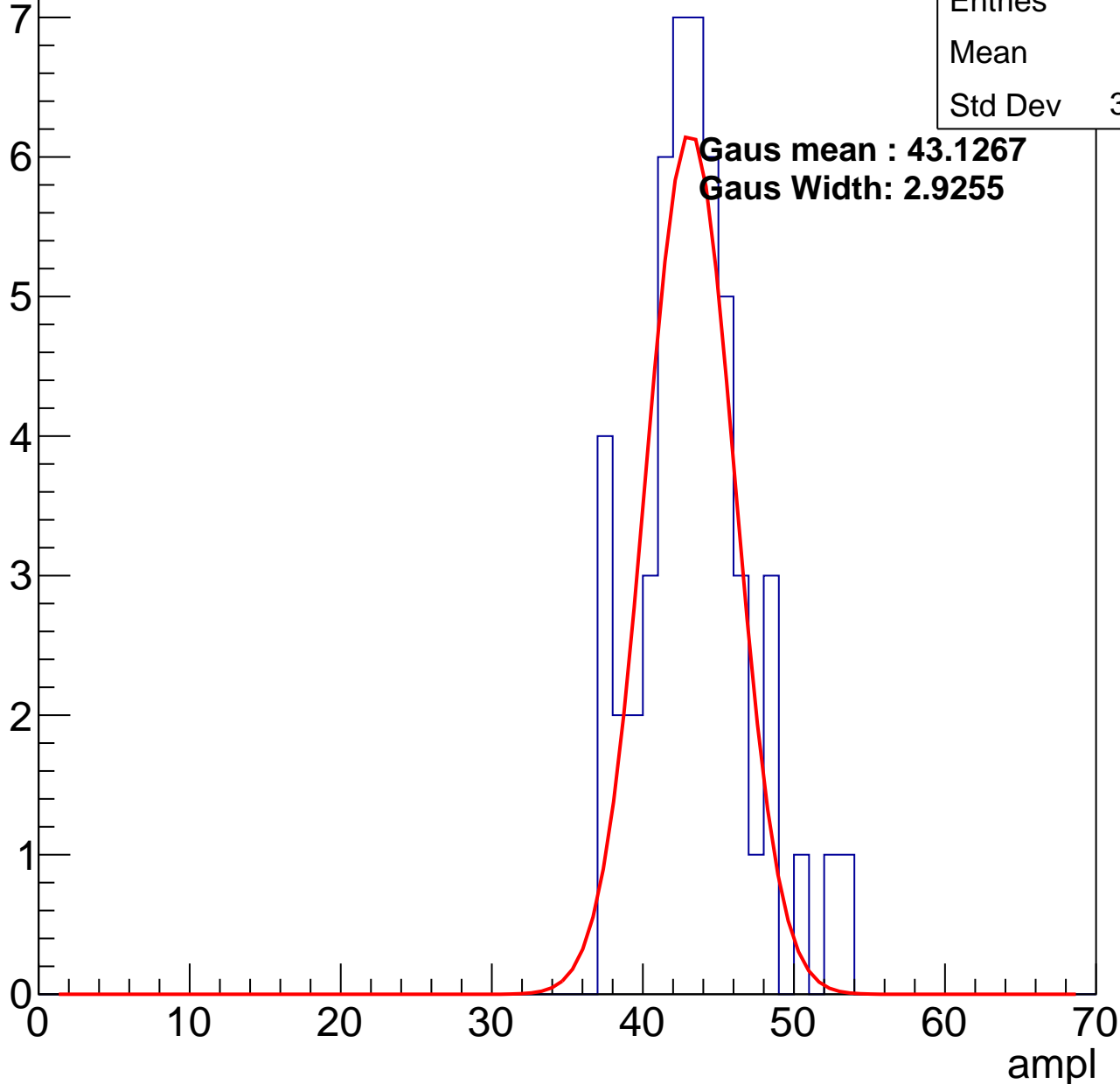


B1L103S, U12-ch76, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	43
Std Dev	3.557

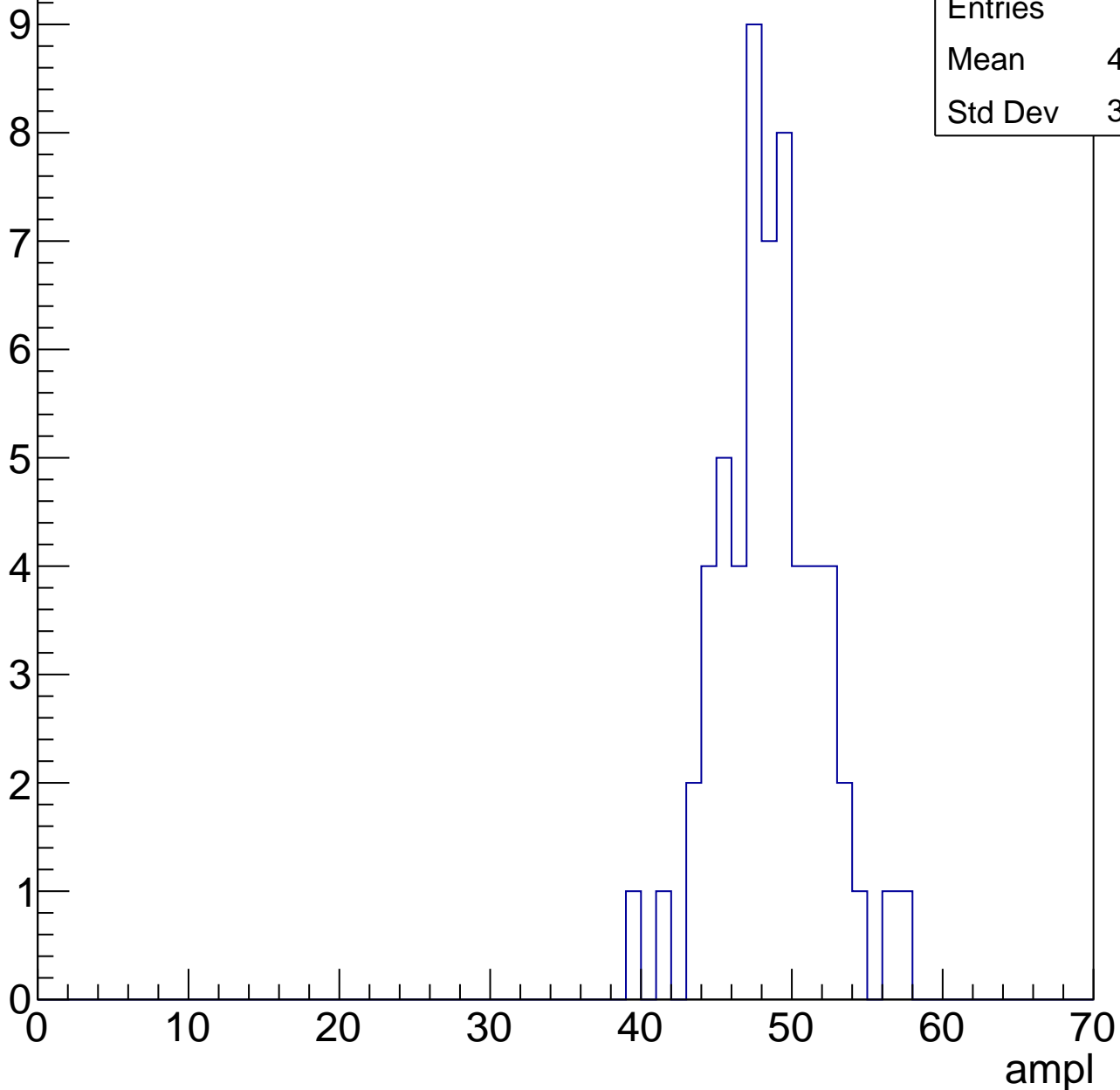


B1L103S, U12-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.05
Std Dev	3.396



B1L103S, U12-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	54.24
Std Dev	3.353

Entry

10

8

6

4

2

0

0

10

20

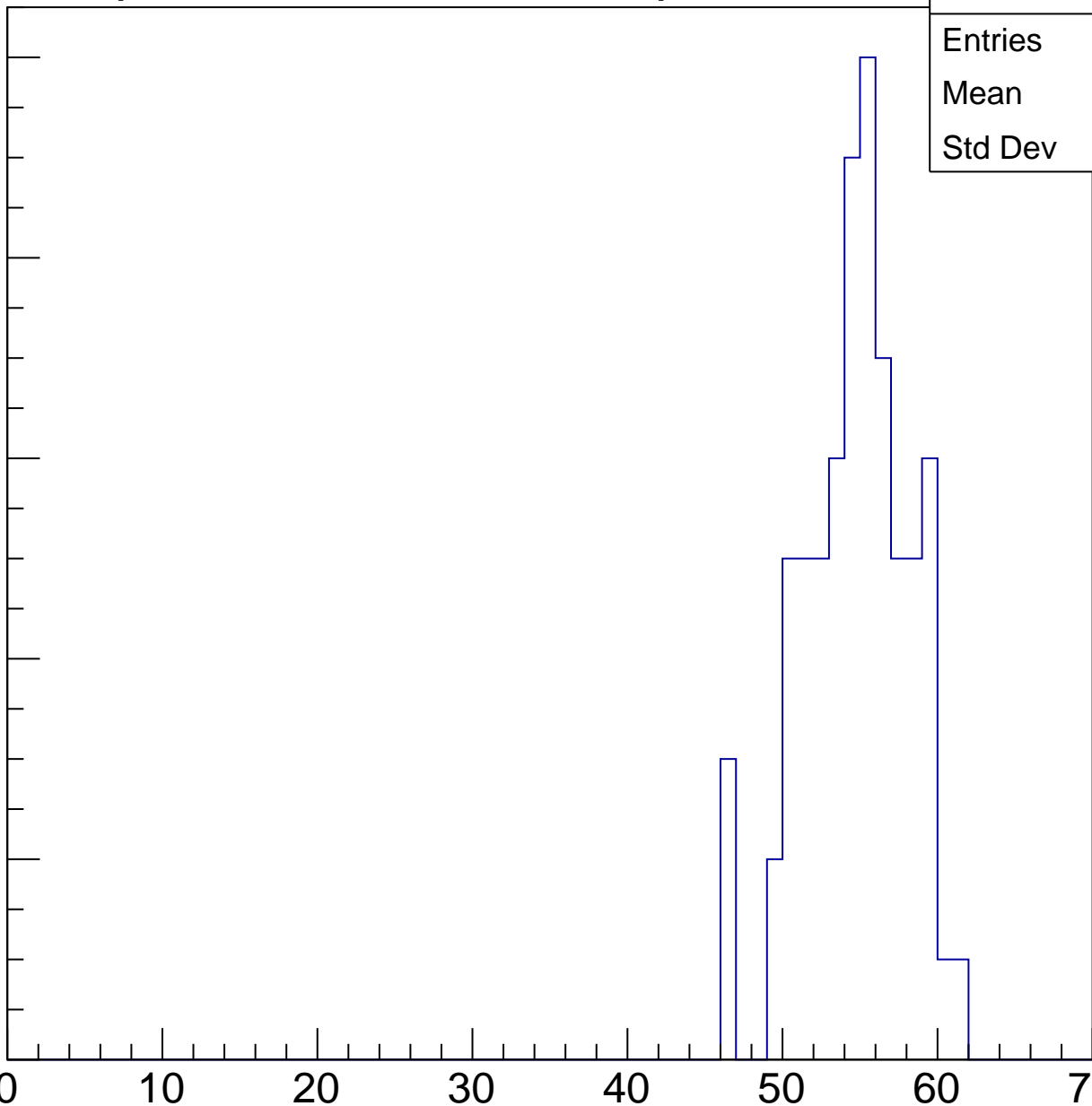
30

40

50

60

ampl

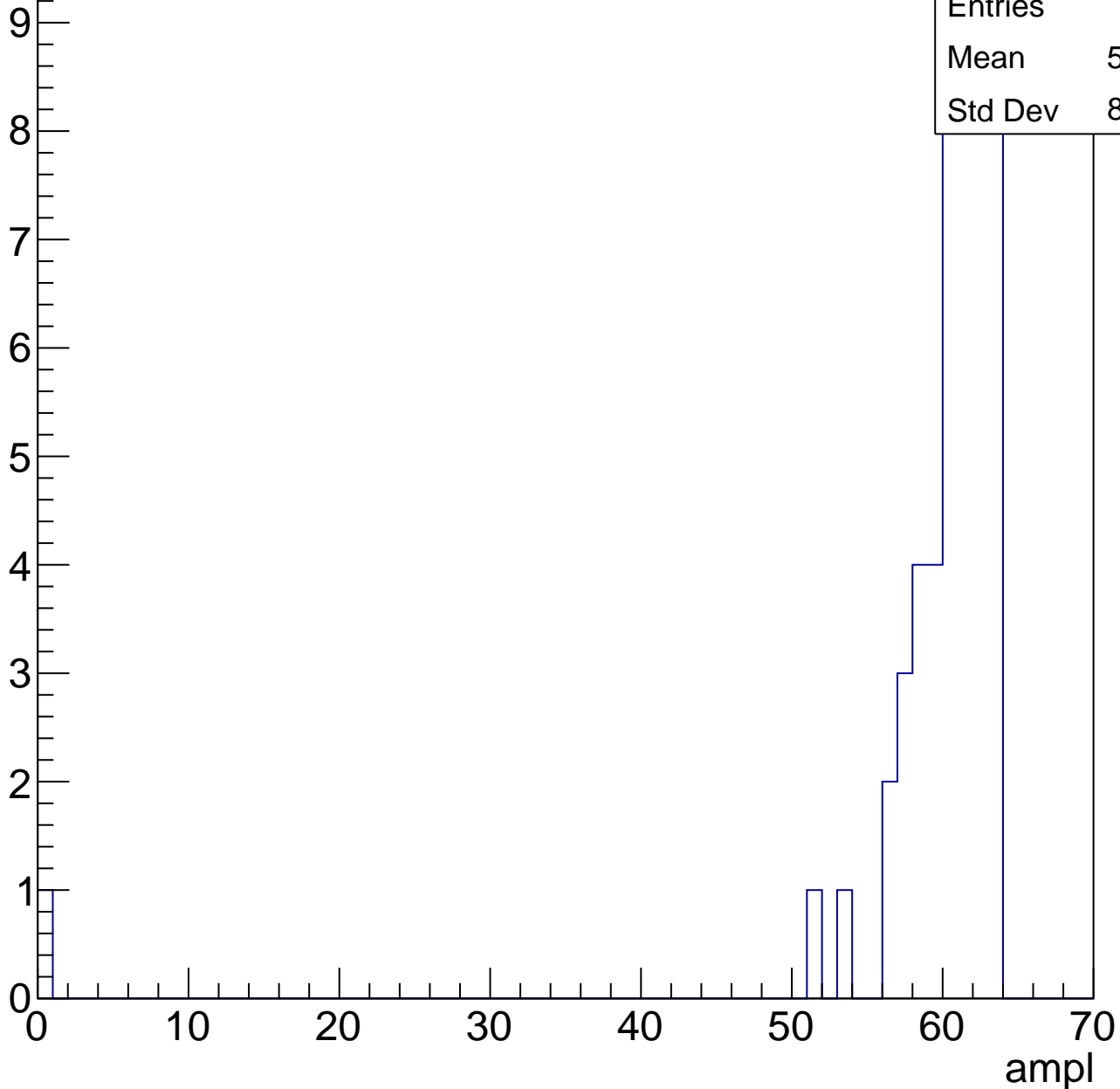


B1L103S, U12-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

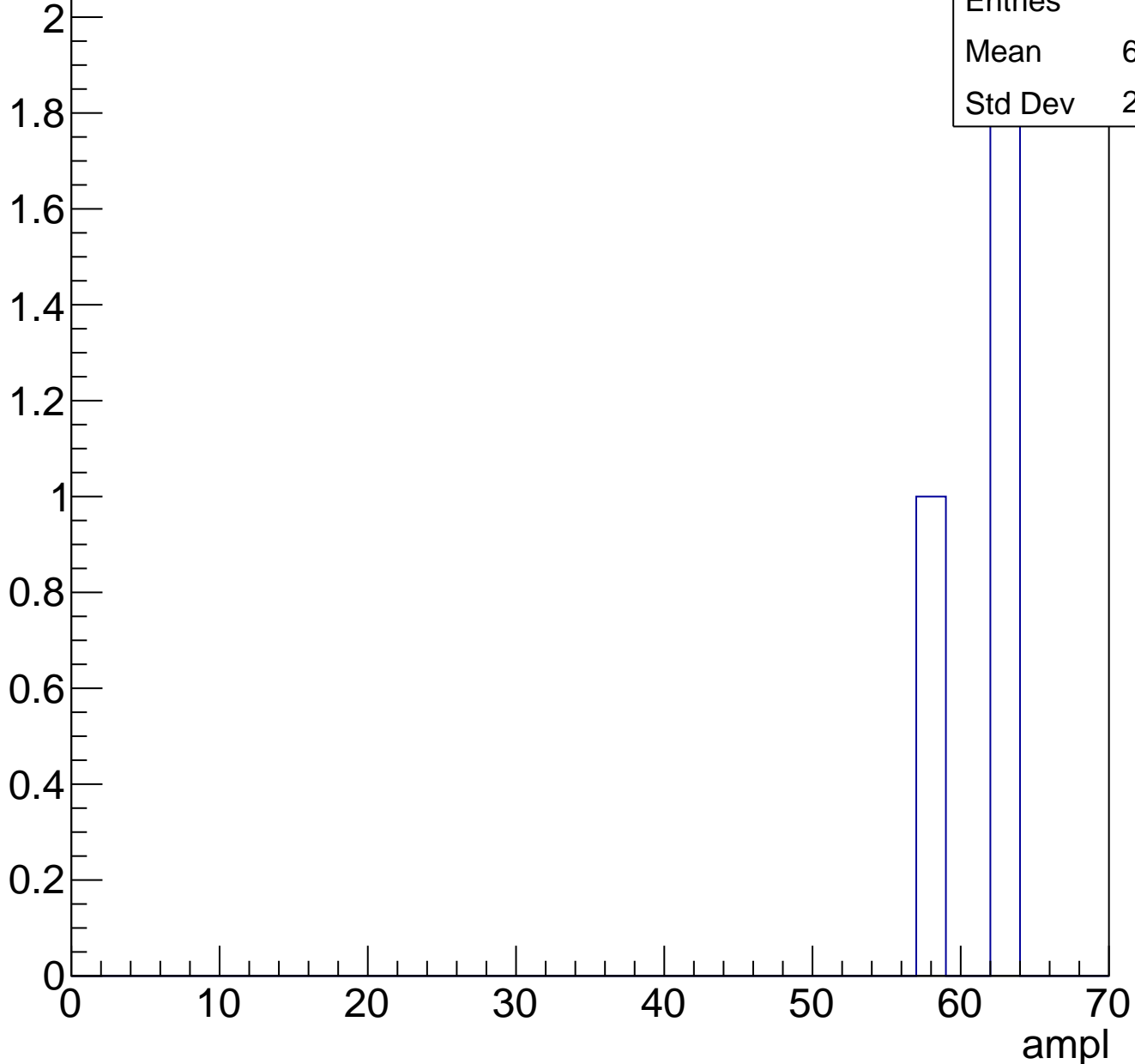
Entries	50
Mean	58.88
Std Dev	8.786



B1L103S, U12-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch77, adc0

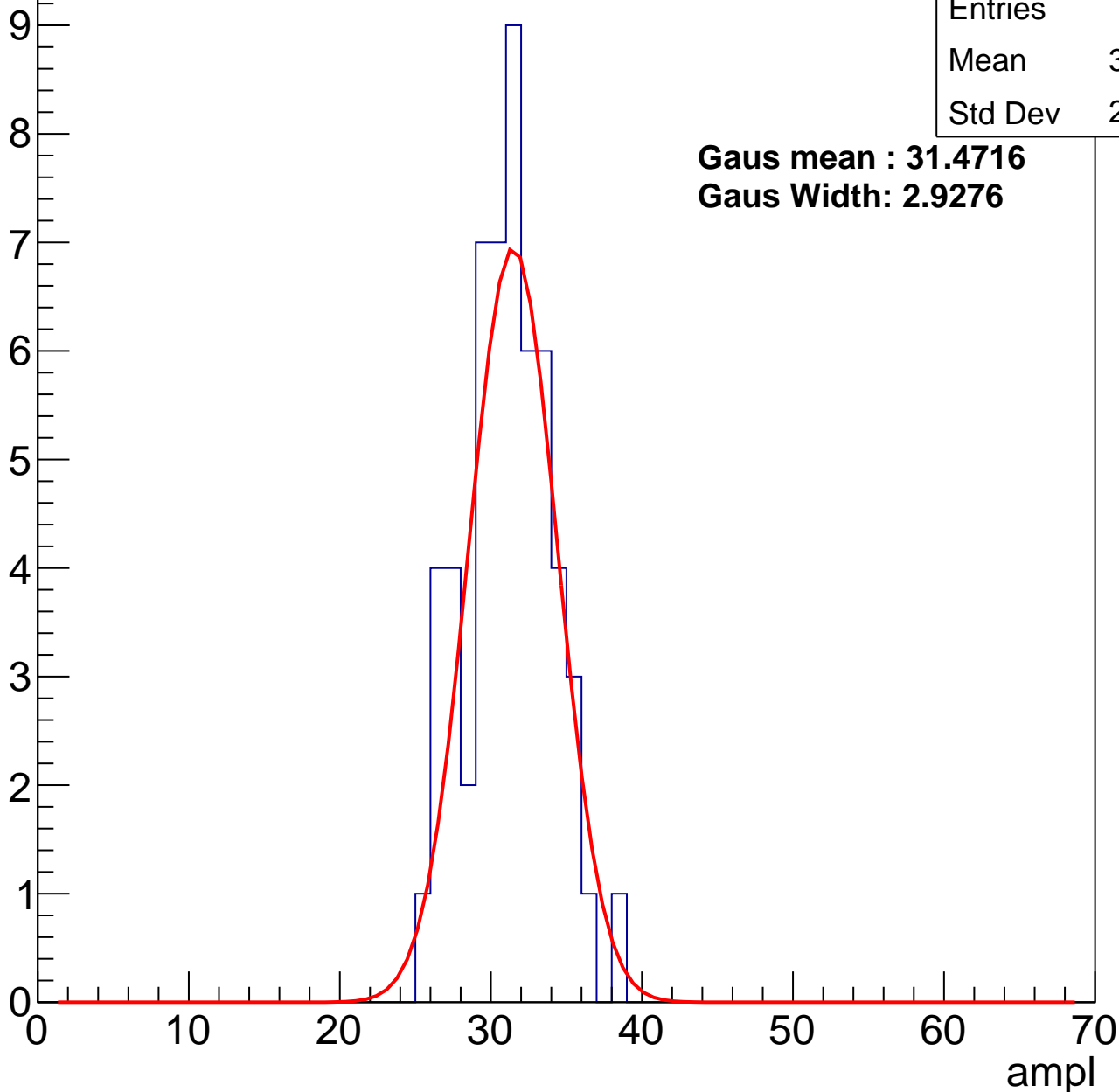
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	30.73
Std Dev	2.818

Gaus mean : 31.4716

Gaus Width: 2.9276



B1L103S, U12-ch77, adc1

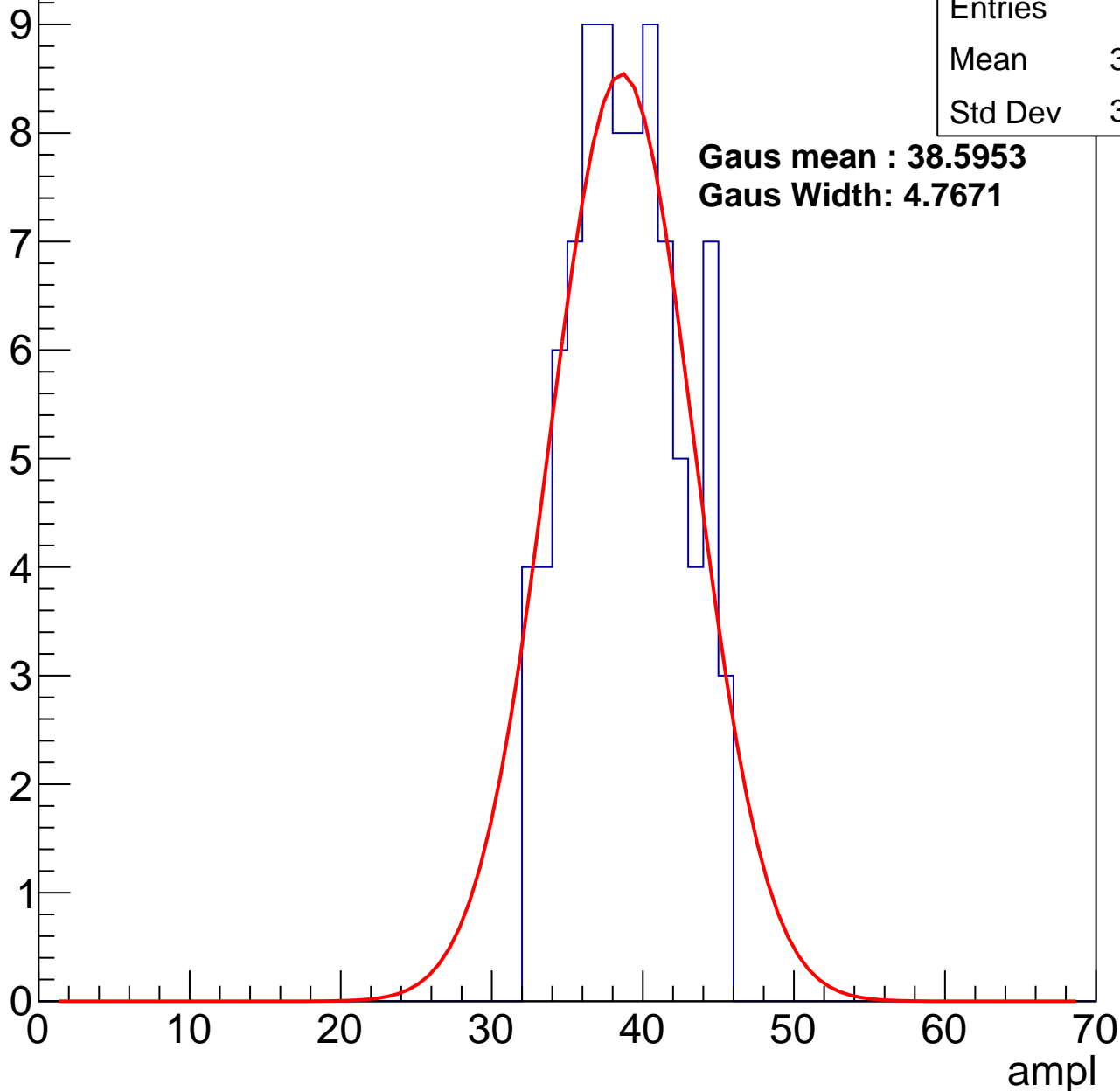
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	38.38
Std Dev	3.529

Gaus mean : 38.5953

Gaus Width: 4.7671



B1L103S, U12-ch77, adc2

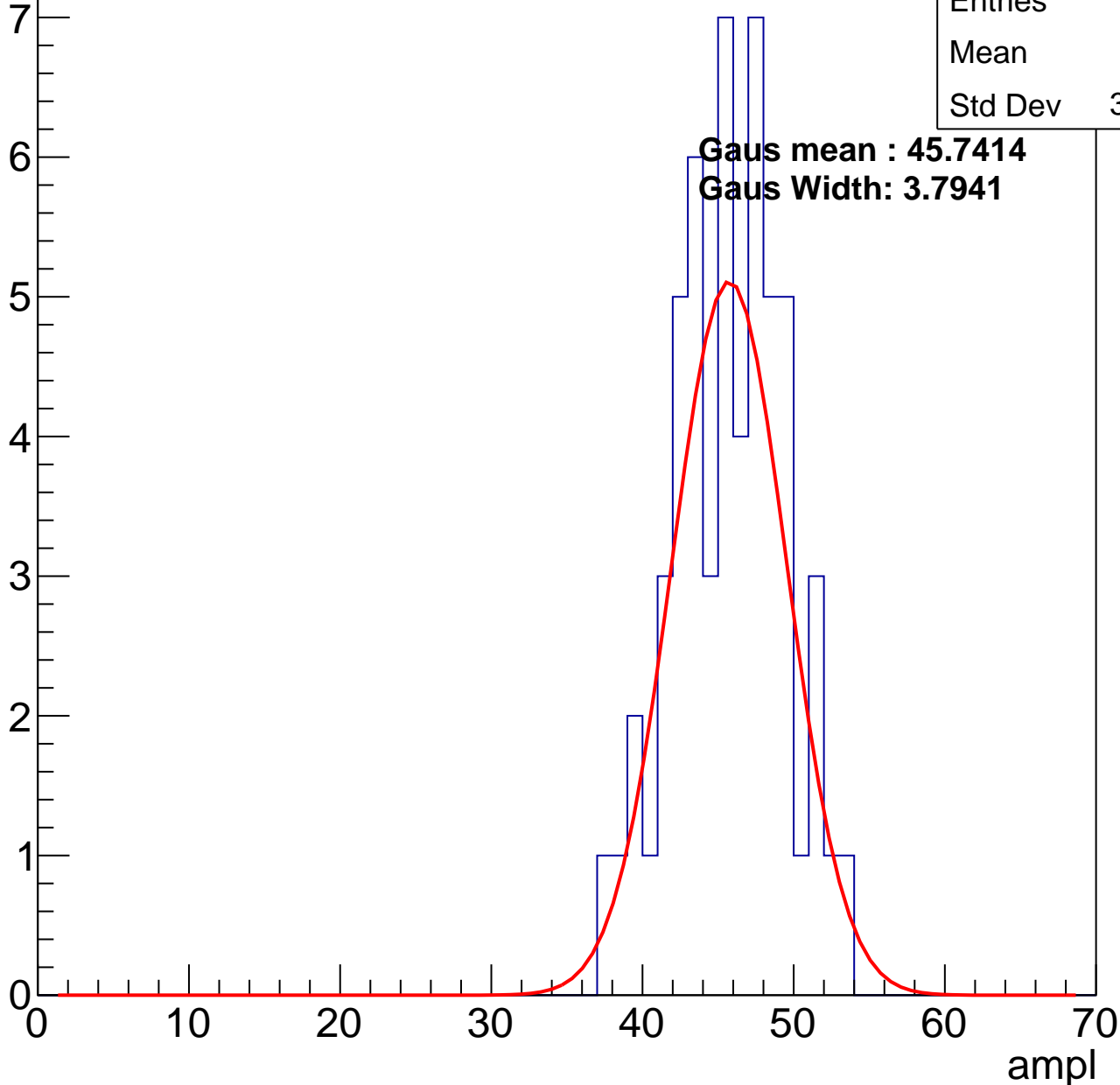
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	45.3
Std Dev	3.605

Gaus mean : 45.7414

Gaus Width: 3.7941

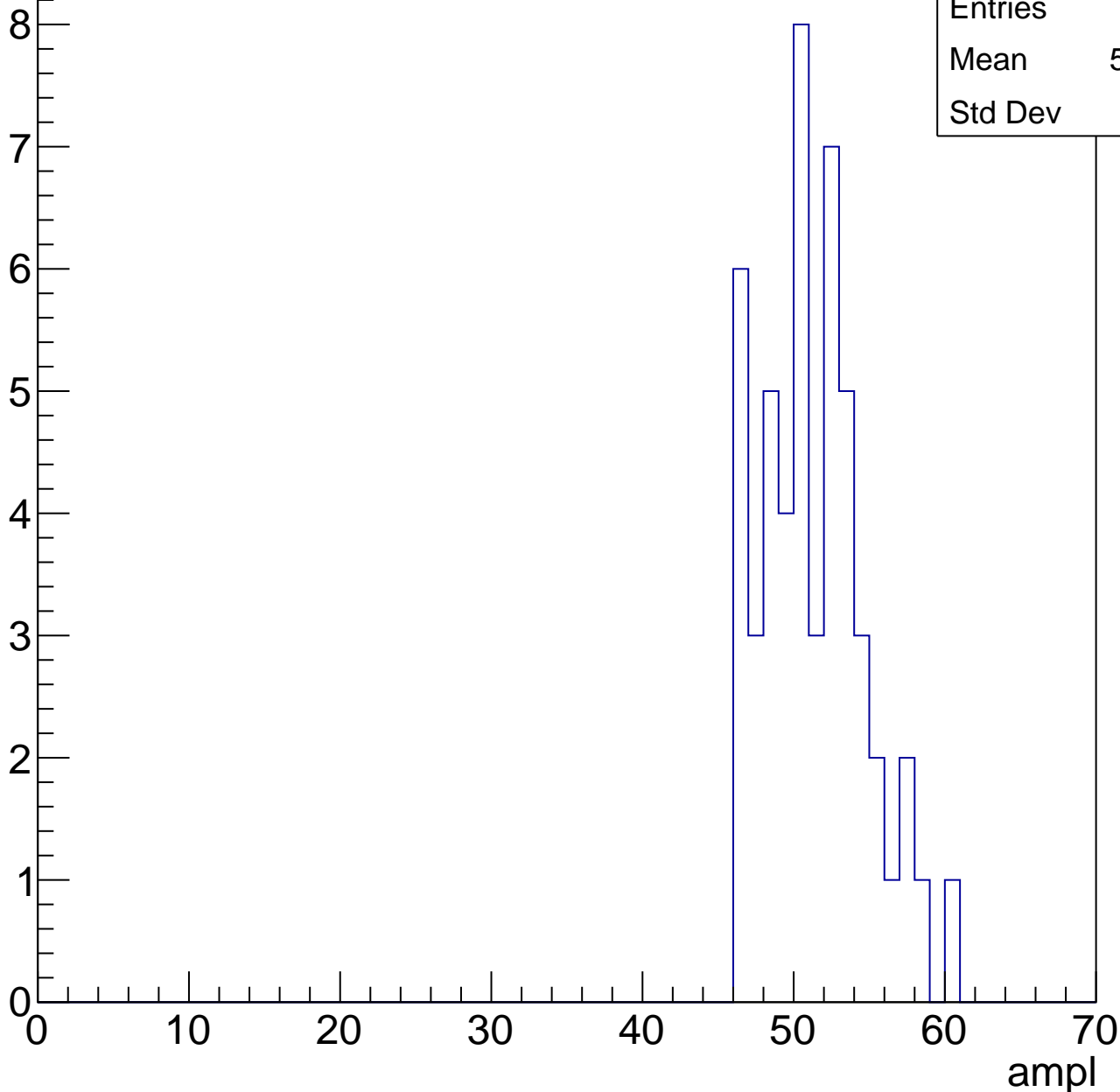


B1L103S, U12-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	50.88
Std Dev	3.37

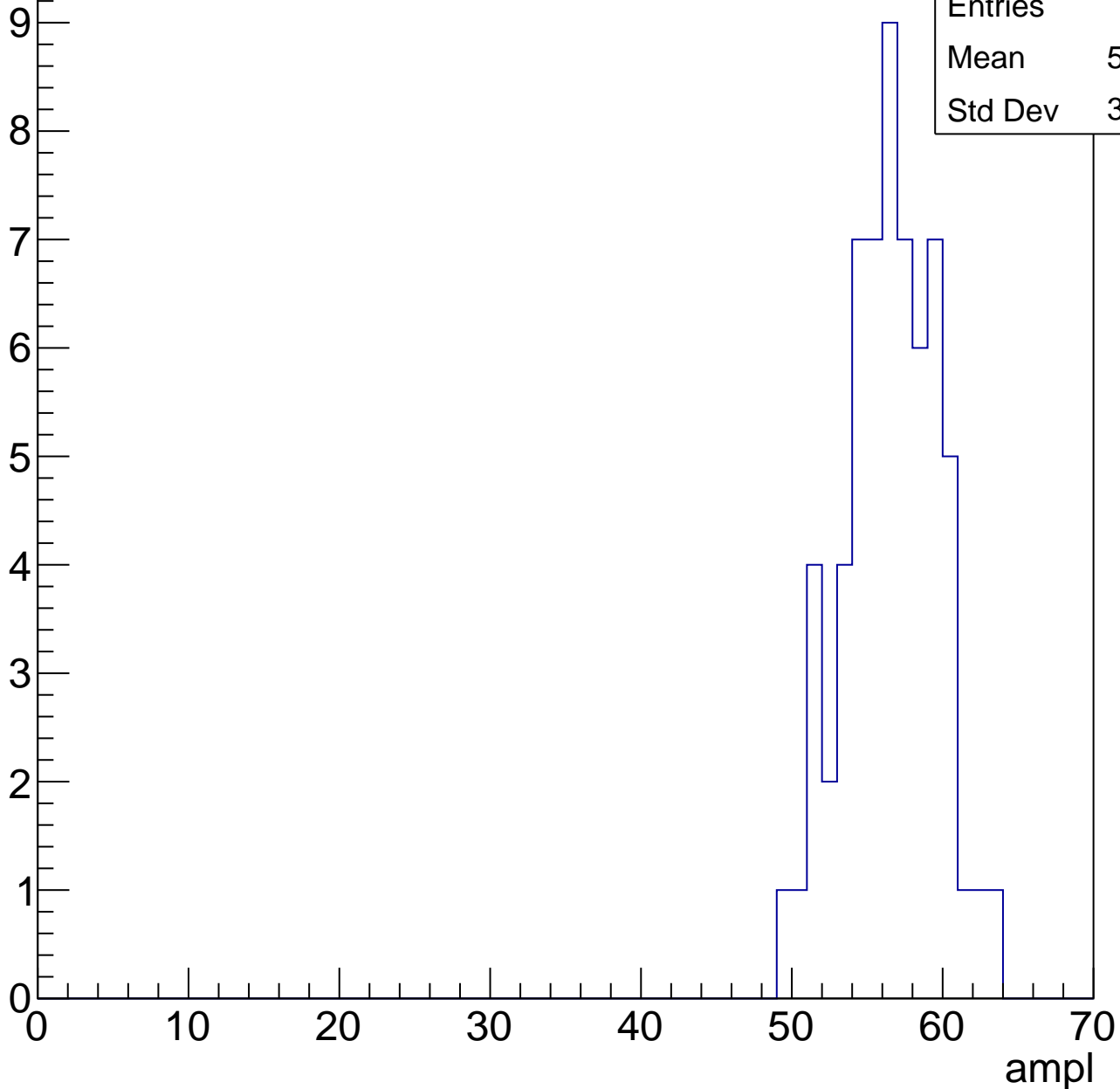


B1L103S, U12-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.06
Std Dev	3.013

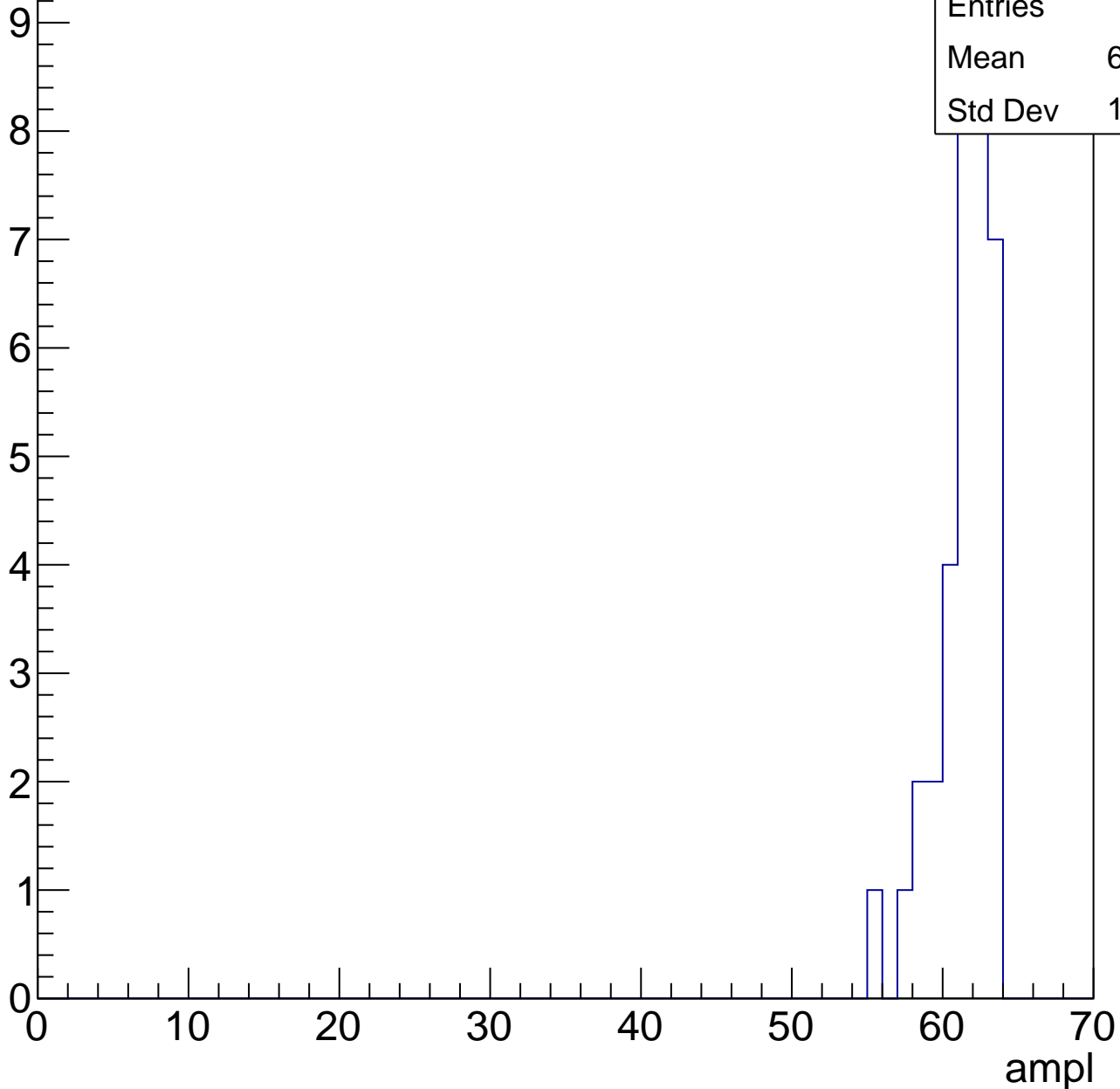


B1L103S, U12-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

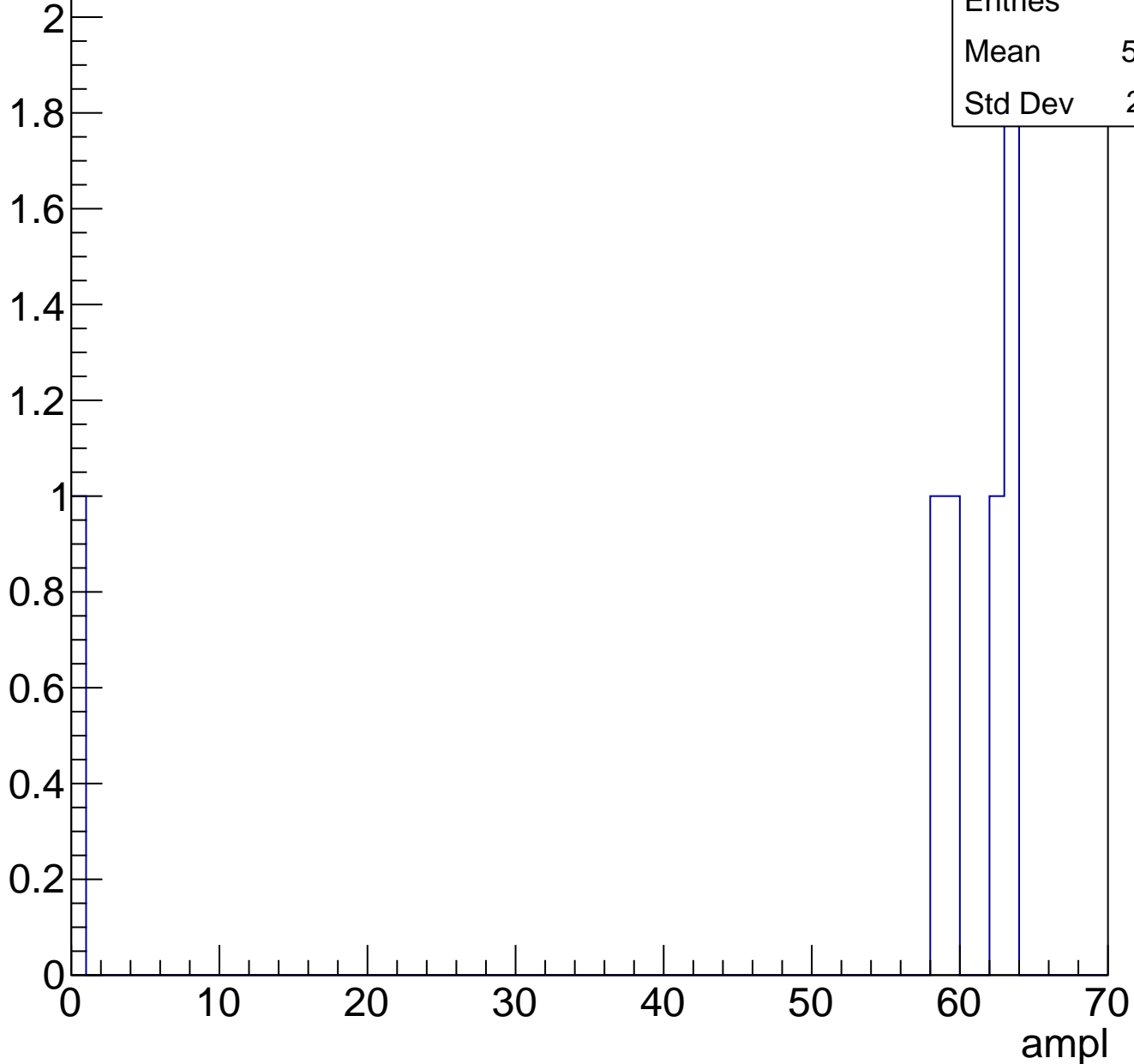
Entries	34
Mean	60.94
Std Dev	1.862



B1L103S, U12-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

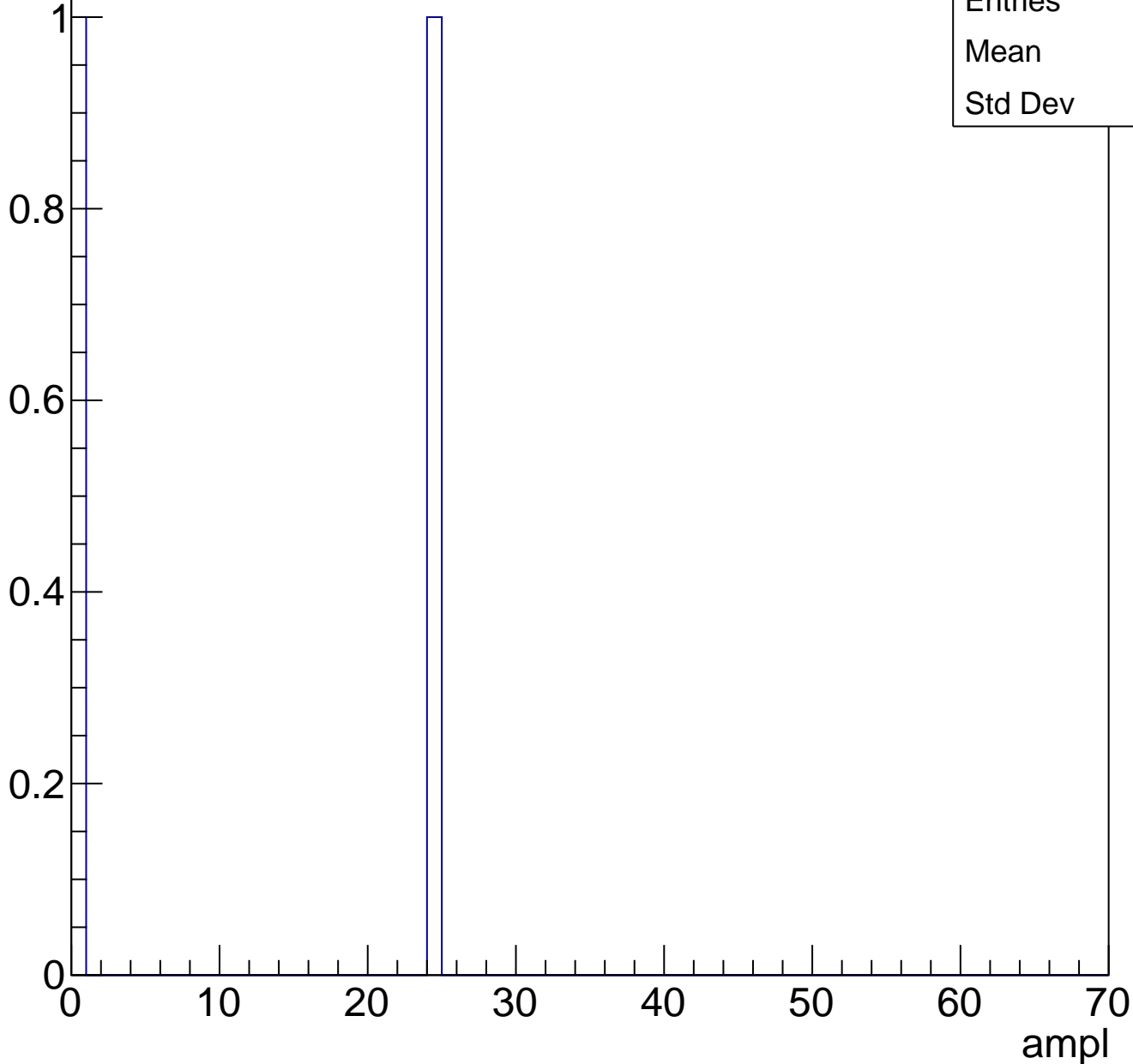
Entry



B1L103S, U12-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch78, adc0

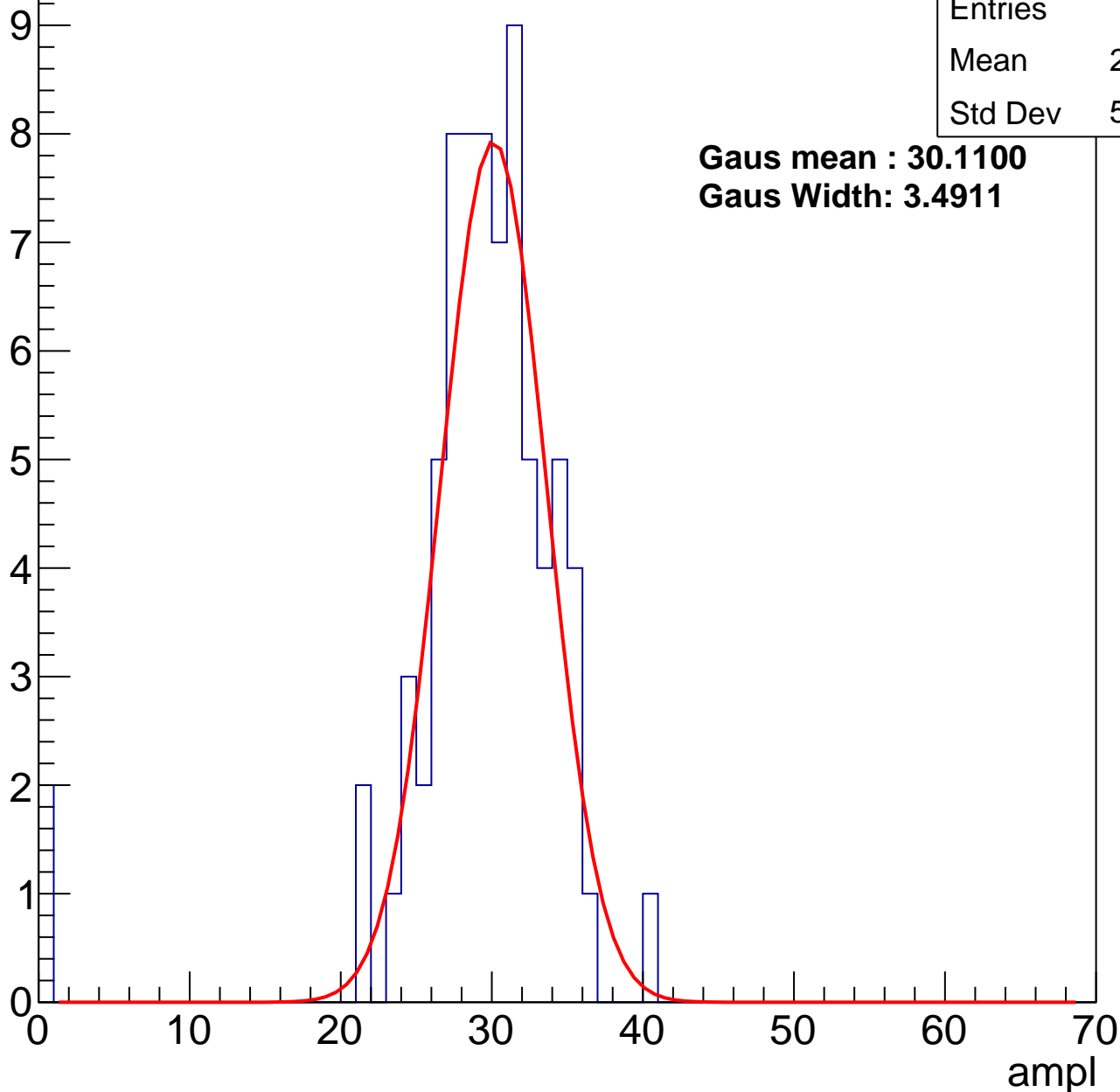
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.75
Std Dev	5.915

Gaus mean : 30.1100

Gaus Width: 3.4911



B1L103S, U12-ch78, adc1

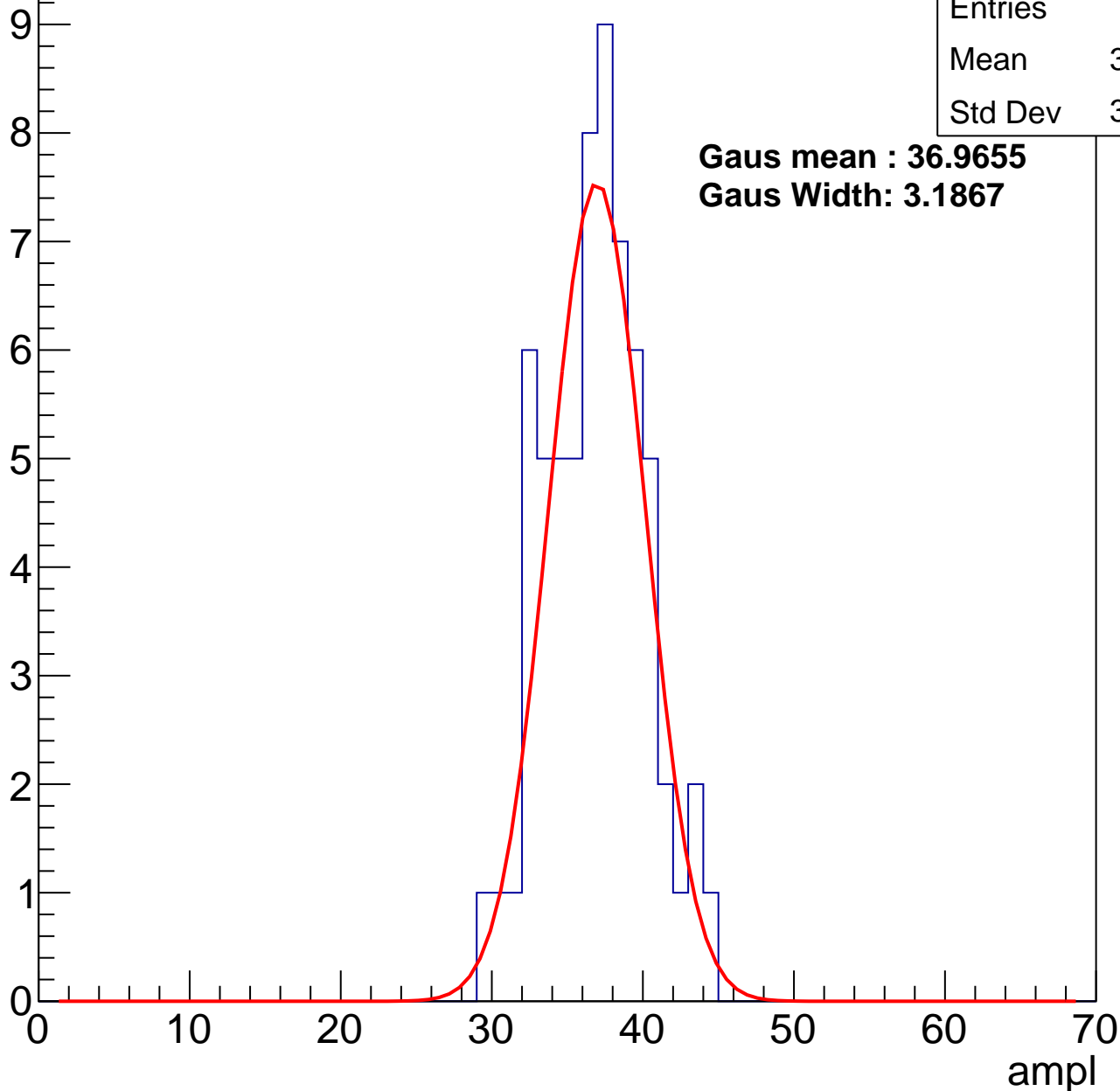
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.42
Std Dev	3.243

Gaus mean : 36.9655

Gaus Width: 3.1867



B1L103S, U12-ch78, adc2

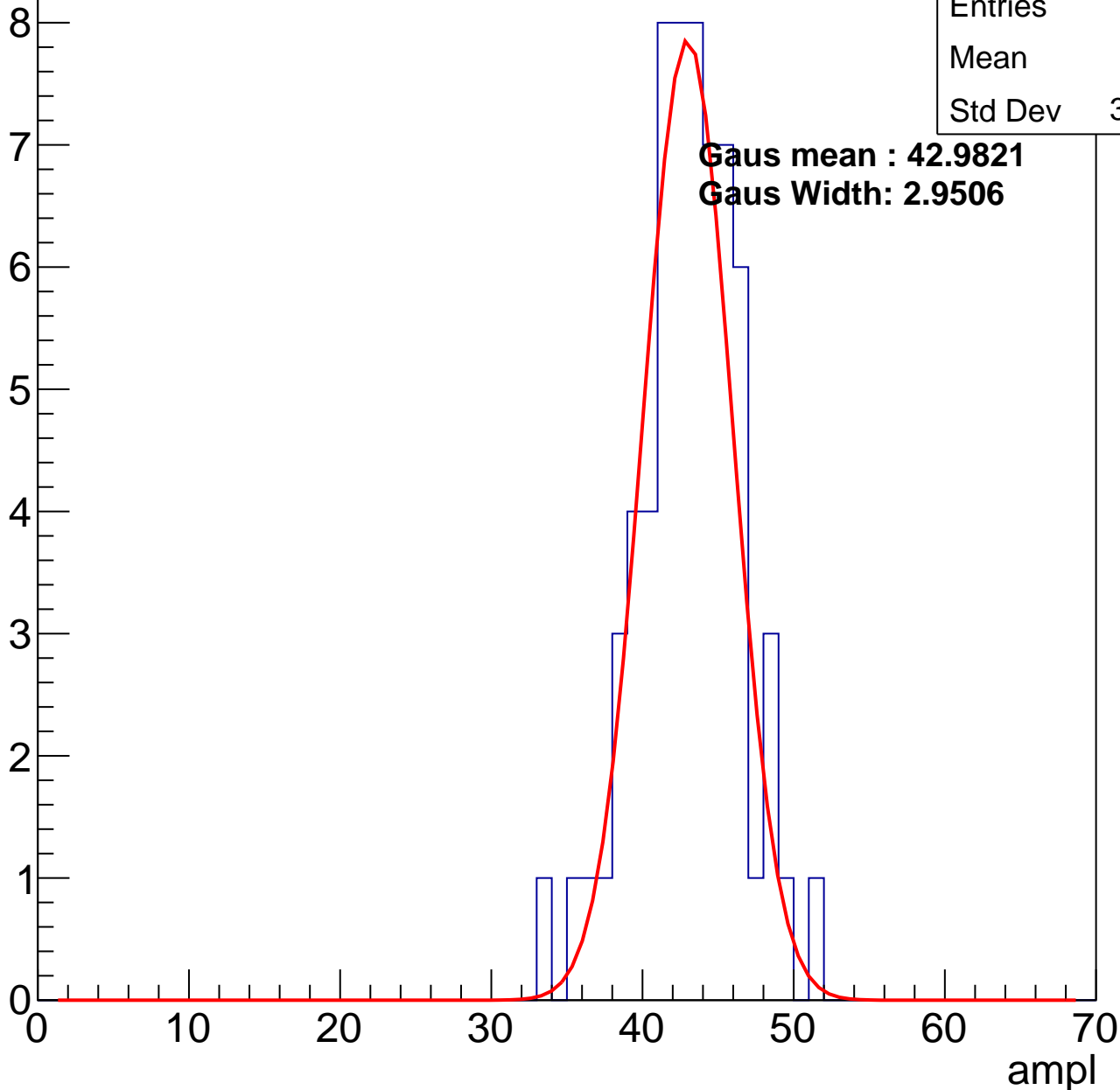
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.6
Std Dev	3.359

Gaus mean : 42.9821

Gaus Width: 2.9506

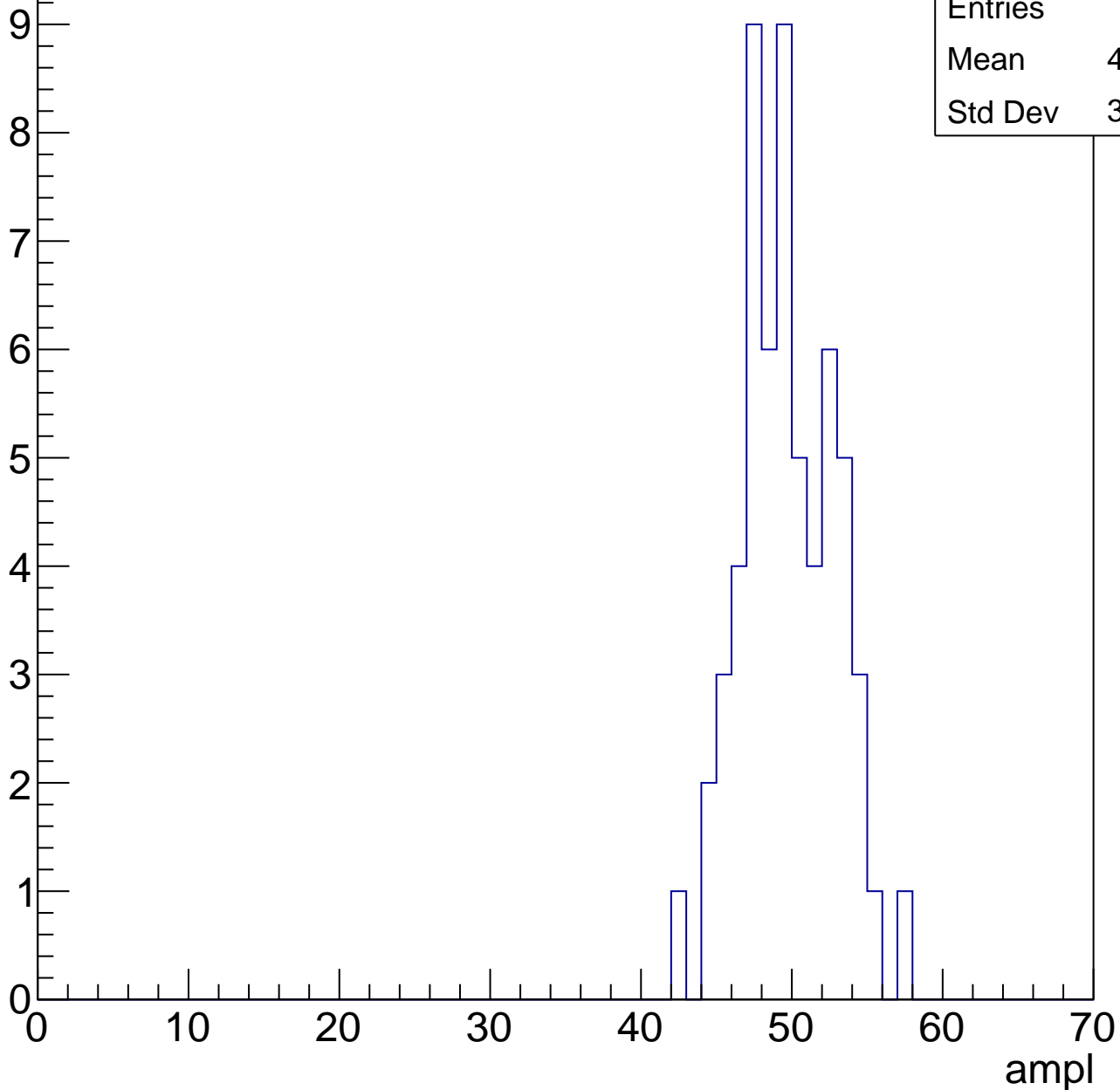


B1L103S, U12-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.25
Std Dev	3.056

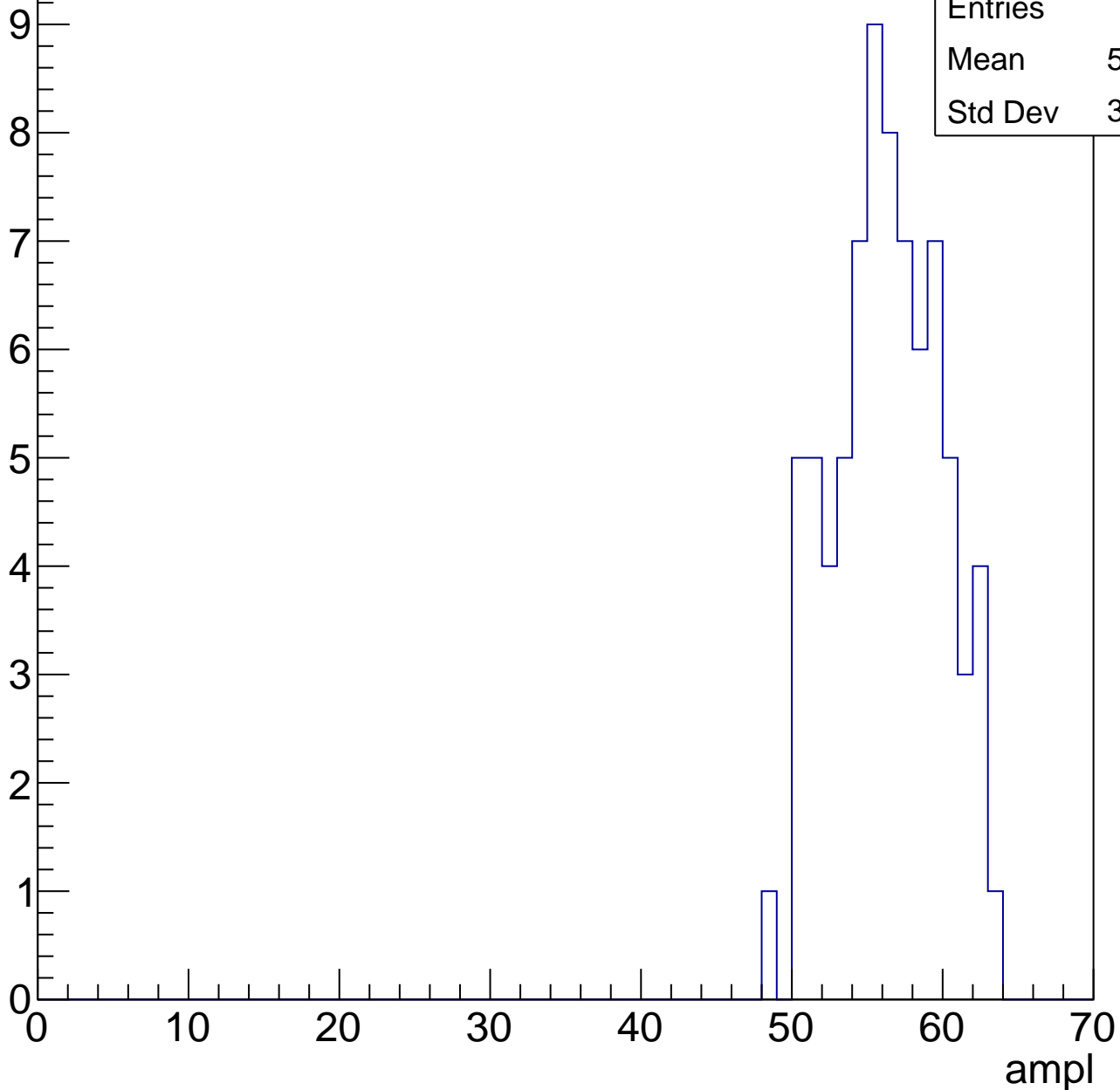


B1L103S, U12-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	55.86
Std Dev	3.523

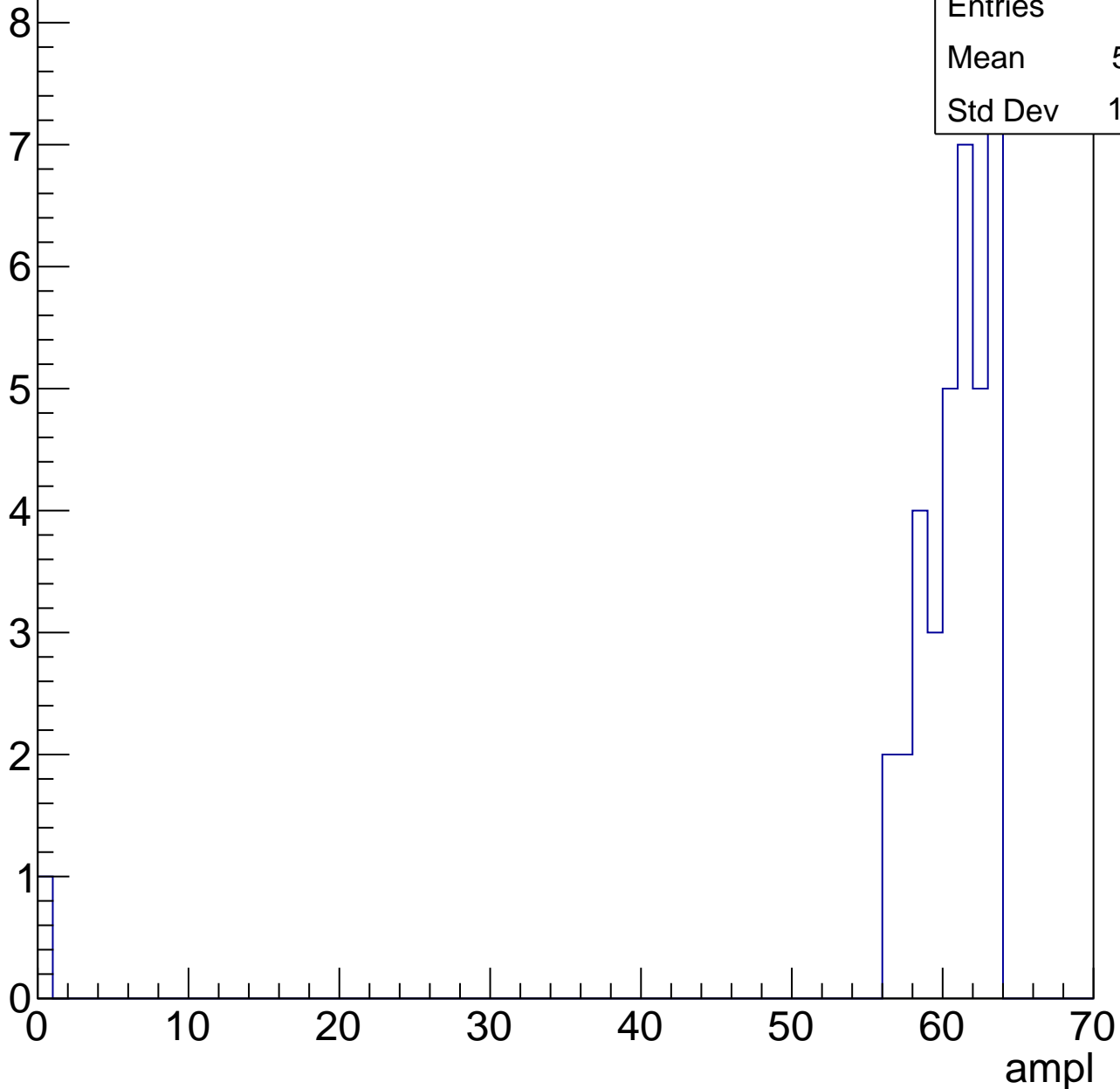


B1L103S, U12-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.81
Std Dev	10.02



B1L103S, U12-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch79, adc0

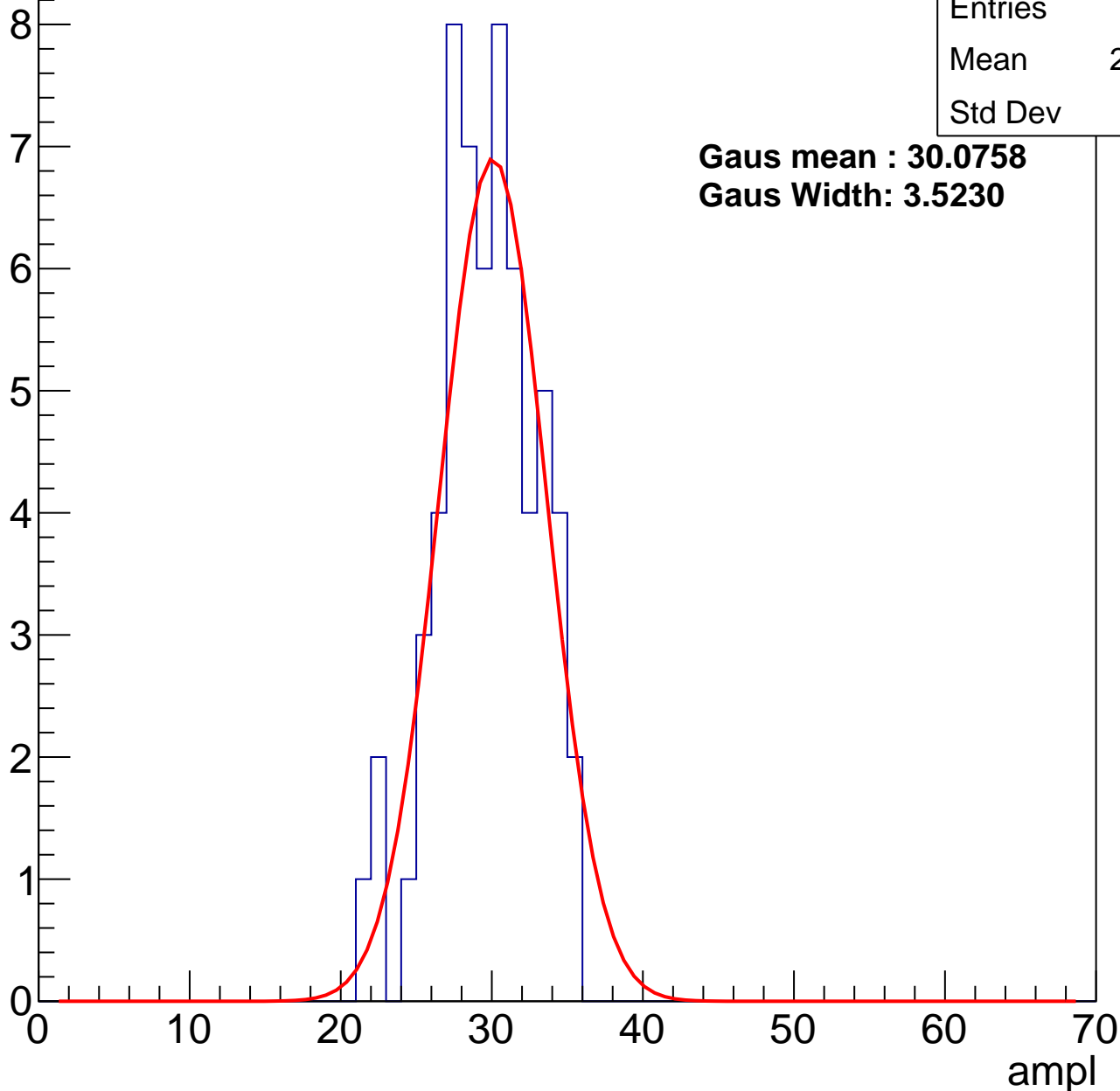
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	29.16
Std Dev	3.21

Gaus mean : 30.0758

Gaus Width: 3.5230



B1L103S, U12-ch79, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	100
Mean	37.04
Std Dev	4.209

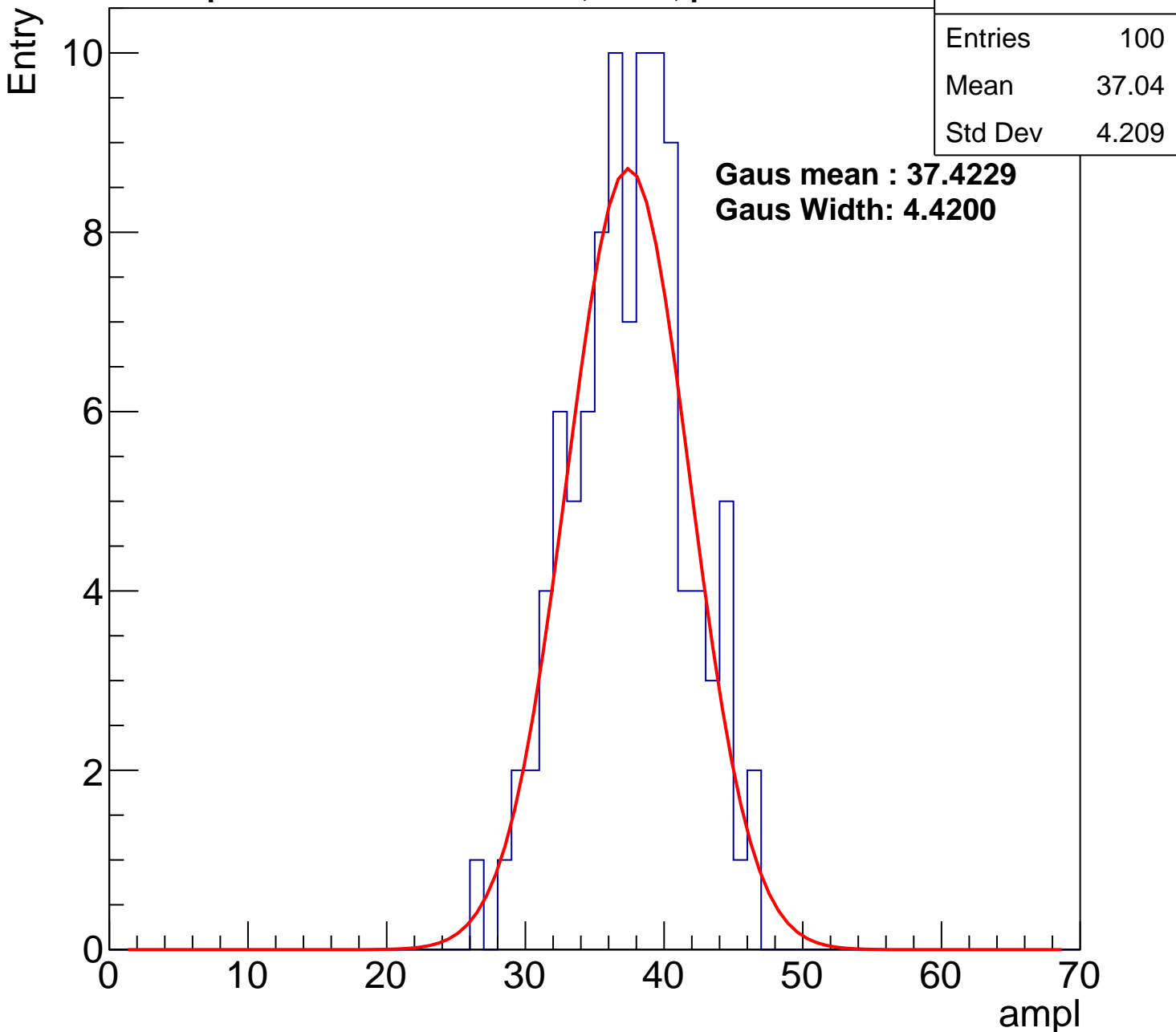
Gaus mean : 37.4229
Gaus Width: 4.4200

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch79, adc2

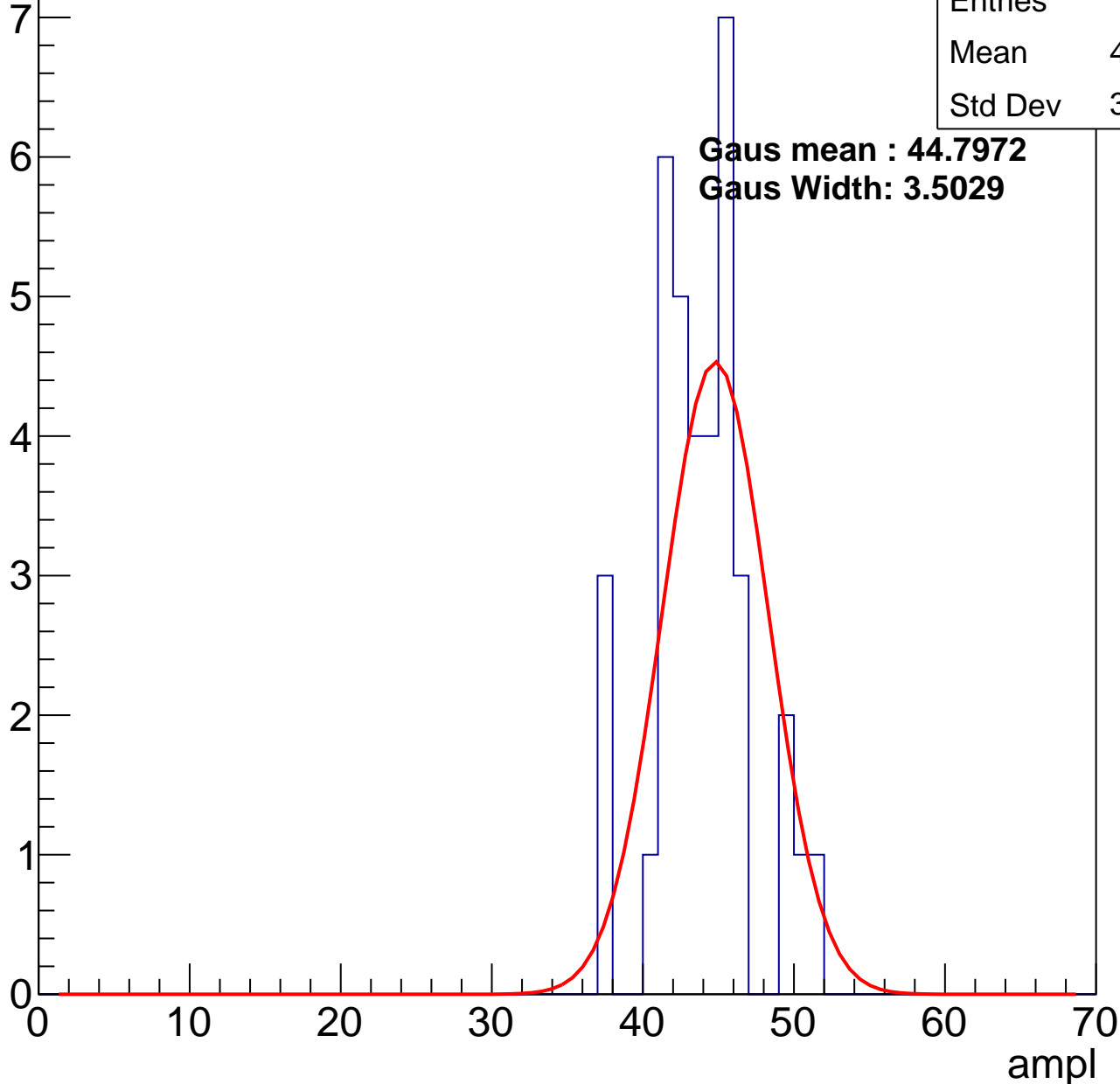
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	43.43
Std Dev	3.218

Gaus mean : 44.7972

Gaus Width: 3.5029



B1L103S, U12-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

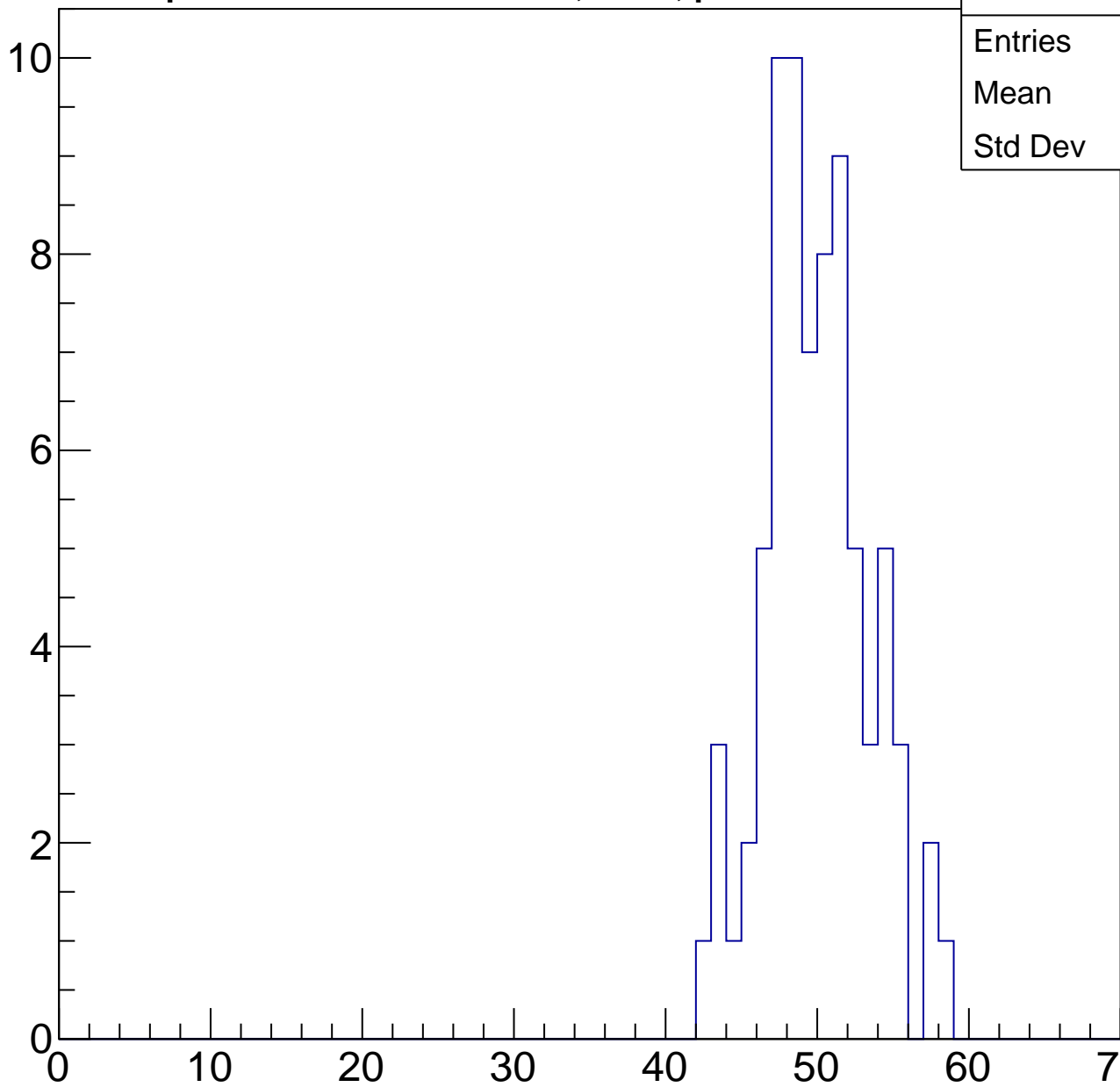
Entries	75
Mean	49.51
Std Dev	3.403

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

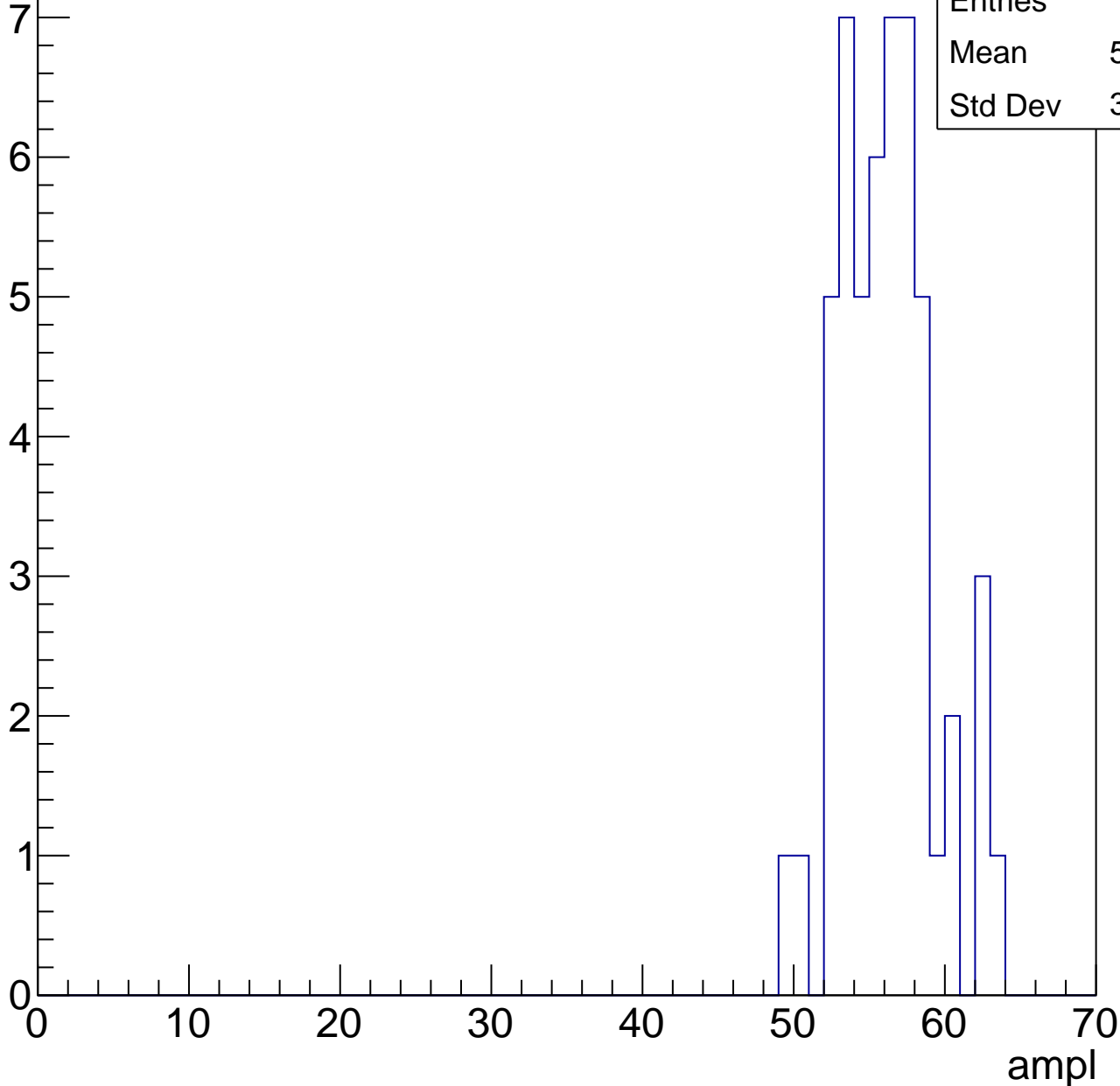


B1L103S, U12-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

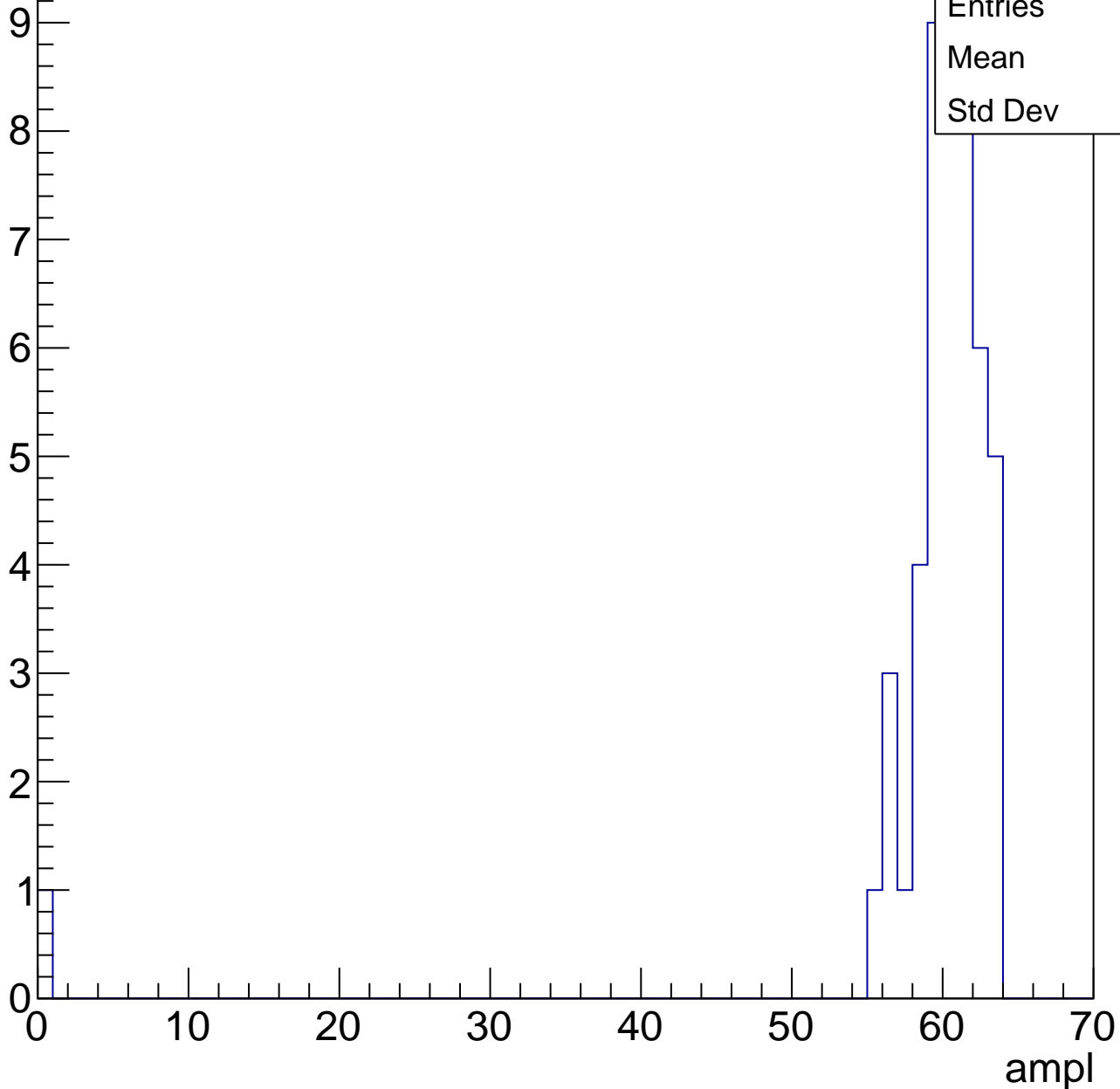
Entries	51
Mean	55.67
Std Dev	3.046



B1L103S, U12-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch80, adc0

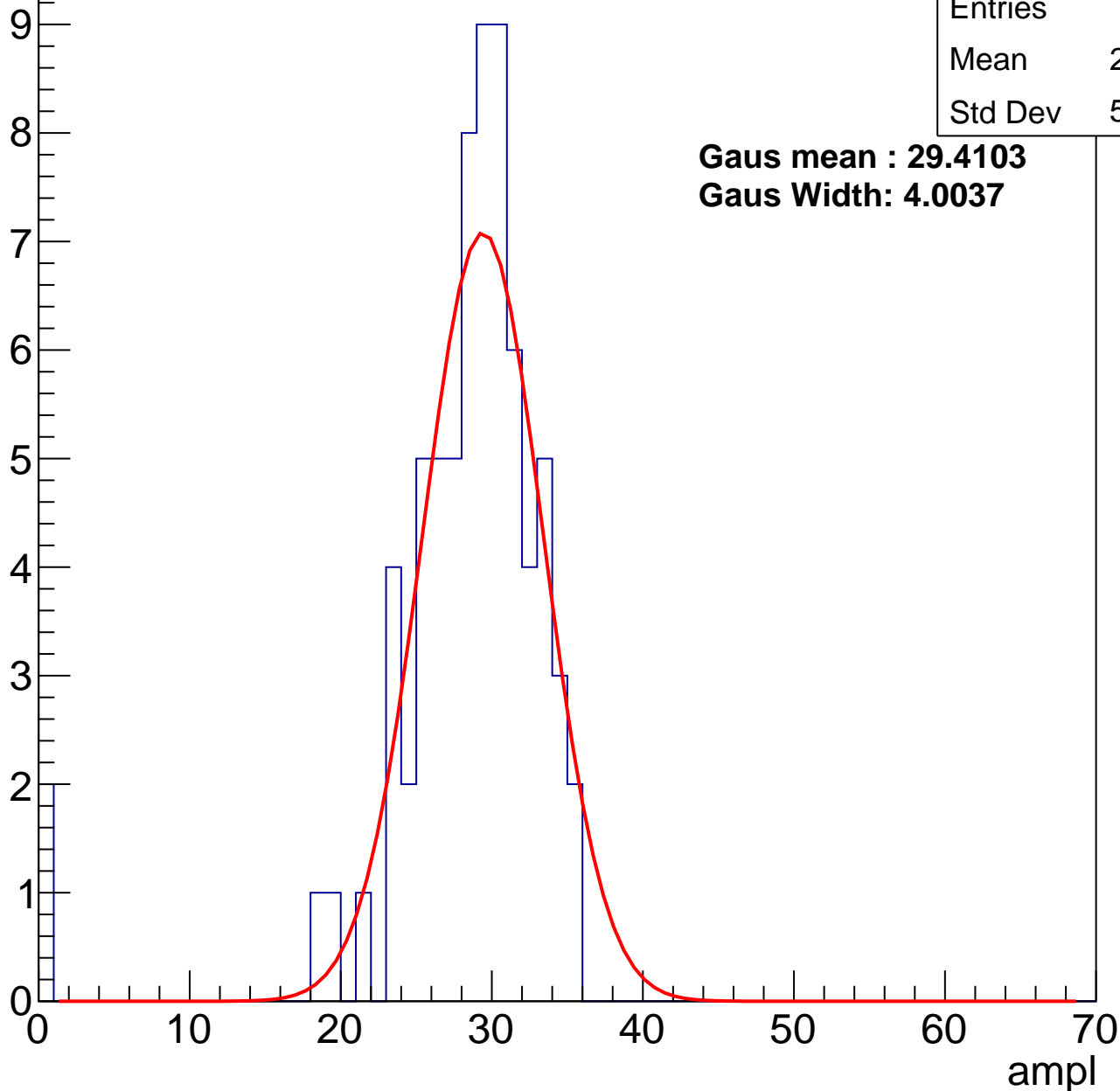
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.69
Std Dev	5.878

Gaus mean : 29.4103

Gaus Width: 4.0037



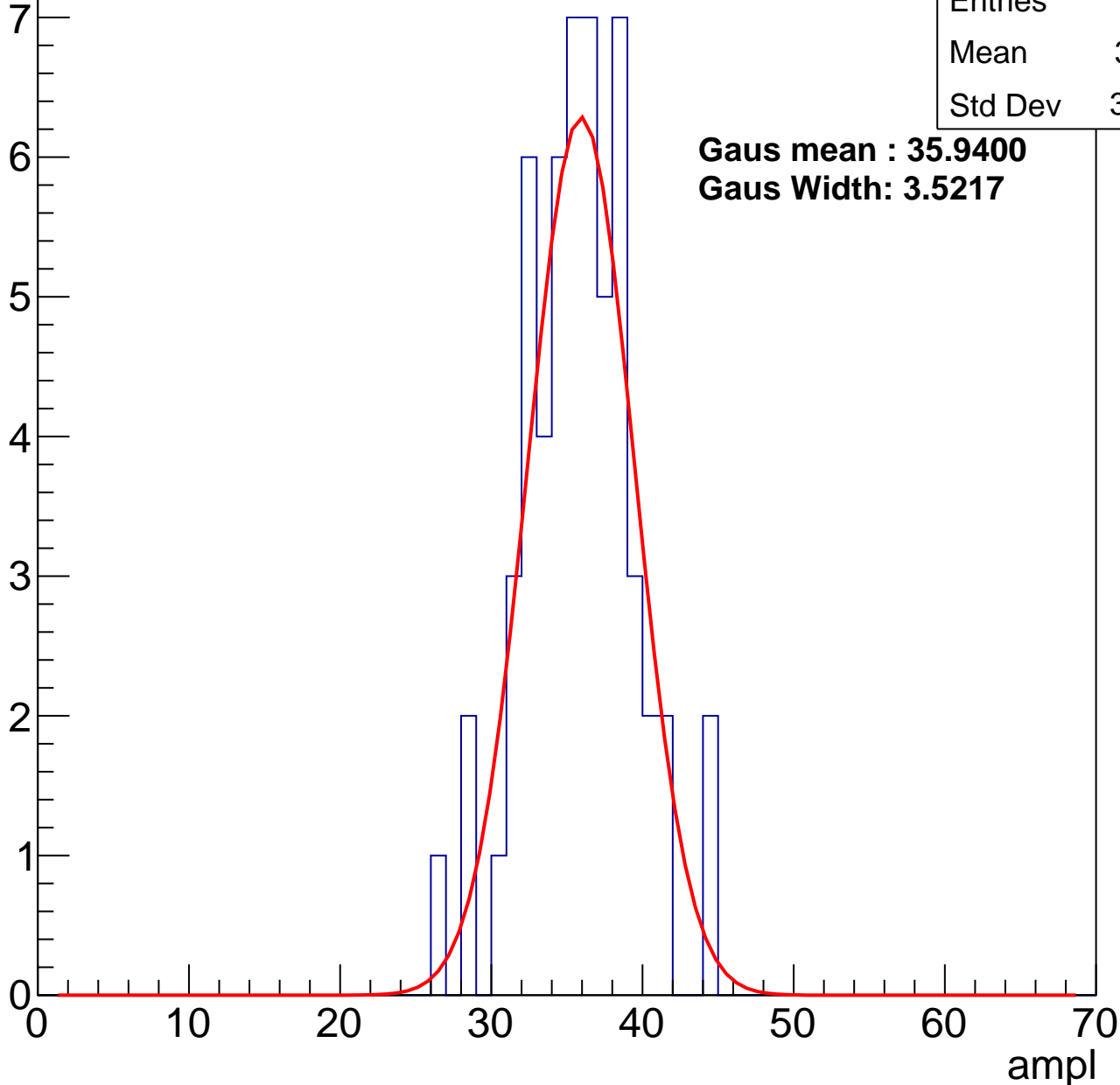
B1L103S, U12-ch80, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.31
Std Dev	3.583

Gaus mean : 35.9400
Gaus Width: 3.5217



B1L103S, U12-ch80, adc2

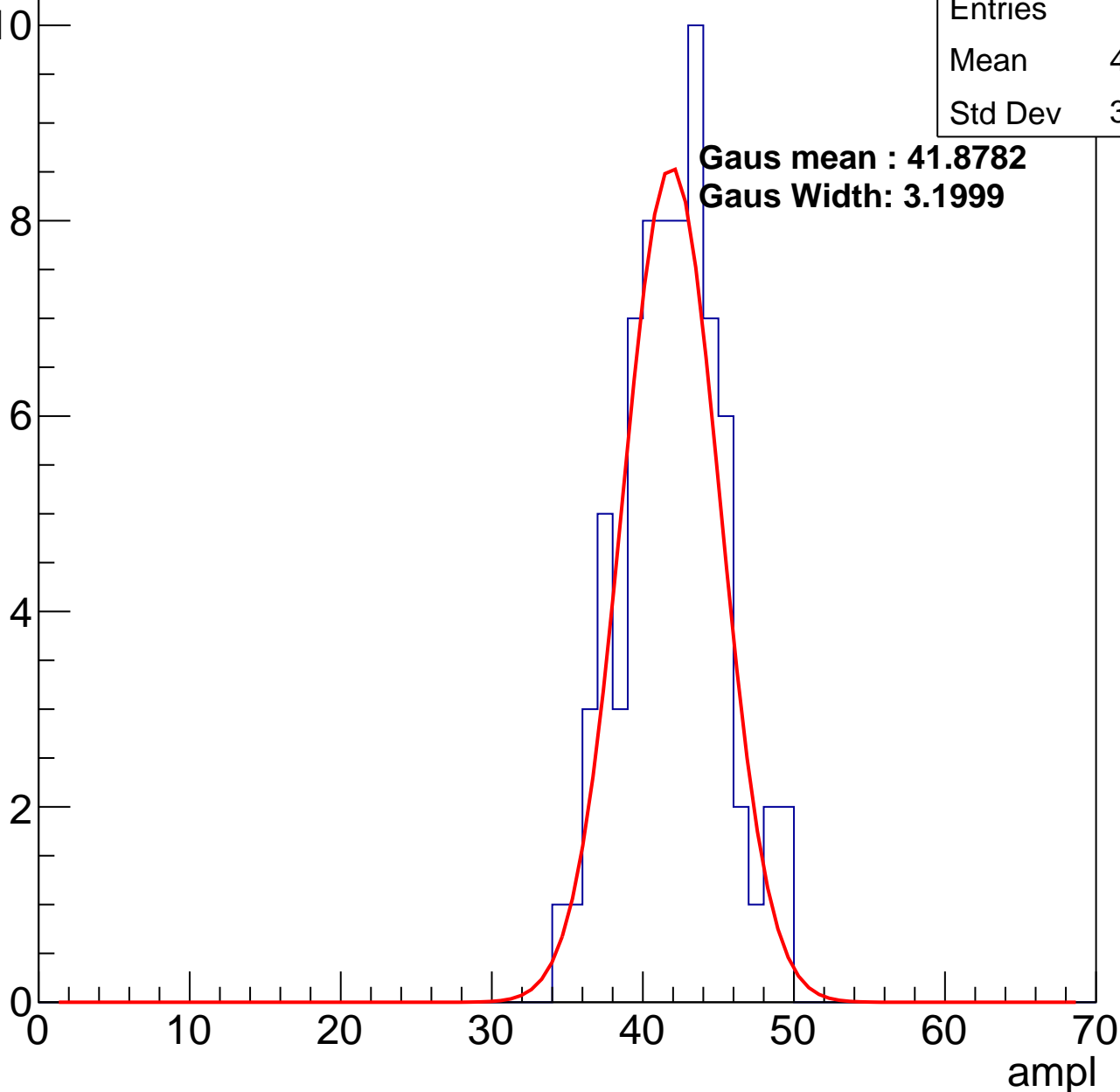
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	41.54
Std Dev	3.289

Gaus mean : 41.8782

Gaus Width: 3.1999

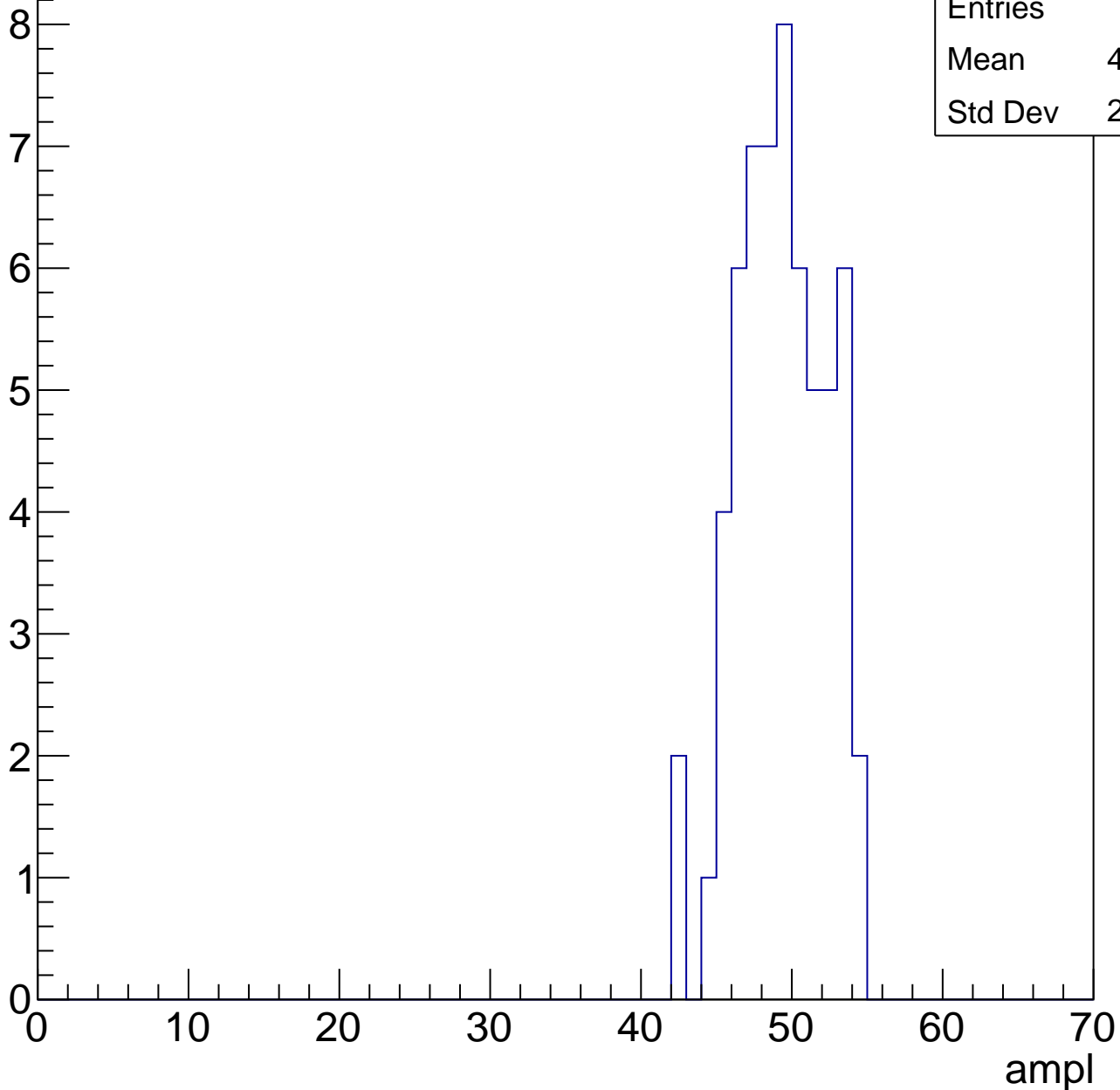


B1L103S, U12-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	48.85
Std Dev	2.887

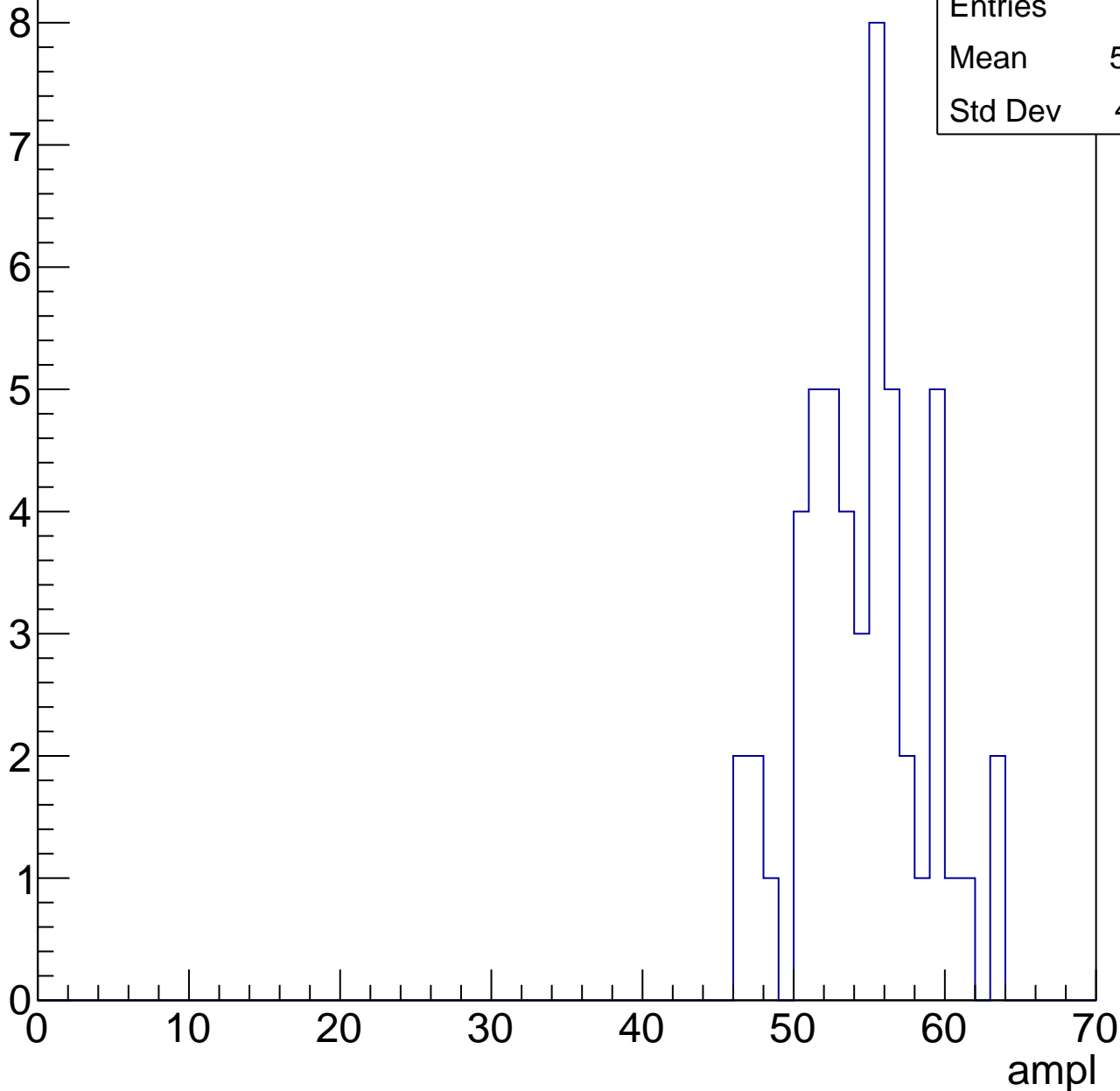


B1L103S, U12-ch80, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

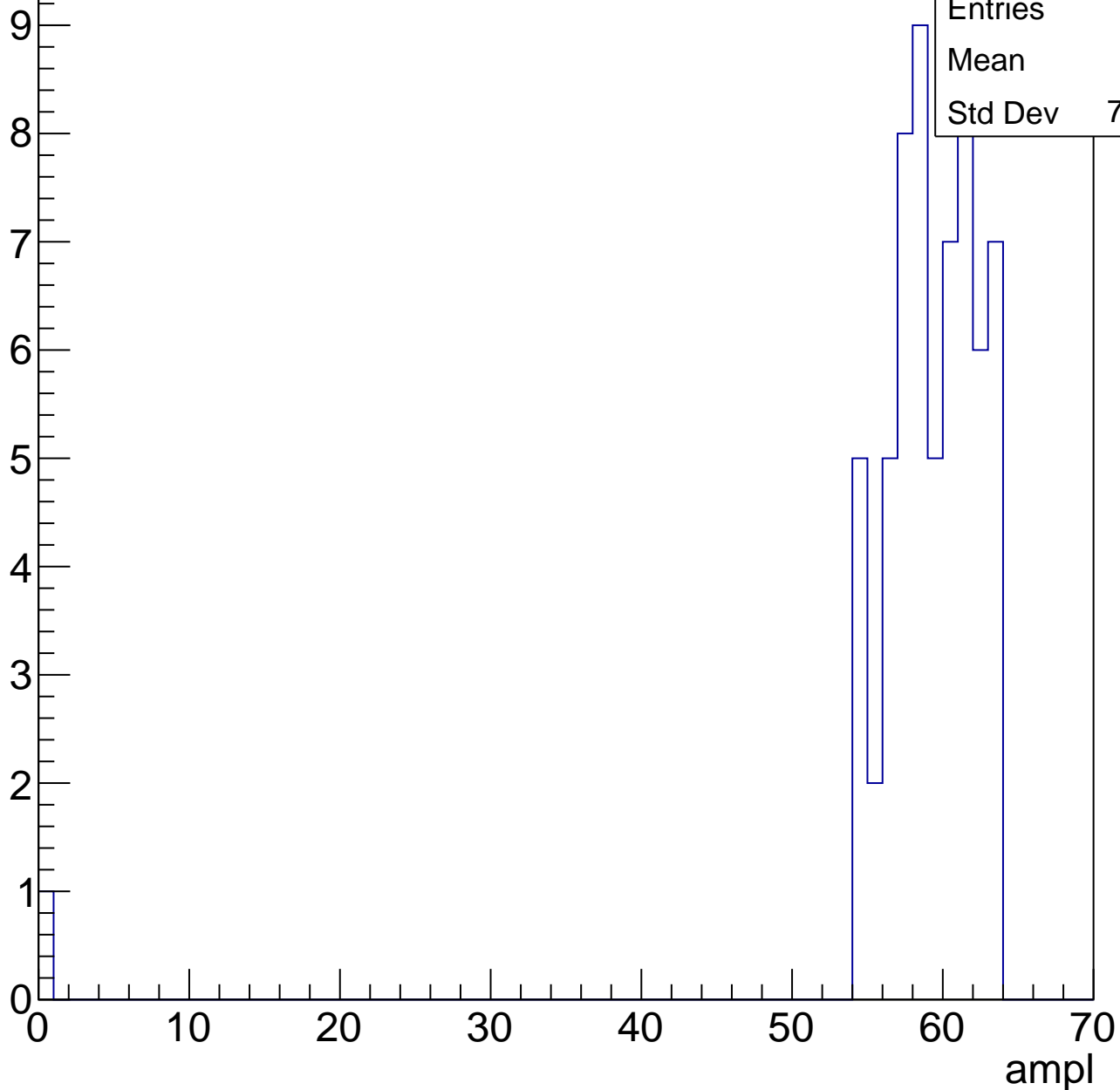
Entries	51
Mean	54.06
Std Dev	4.031



B1L103S, U12-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

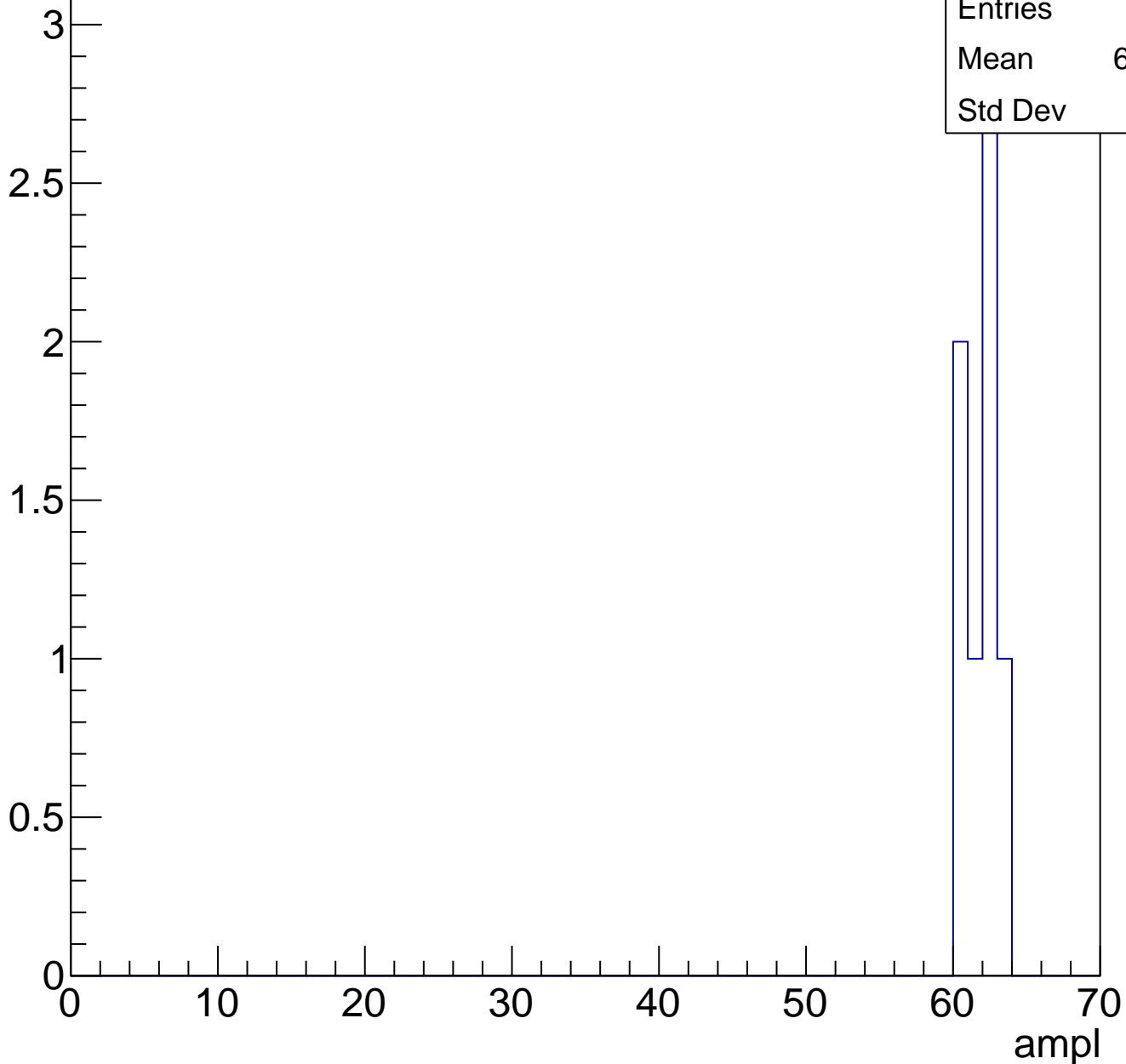
Entry



B1L103S, U12-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch81, adc0

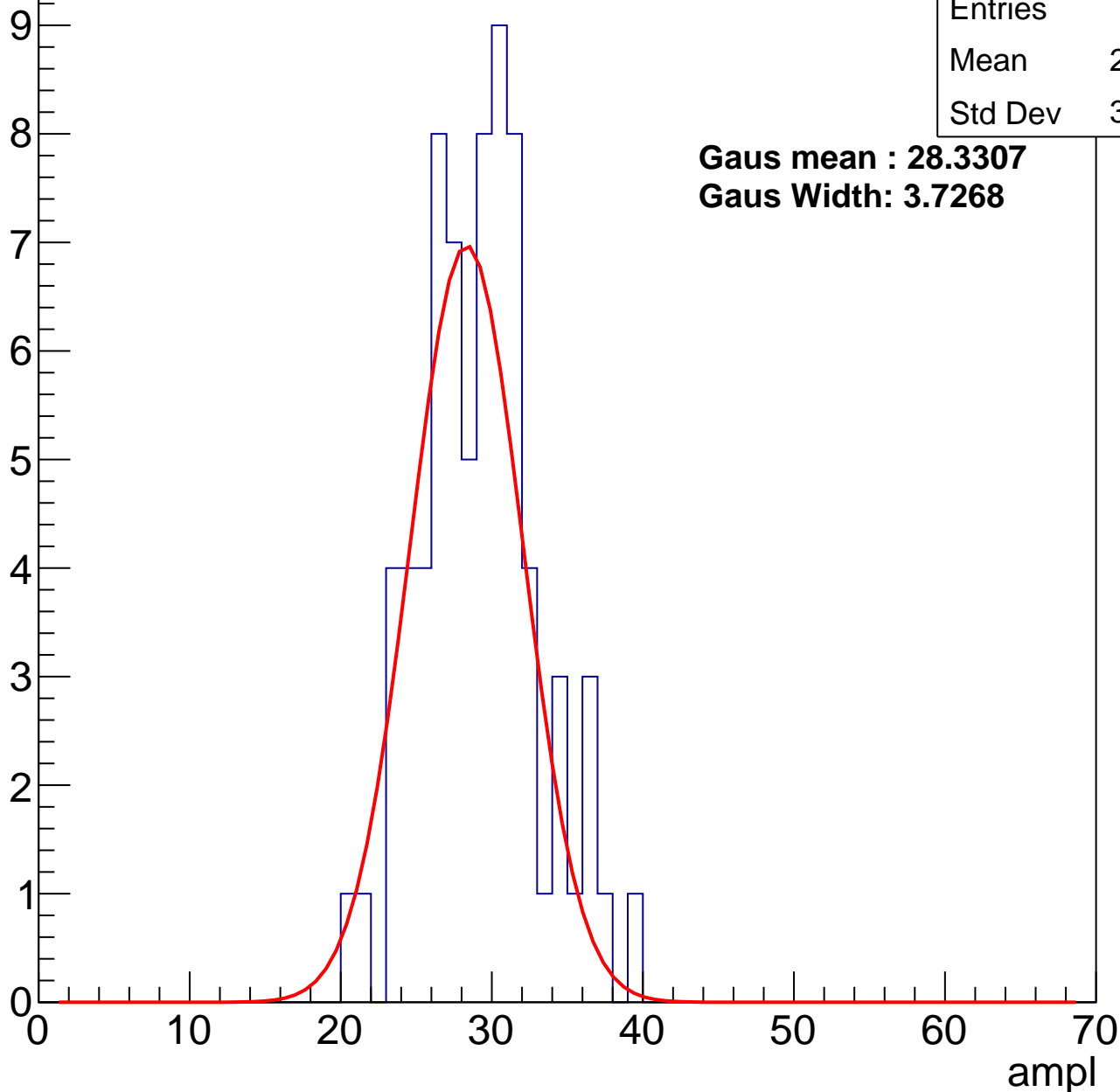
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	28.74
Std Dev	3.825

Gaus mean : 28.3307

Gaus Width: 3.7268



B1L103S, U12-ch81, adc1

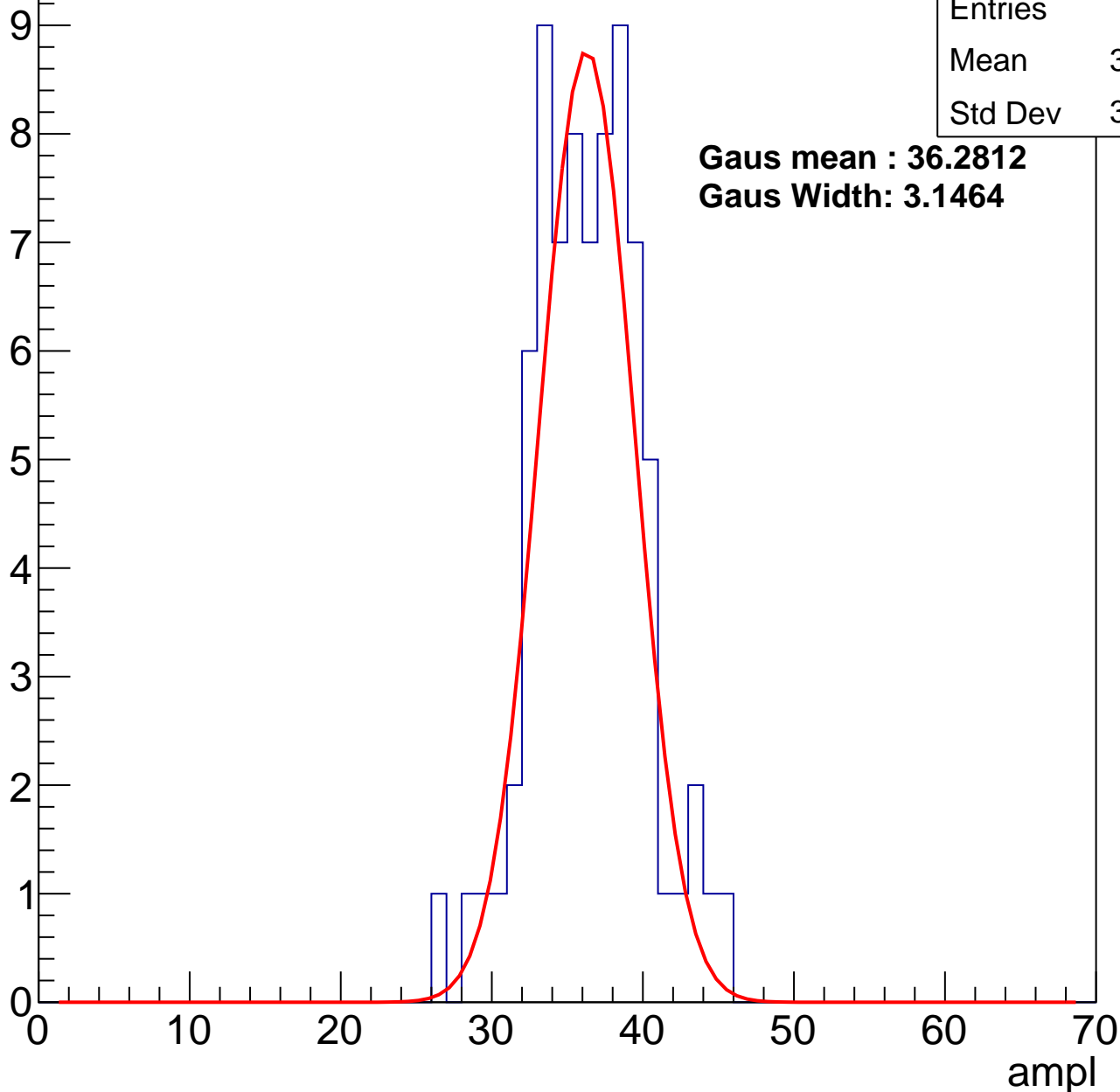
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.94
Std Dev	3.585

Gaus mean : 36.2812

Gaus Width: 3.1464



B1L103S, U12-ch81, adc2

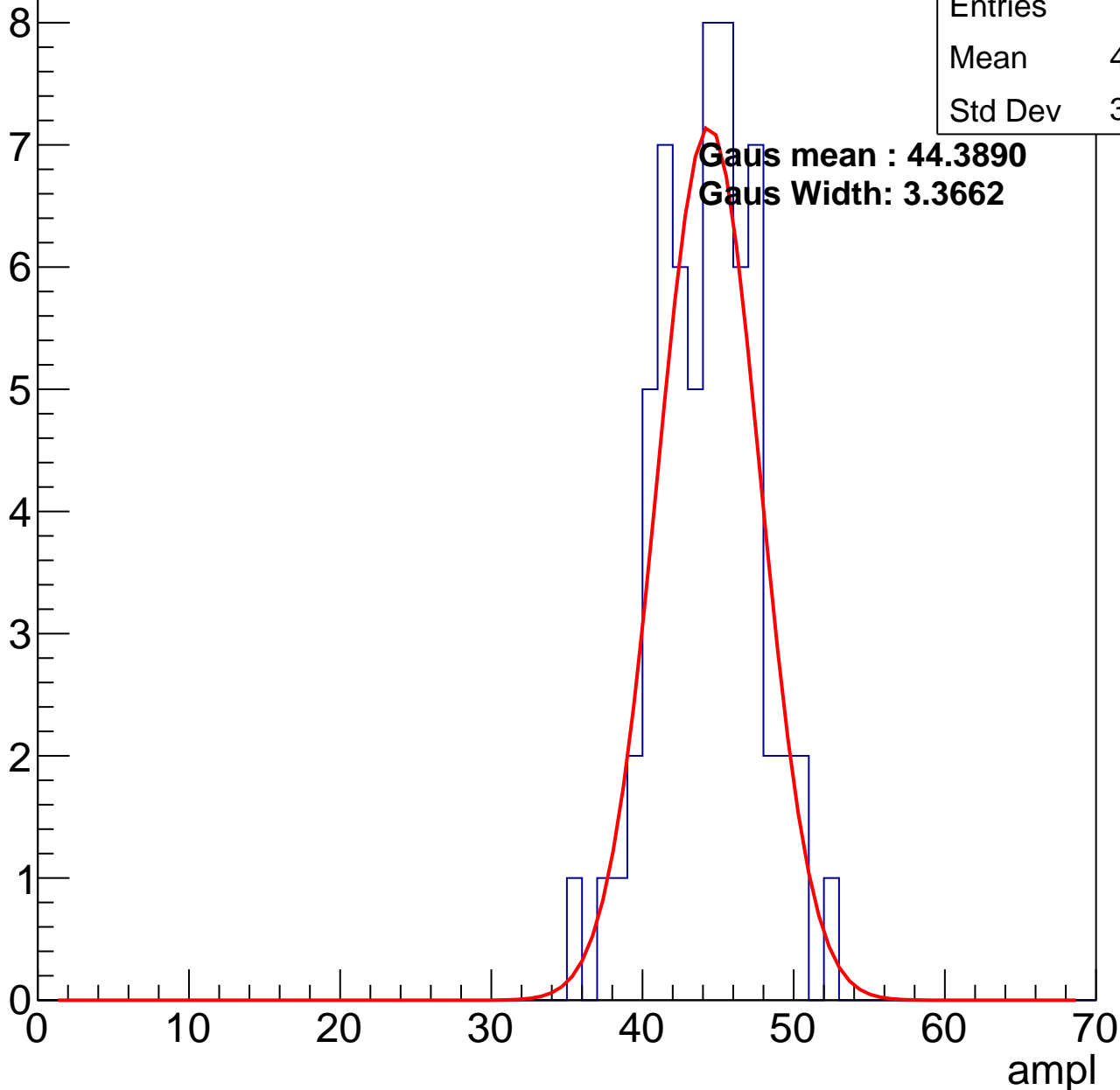
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.83
Std Dev	3.305

Gaus mean : 44.3890

Gaus Width: 3.3662

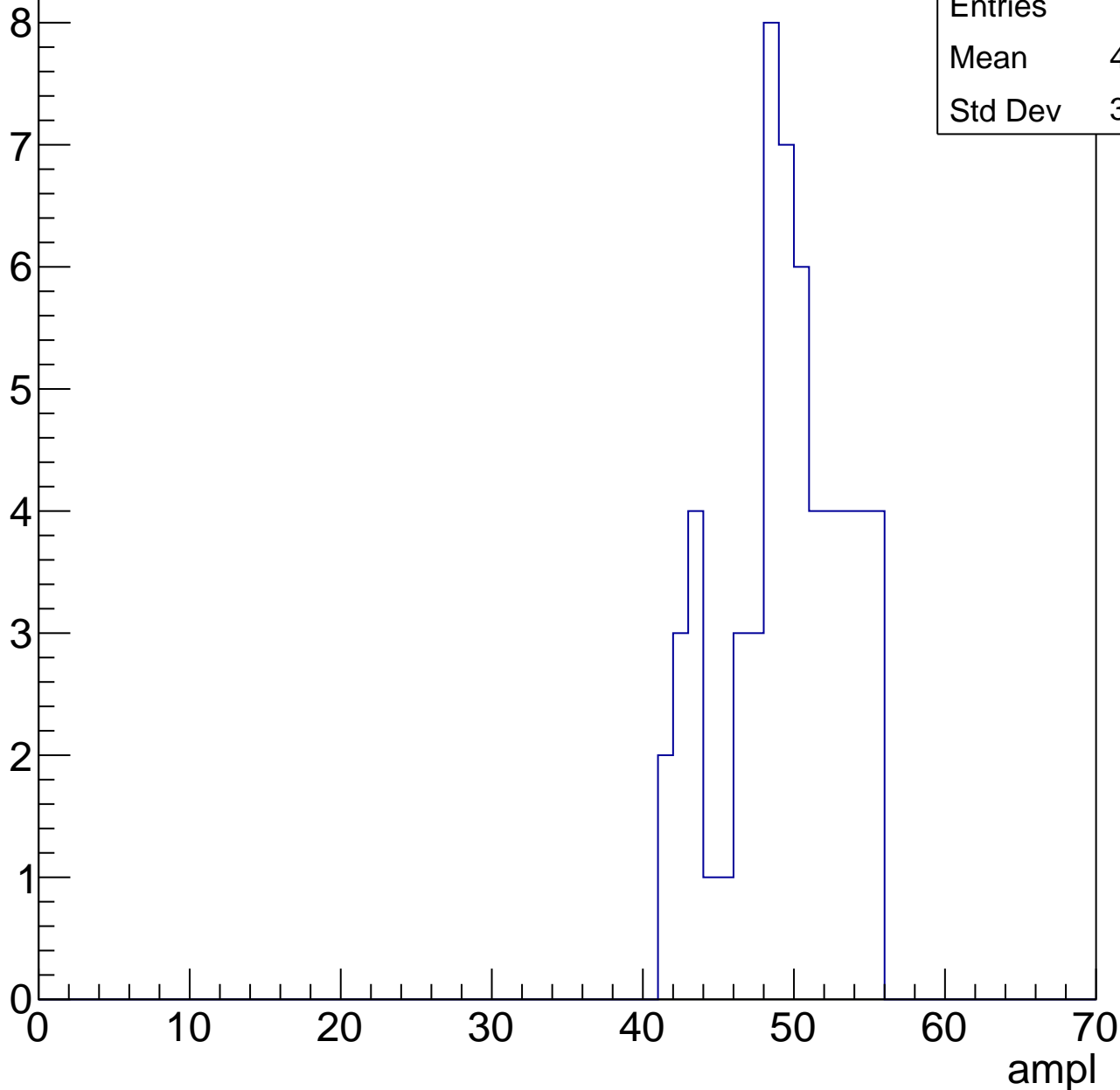


B1L103S, U12-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.88
Std Dev	3.878

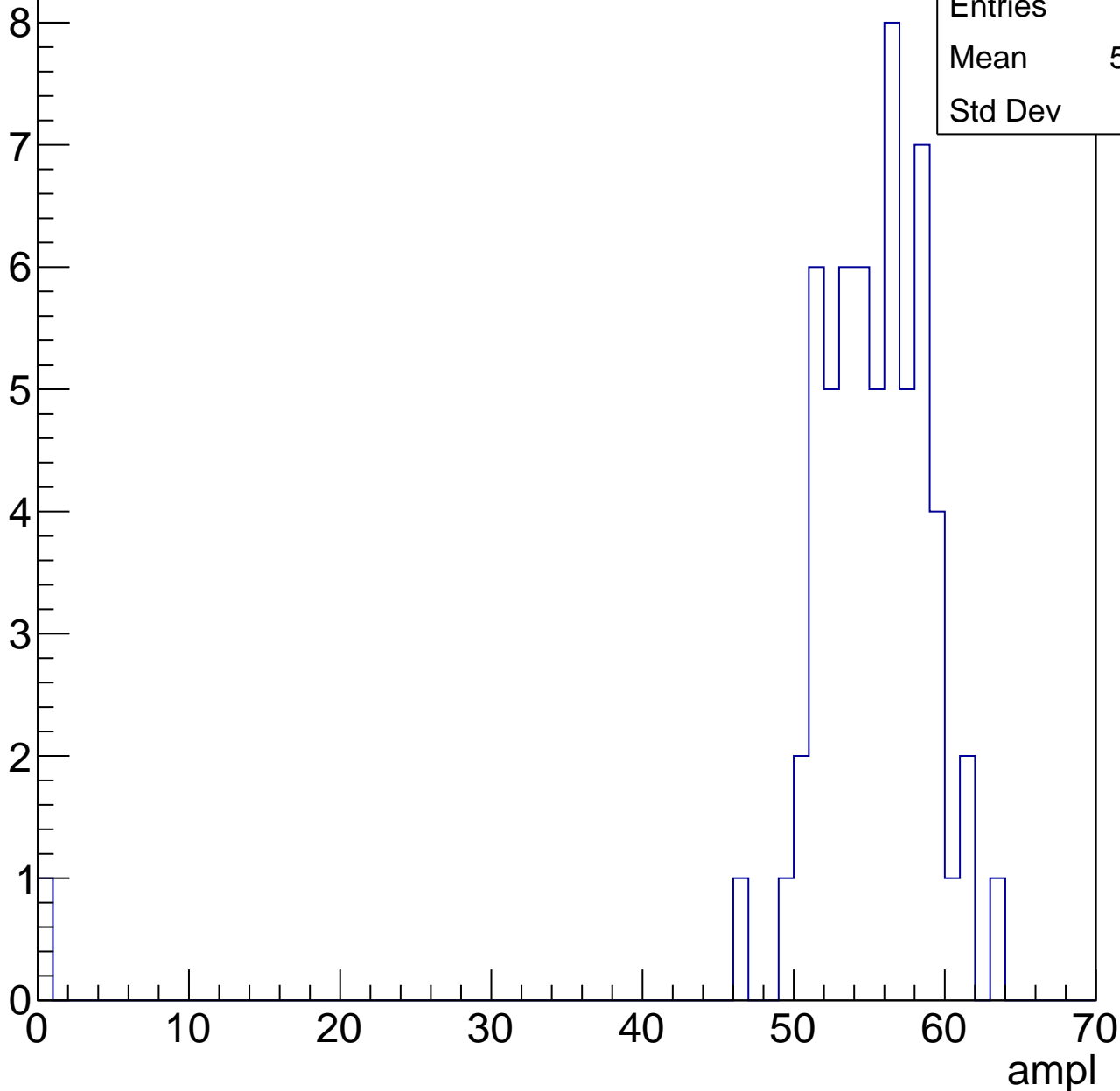


B1L103S, U12-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

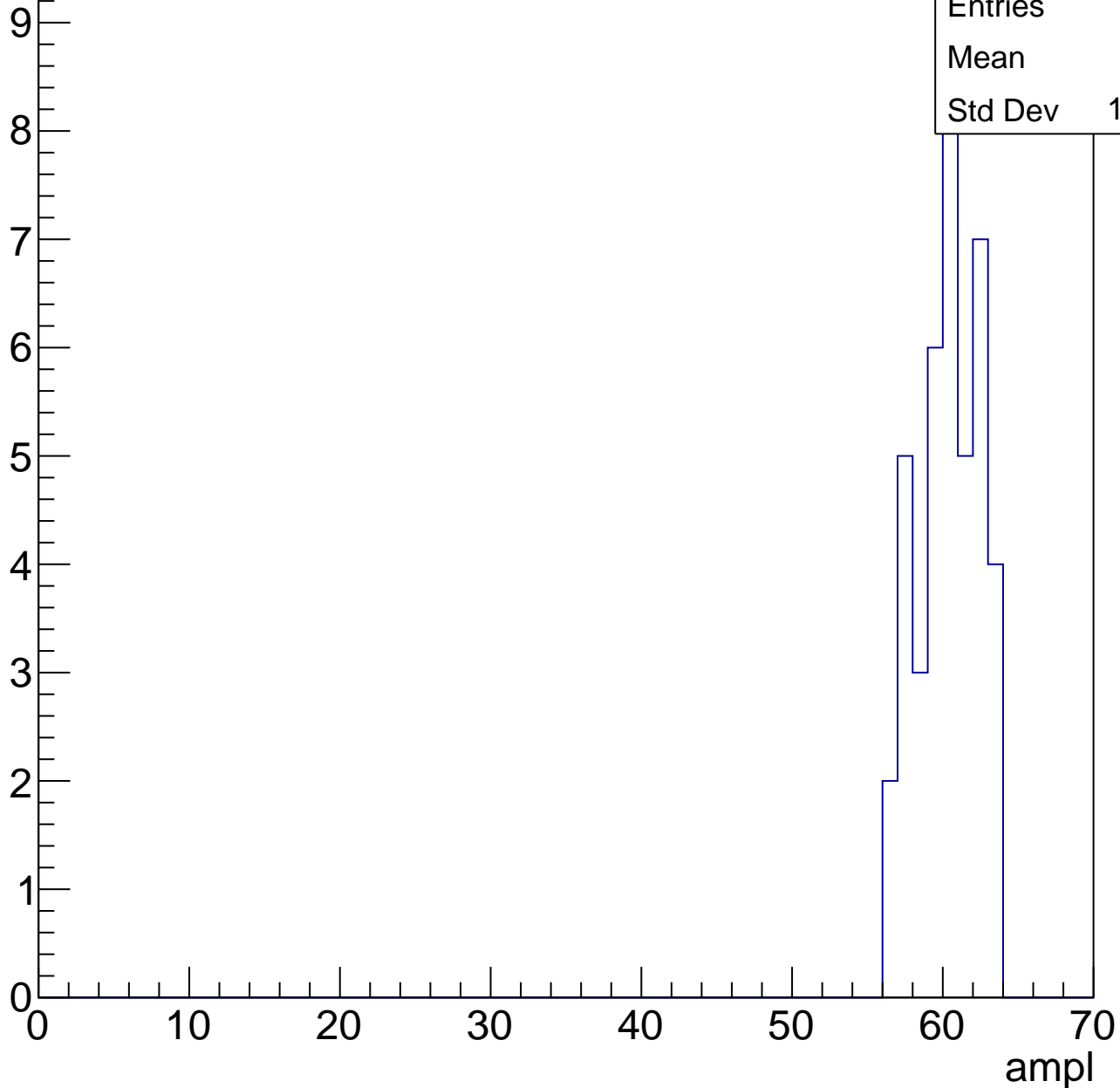
Entries	61
Mean	54.07
Std Dev	7.71



B1L103S, U12-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

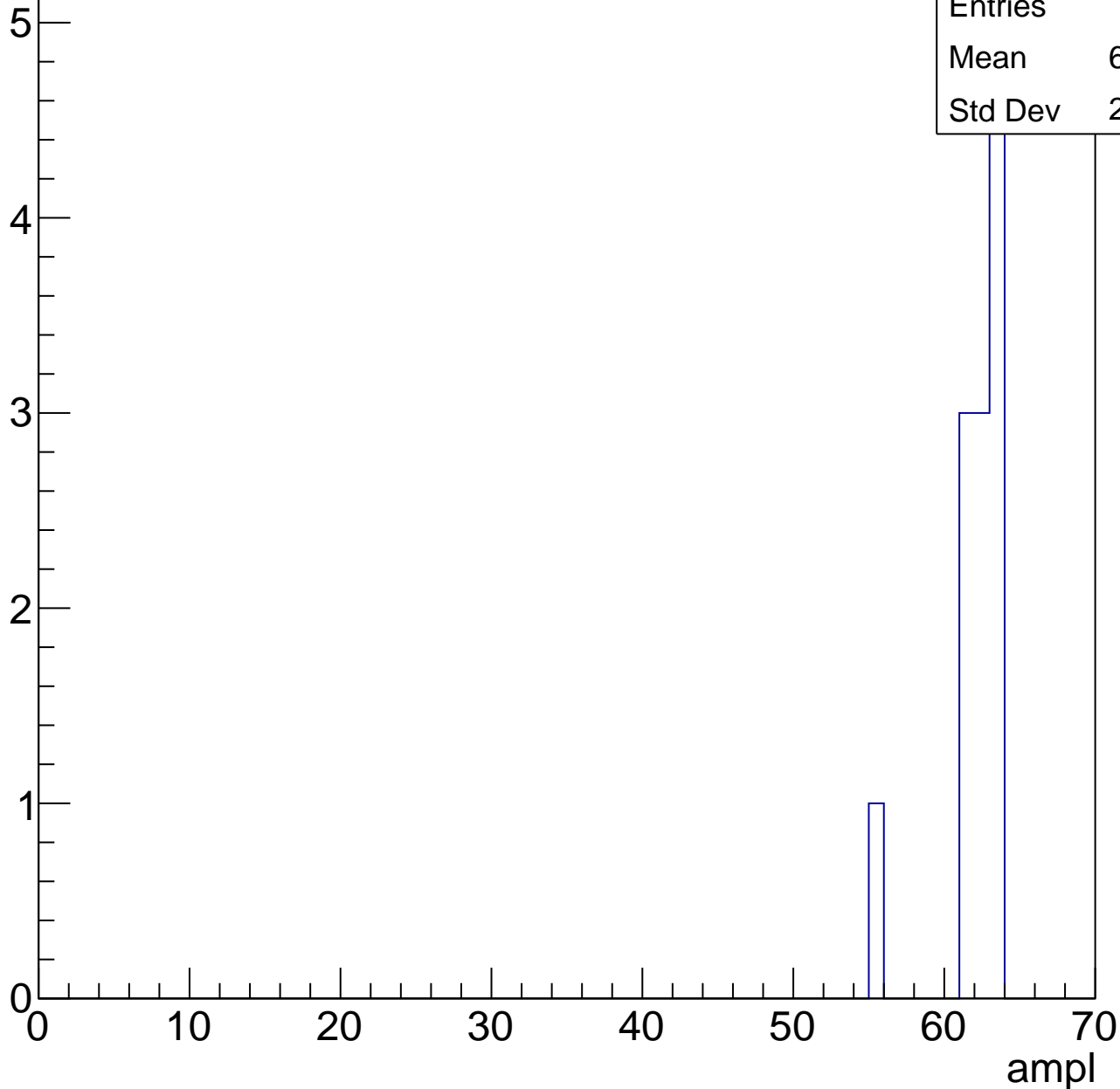


B1L103S, U12-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.58
Std Dev	2.139



B1L103S, U12-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch82, adc0

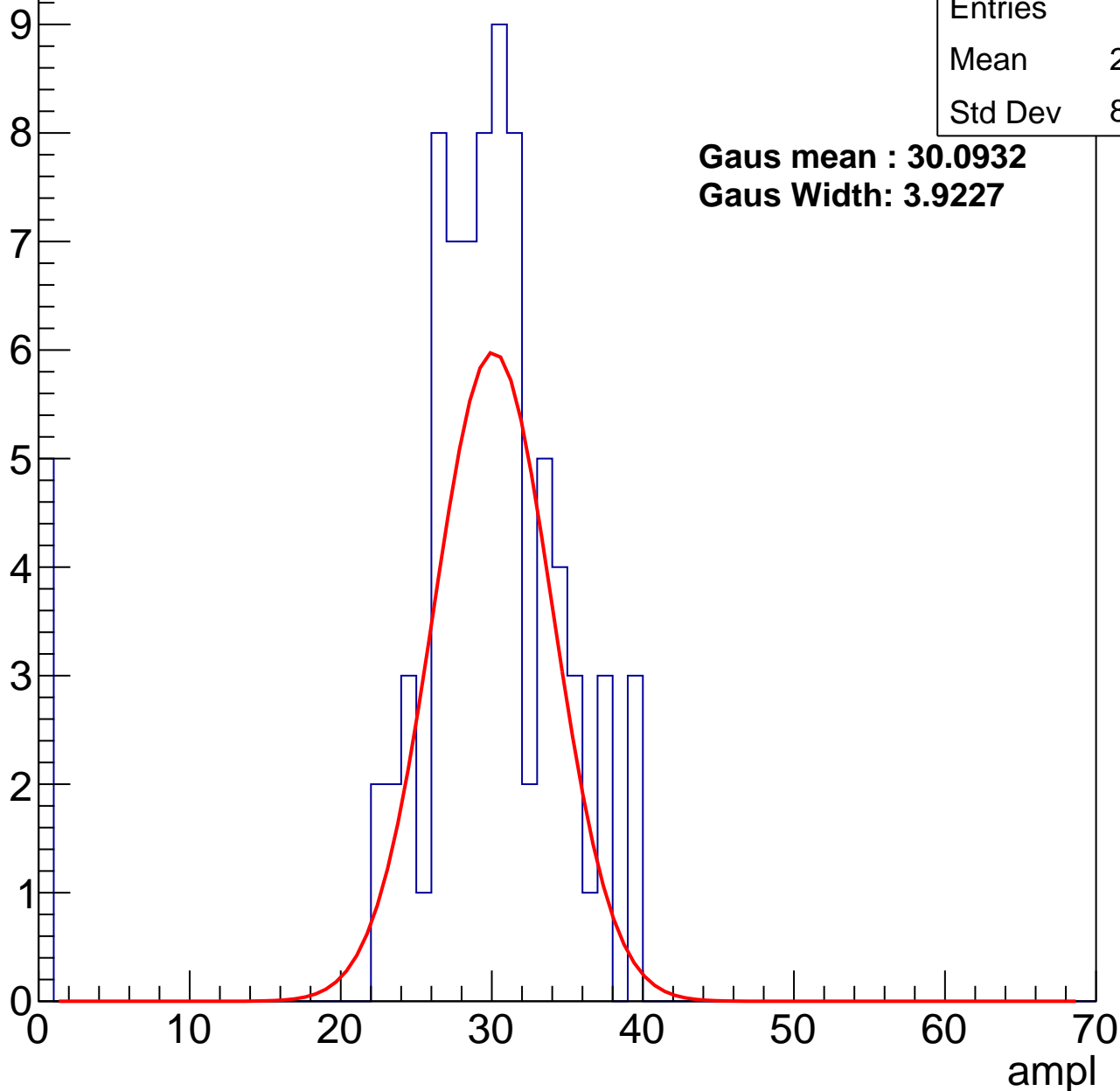
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	27.95
Std Dev	8.144

Gaus mean : 30.0932

Gaus Width: 3.9227



B1L103S, U12-ch82, adc1

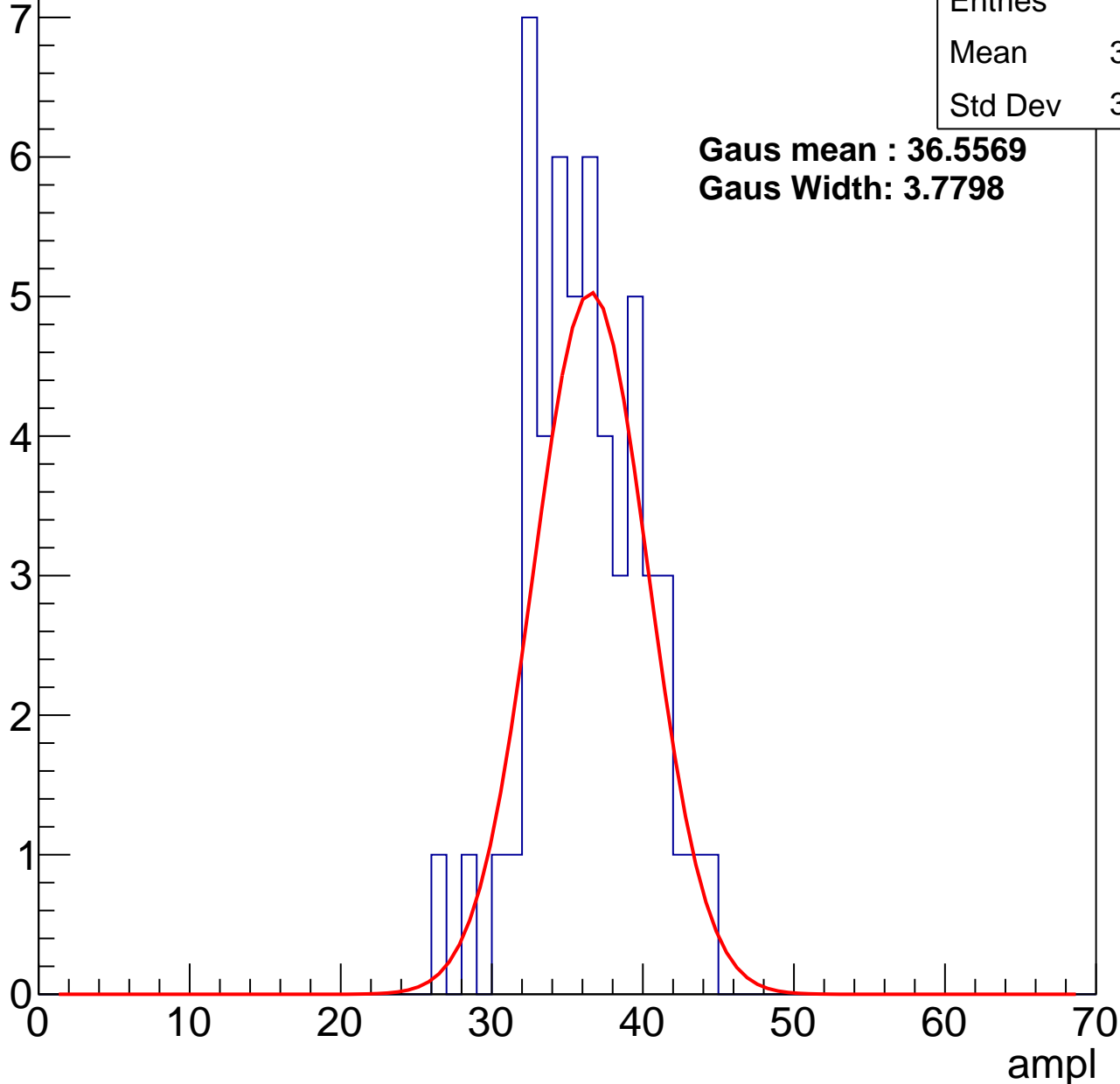
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	35.75
Std Dev	3.716

Gaus mean : 36.5569

Gaus Width: 3.7798



B1L103S, U12-ch82, adc2

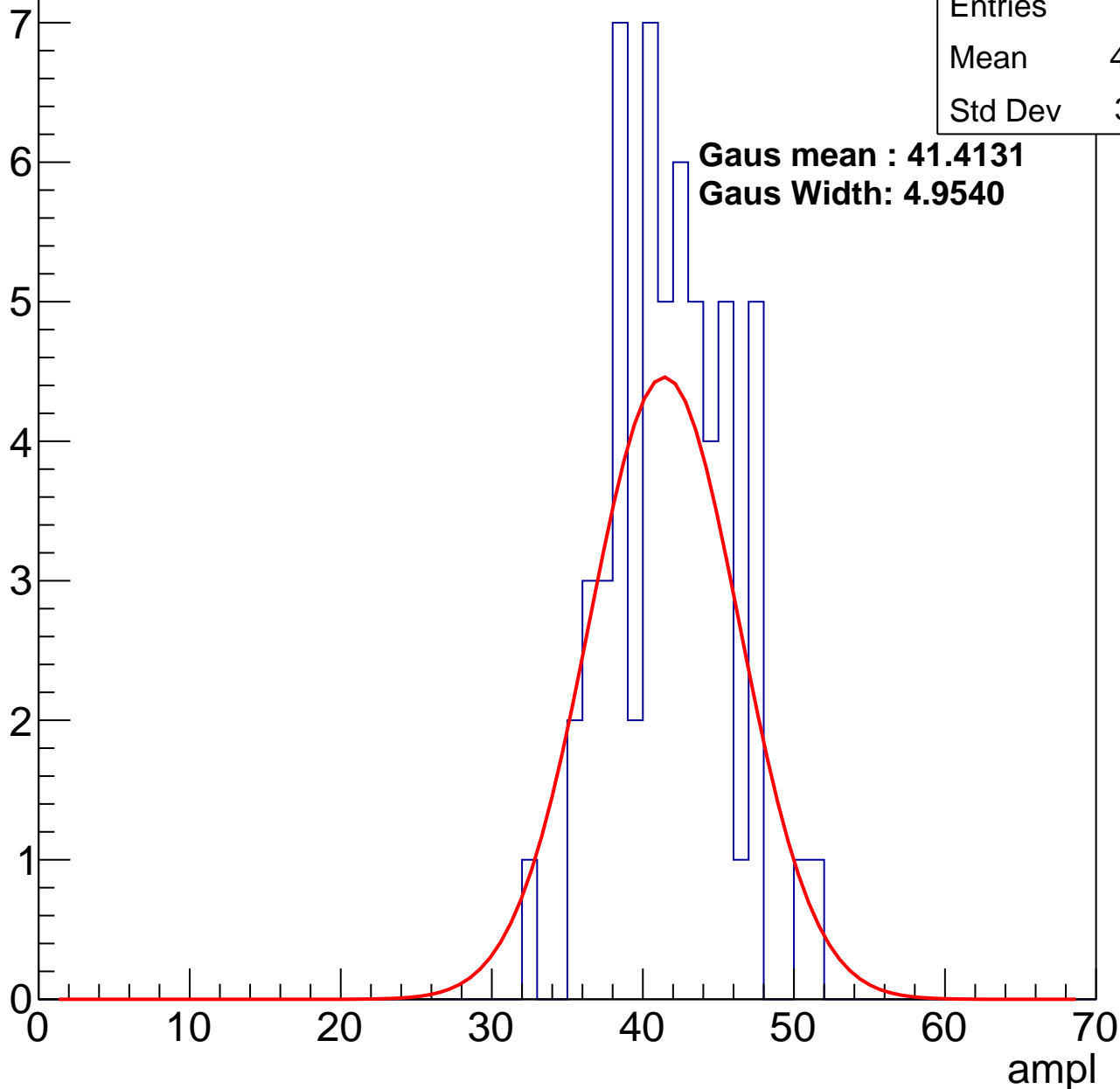
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.38
Std Dev	3.881

Gaus mean : 41.4131

Gaus Width: 4.9540



B1L103S, U12-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

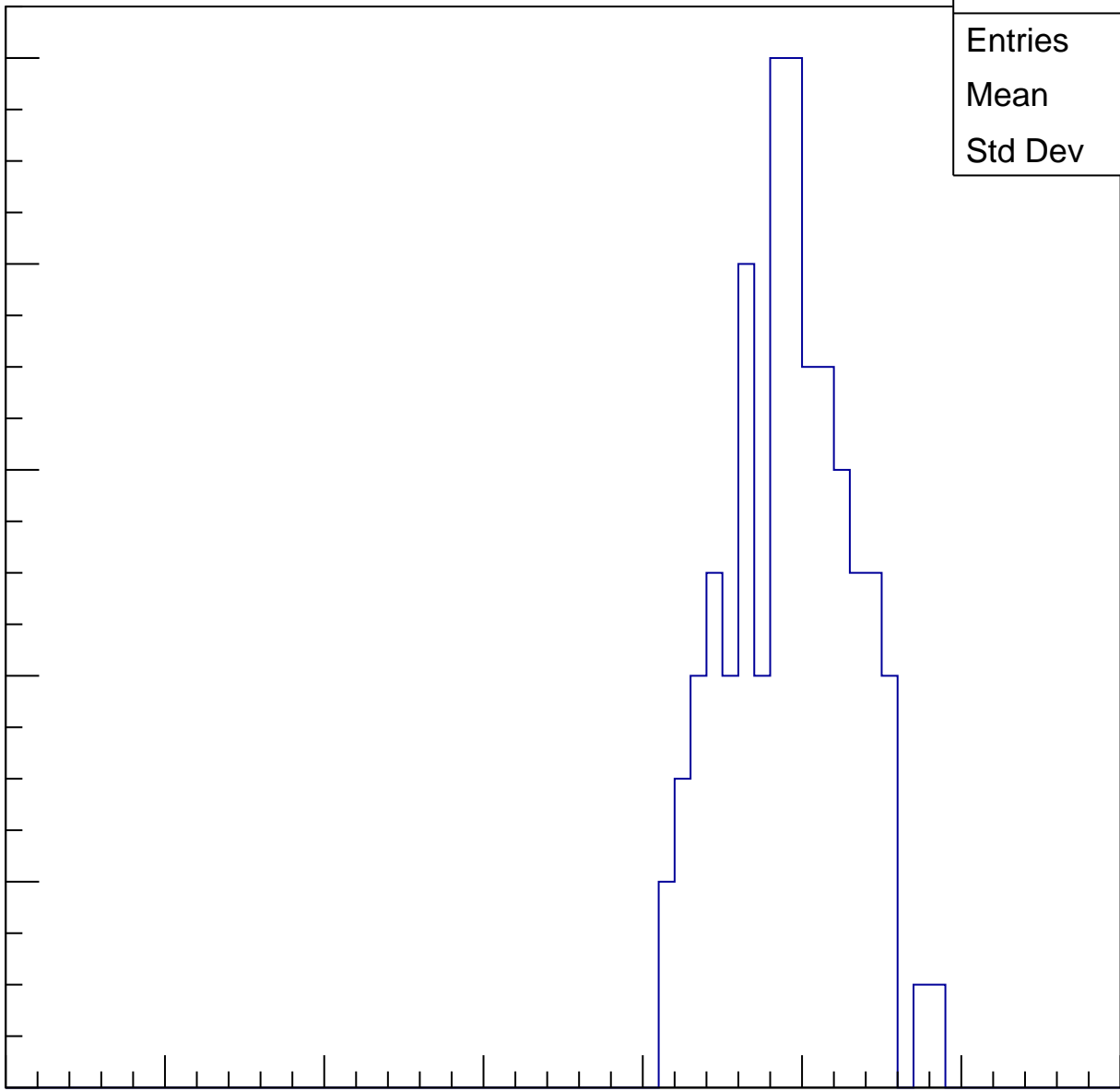
Entries	86
Mean	48.78
Std Dev	3.865

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

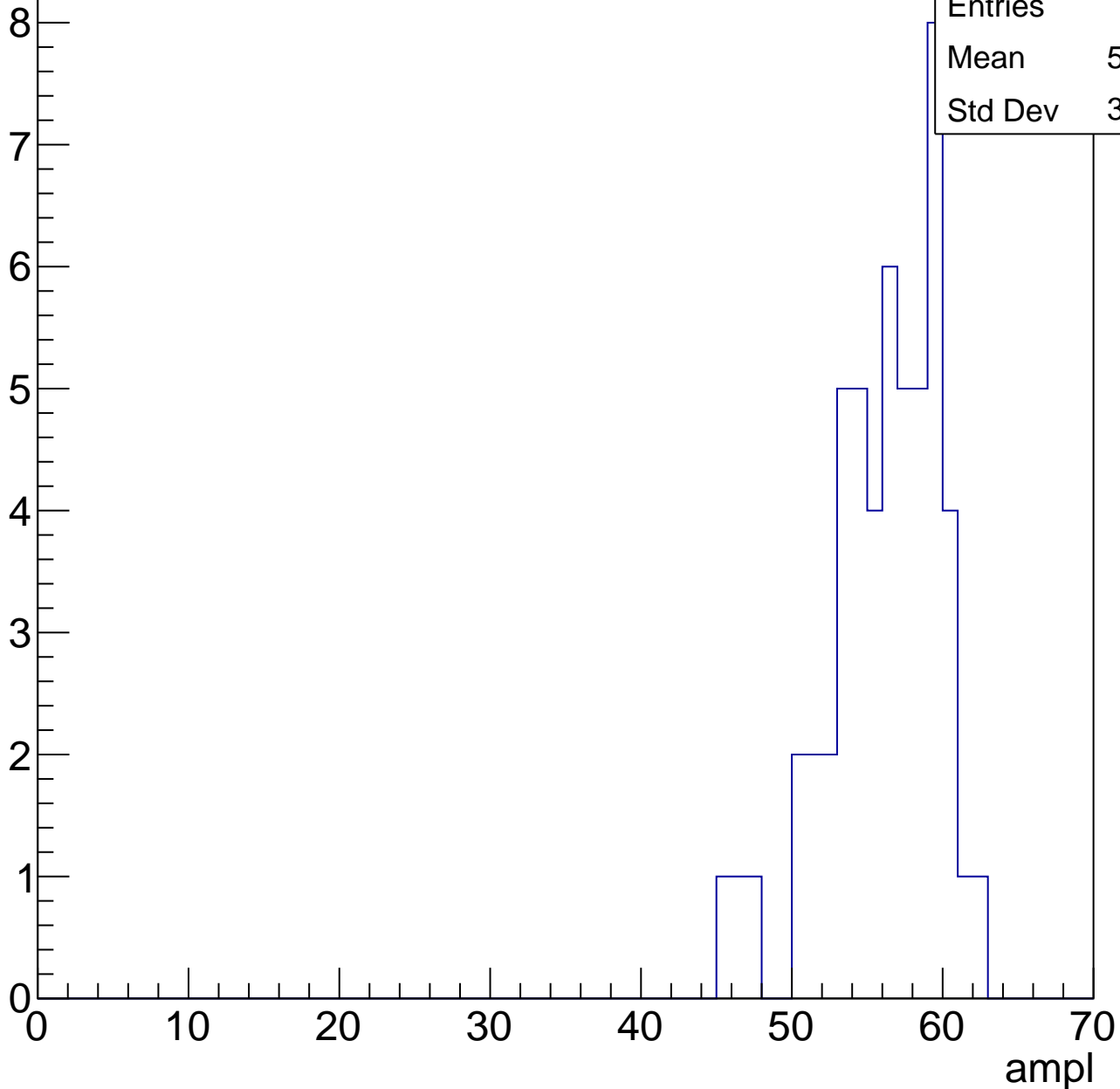


B1L103S, U12-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.57
Std Dev	3.729

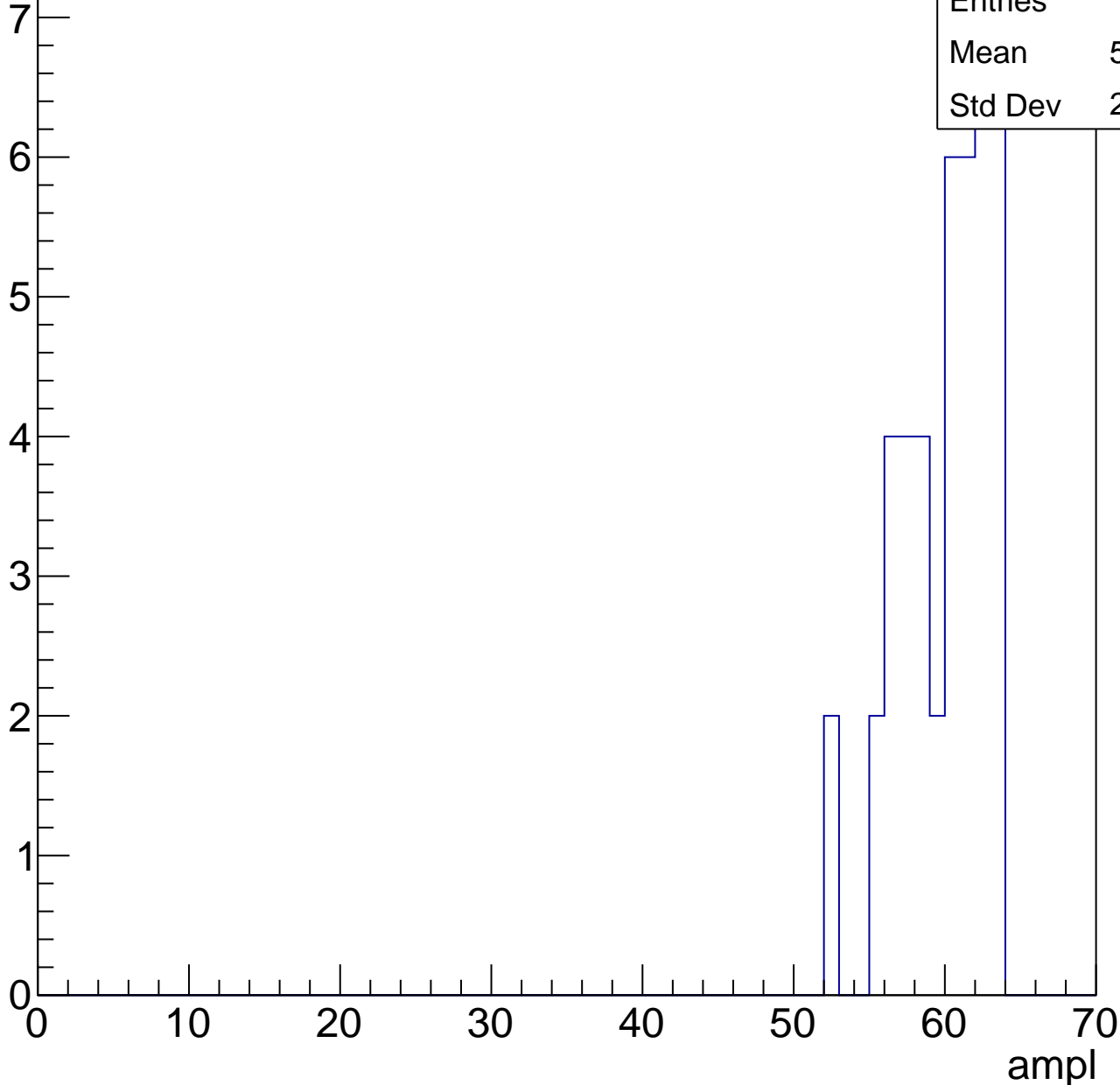


B1L103S, U12-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

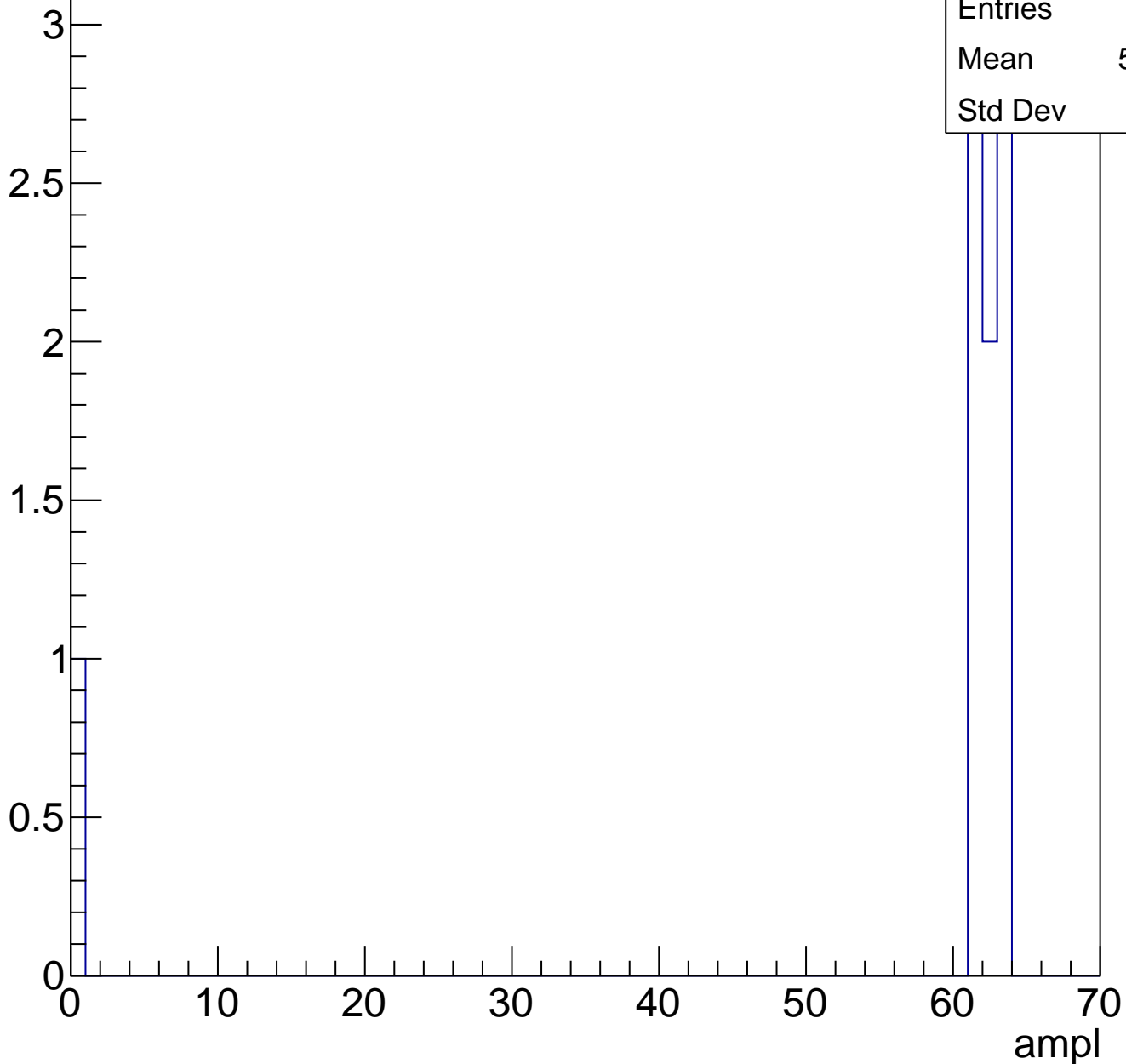
Entries	44
Mean	59.48
Std Dev	2.943



B1L103S, U12-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch83, adc0

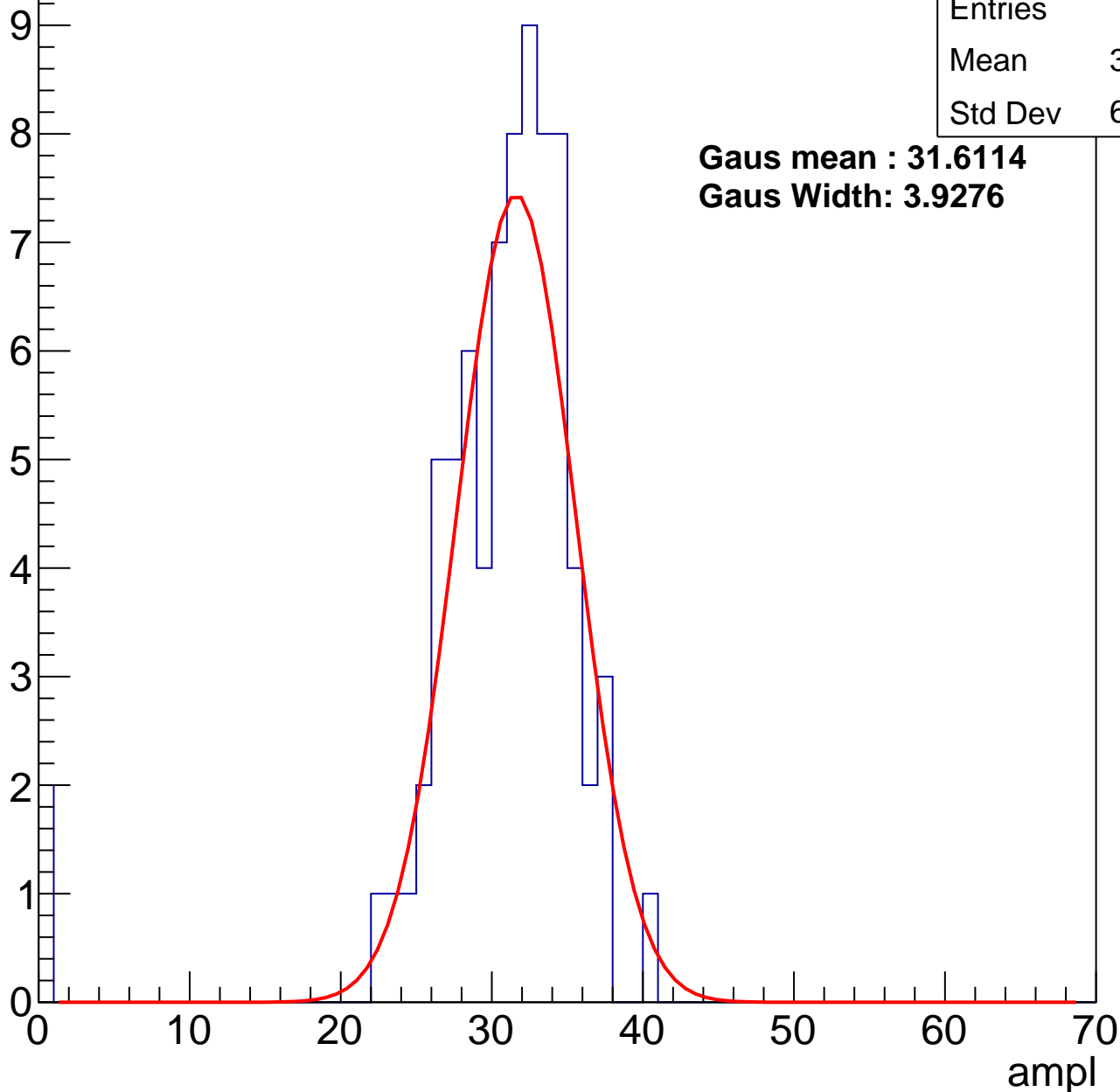
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	30.04
Std Dev	6.046

Gaus mean : 31.6114

Gaus Width: 3.9276



B1L103S, U12-ch83, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	38.91
Std Dev	3.739

Gaus mean : 39.6140

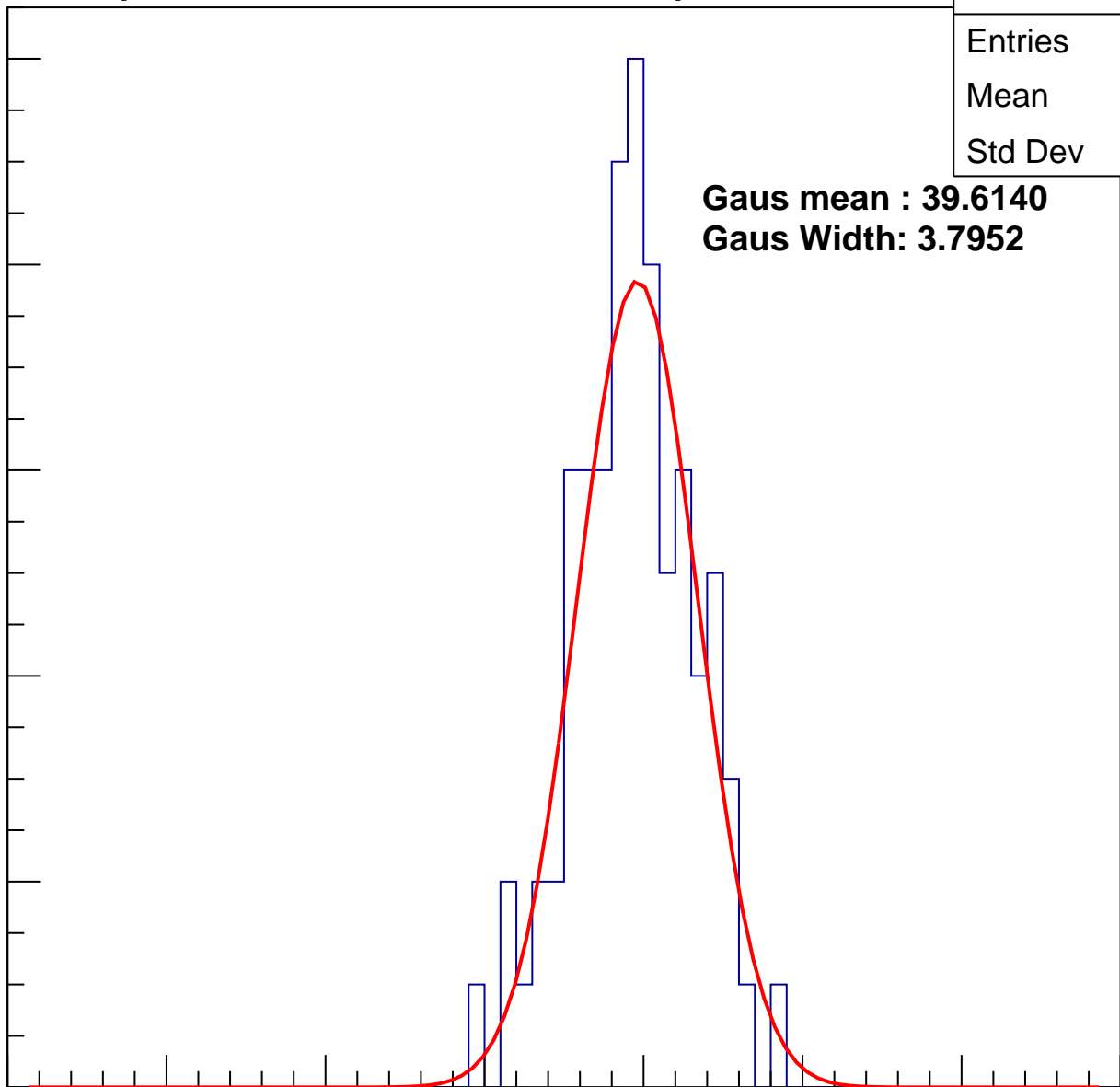
Gaus Width: 3.7952

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch83, adc2

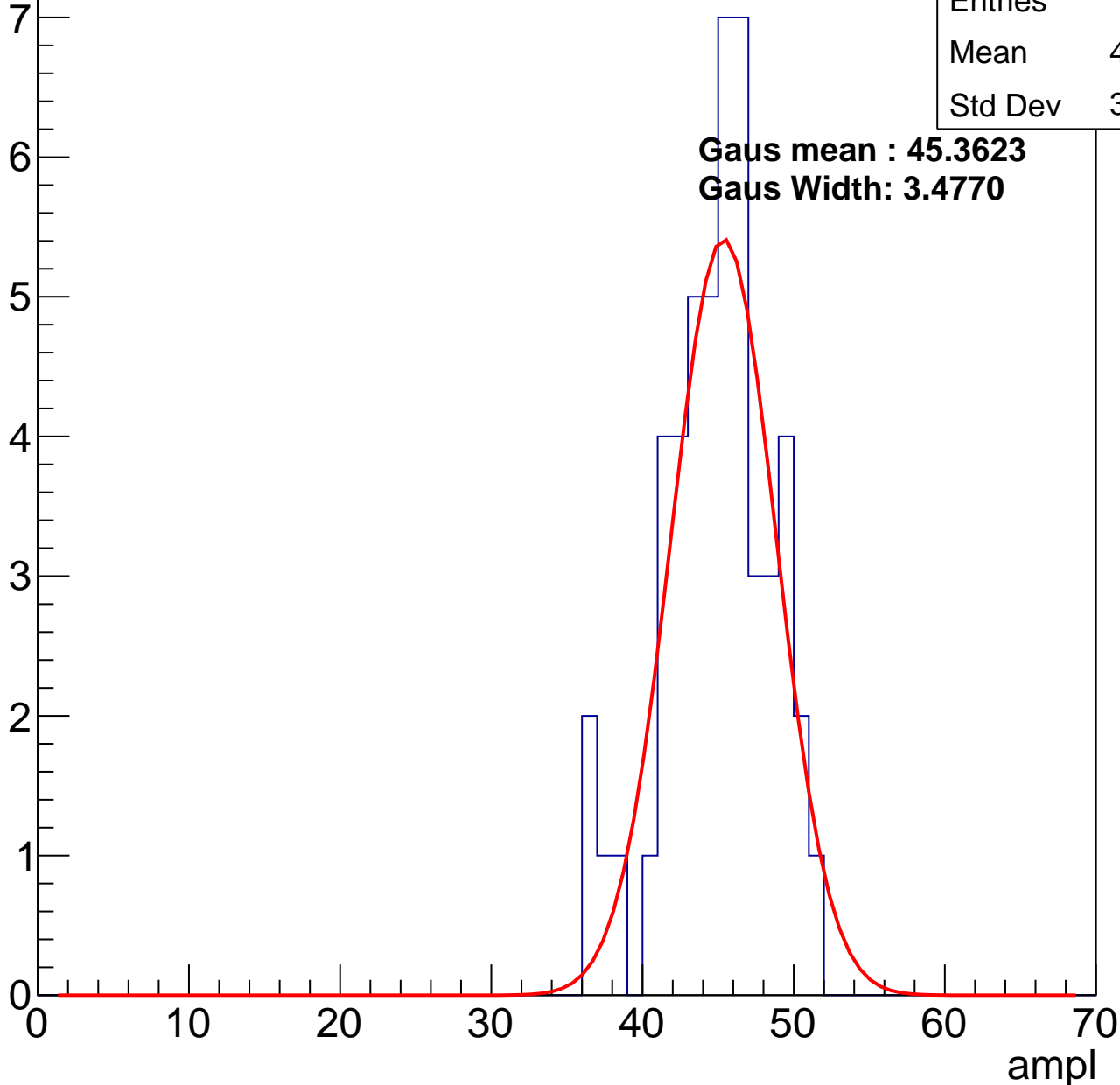
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	44.46
Std Dev	3.483

Gaus mean : 45.3623

Gaus Width: 3.4770

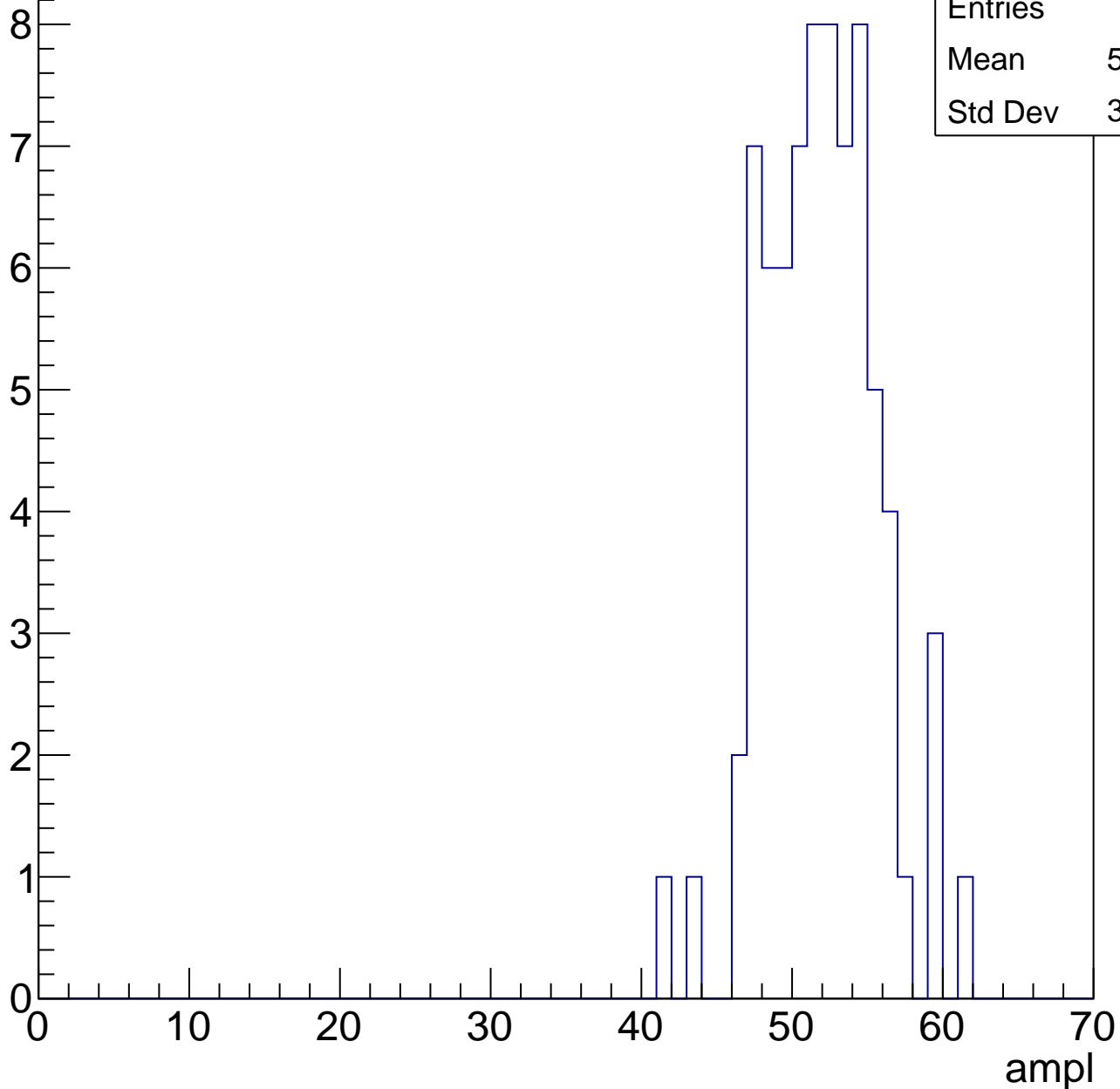


B1L103S, U12-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	51.44
Std Dev	3.674

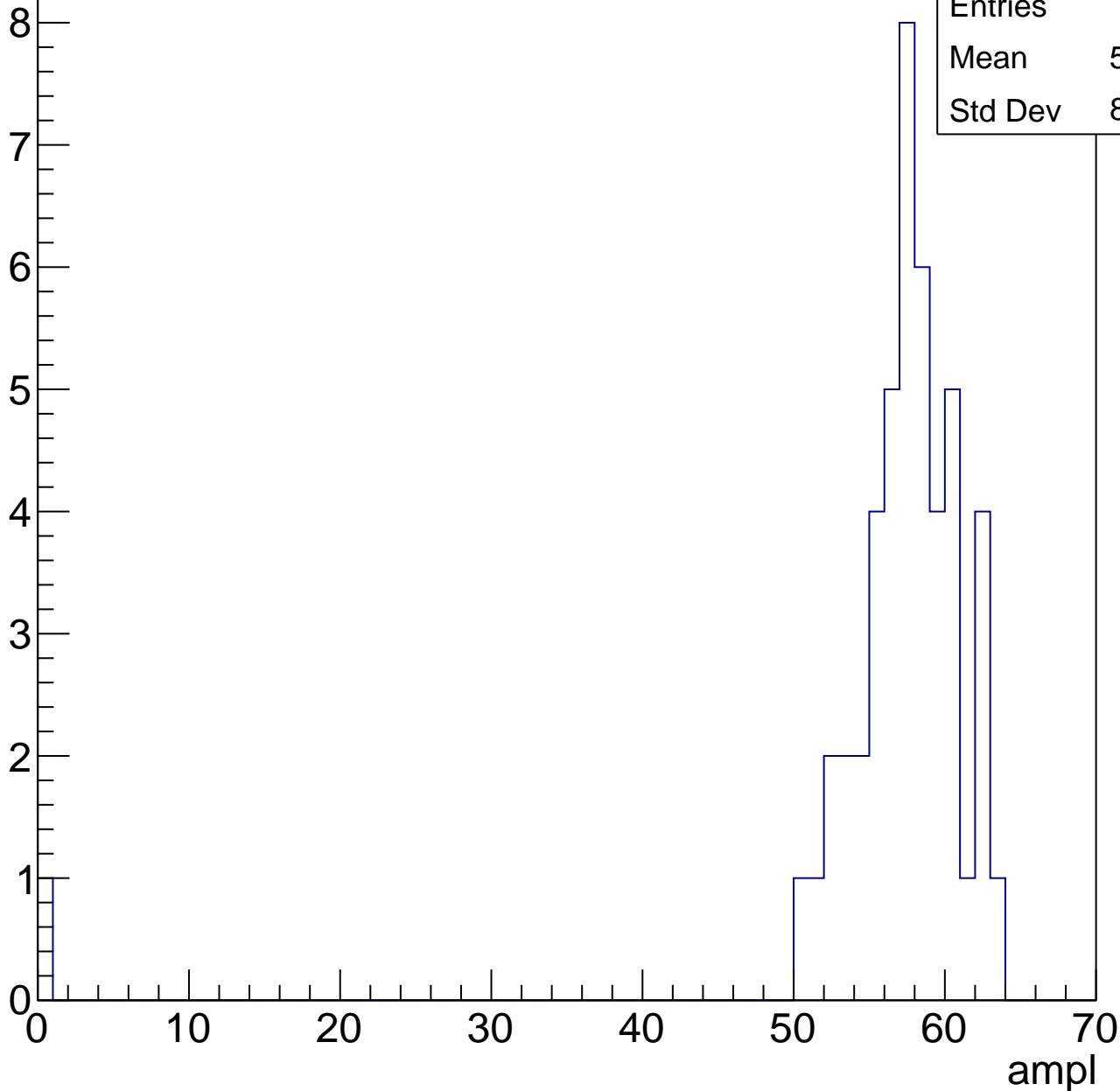


B1L103S, U12-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	55.98
Std Dev	8.782

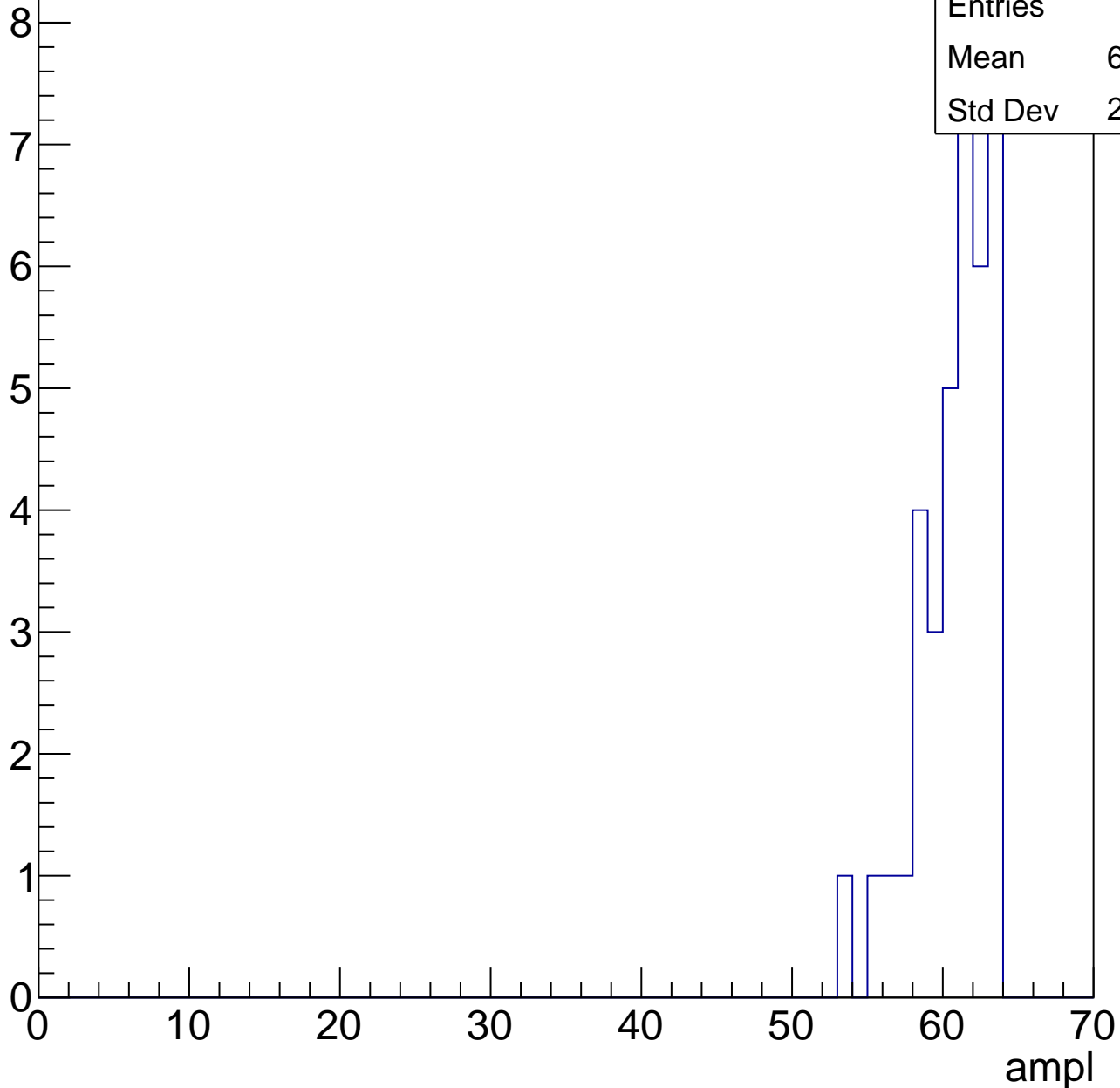


B1L103S, U12-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	60.37
Std Dev	2.389



B1L103S, U12-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch84, adc0

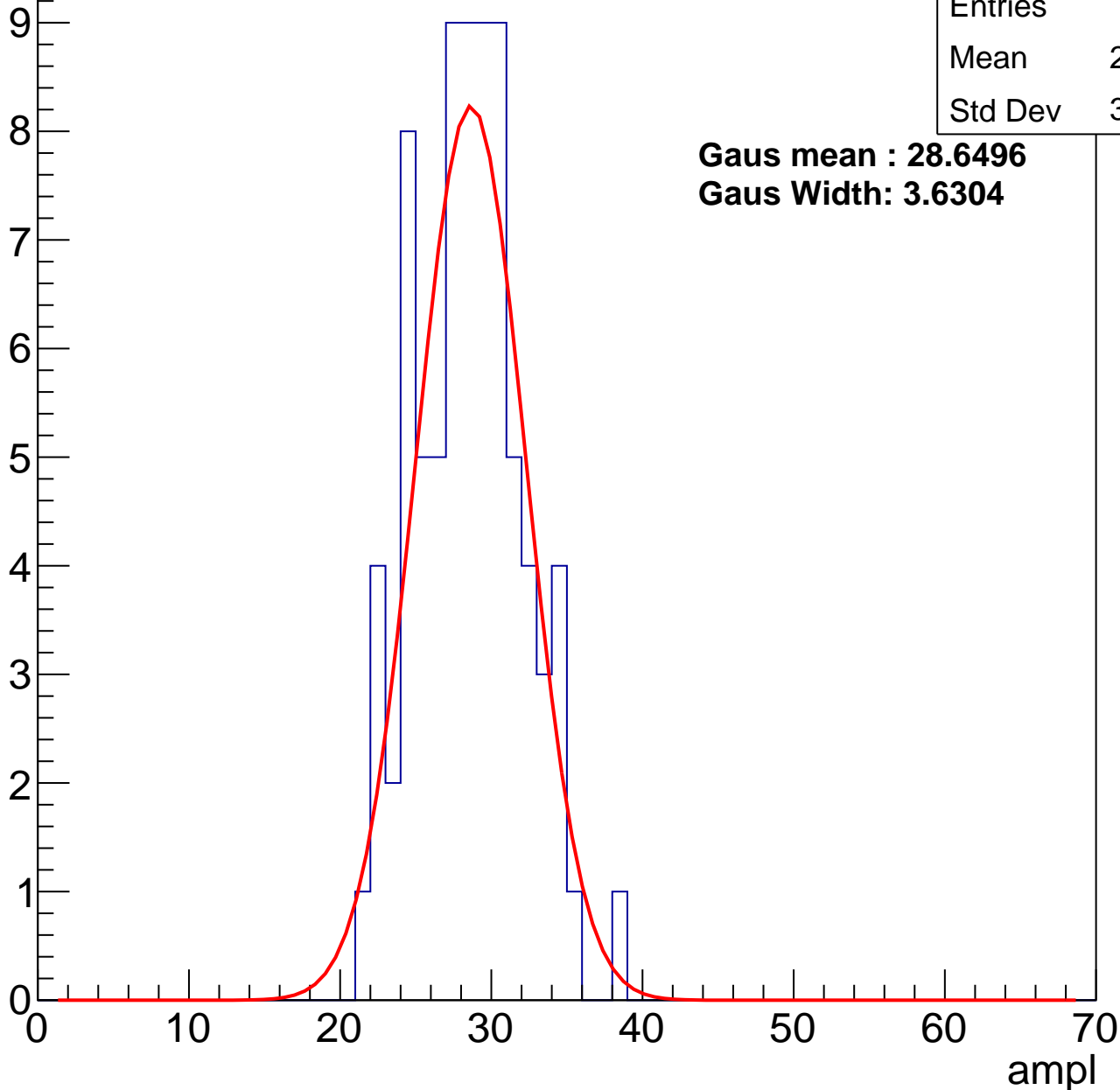
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	28.09
Std Dev	3.498

Gaus mean : 28.6496

Gaus Width: 3.6304



B1L103S, U12-ch84, adc1

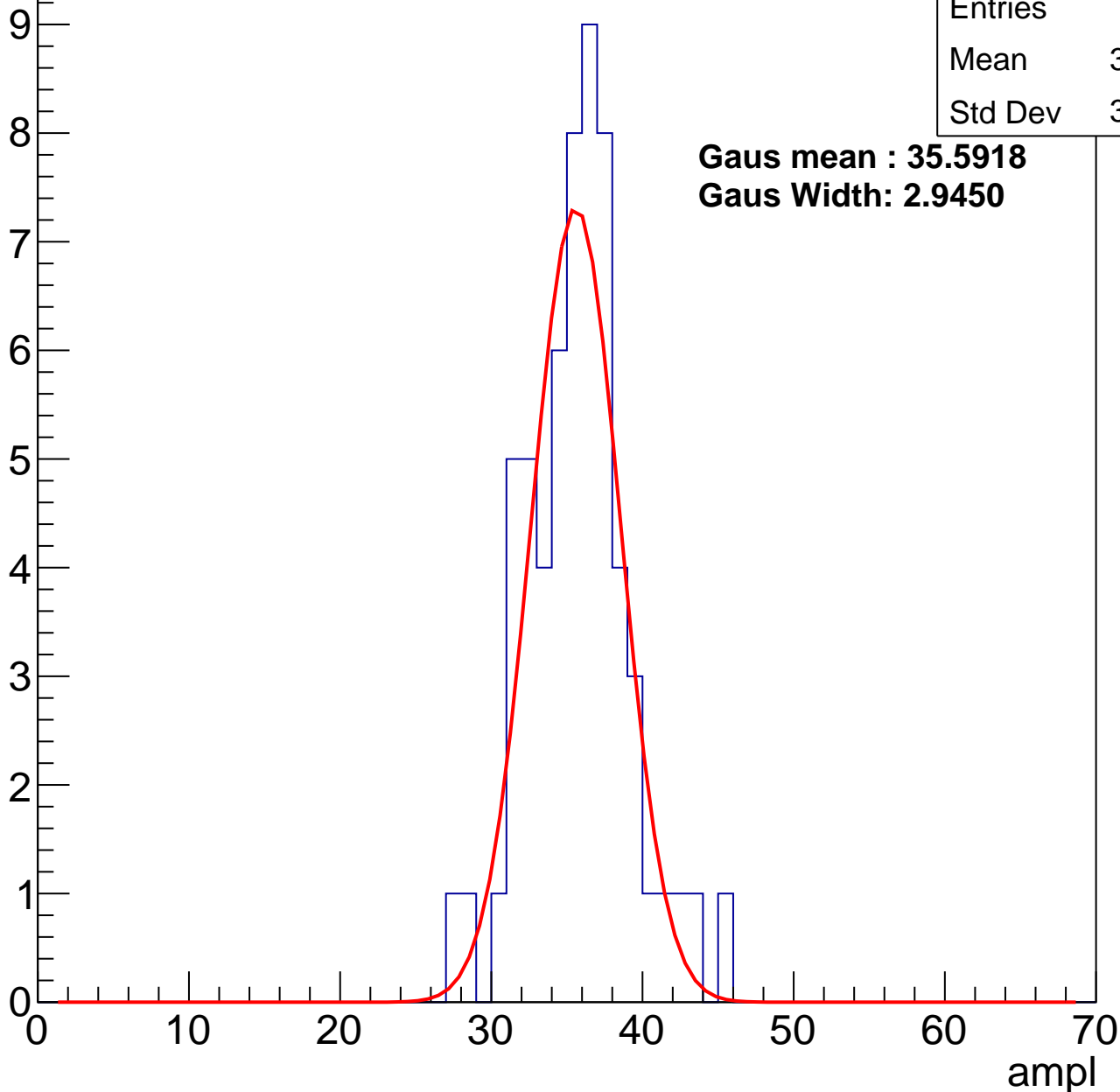
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.27
Std Dev	3.366

Gaus mean : 35.5918

Gaus Width: 2.9450



B1L103S, U12-ch84, adc2

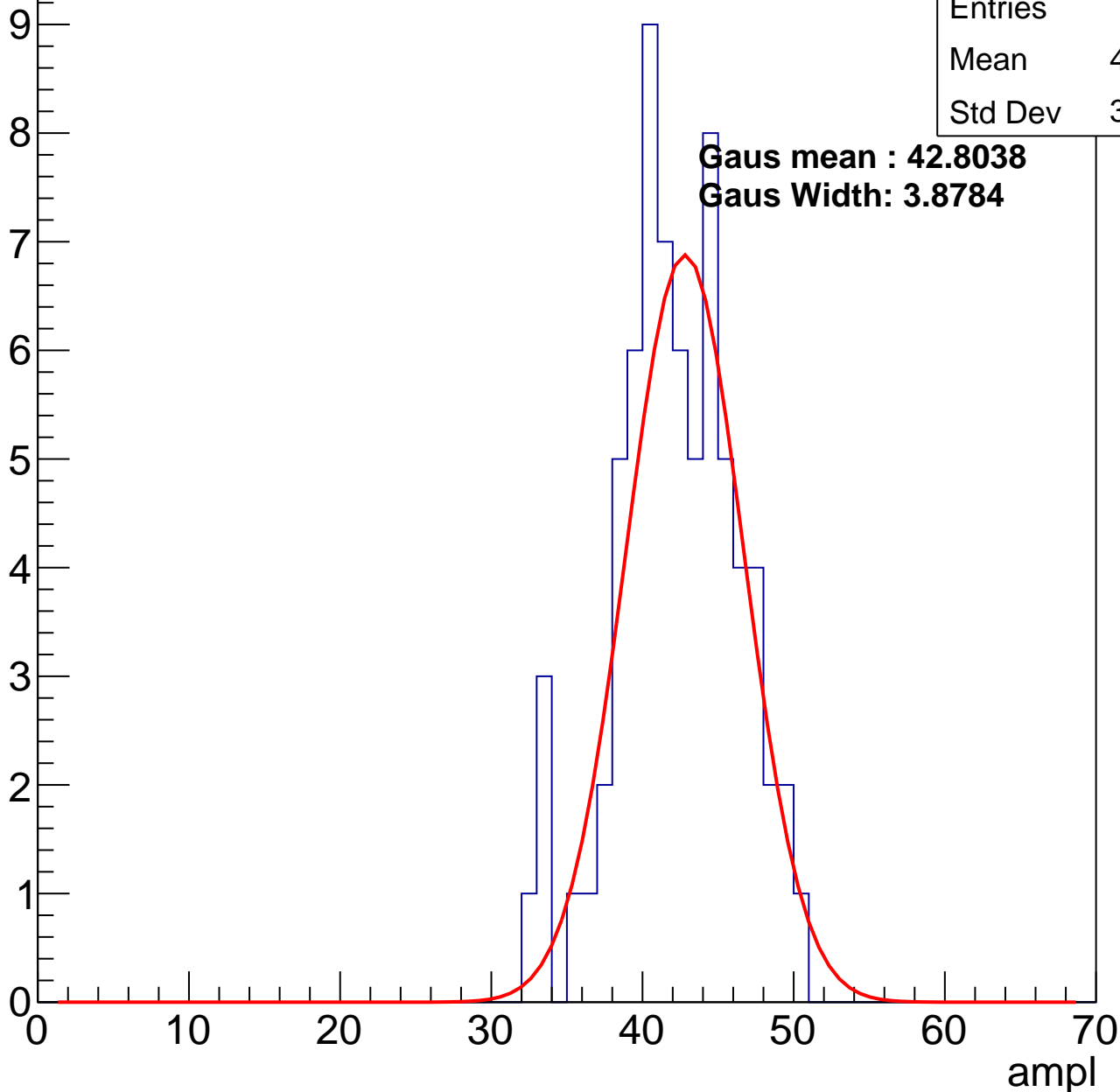
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.76
Std Dev	3.963

Gaus mean : 42.8038

Gaus Width: 3.8784

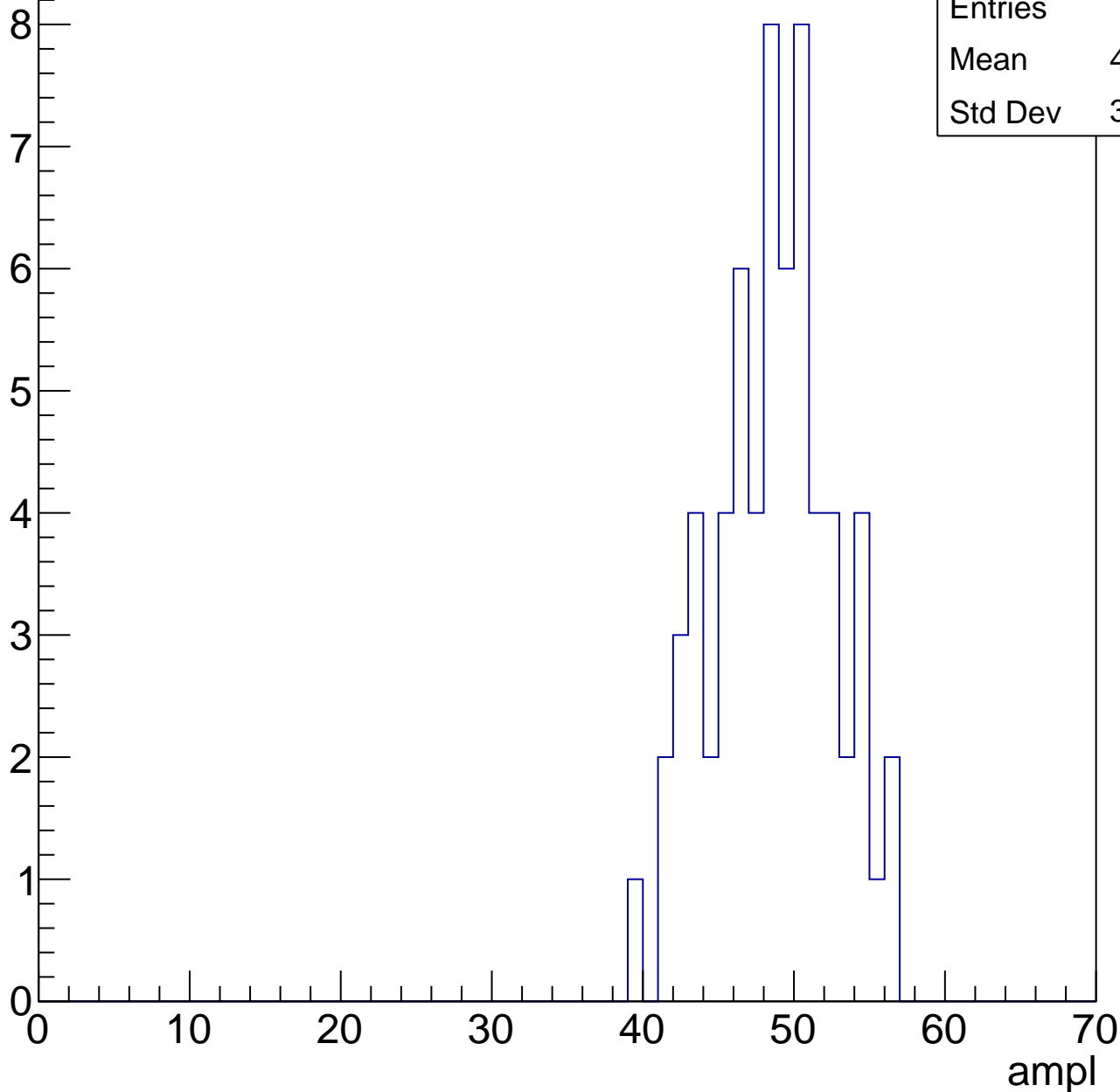


B1L103S, U12-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.15
Std Dev	3.904

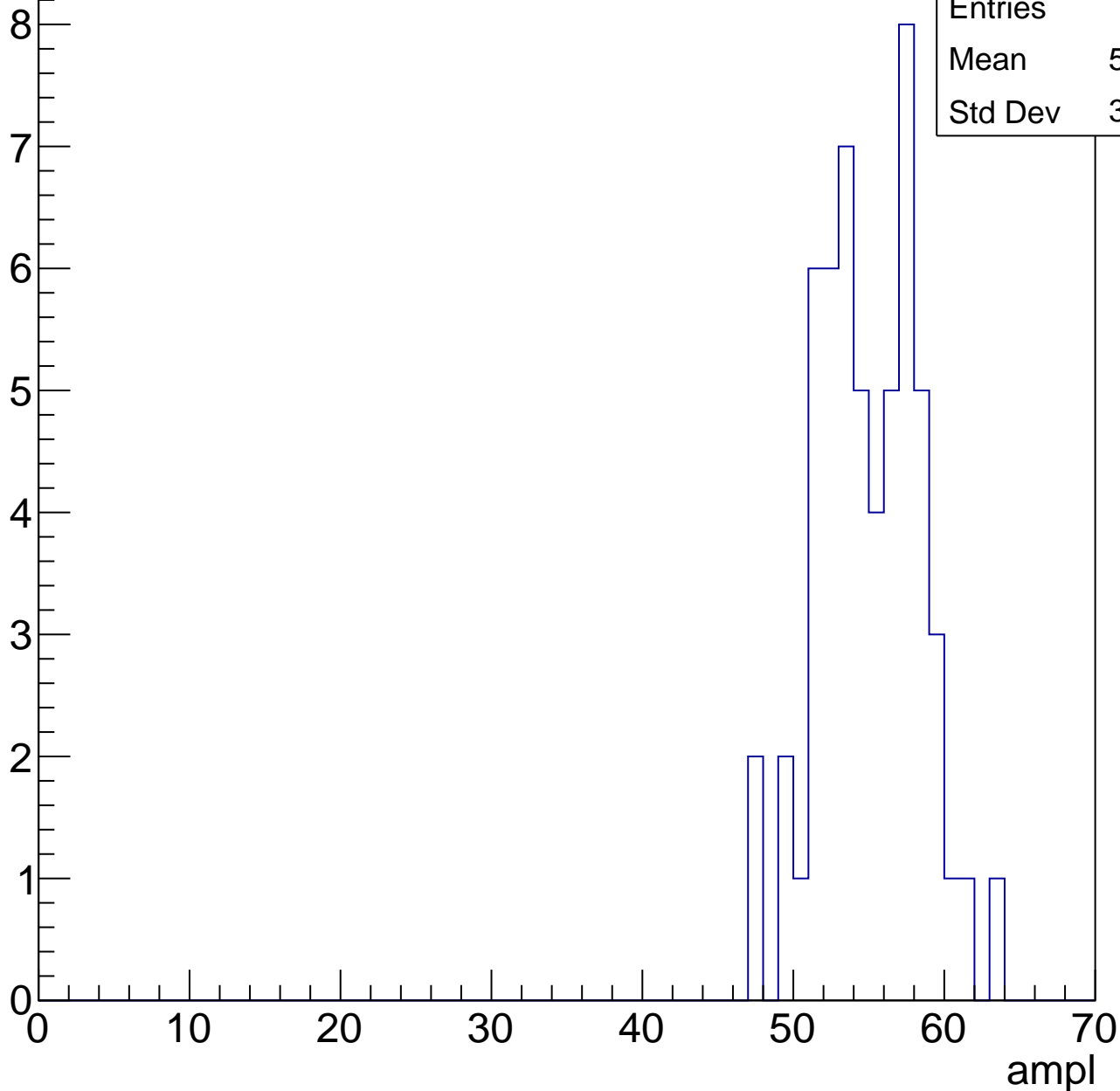


B1L103S, U12-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	54.53
Std Dev	3.377

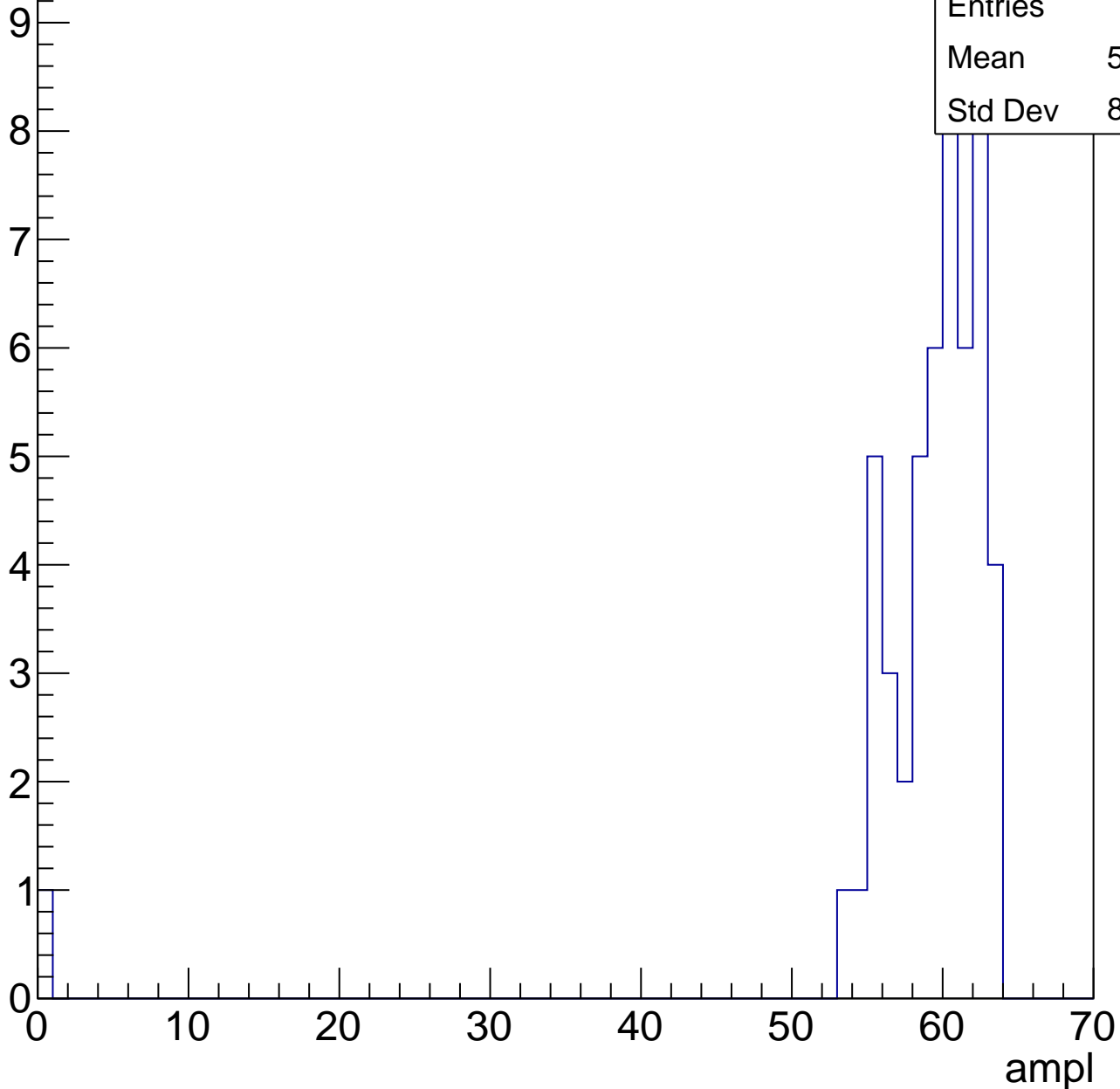


B1L103S, U12-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

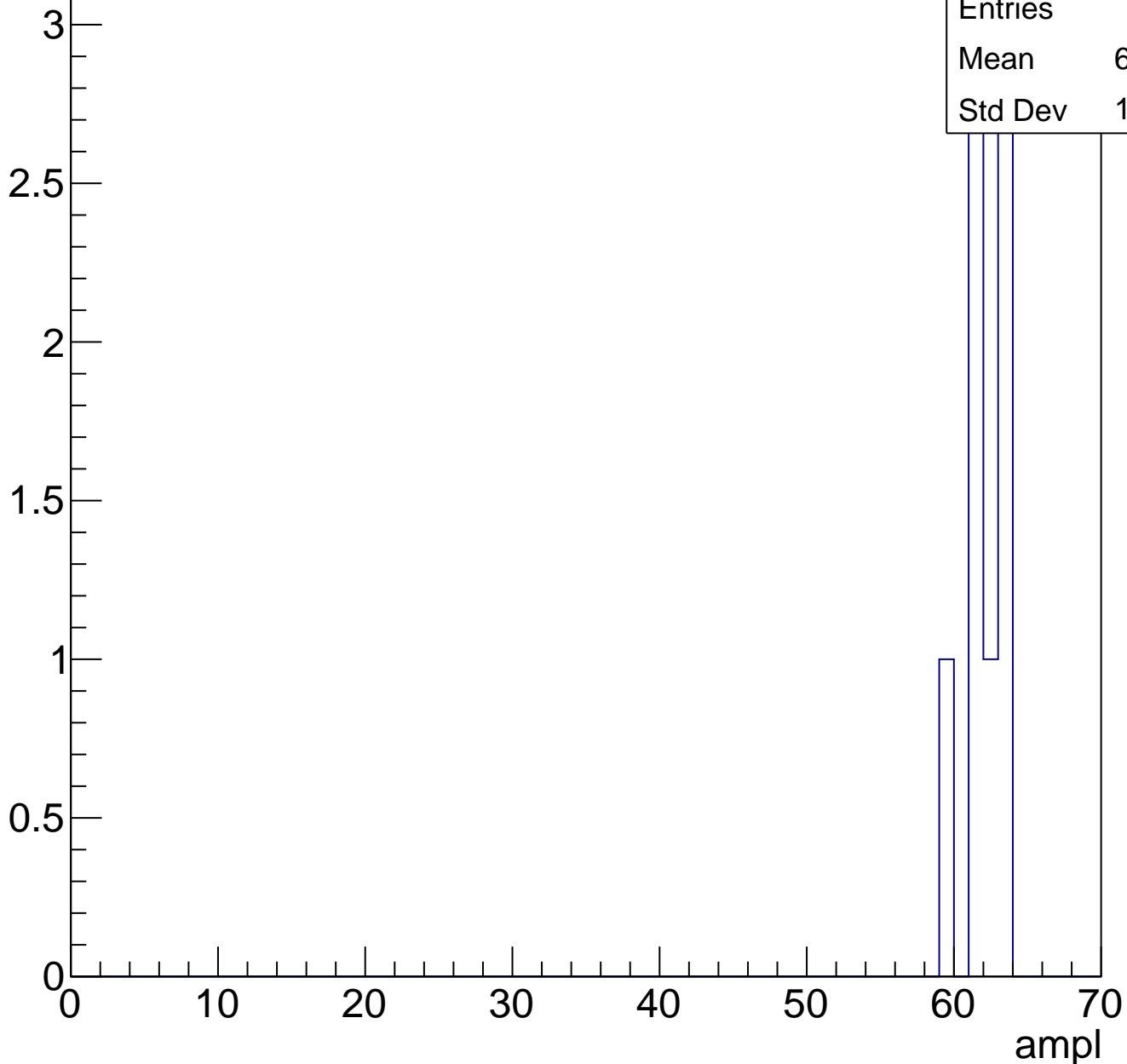
Entries	52
Mean	58.15
Std Dev	8.552



B1L103S, U12-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch85, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	30.26
Std Dev	3.955

Gaus mean : 31.9037

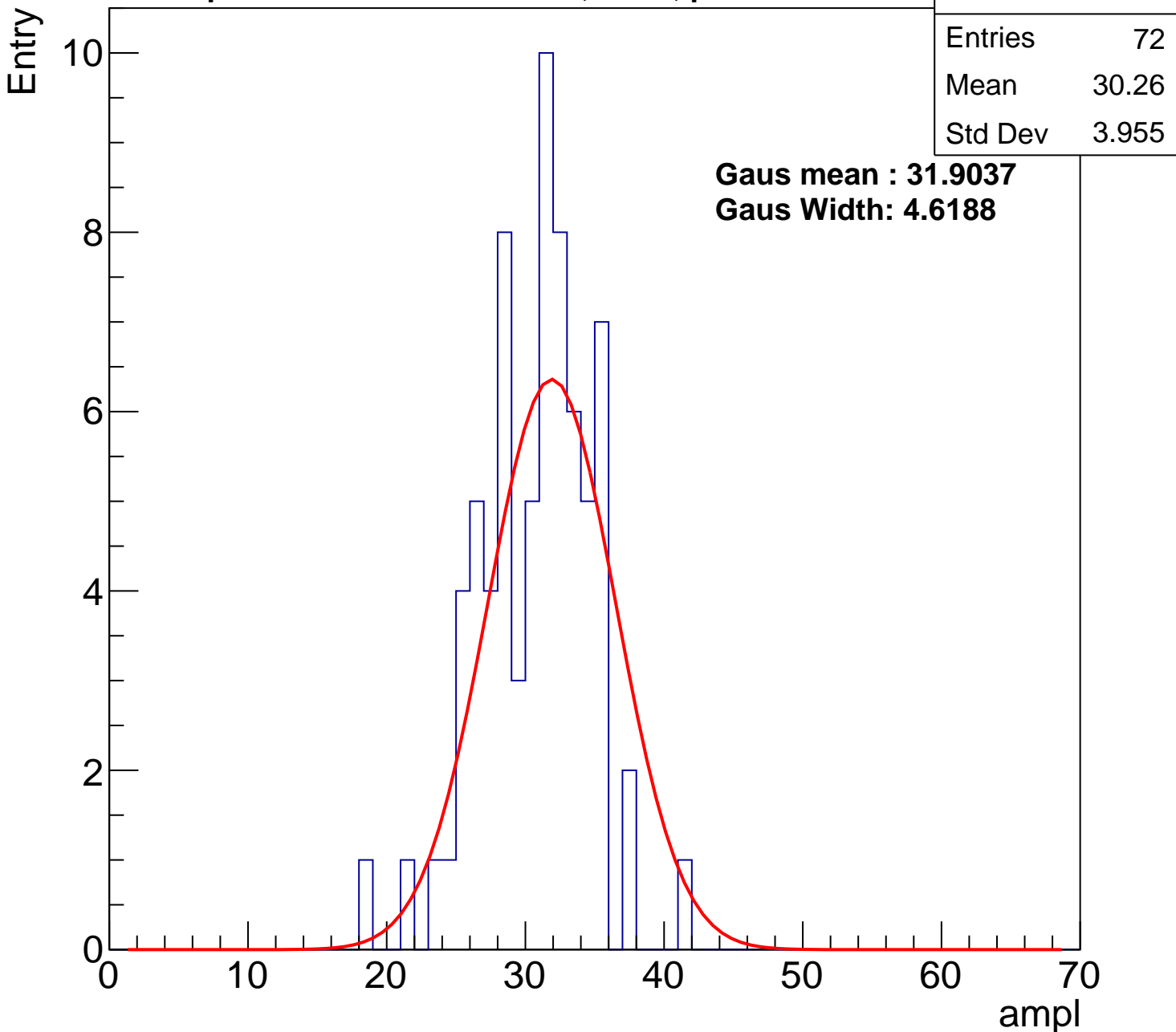
Gaus Width: 4.6188

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch85, adc1

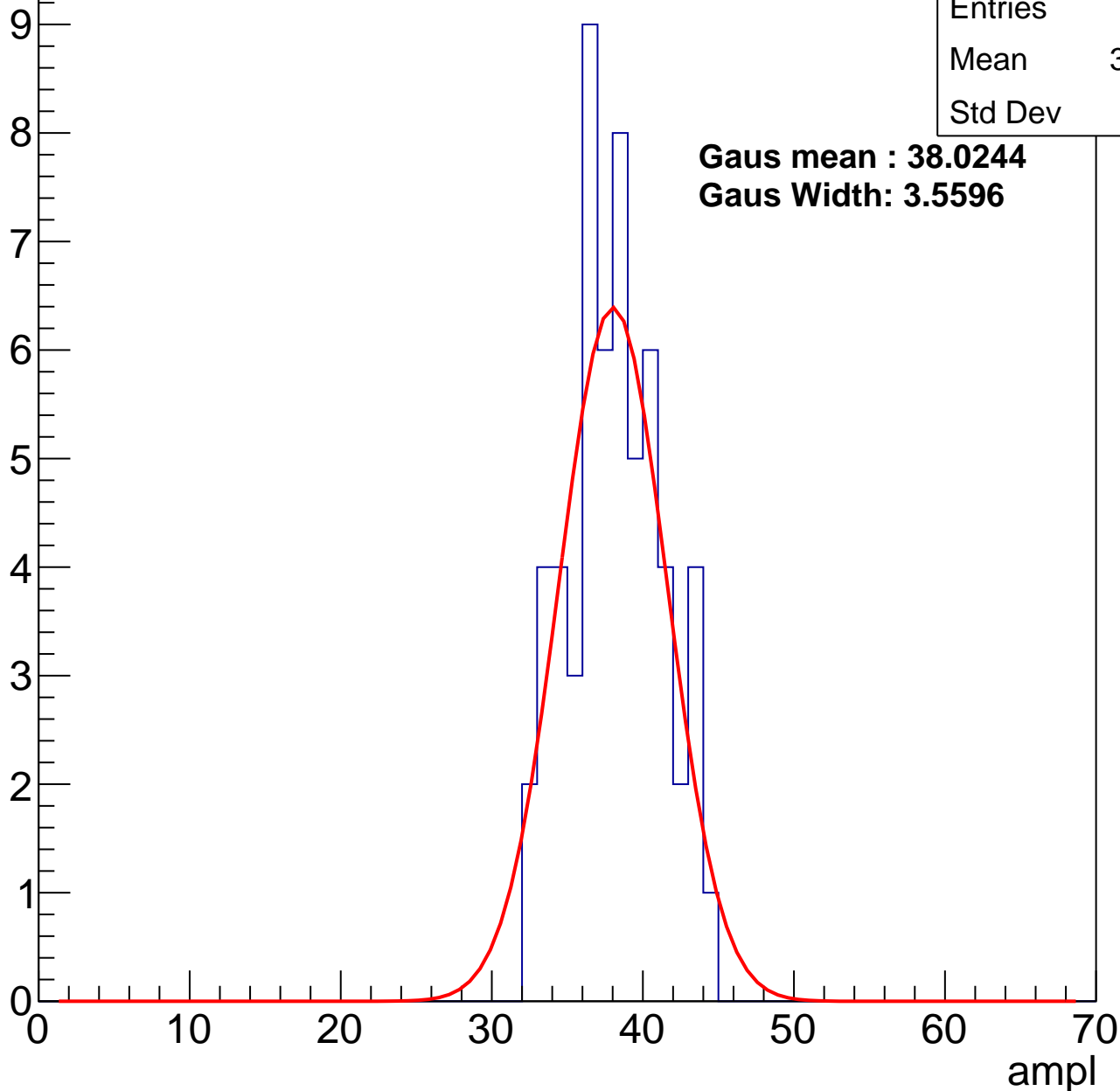
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.69
Std Dev	3.03

Gaus mean : 38.0244

Gaus Width: 3.5596



B1L103S, U12-ch85, adc2

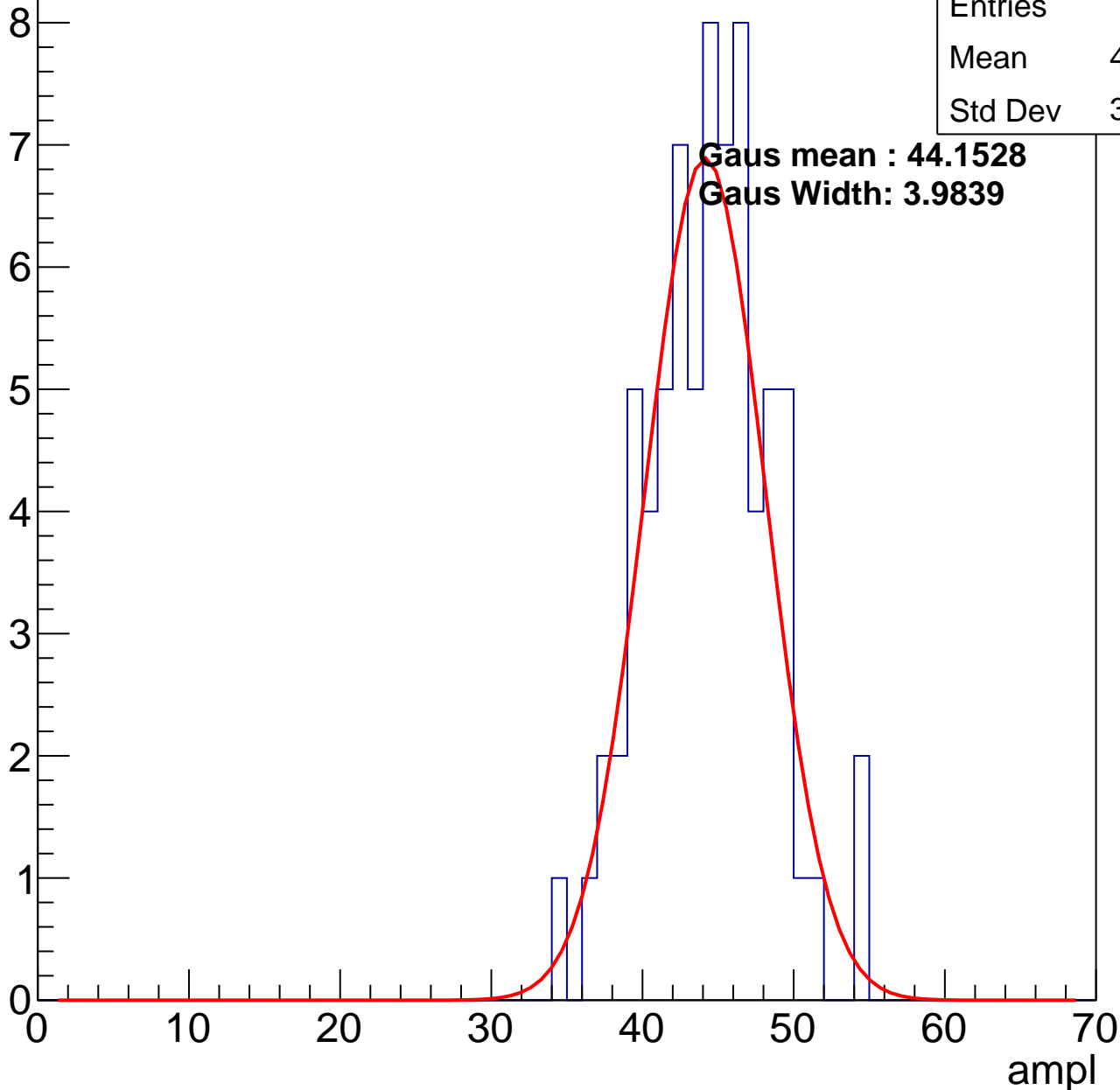
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	43.92
Std Dev	3.996

Gaus mean : 44.1528

Gaus Width: 3.9839

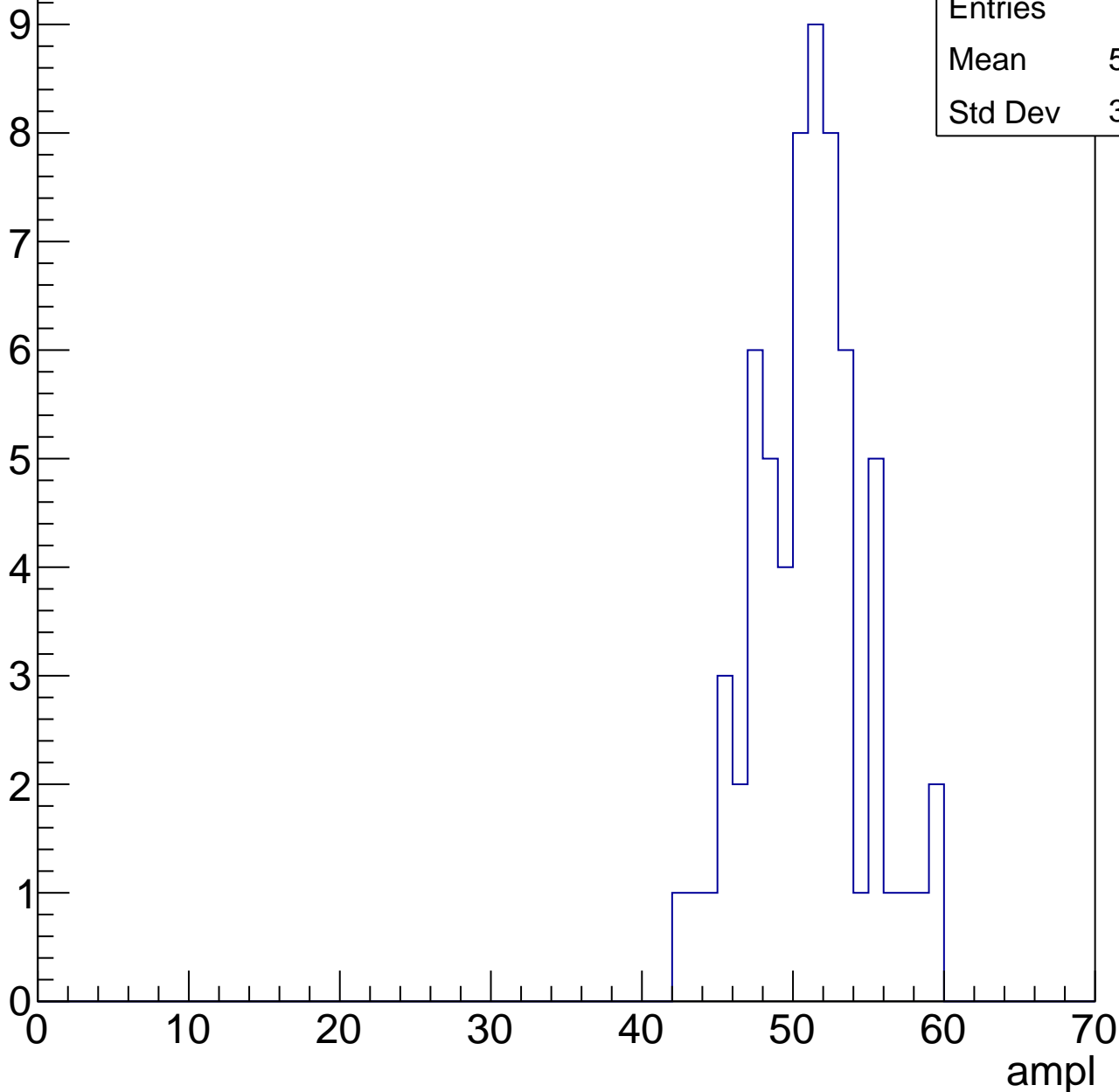


B1L103S, U12-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	50.54
Std Dev	3.642

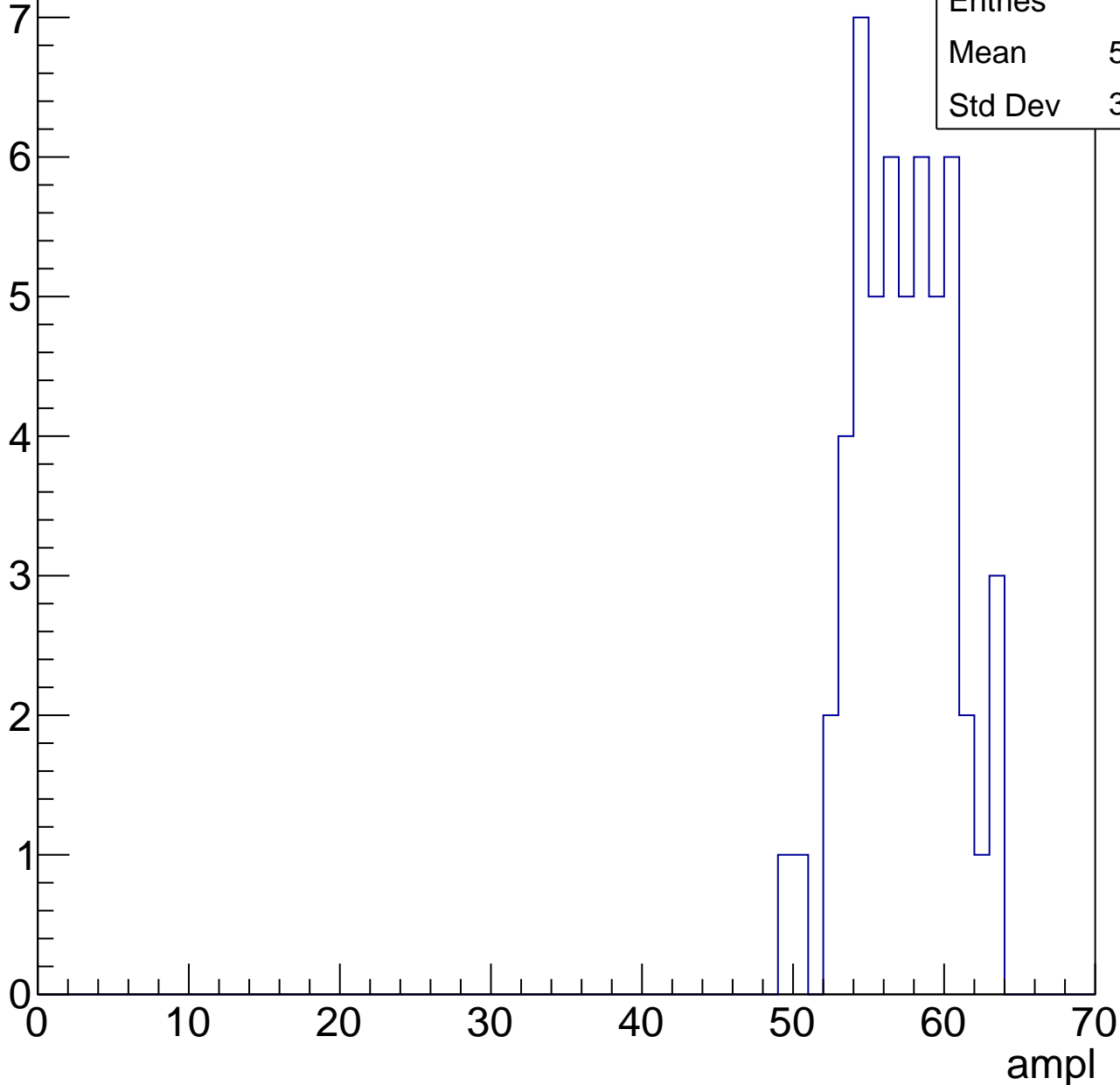


B1L103S, U12-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	56.76
Std Dev	3.214

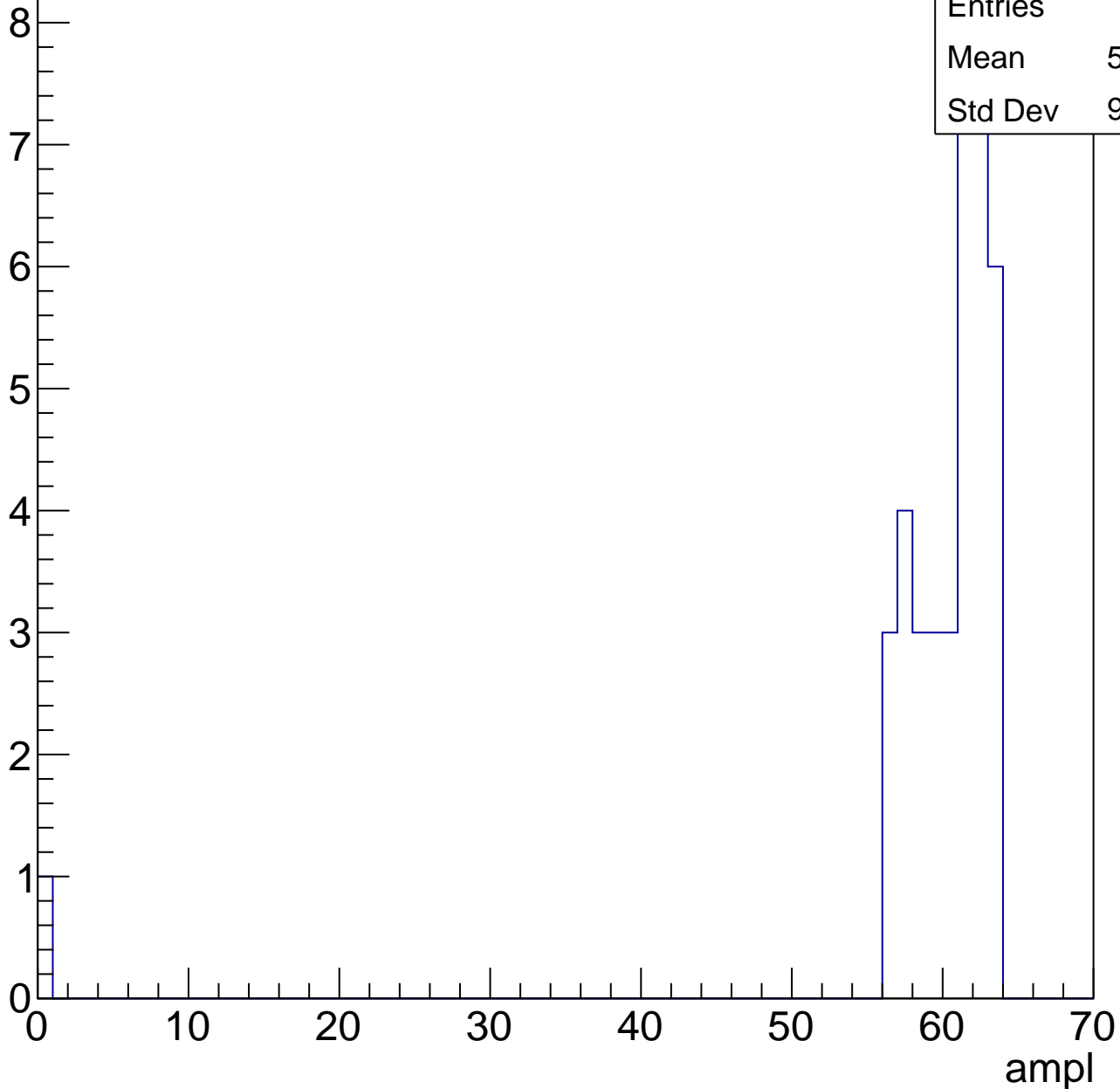


B1L103S, U12-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

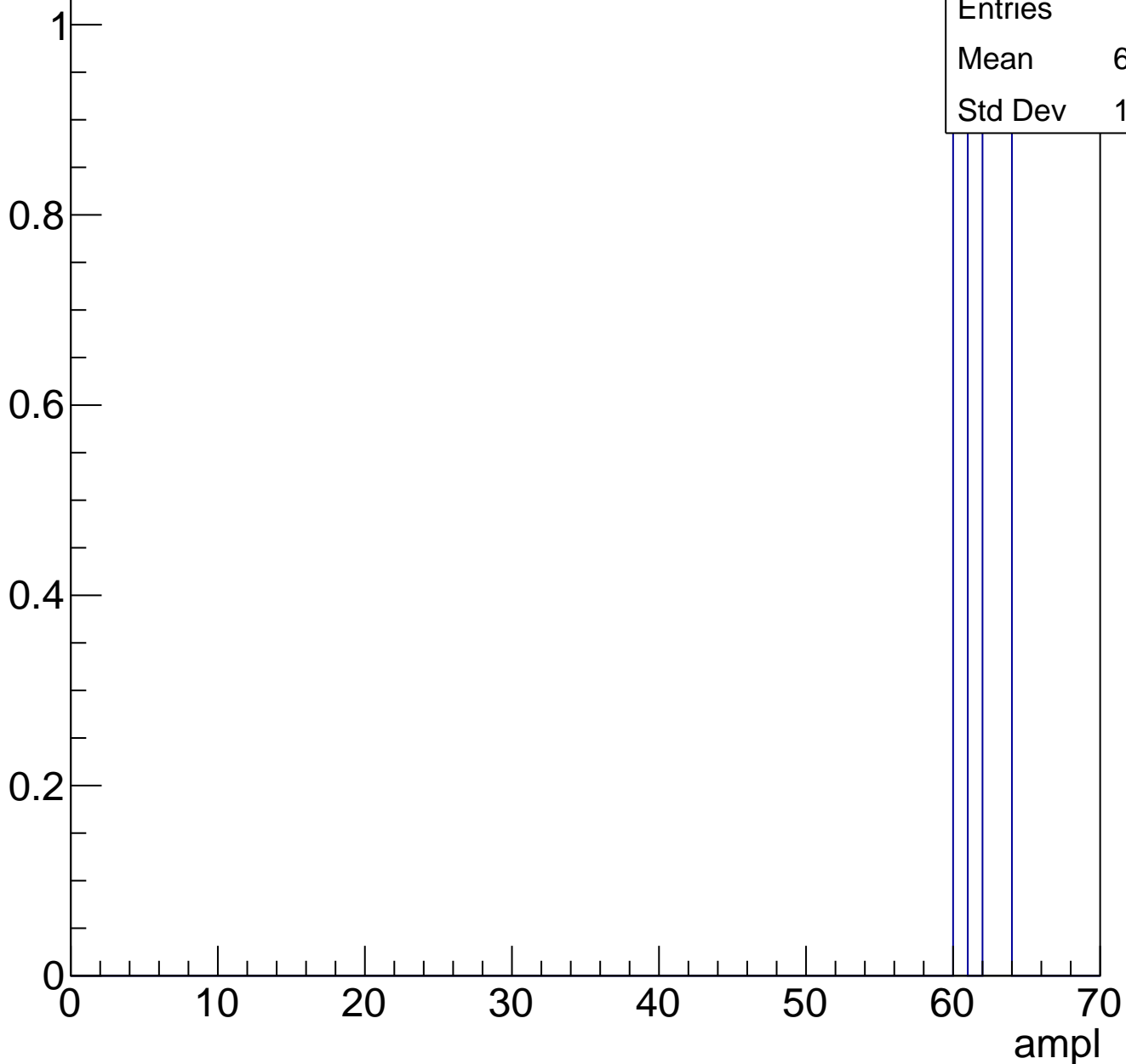
Entries	39
Mean	58.69
Std Dev	9.775



B1L103S, U12-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U12-ch86, adc0

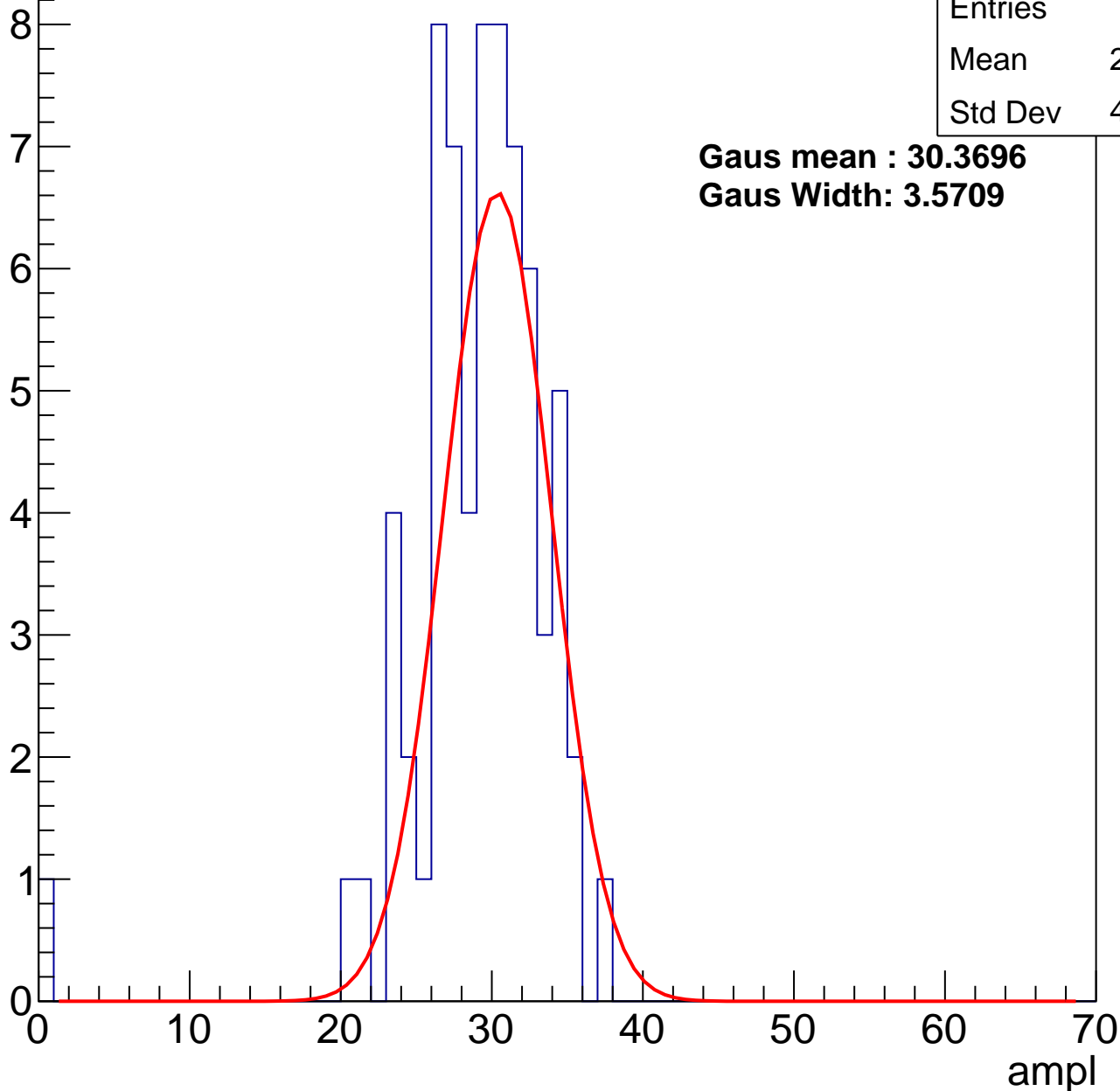
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	28.58
Std Dev	4.944

Gaus mean : 30.3696

Gaus Width: 3.5709



B1L103S, U12-ch86, adc1

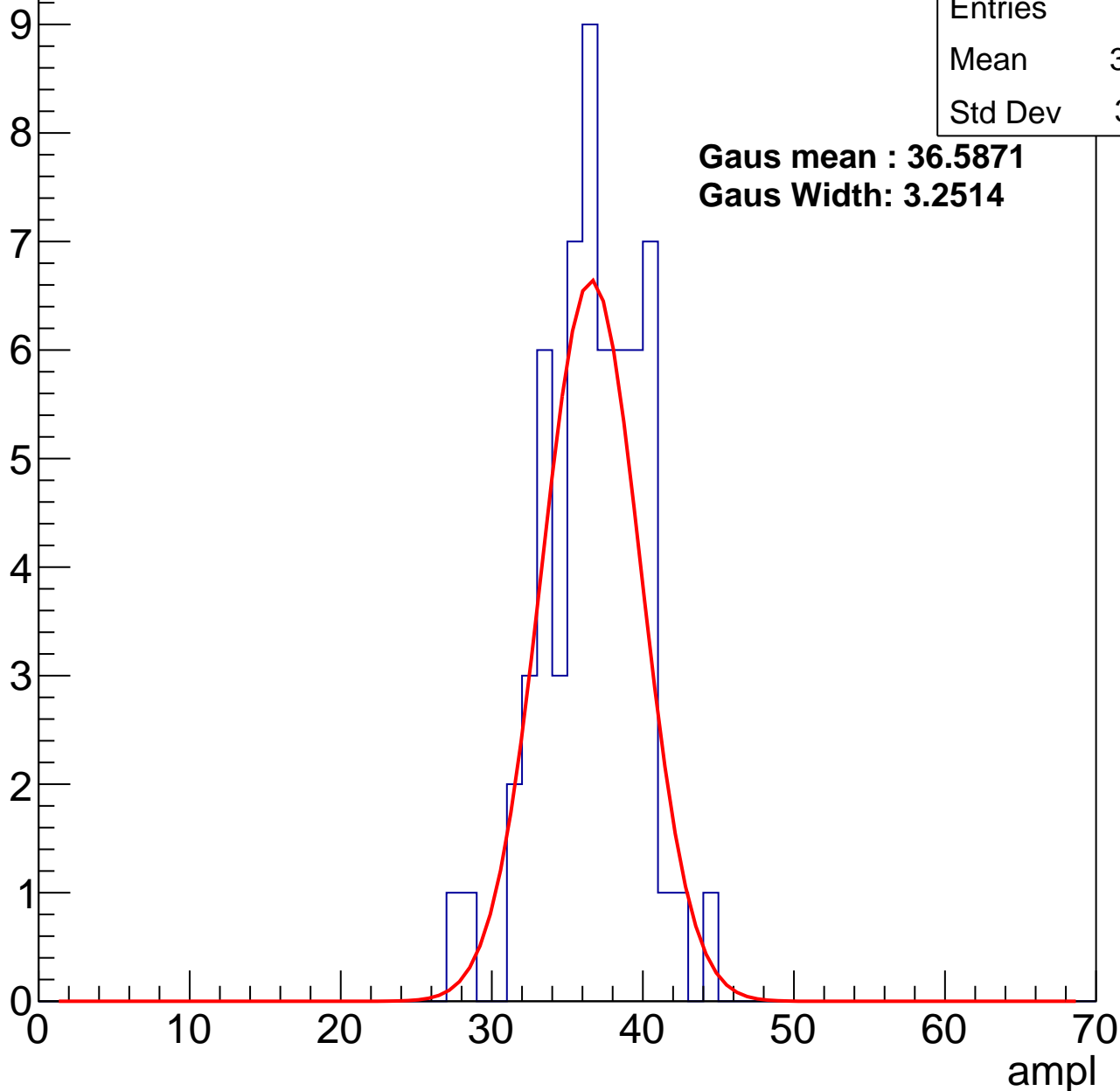
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.22
Std Dev	3.251

Gaus mean : 36.5871

Gaus Width: 3.2514



B1L103S, U12-ch86, adc2

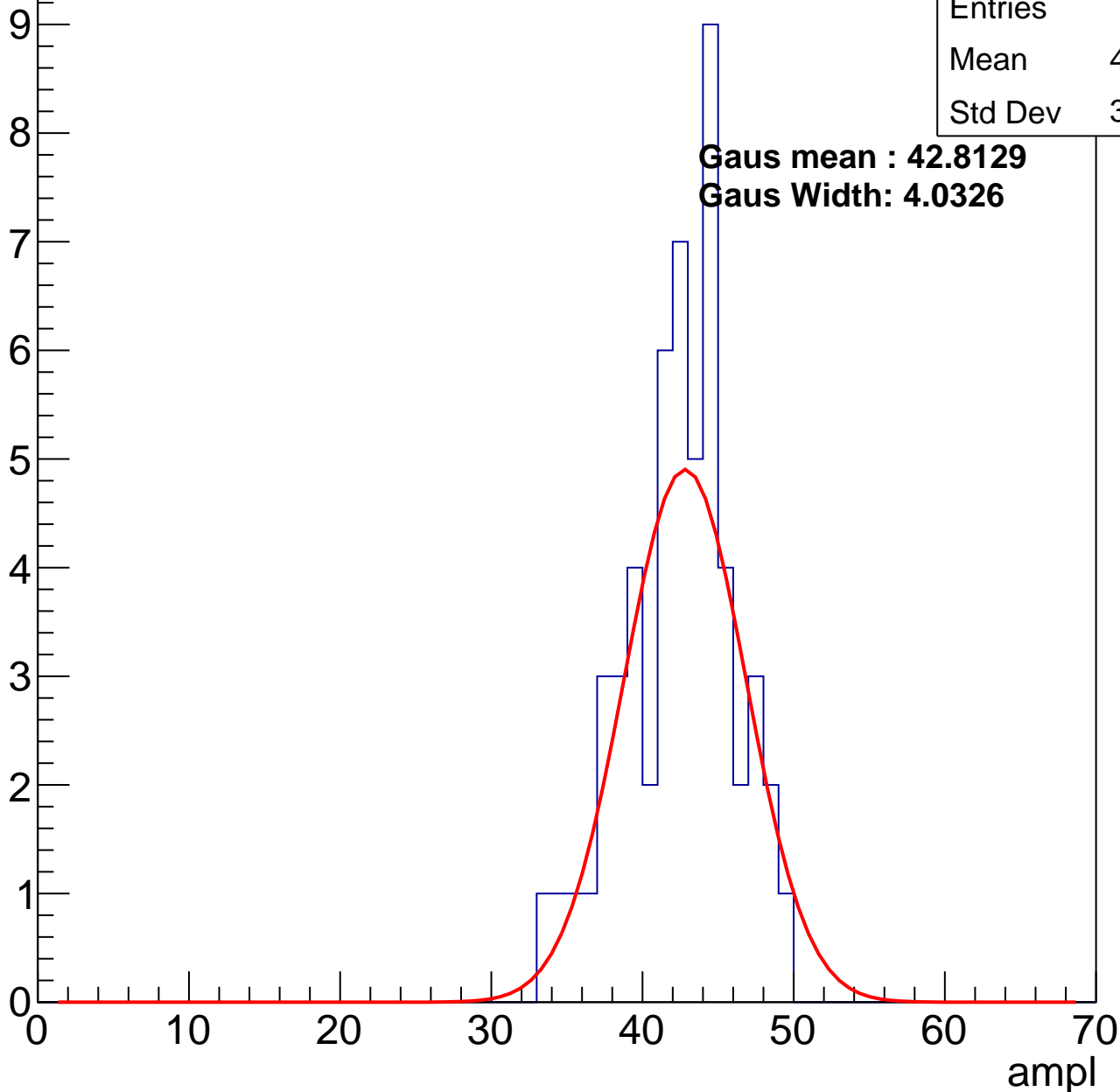
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.96
Std Dev	3.588

Gaus mean : 42.8129

Gaus Width: 4.0326

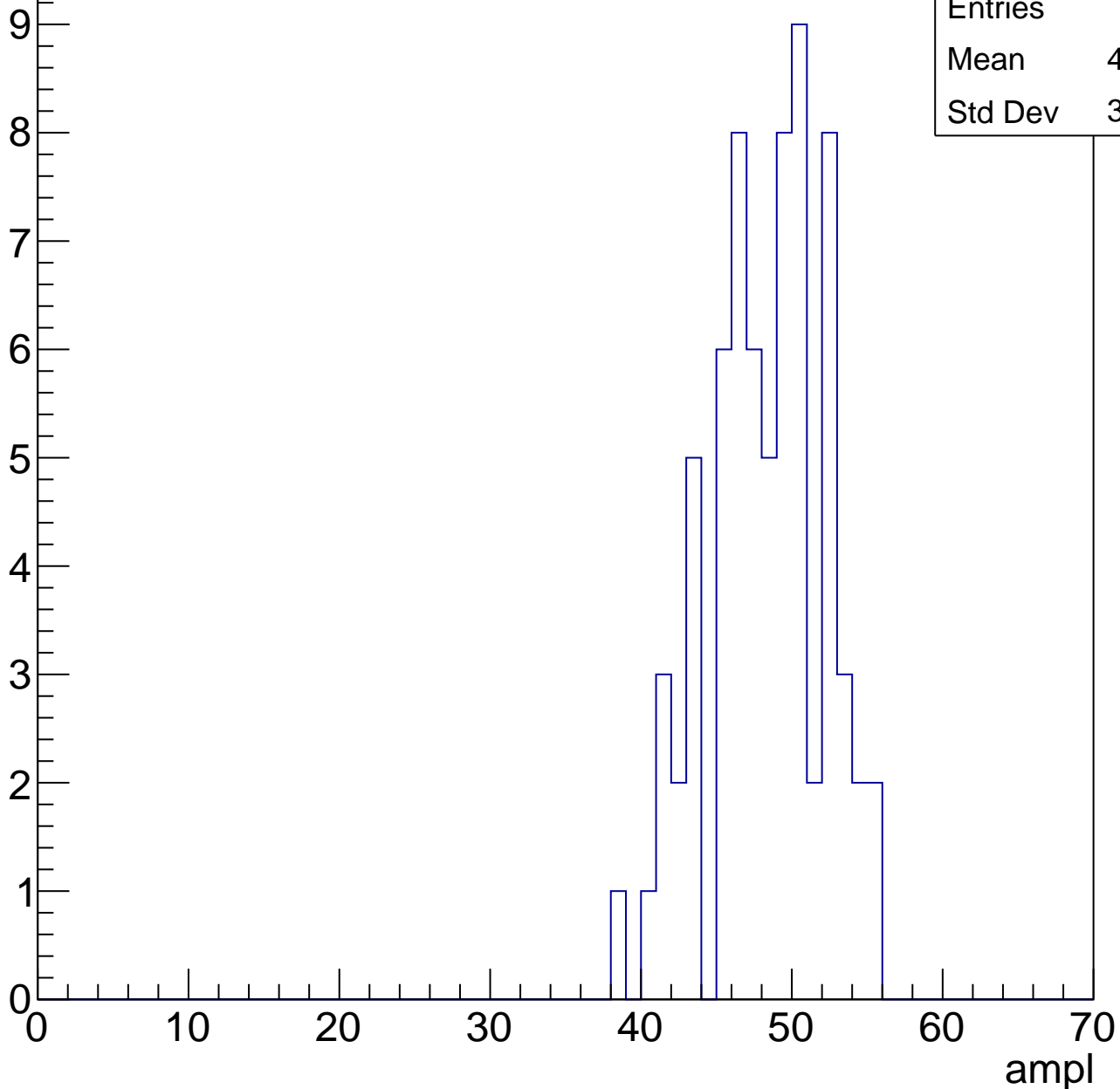


B1L103S, U12-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	47.85
Std Dev	3.815

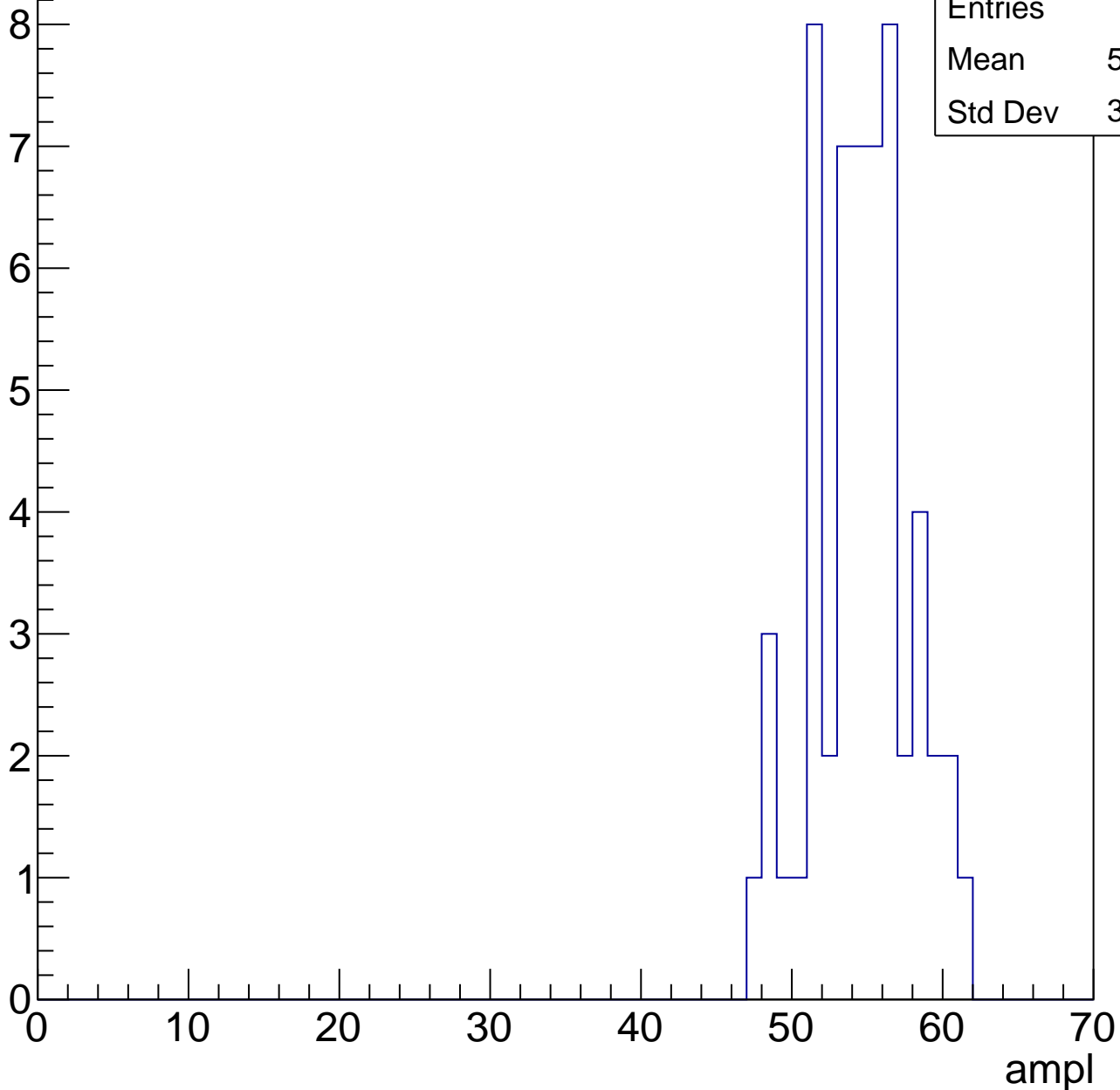


B1L103S, U12-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.09
Std Dev	3.209

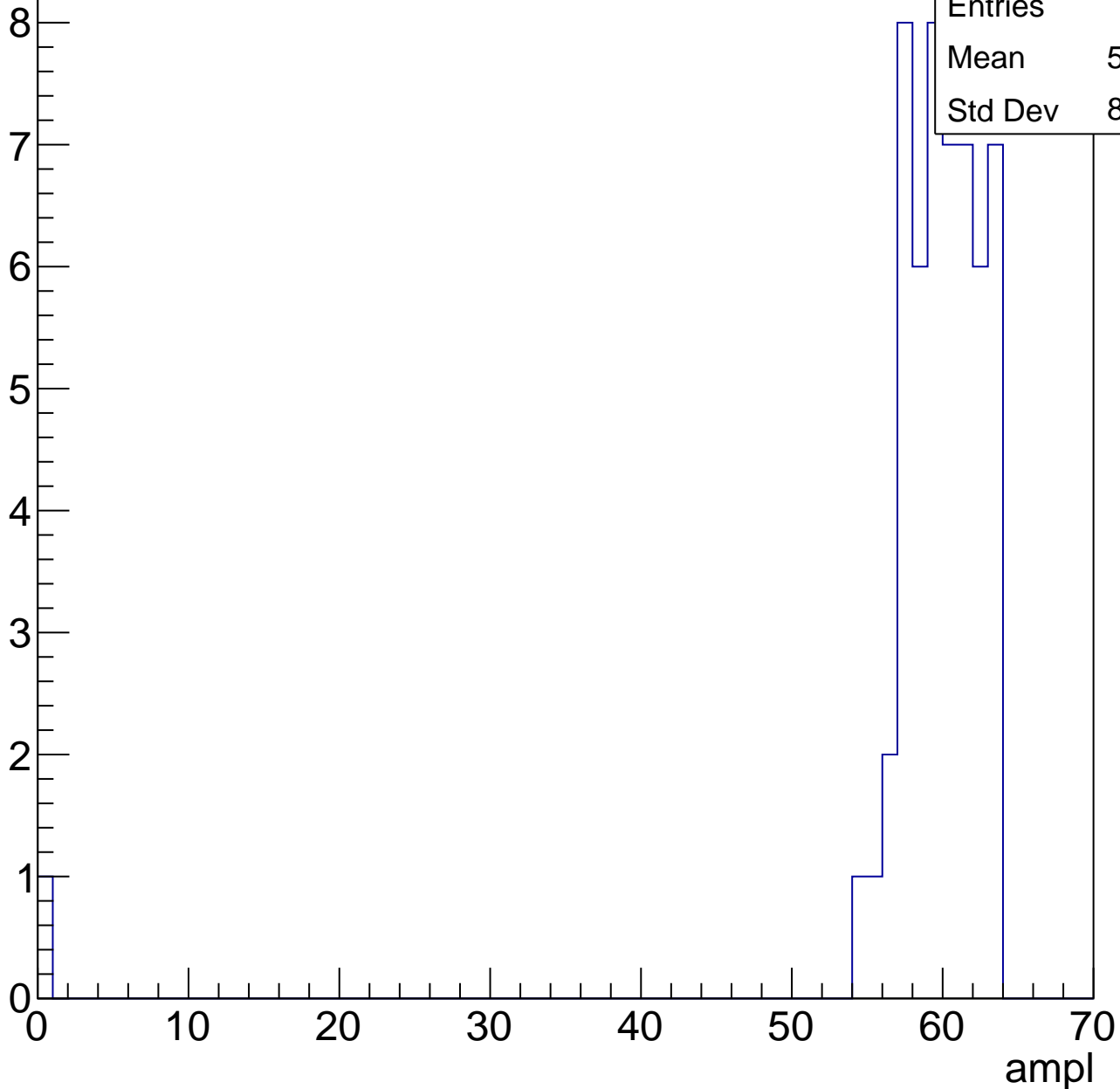


B1L103S, U12-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

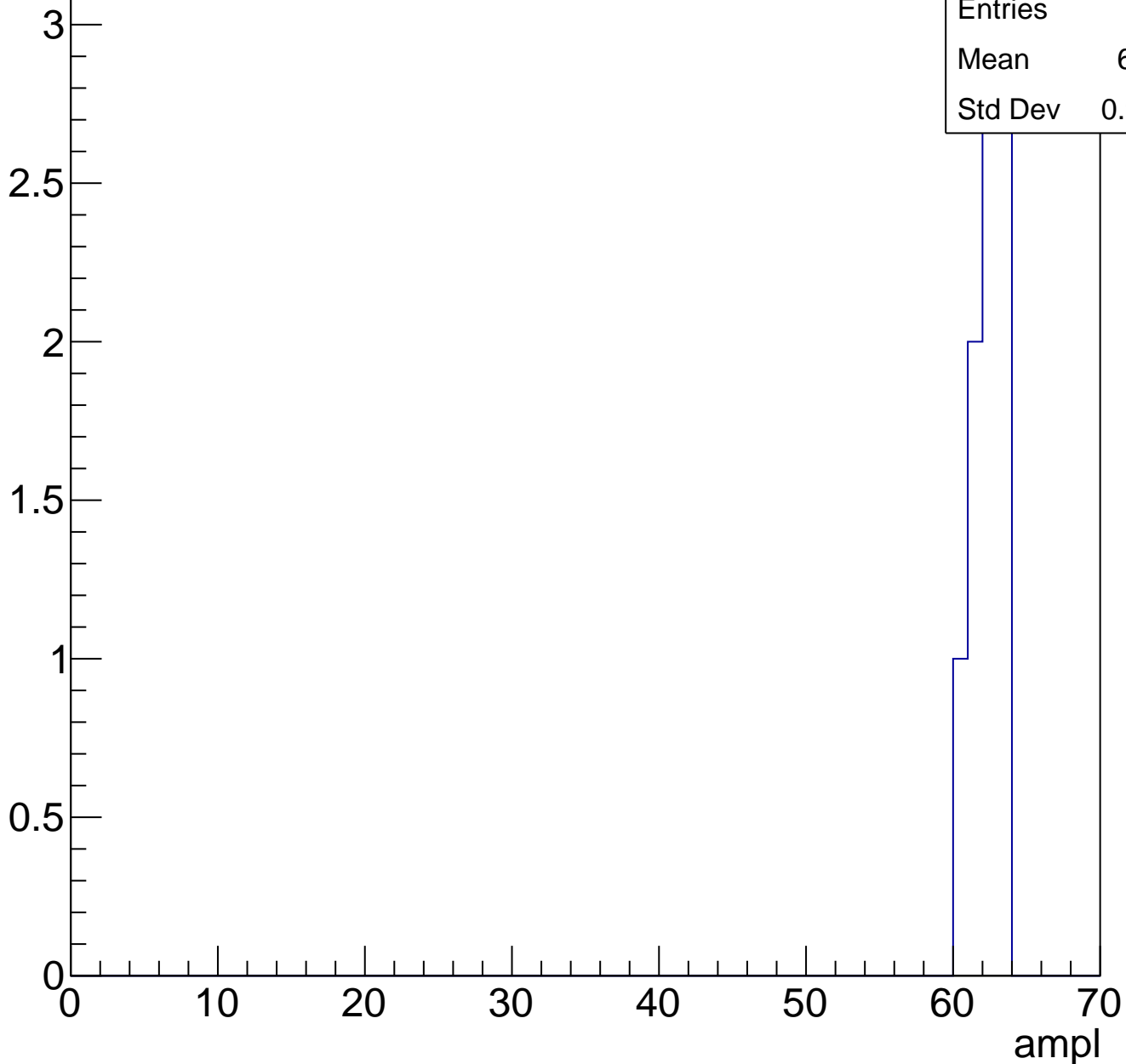
Entries	54
Mean	58.46
Std Dev	8.348



B1L103S, U12-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

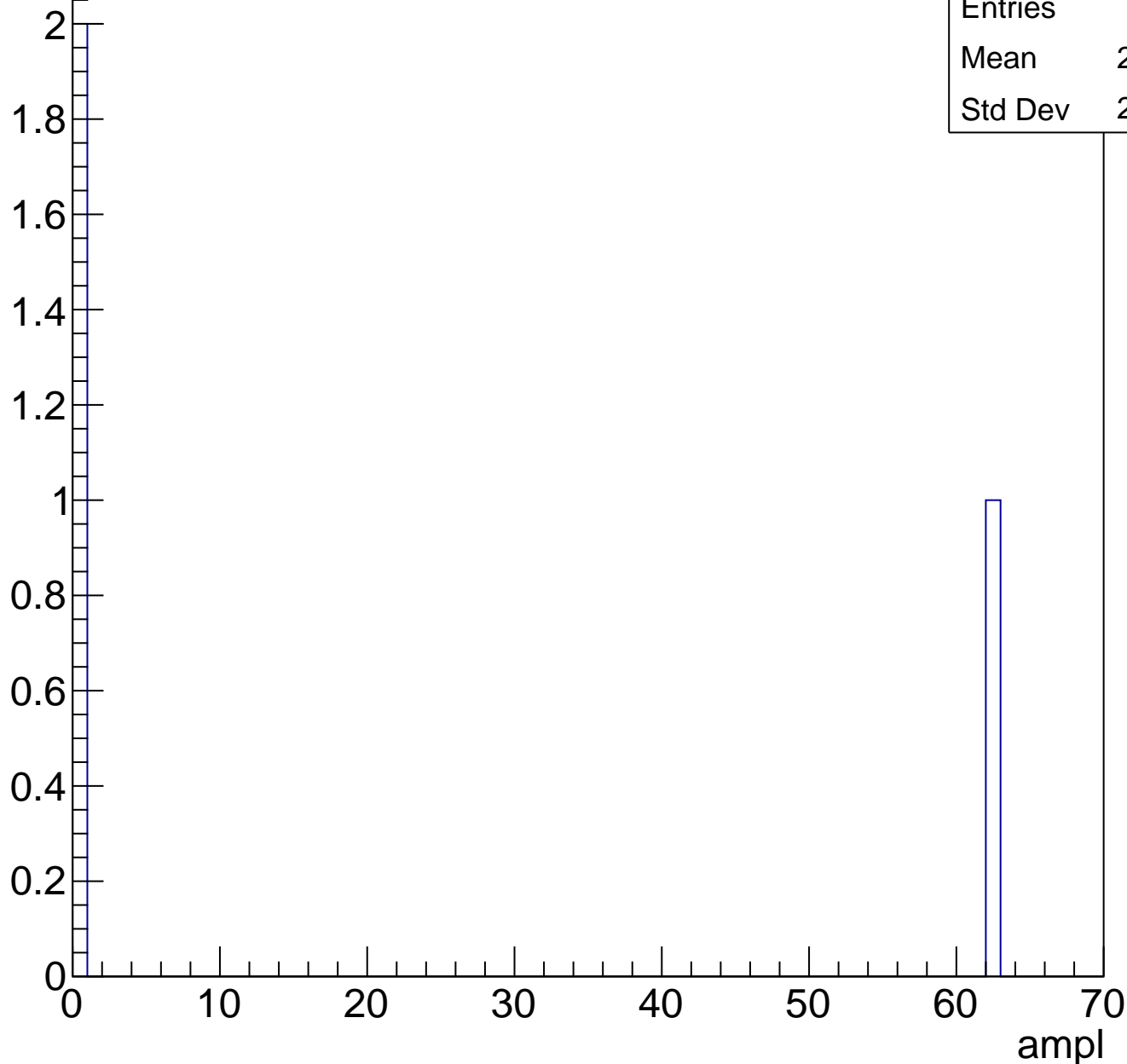
Entry



B1L103S, U12-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L103S, U12-ch87, adc0

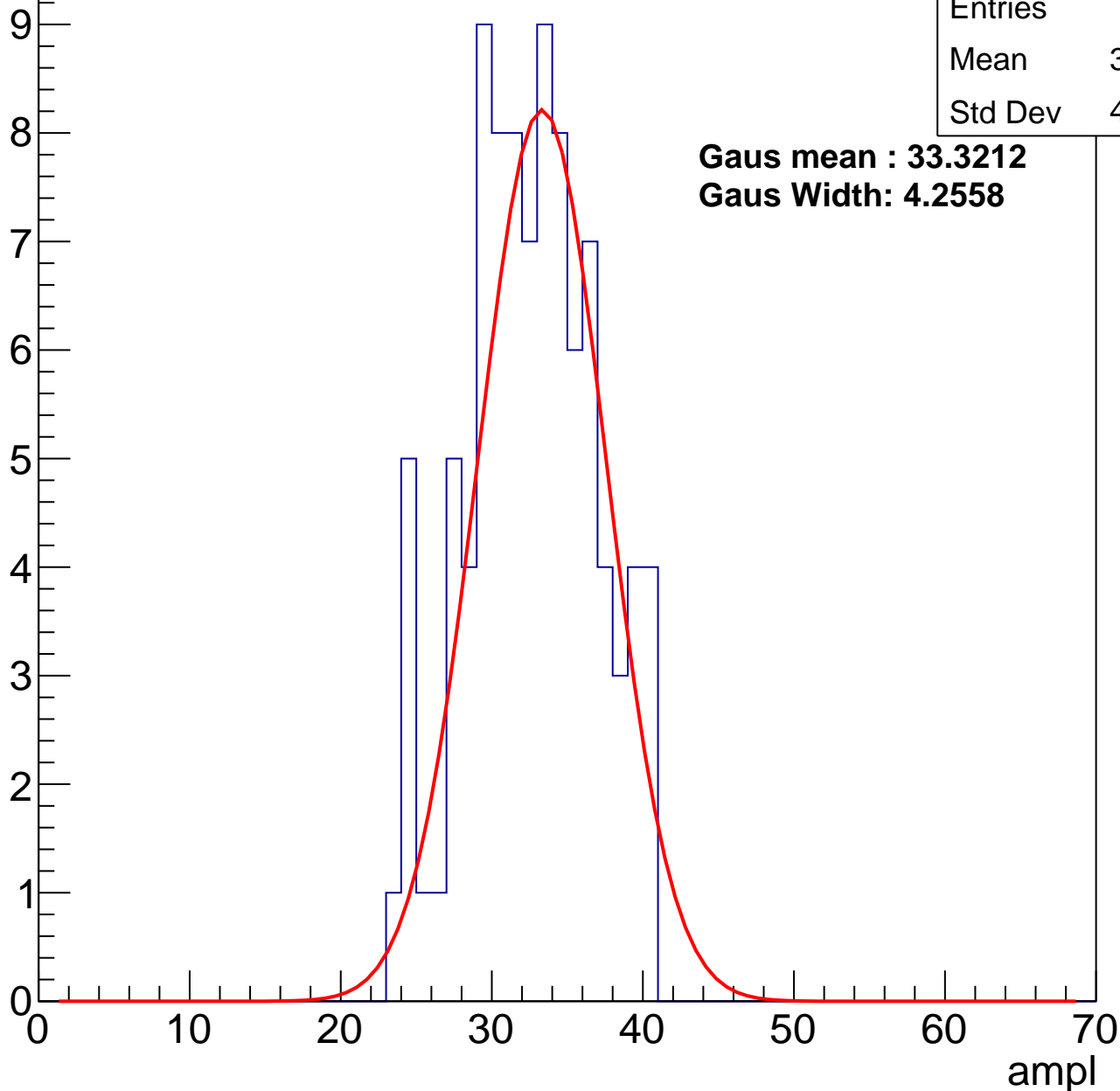
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	94
Mean	32.16
Std Dev	4.203

Gaus mean : 33.3212

Gaus Width: 4.2558



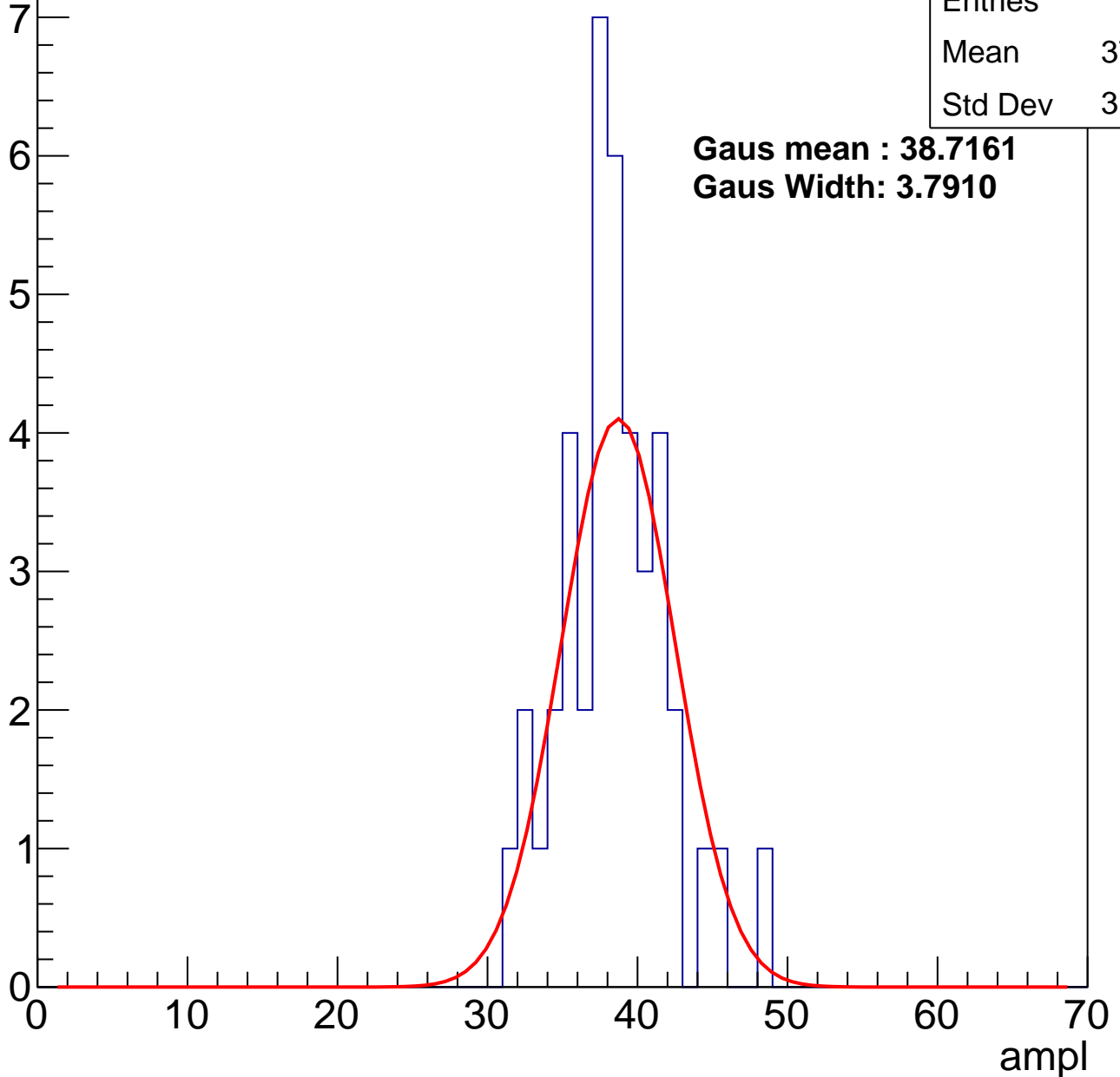
B1L103S, U12-ch87, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	37.95
Std Dev	3.485

Gaus mean : 38.7161
Gaus Width: 3.7910



B1L103S, U12-ch87, adc2

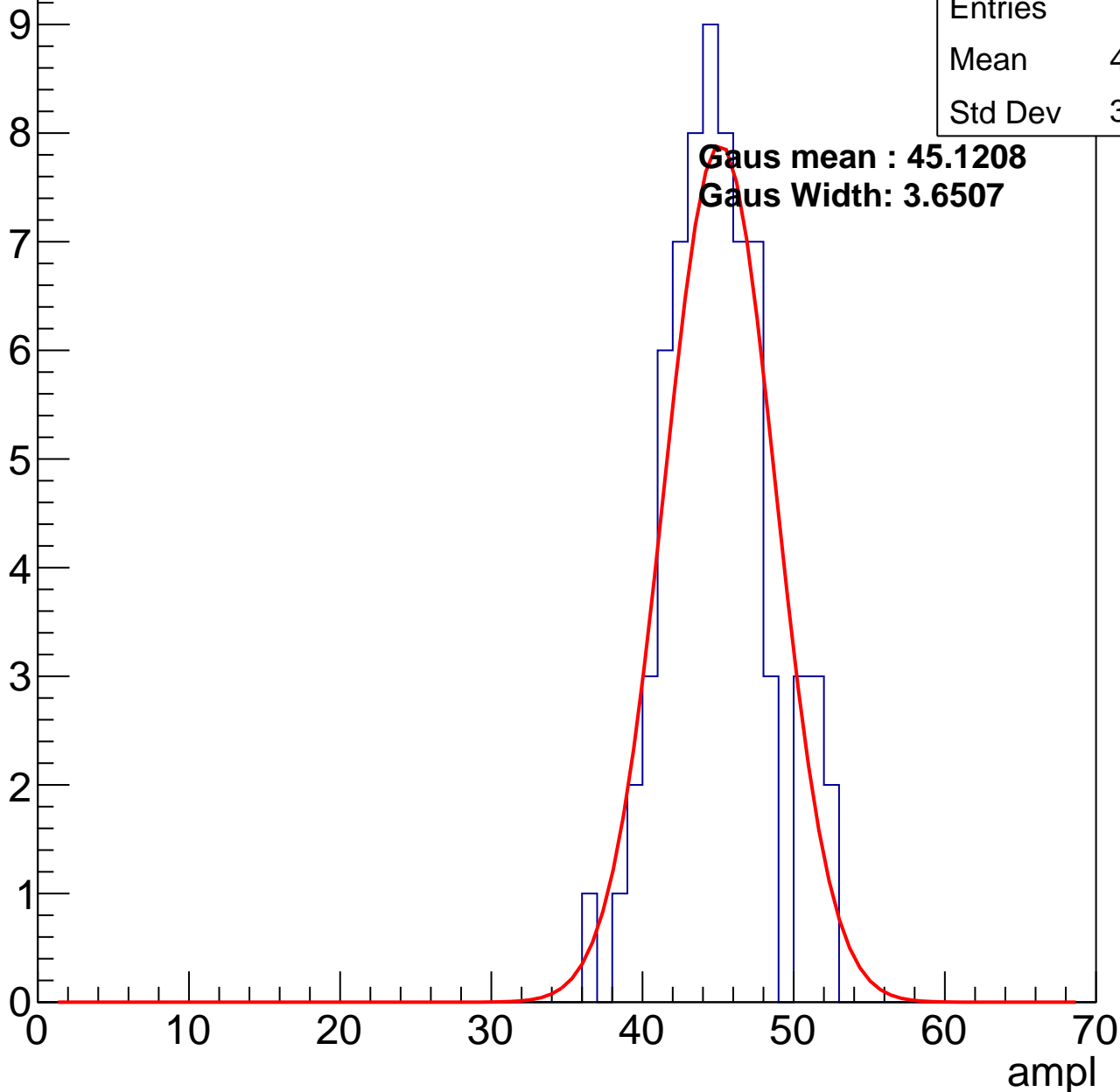
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.49
Std Dev	3.384

Gaus mean : 45.1208

Gaus Width: 3.6507



B1L103S, U12-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

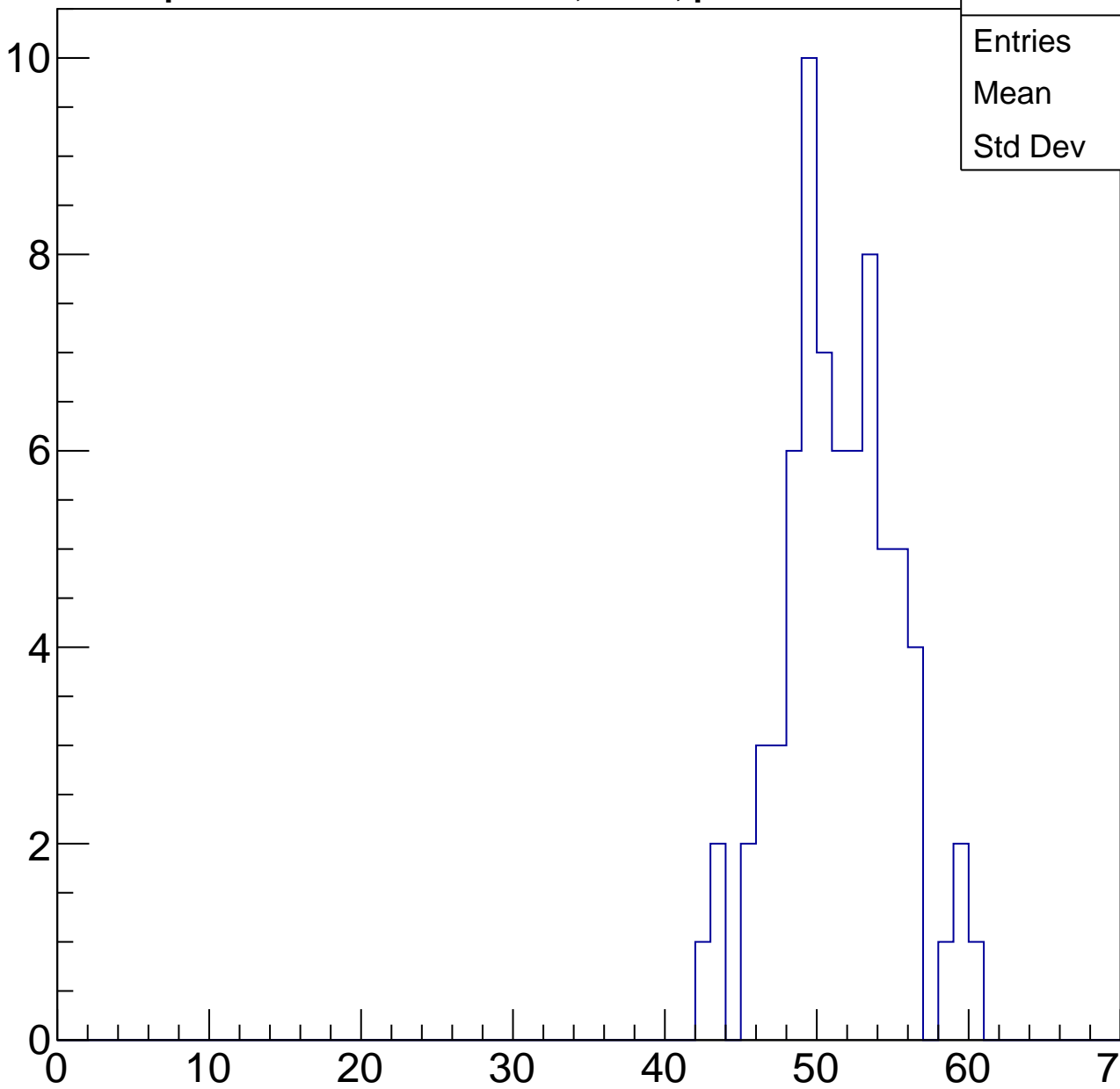
Entries	72
Mean	51
Std Dev	3.775

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

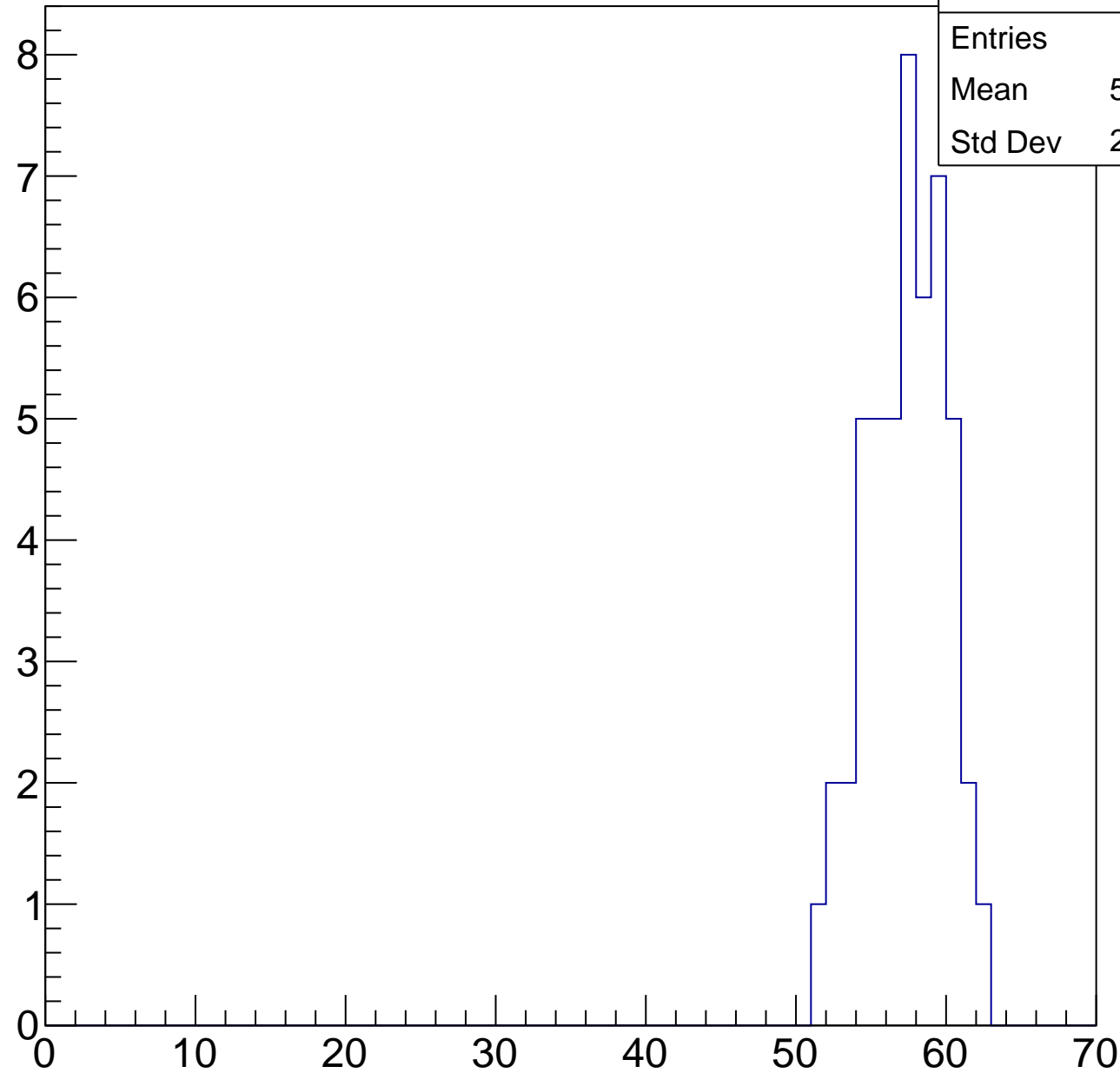
Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	56.88
Std Dev	2.569

ampl

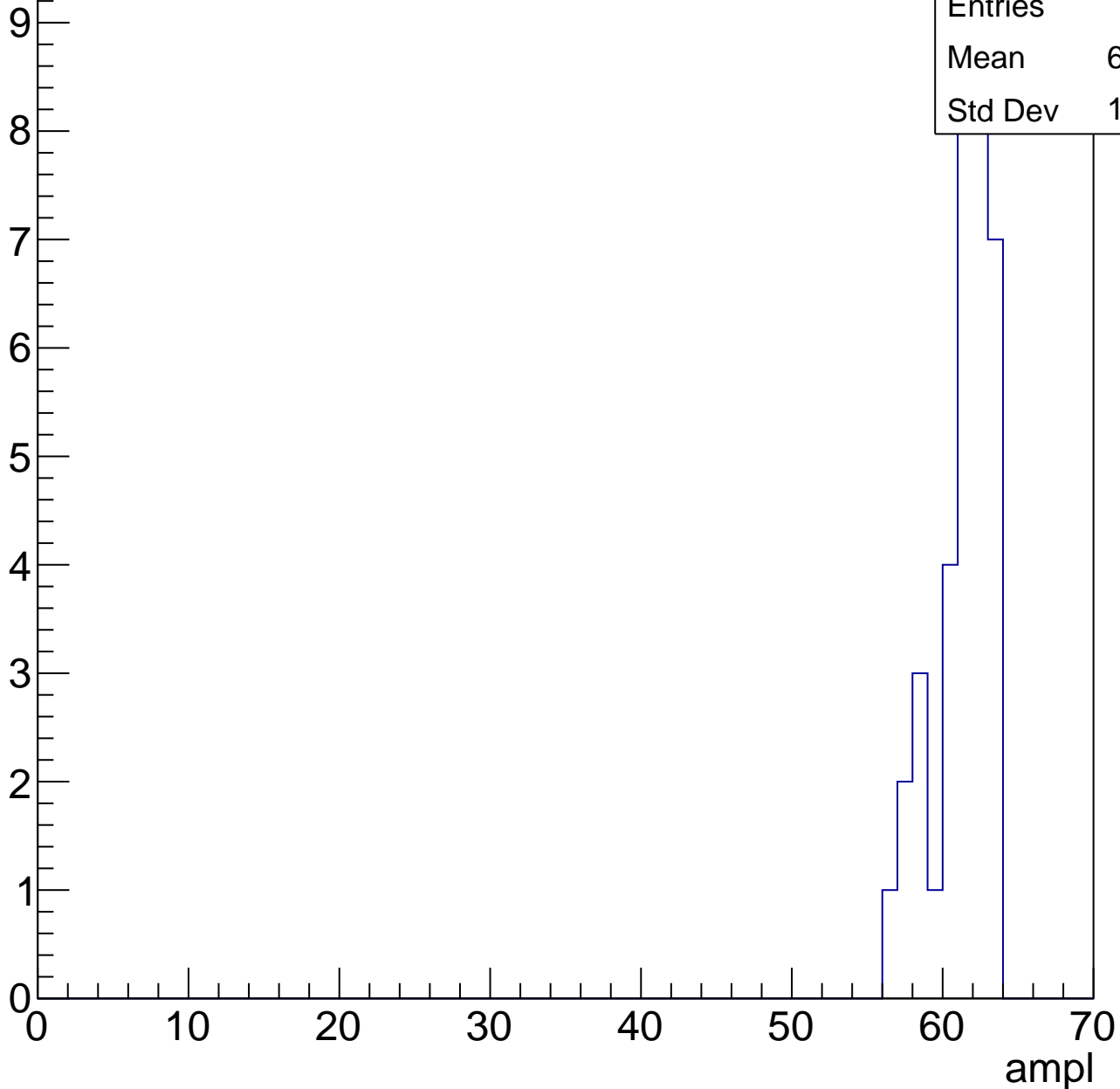
0 10 20 30 40 50 60 70



B1L103S, U12-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

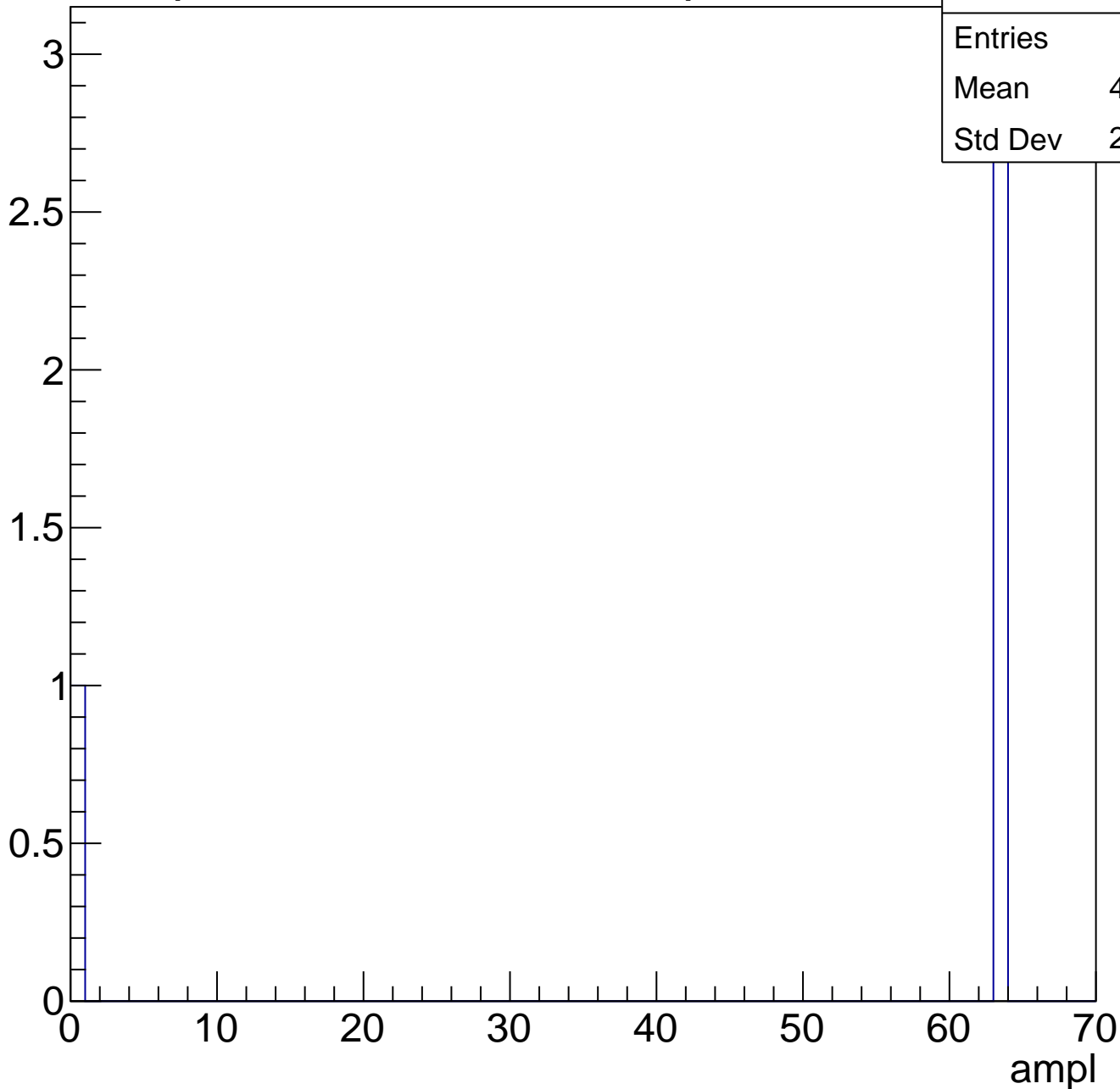
Entry



B1L103S, U12-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	47.25
Std Dev	27.28

B1L103S, U12-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch88, adc0

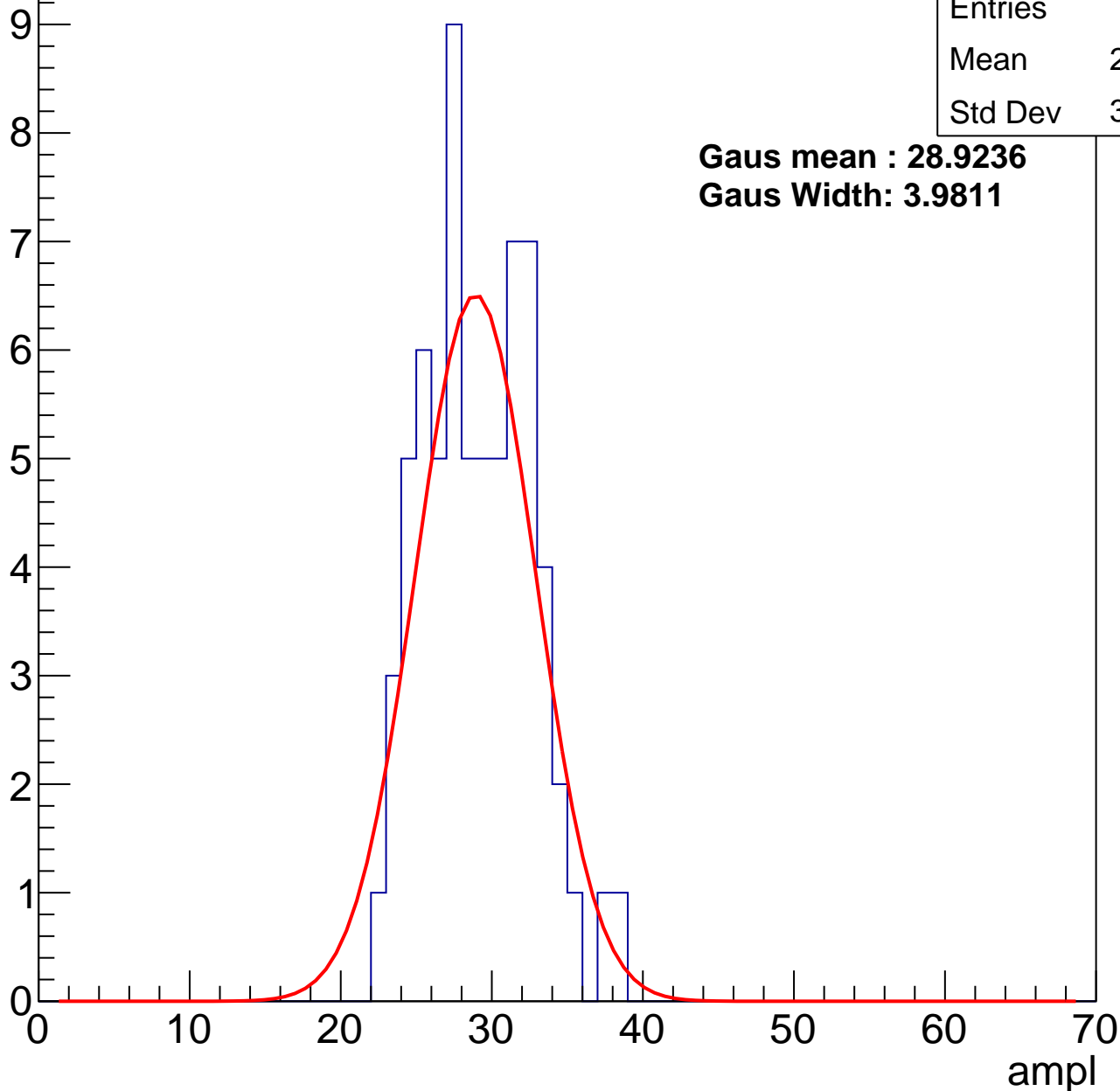
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	28.66
Std Dev	3.556

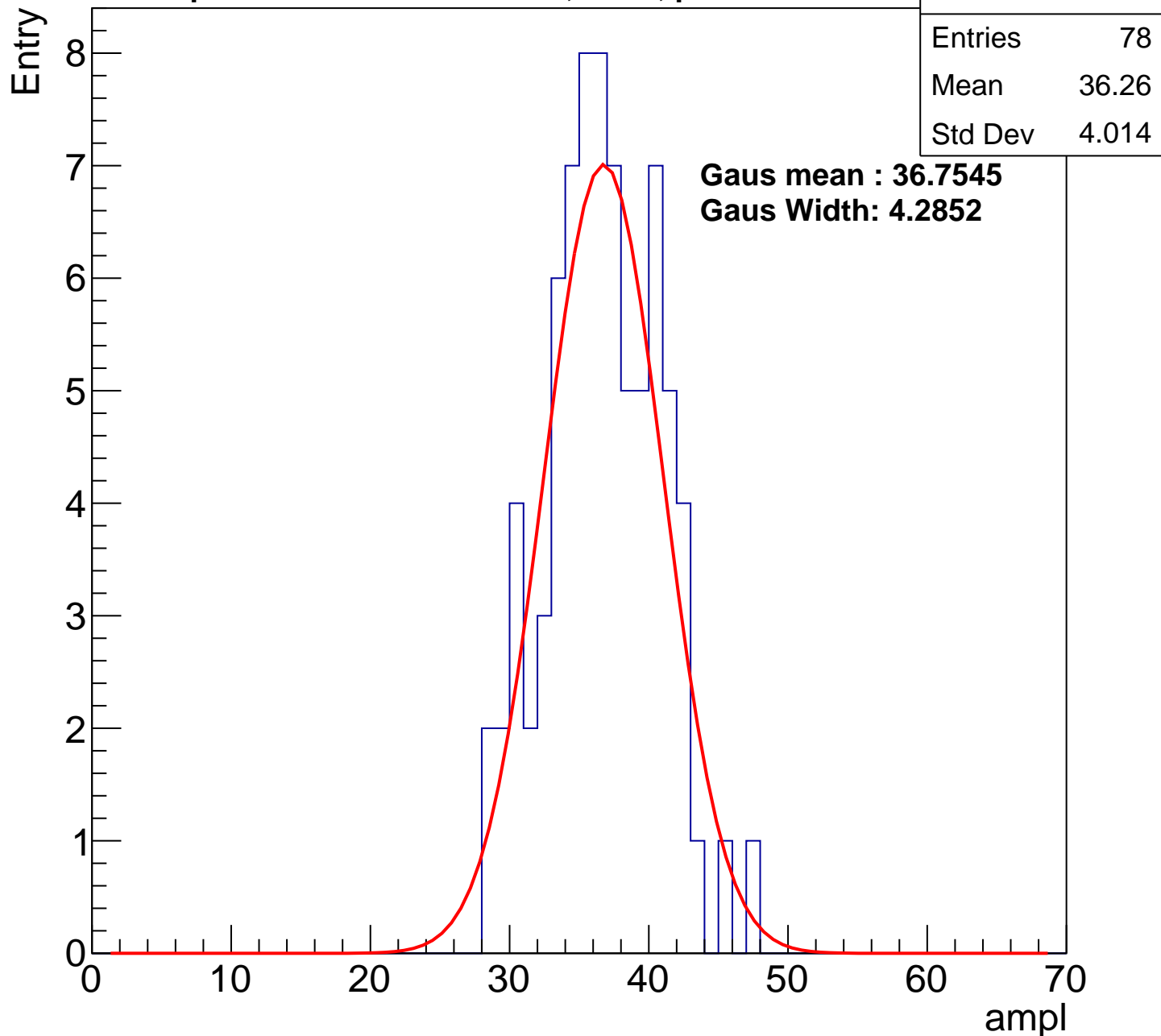
Gaus mean : 28.9236

Gaus Width: 3.9811



B1L103S, U12-ch88, adc1

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U12-ch88, adc2

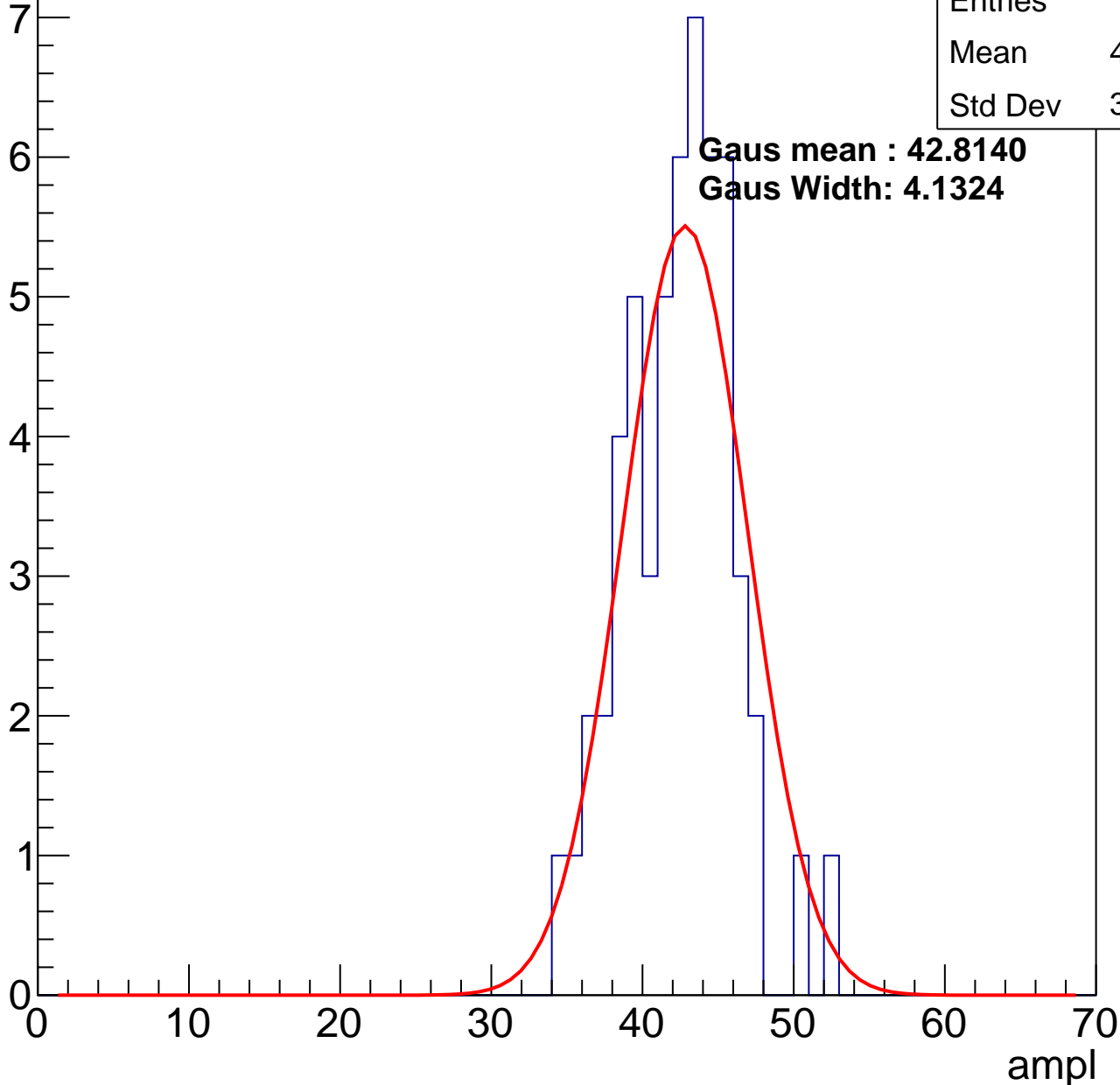
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	41.96
Std Dev	3.588

Gaus mean : 42.8140

Gaus Width: 4.1324

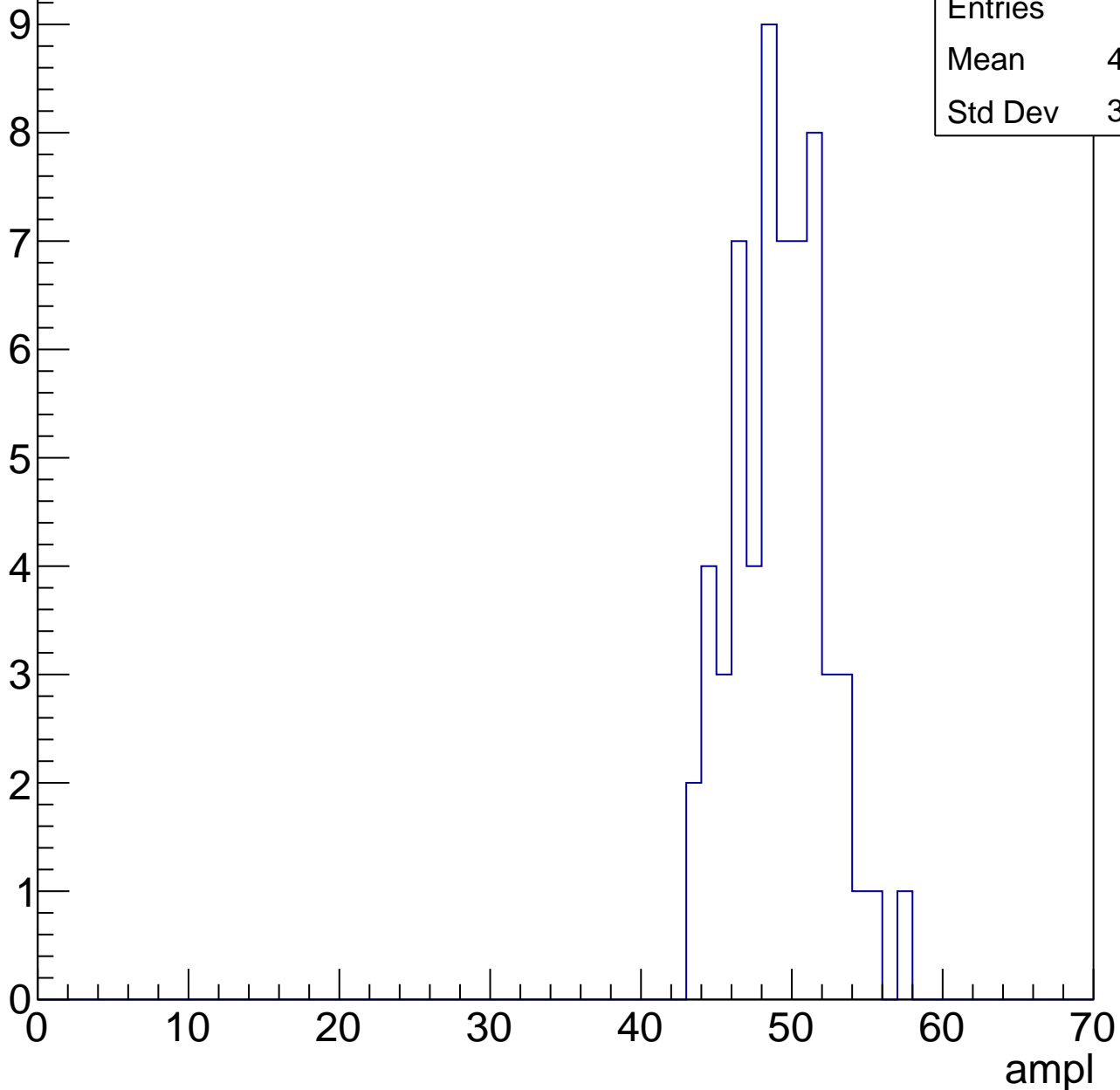


B1L103S, U12-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	48.68
Std Dev	3.003

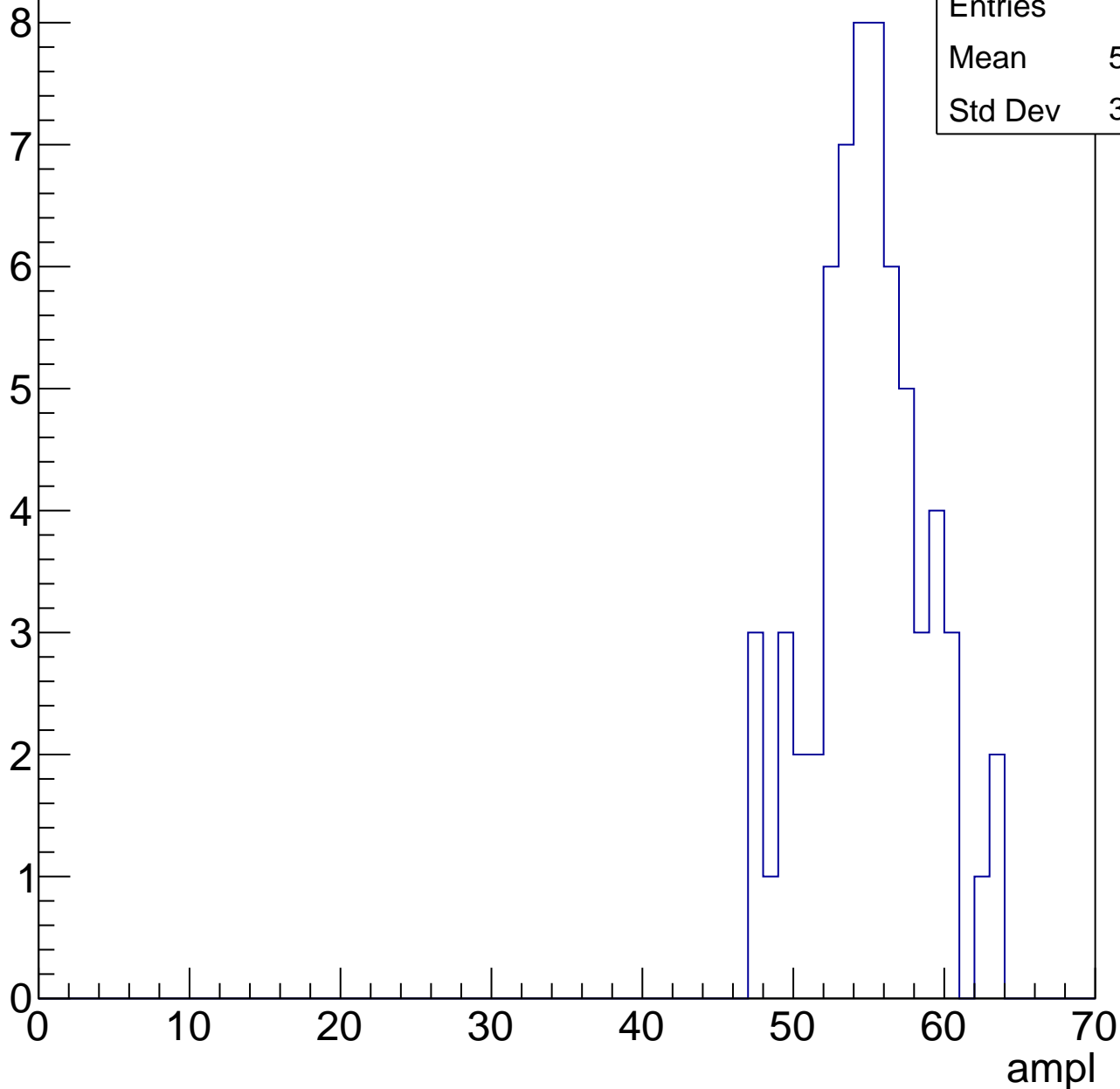


B1L103S, U12-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	54.56
Std Dev	3.703

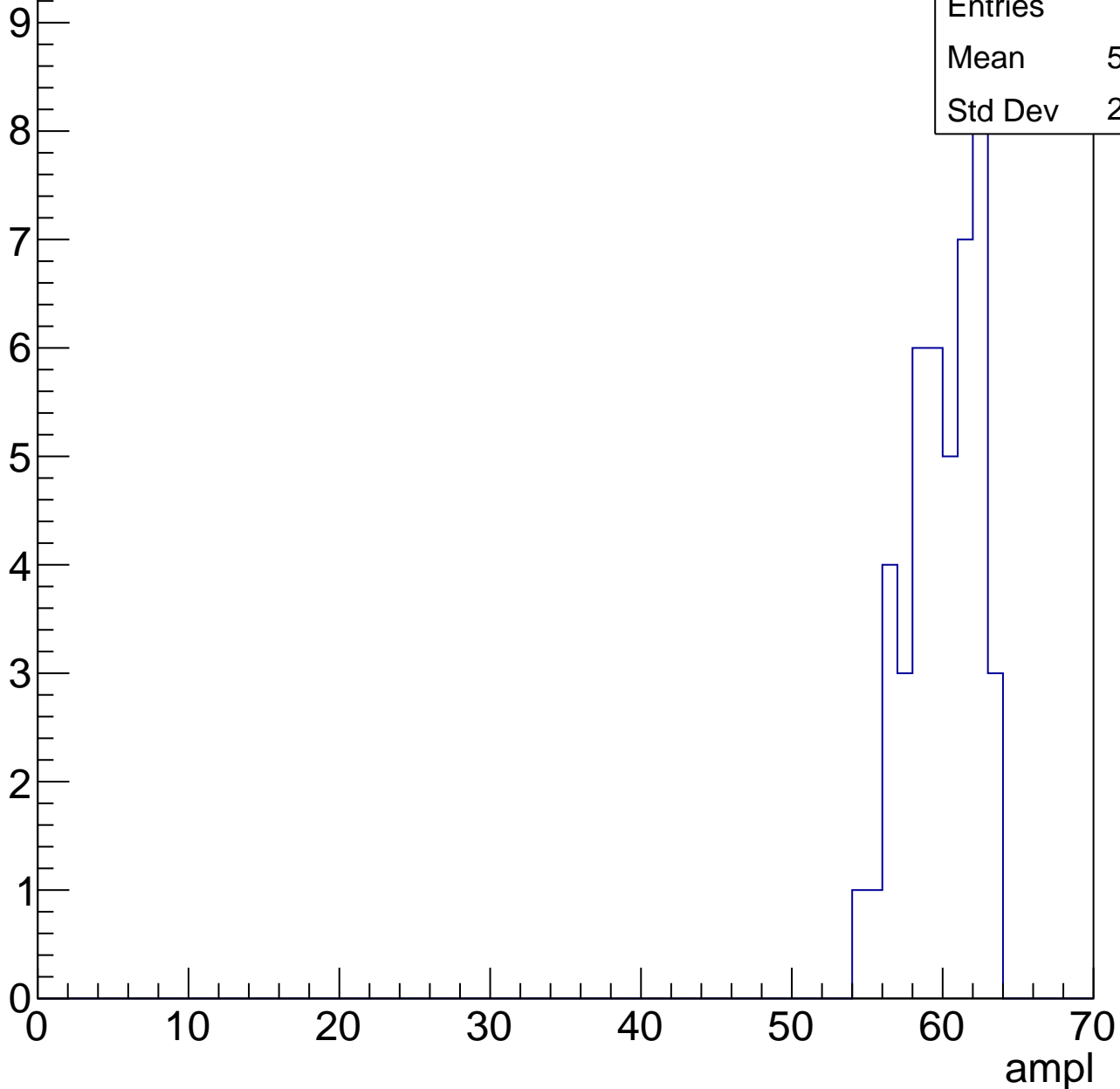


B1L103S, U12-ch88, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	59.56
Std Dev	2.324

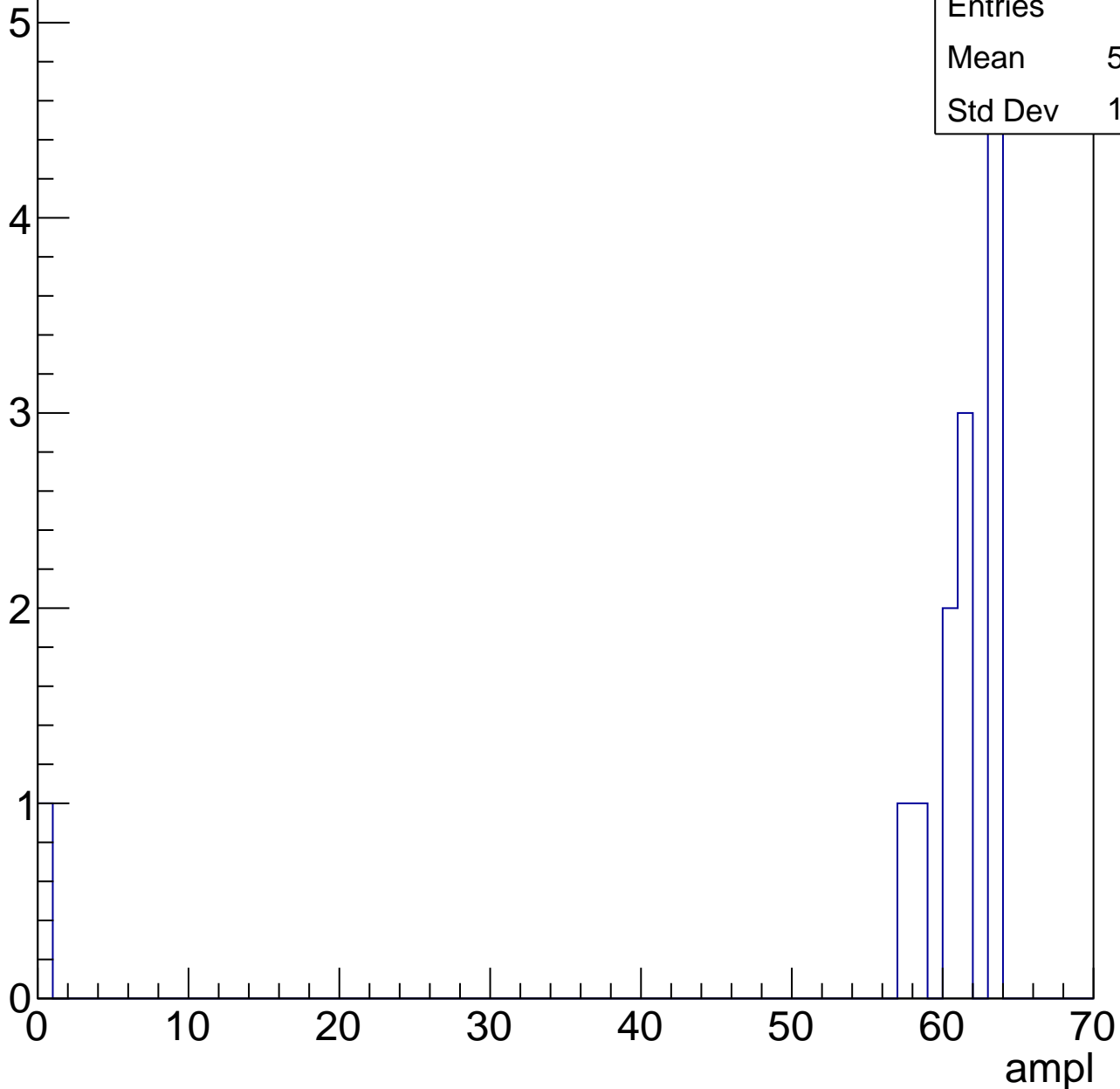


B1L103S, U12-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	56.38
Std Dev	16.39



B1L103S, U12-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch89, adc0

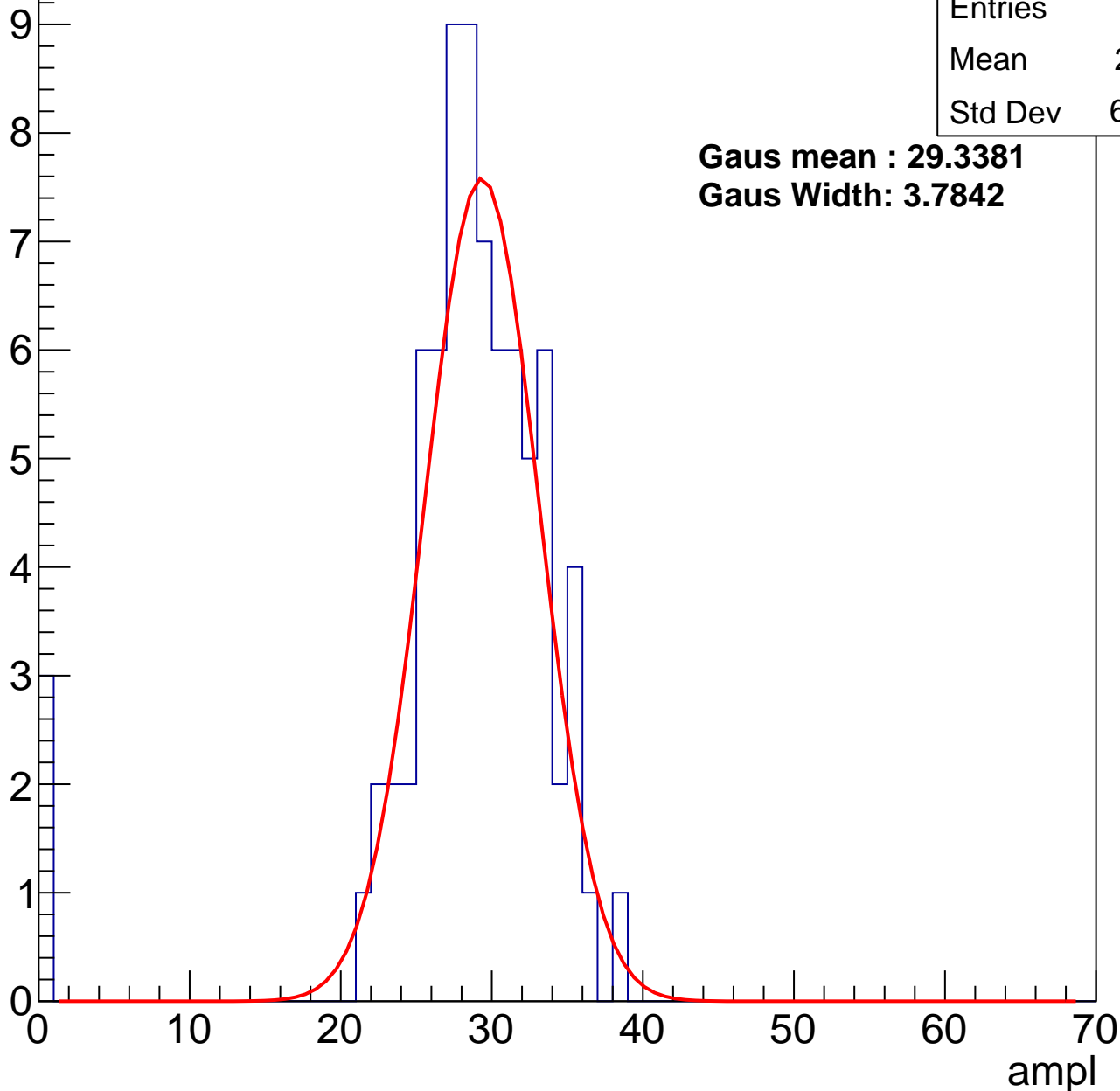
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.81
Std Dev	6.589

Gaus mean : 29.3381

Gaus Width: 3.7842



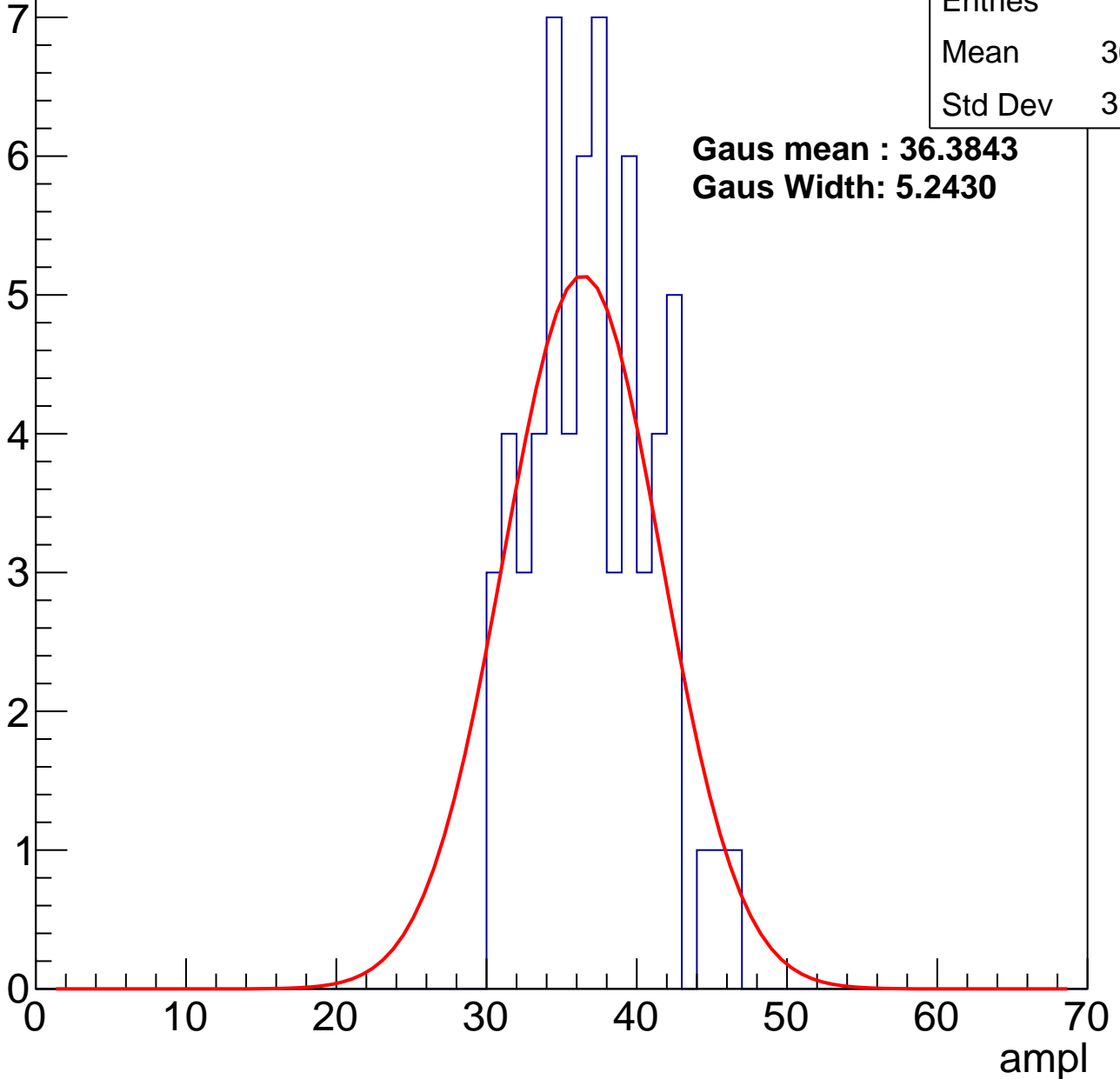
B1L103S, U12-ch89, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.65
Std Dev	3.902

Gaus mean : 36.3843
Gaus Width: 5.2430



B1L103S, U12-ch89, adc2

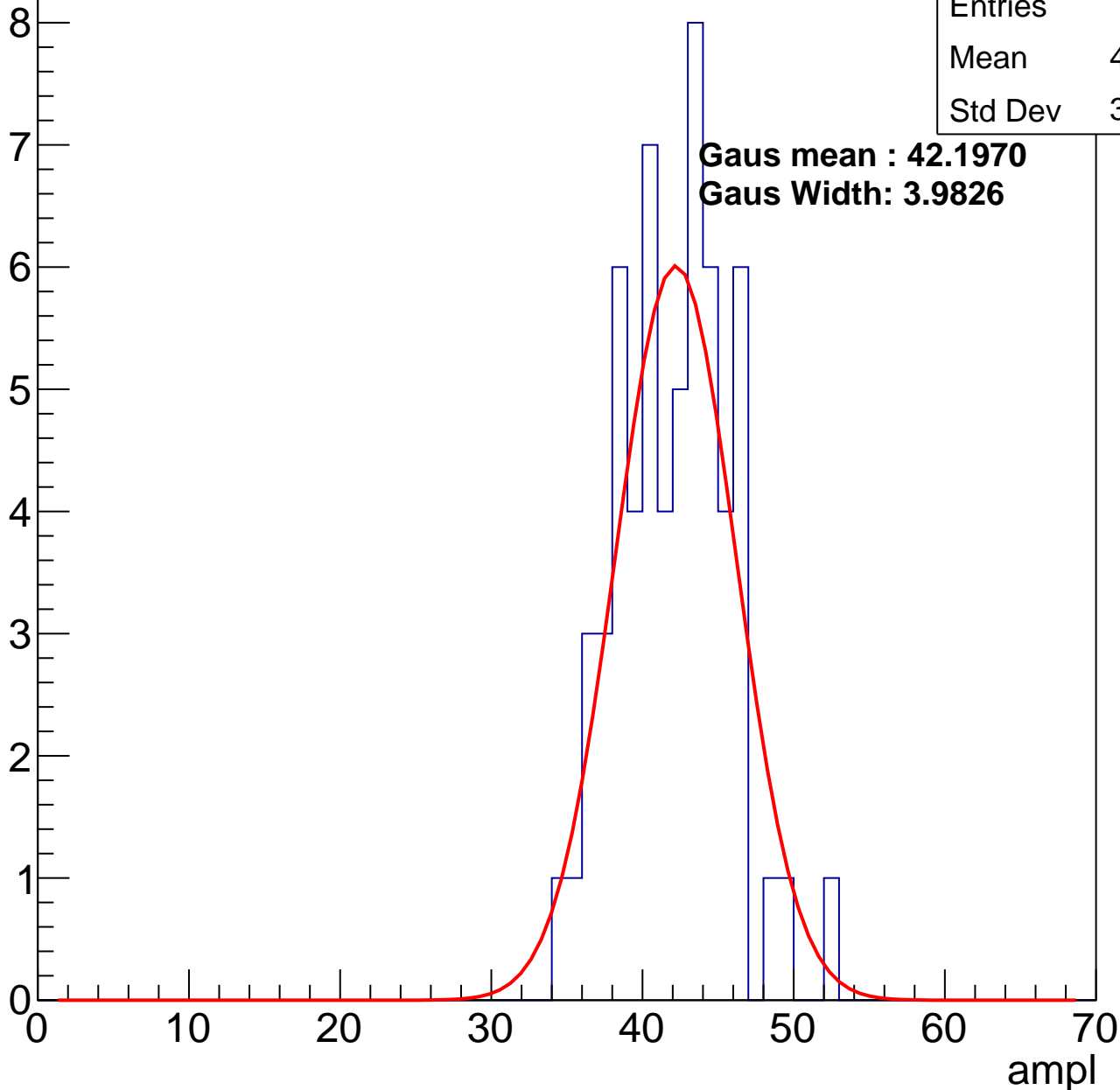
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.62
Std Dev	3.622

Gaus mean : 42.1970

Gaus Width: 3.9826



B1L103S, U12-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

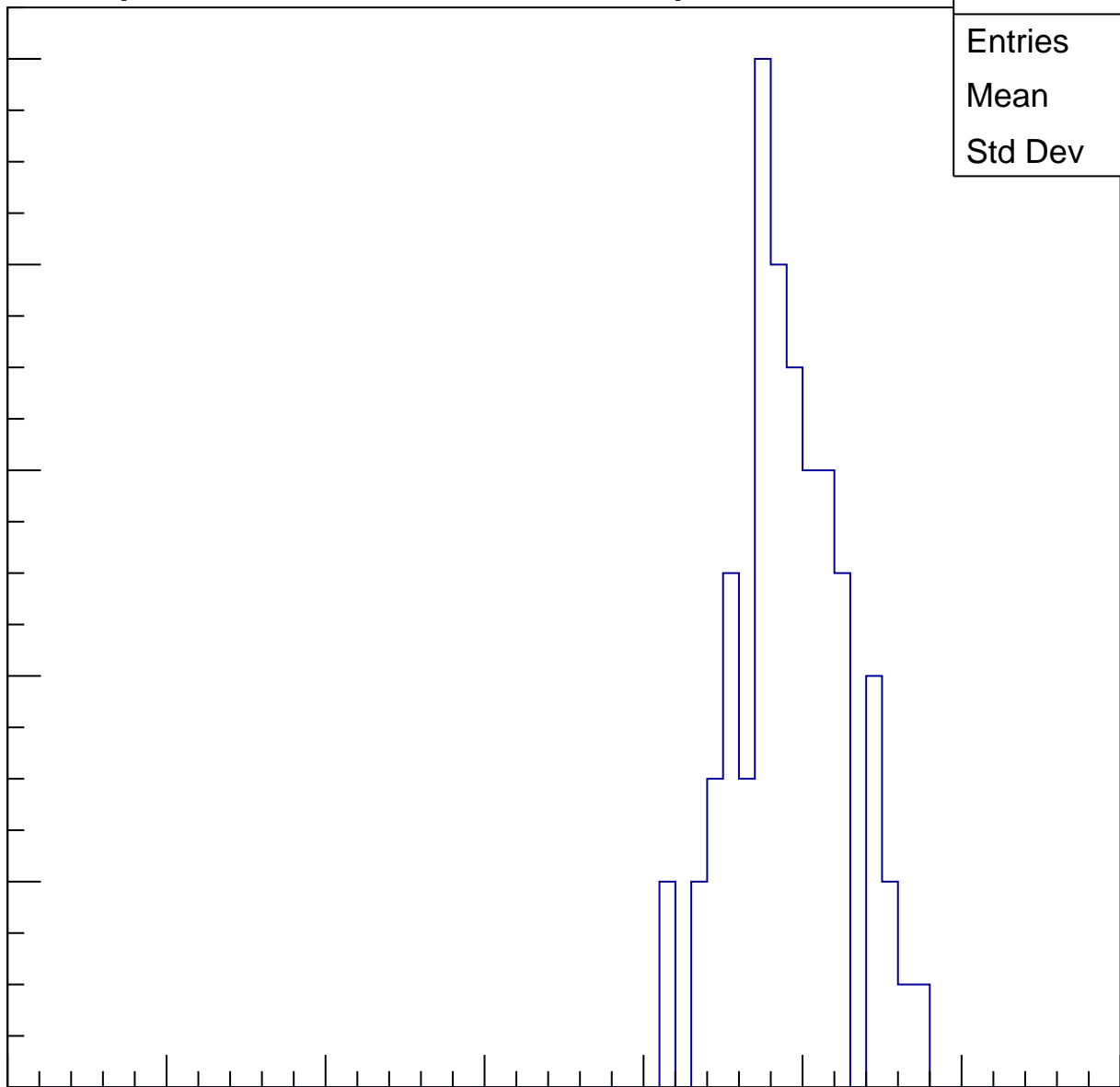
Entries	65
Mean	48.69
Std Dev	3.473

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	54.91
Std Dev	3.362

Entry

10

8

6

4

2

0

0

10

20

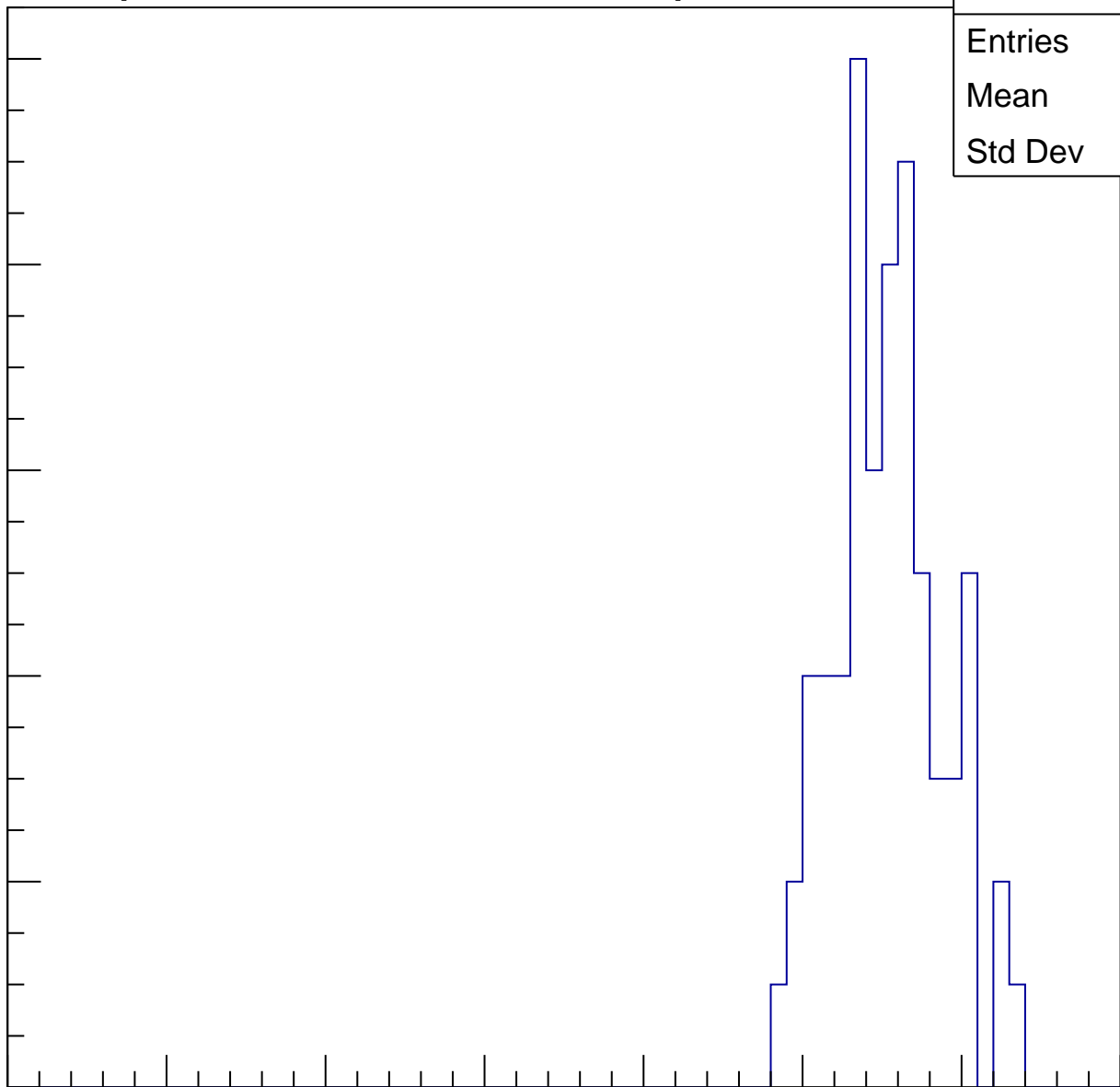
30

40

50

60

ampl

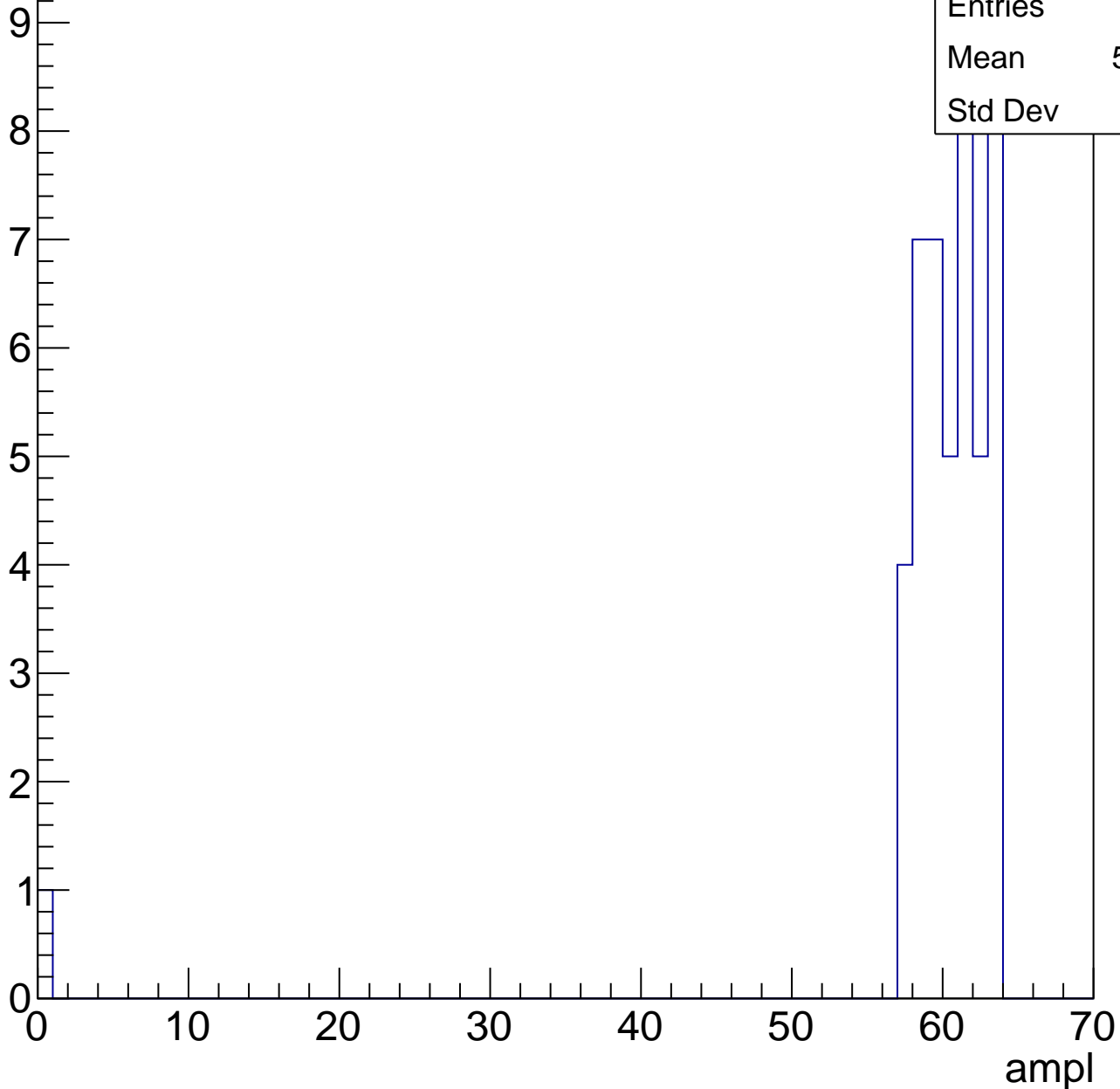


B1L103S, U12-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	58.91
Std Dev	8.99



B1L103S, U12-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	16
Std Dev	0

ampl

B1L103S, U12-ch90, adc0

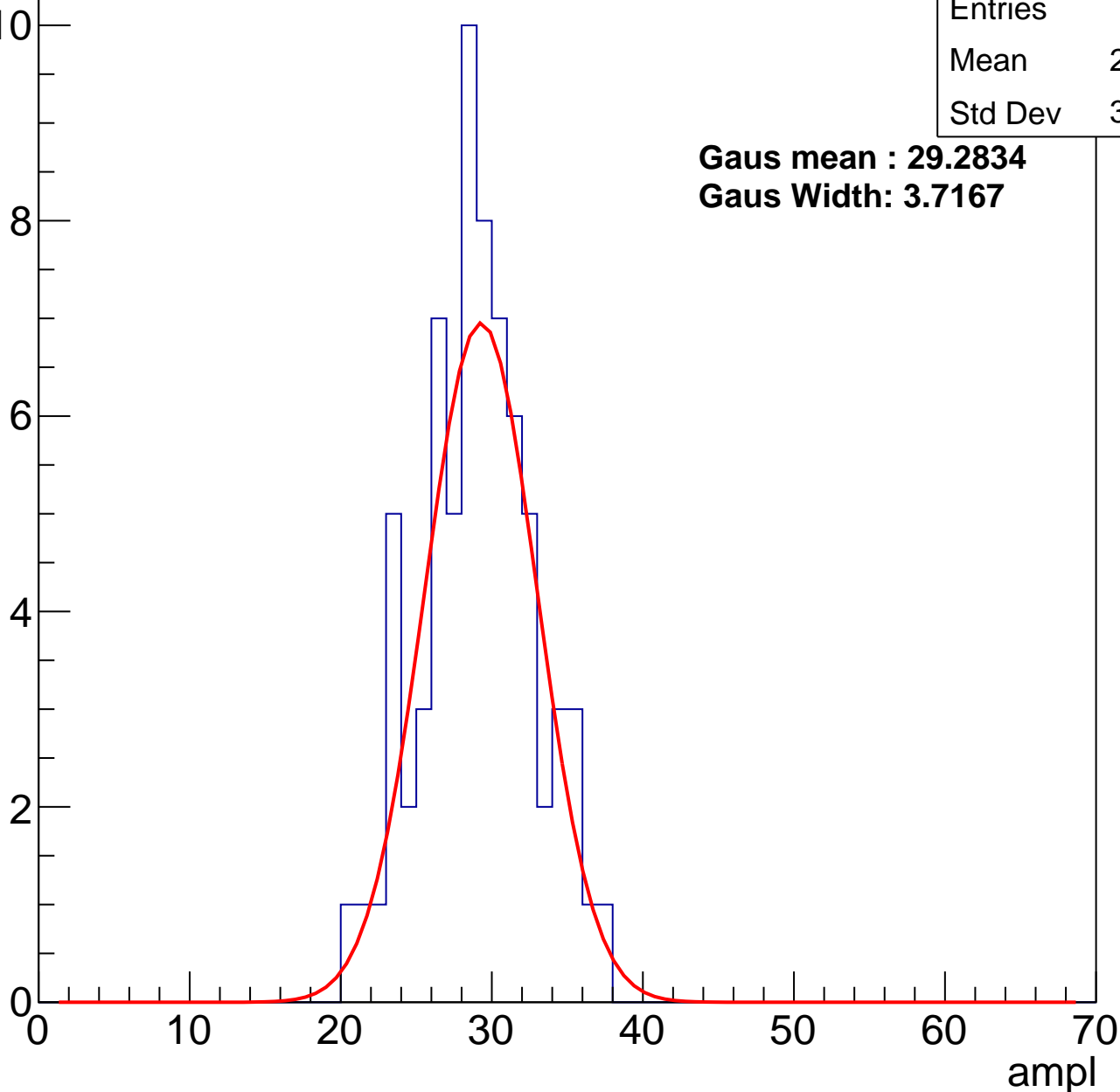
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.62
Std Dev	3.675

Gaus mean : 29.2834

Gaus Width: 3.7167



B1L103S, U12-ch90, adc1

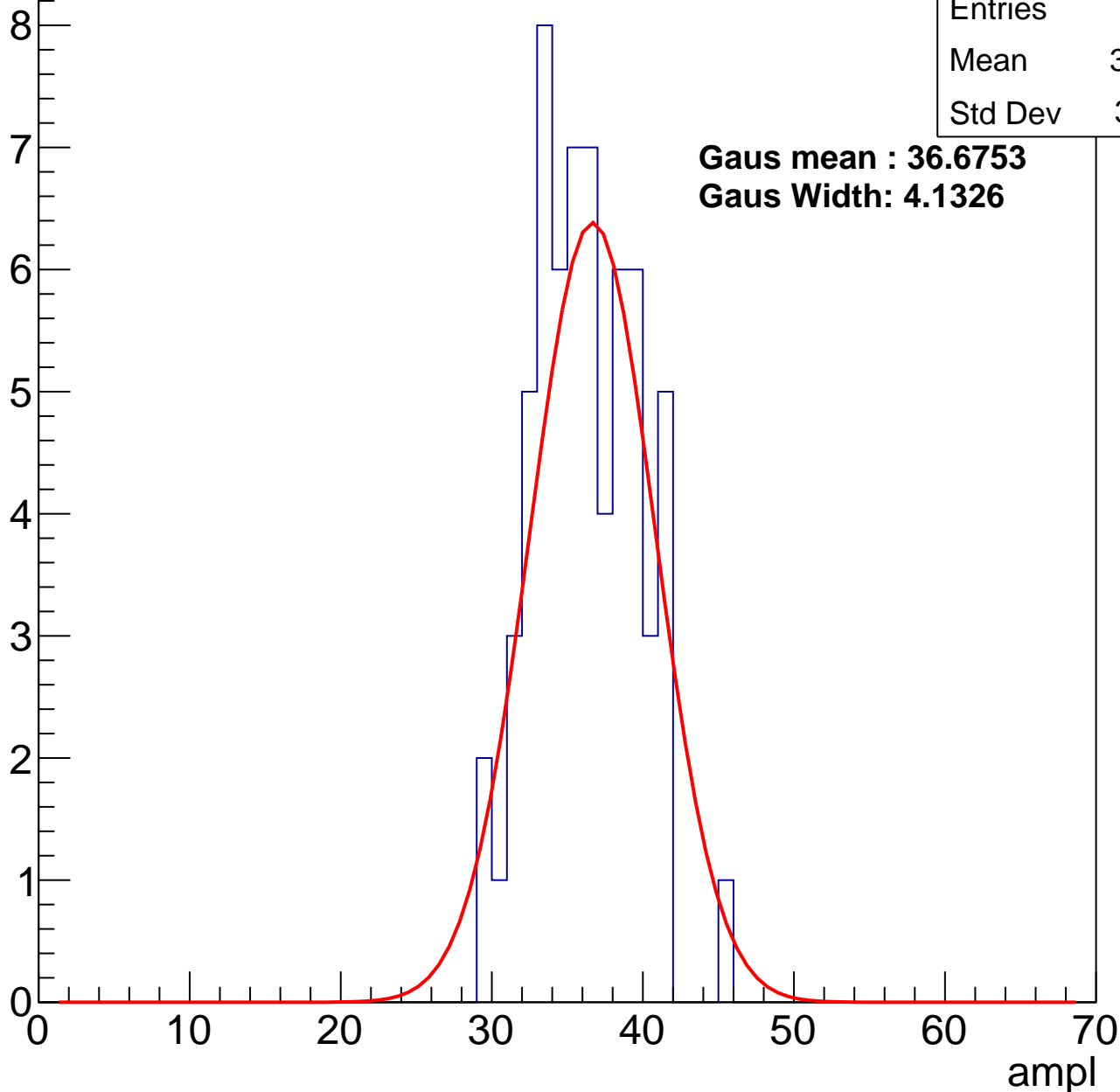
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	35.72
Std Dev	3.361

Gaus mean : 36.6753

Gaus Width: 4.1326



B1L103S, U12-ch90, adc2

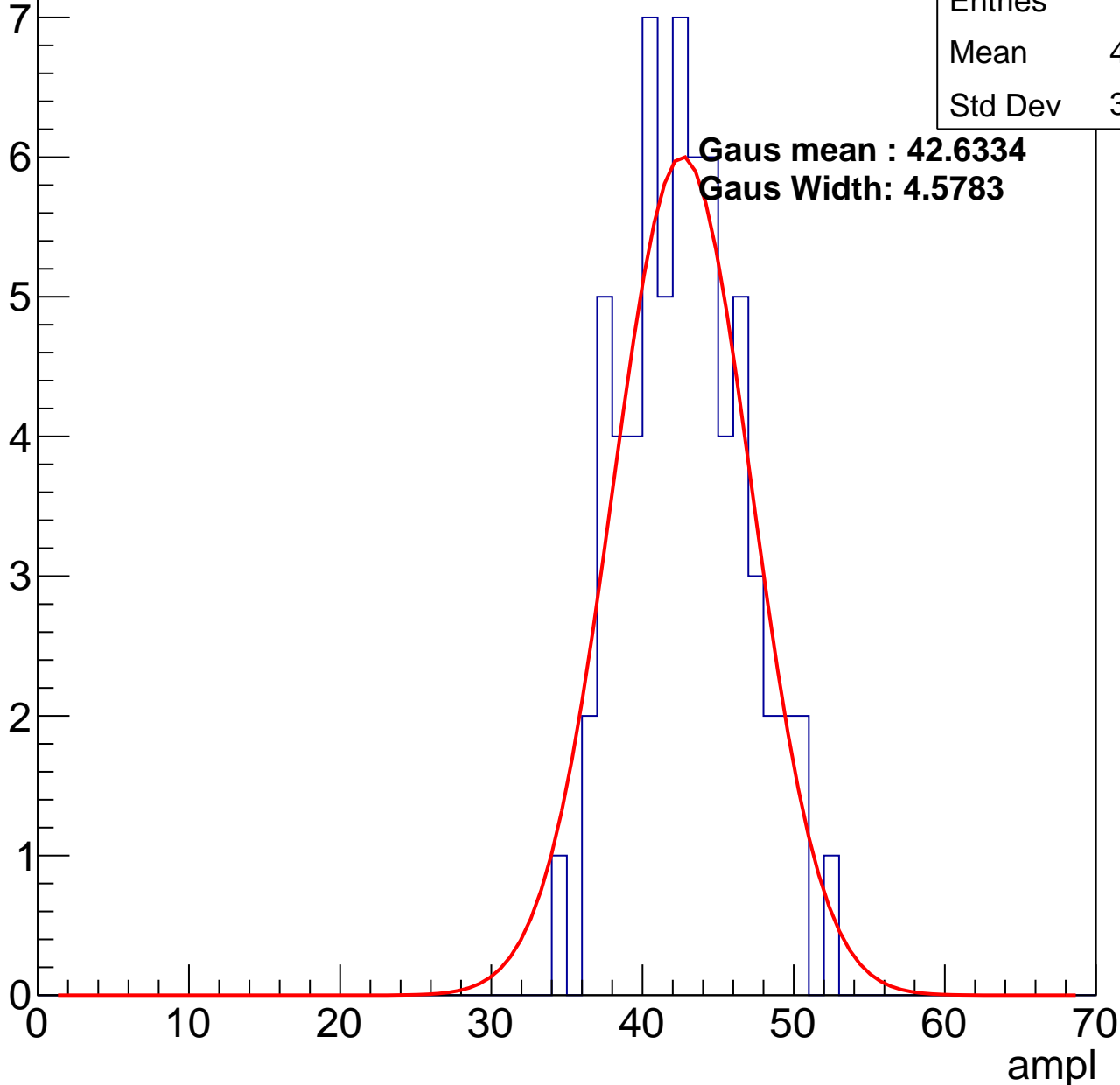
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.38
Std Dev	3.899

Gaus mean : 42.6334

Gaus Width: 4.5783

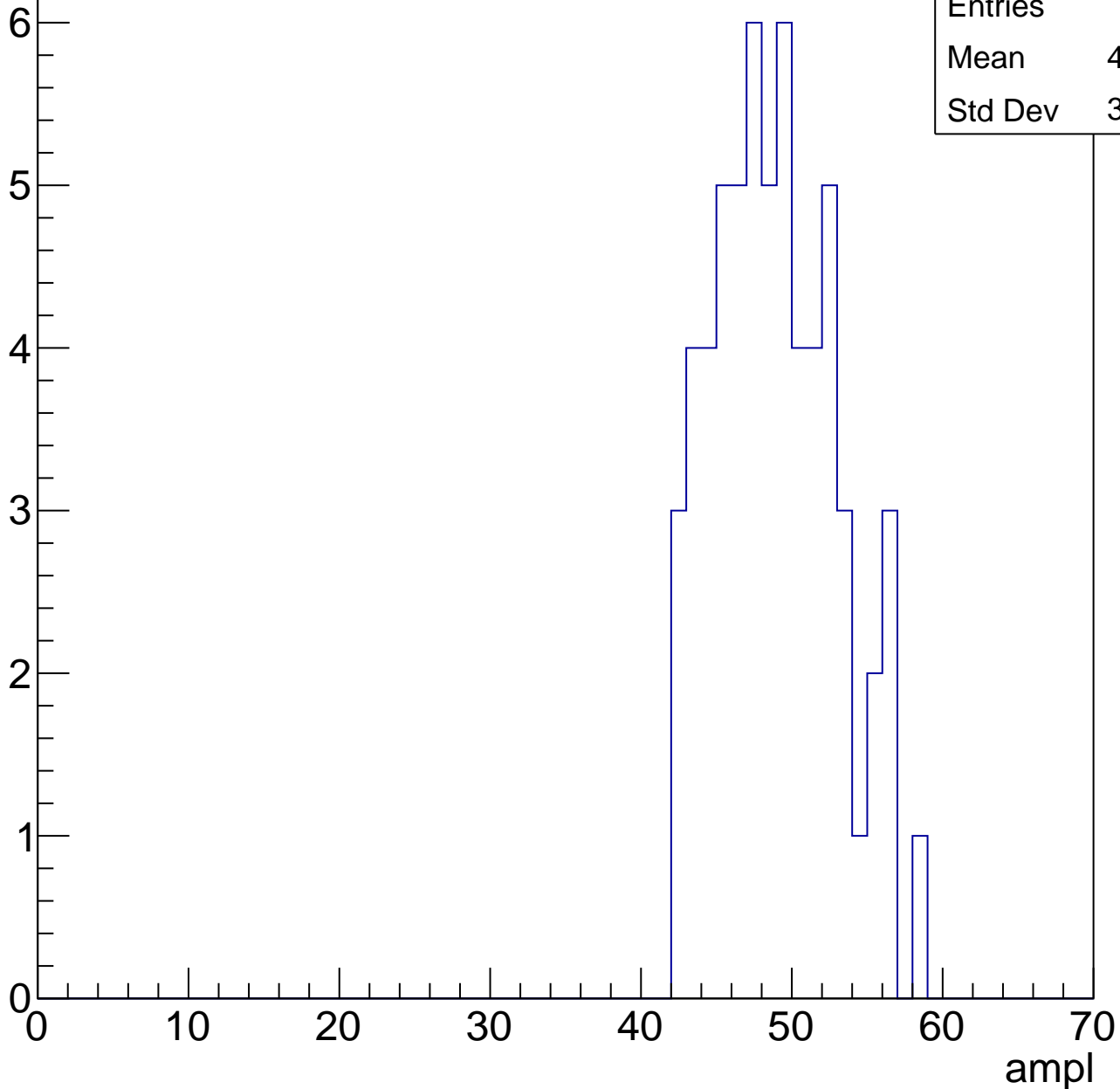


B1L103S, U12-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	48.49
Std Dev	3.982

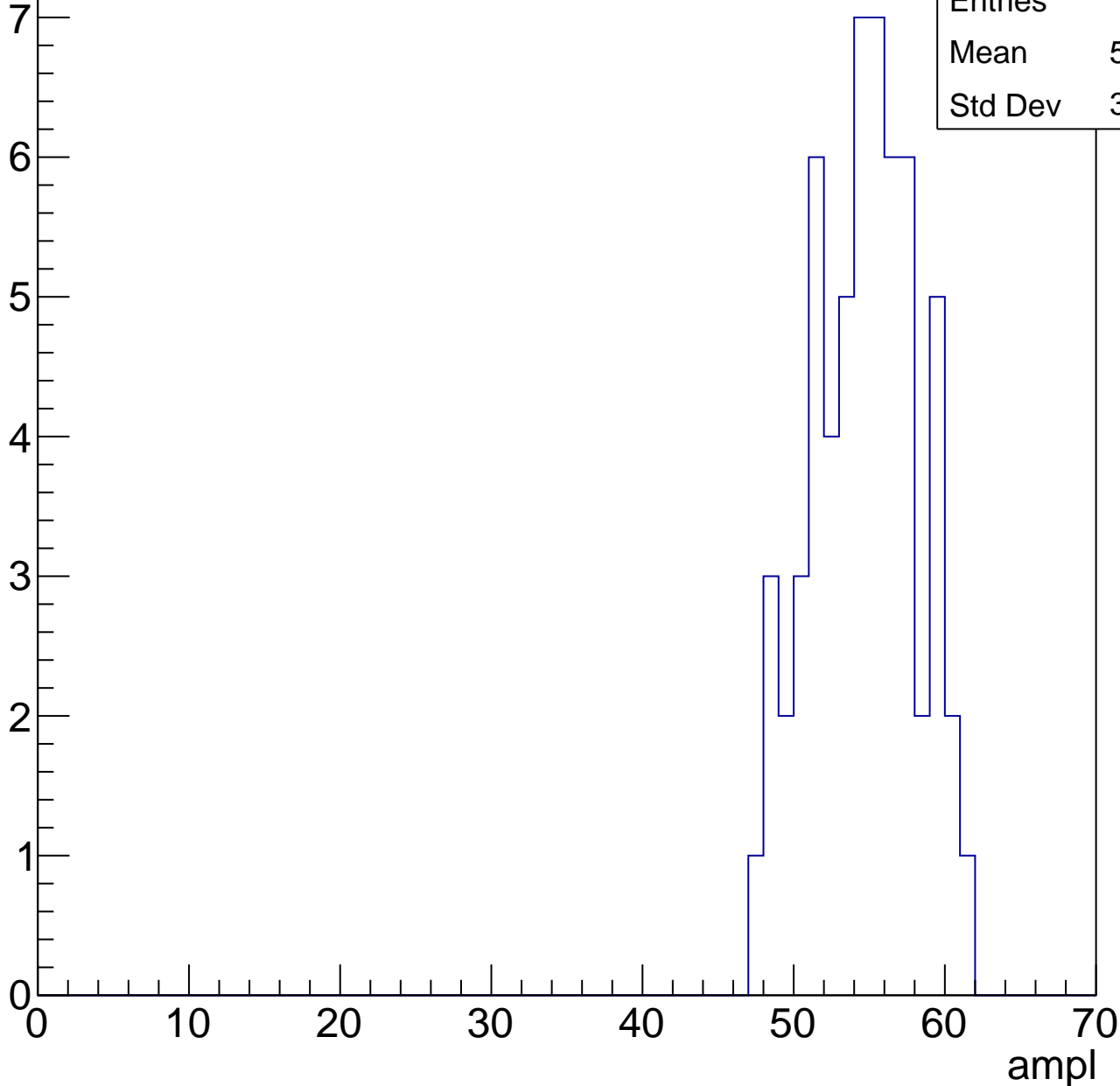


B1L103S, U12-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.18
Std Dev	3.394

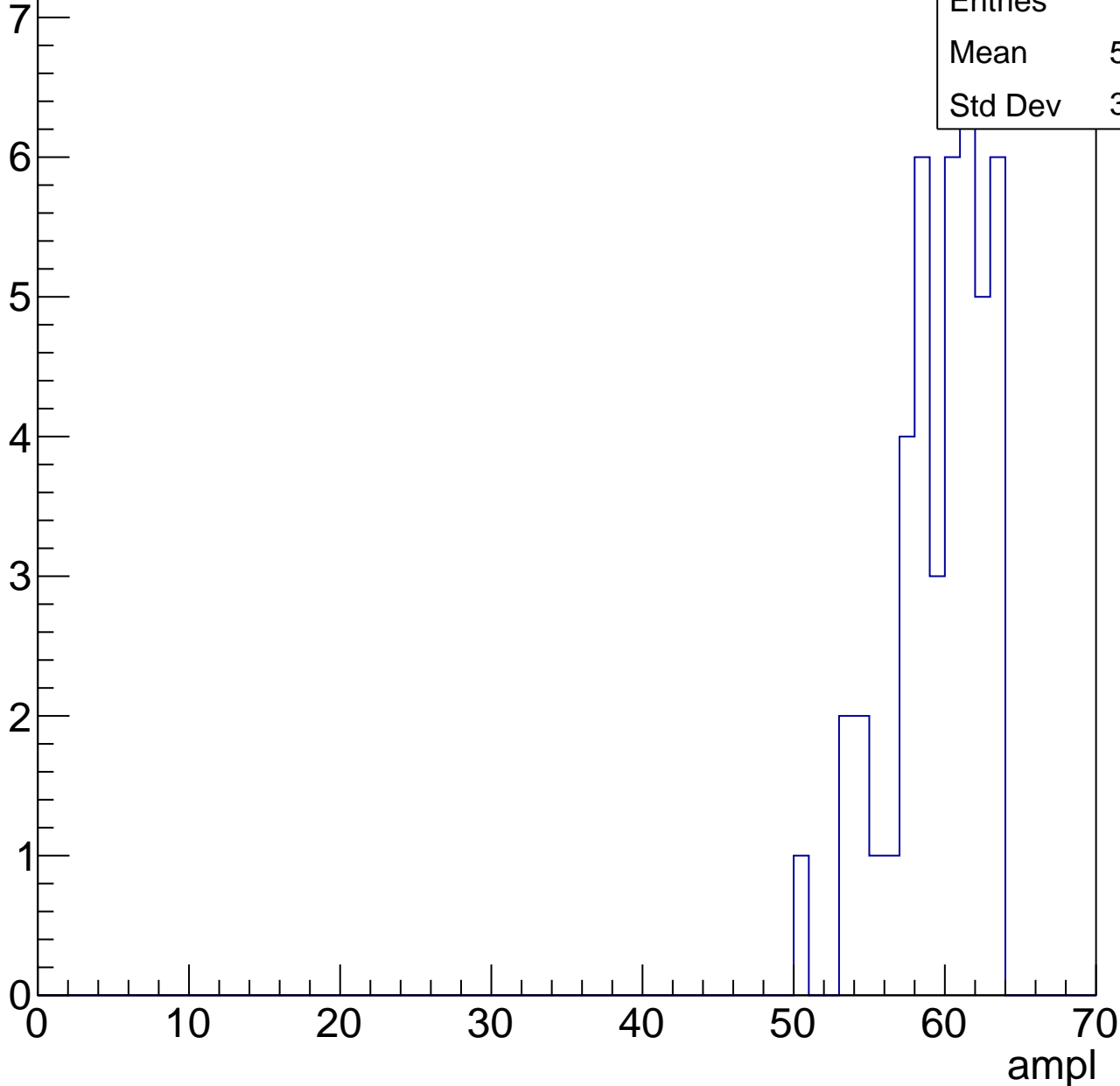


B1L103S, U12-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

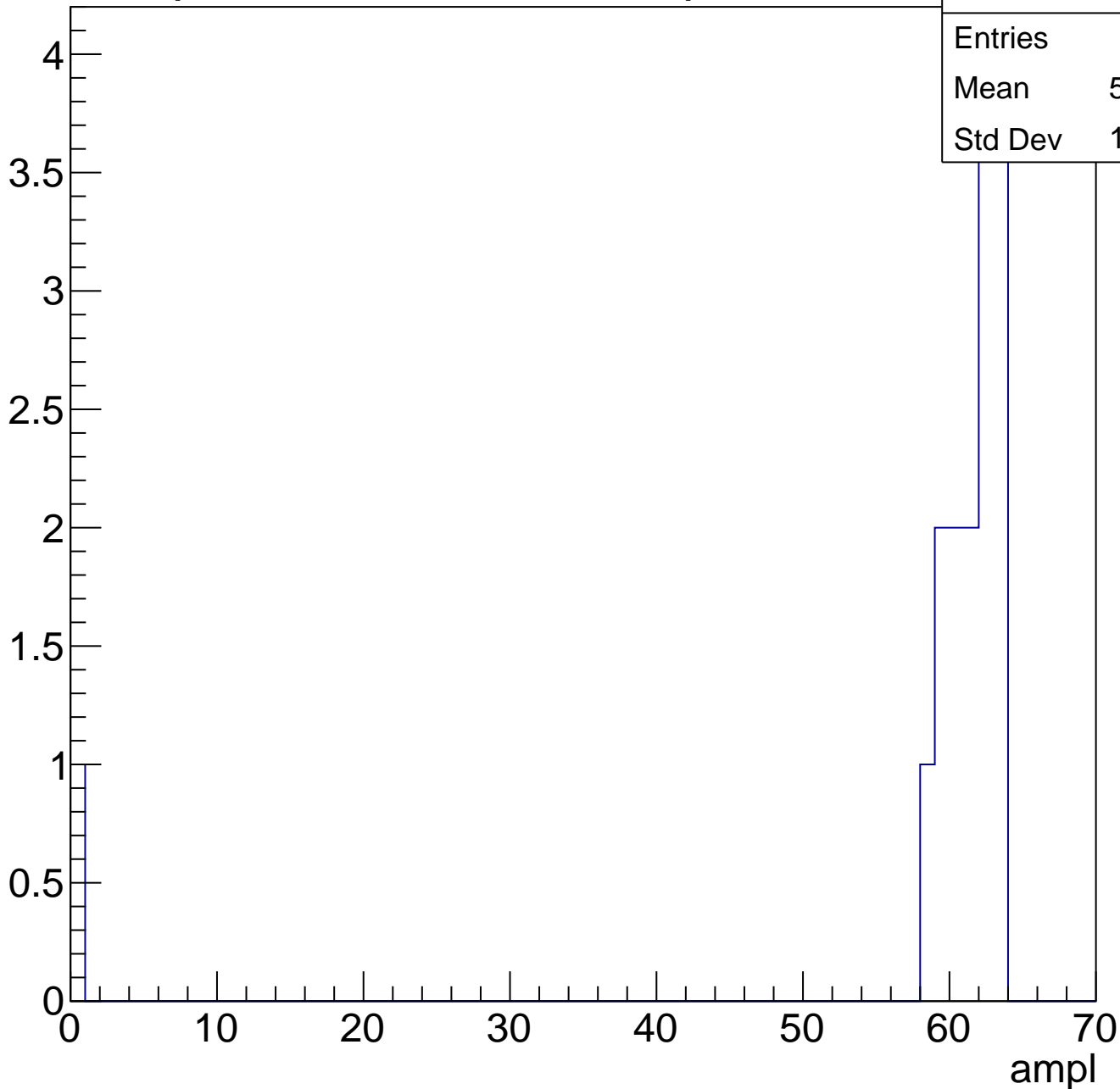
Entries	44
Mean	59.16
Std Dev	3.104



B1L103S, U12-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

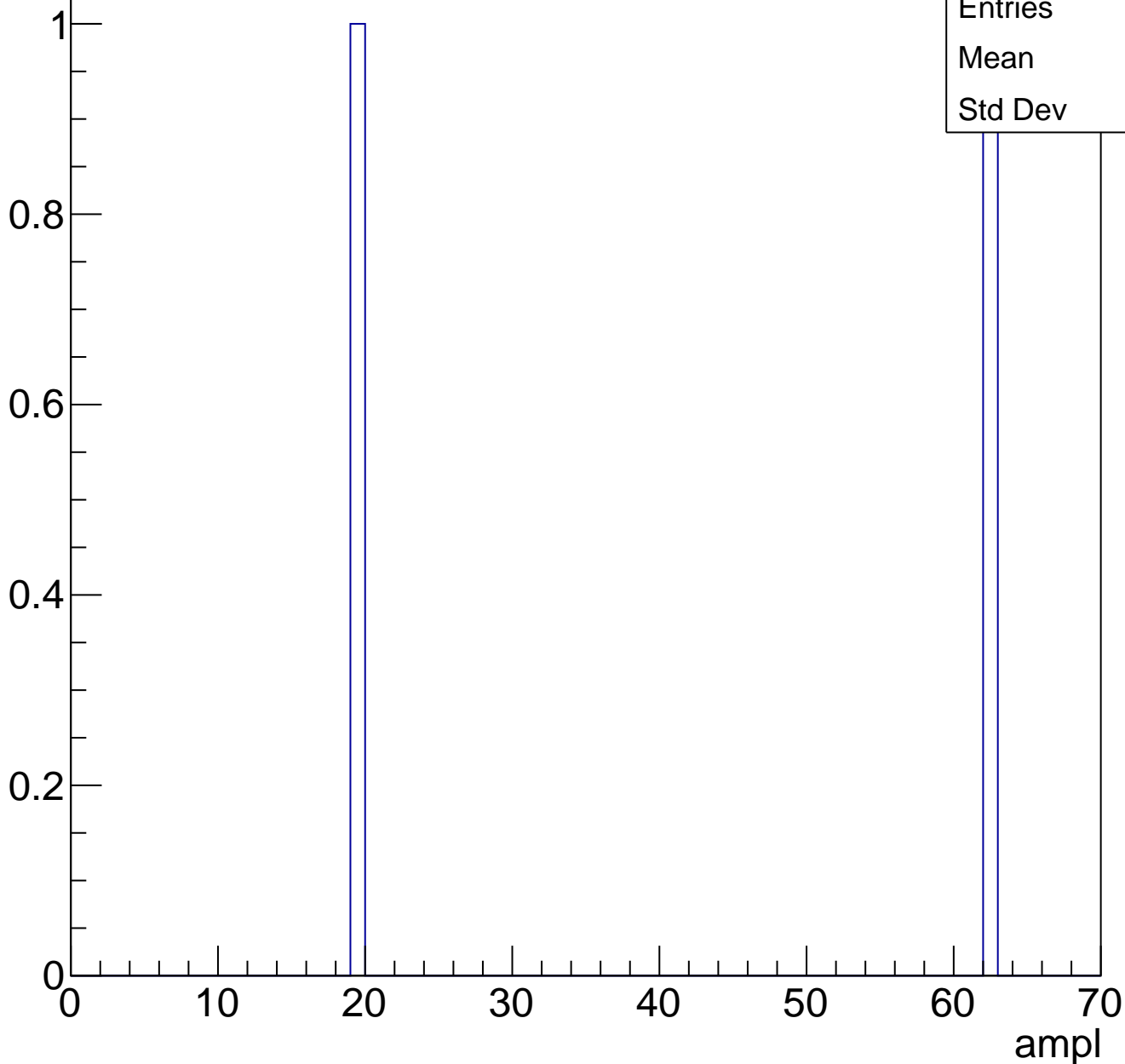
Entry



B1L103S, U12-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch91, adc0

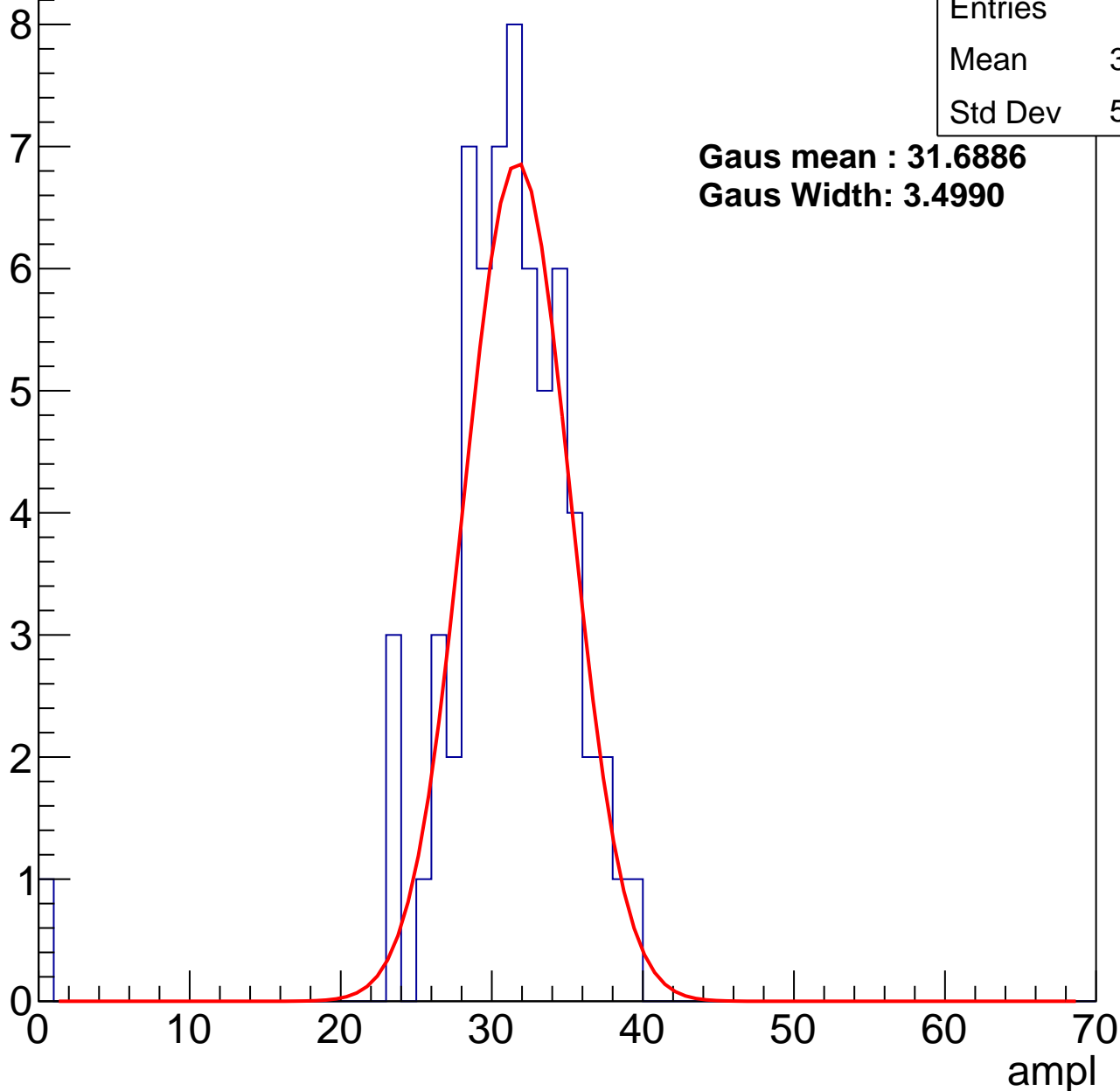
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	30.43
Std Dev	5.186

Gaus mean : 31.6886

Gaus Width: 3.4990



B1L103S, U12-ch91, adc1

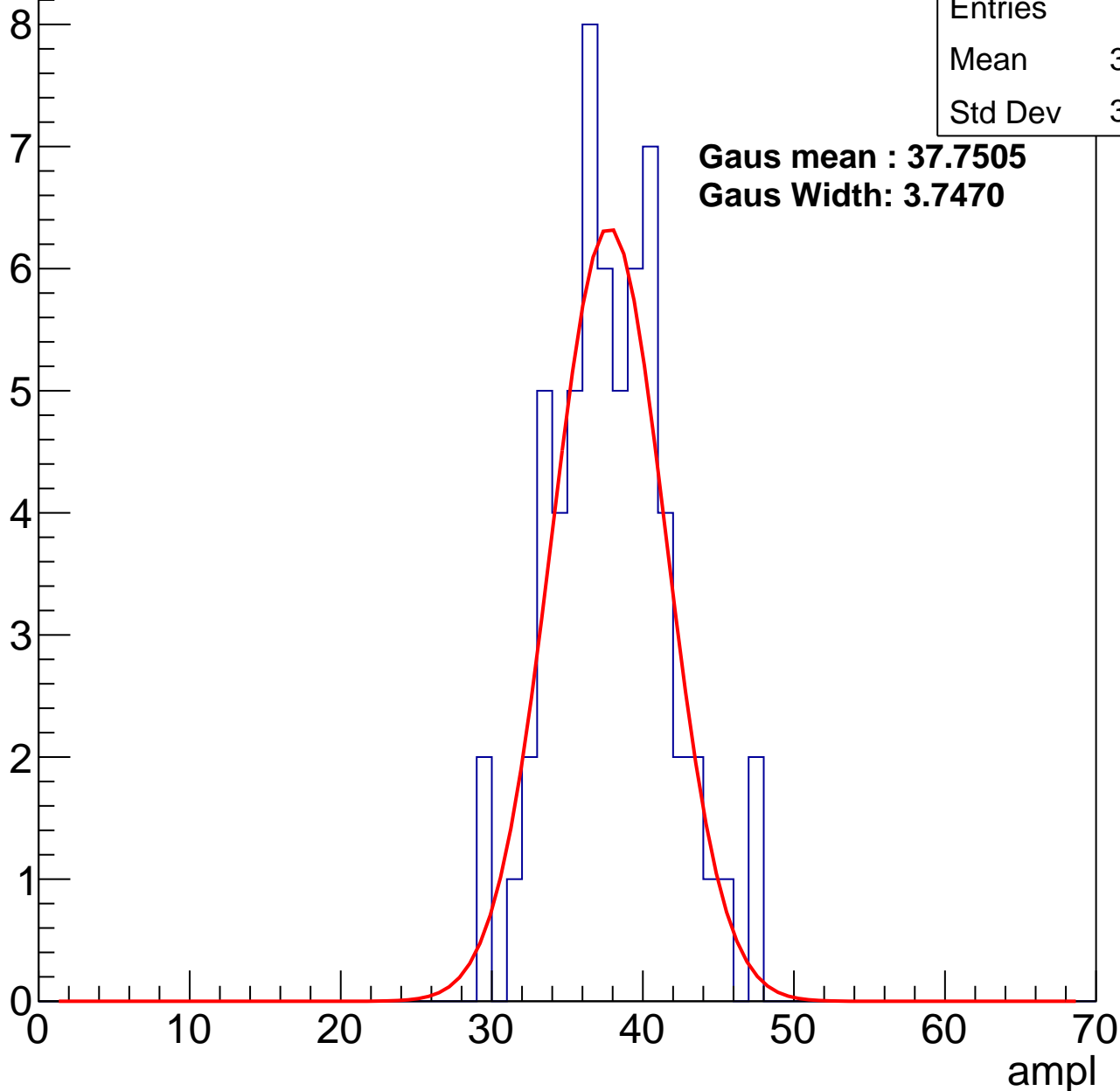
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.46
Std Dev	3.854

Gaus mean : 37.7505

Gaus Width: 3.7470



B1L103S, U12-ch91, adc2

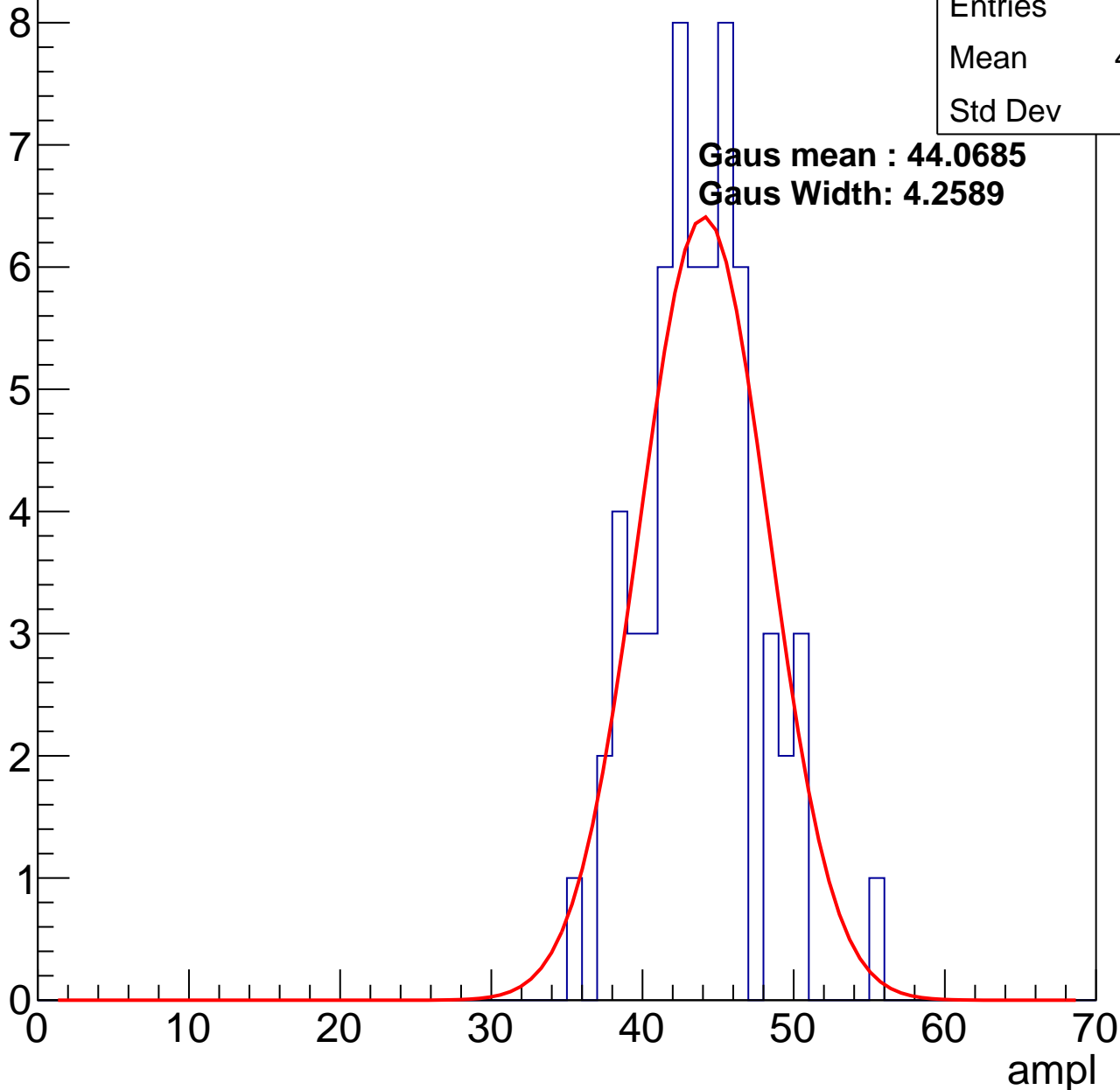
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.31
Std Dev	3.74

Gaus mean : 44.0685

Gaus Width: 4.2589



B1L103S, U12-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	50
Std Dev	3.456

Entry

10

8

6

4

2

0

0

10

20

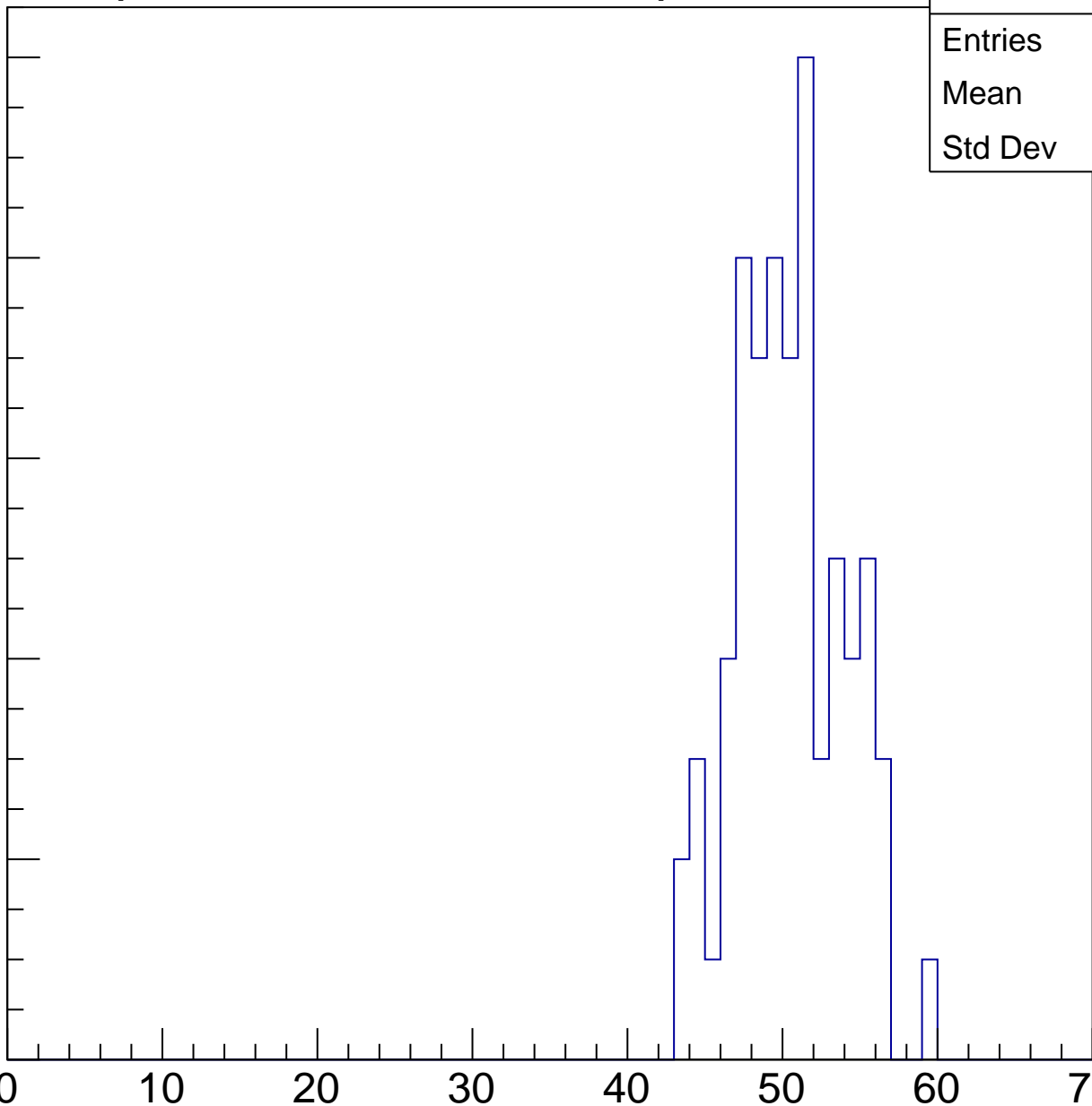
30

40

50

60

ampl

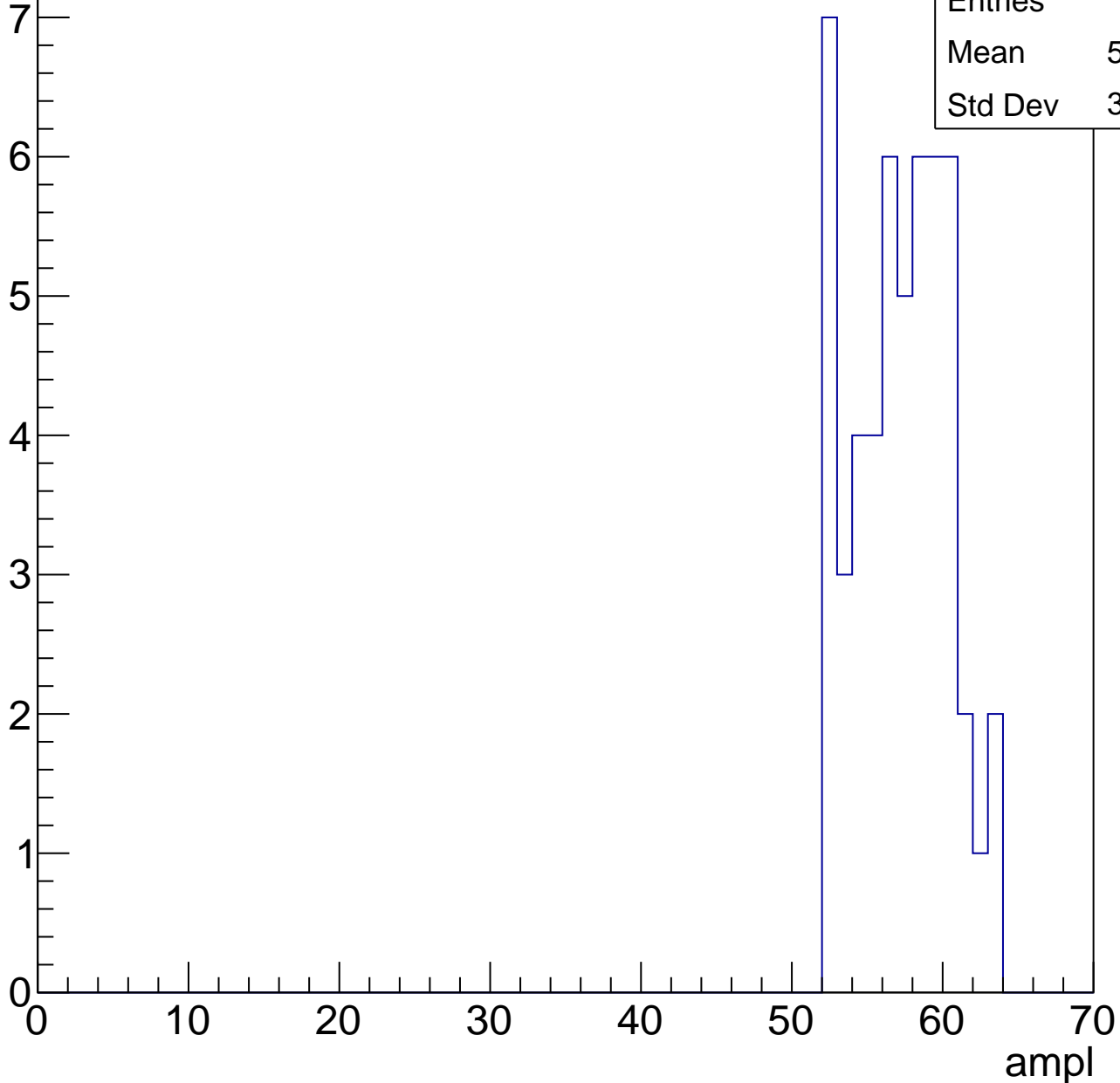


B1L103S, U12-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	56.77
Std Dev	3.074

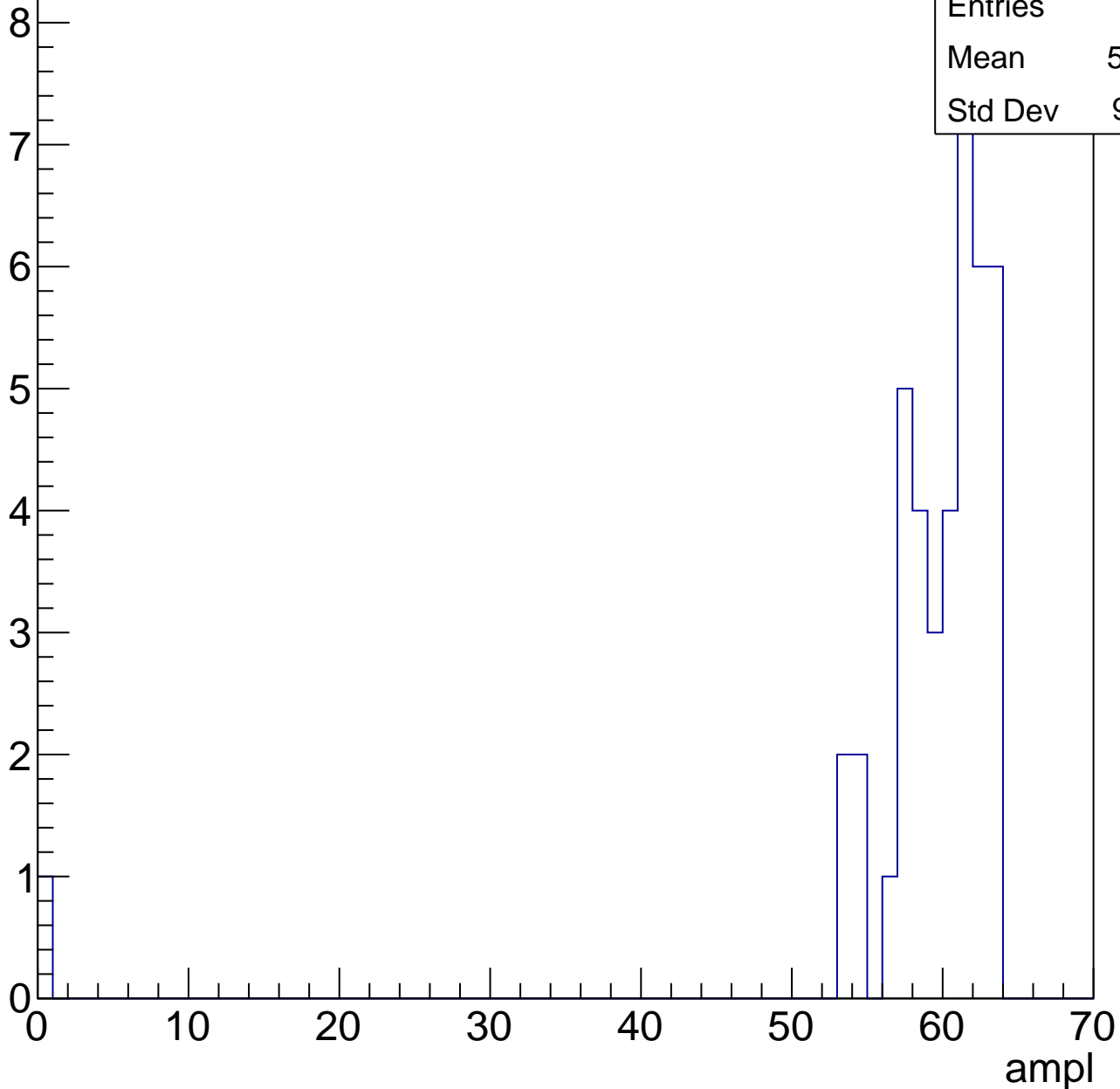


B1L103S, U12-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.14
Std Dev	9.501



B1L103S, U12-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch92, adc0

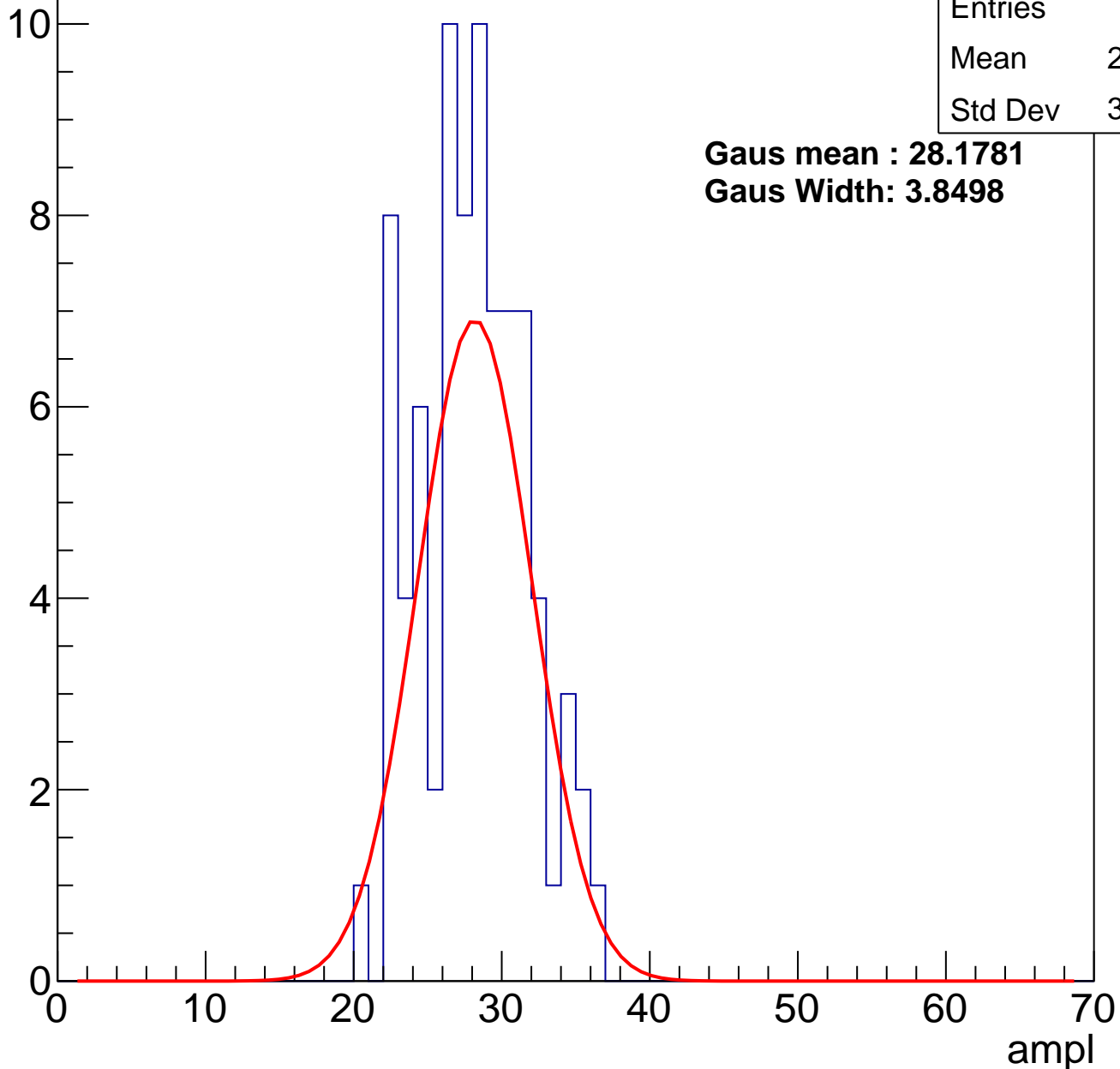
calib_packv5_042523_0143.root, FC#7, port C2

Entries	81
Mean	27.62
Std Dev	3.616

Gaus mean : 28.1781

Gaus Width: 3.8498

Entry



B1L103S, U12-ch92, adc1

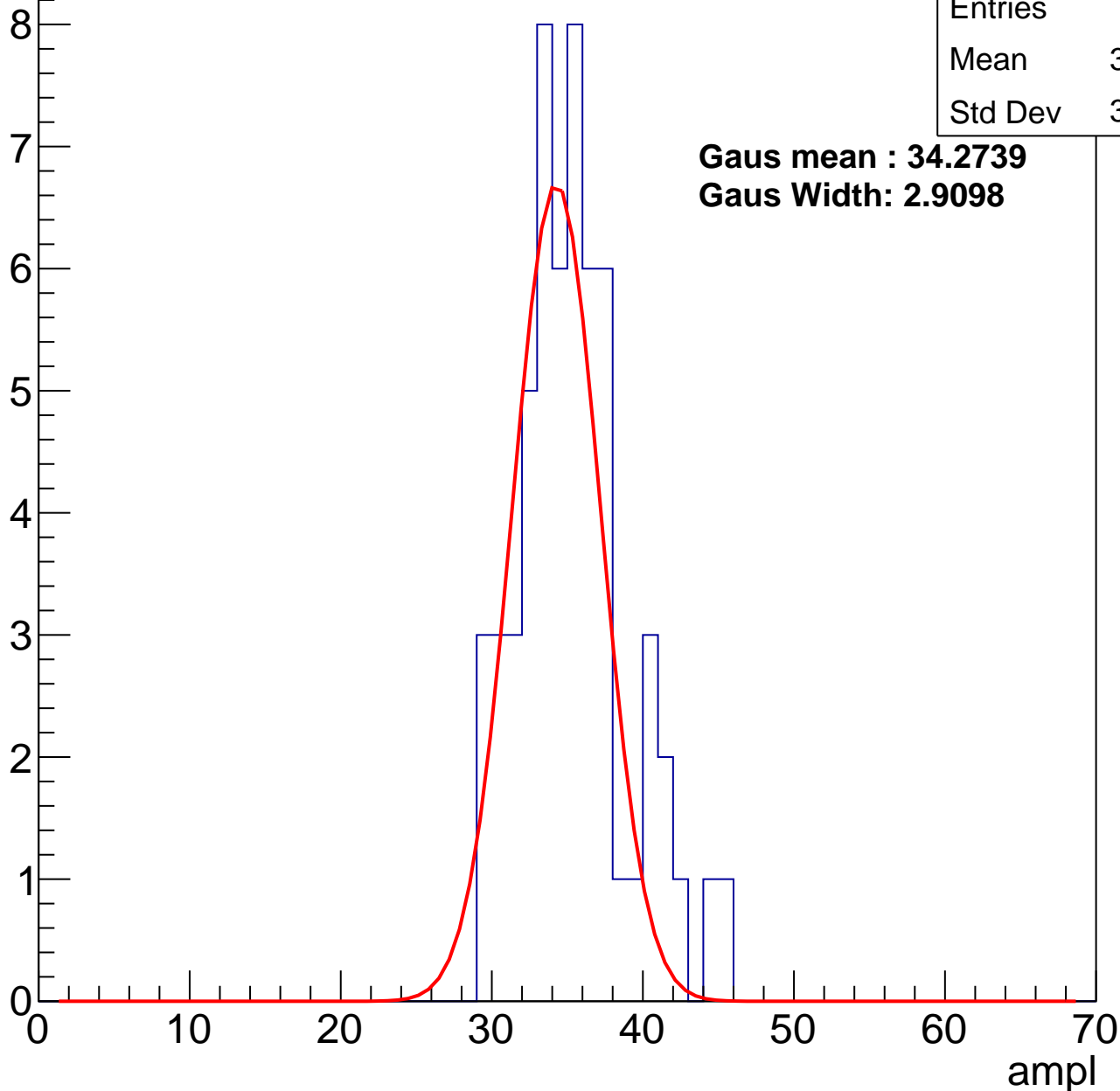
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	34.93
Std Dev	3.586

Gaus mean : 34.2739

Gaus Width: 2.9098



B1L103S, U12-ch92, adc2

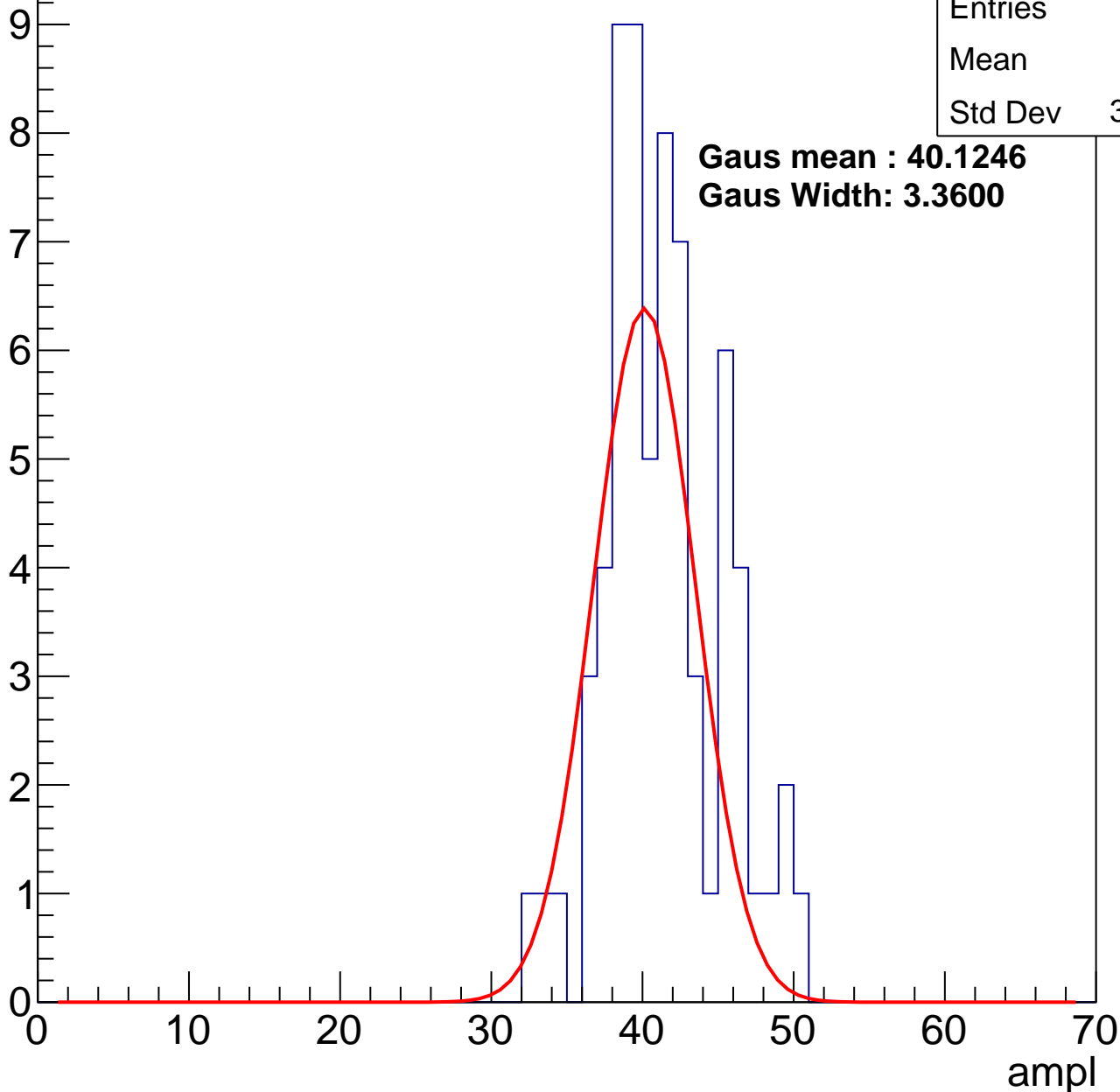
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	40.9
Std Dev	3.809

Gaus mean : 40.1246

Gaus Width: 3.3600

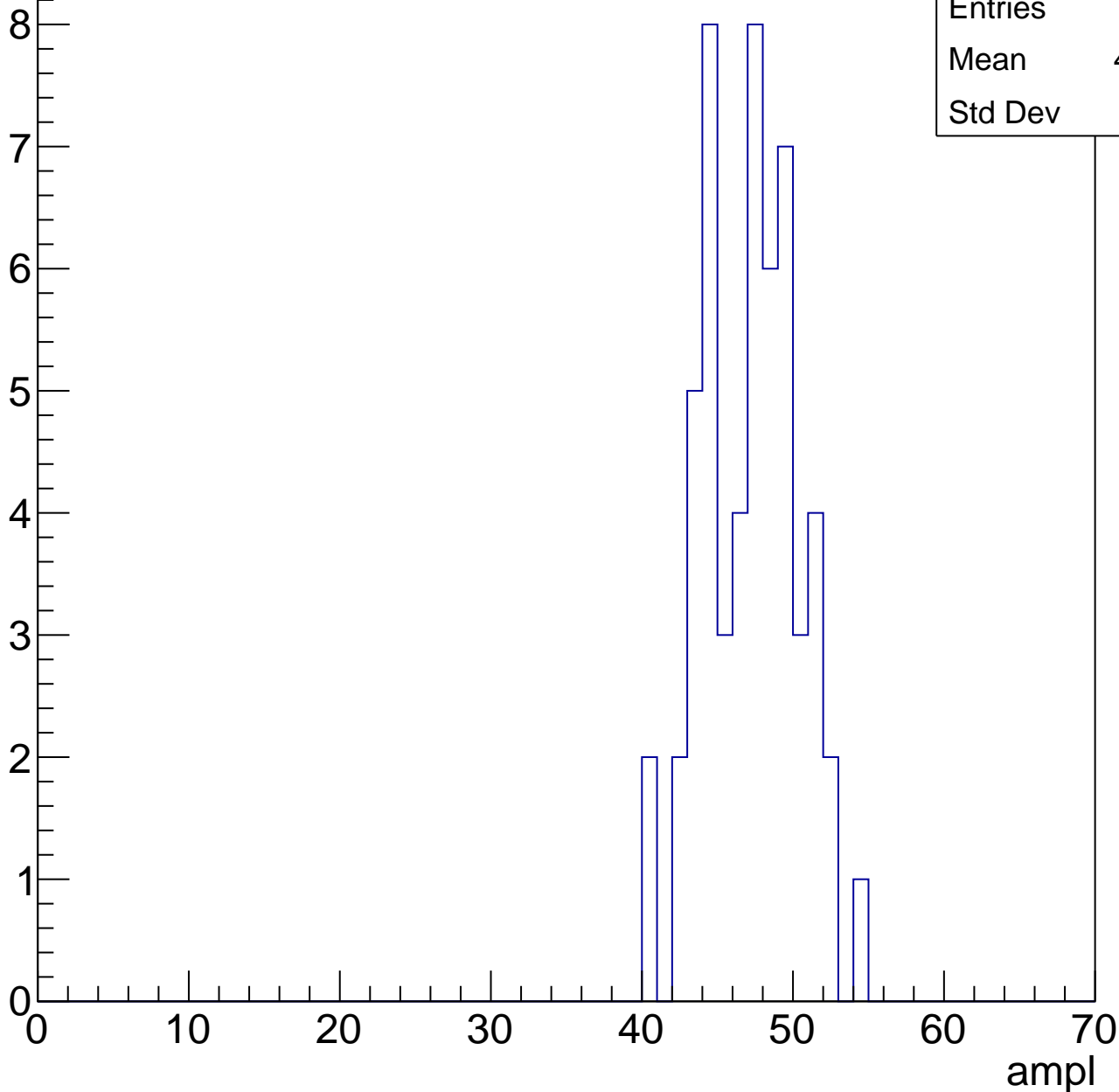


B1L103S, U12-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	46.71
Std Dev	3.12

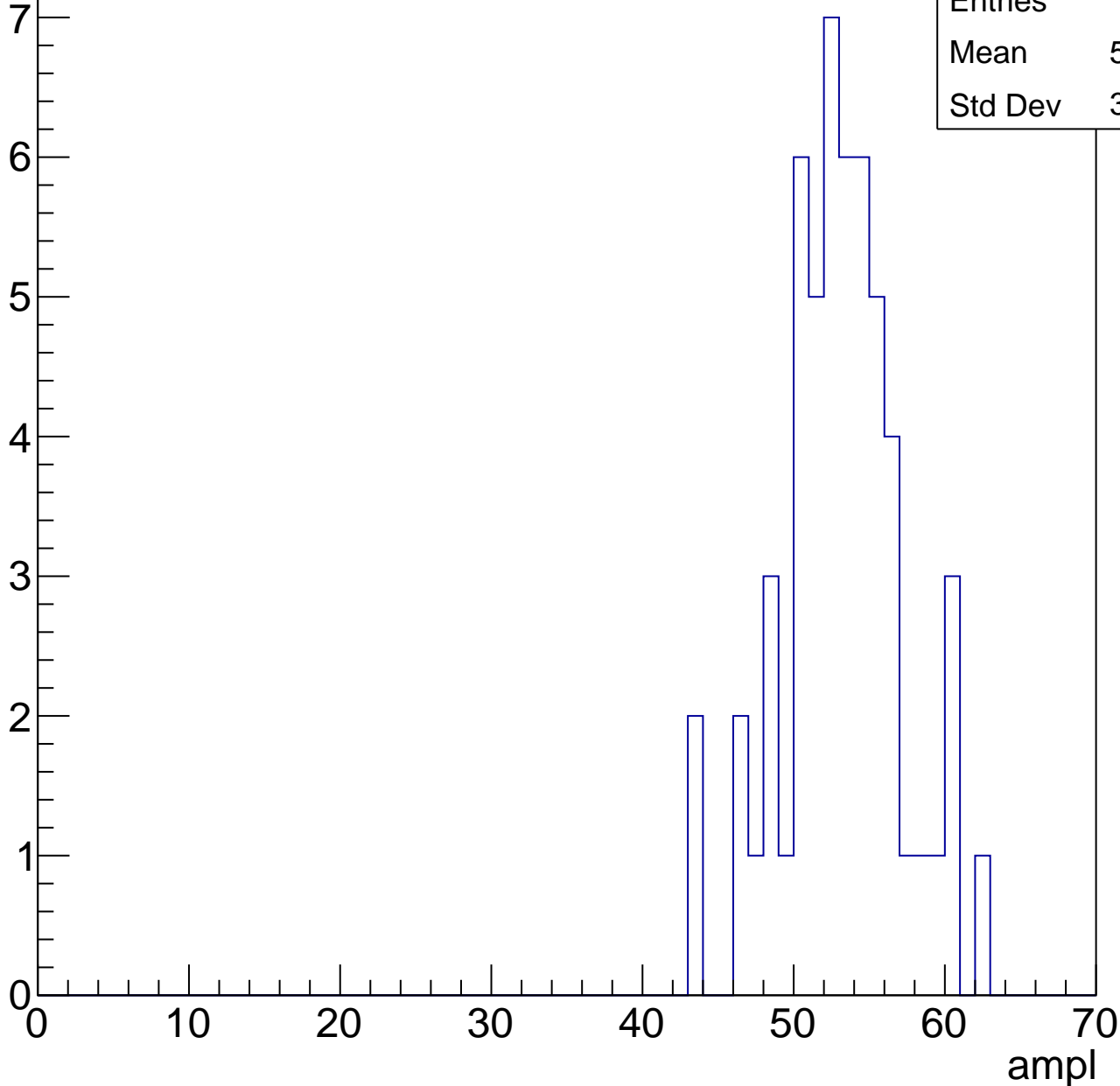


B1L103S, U12-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

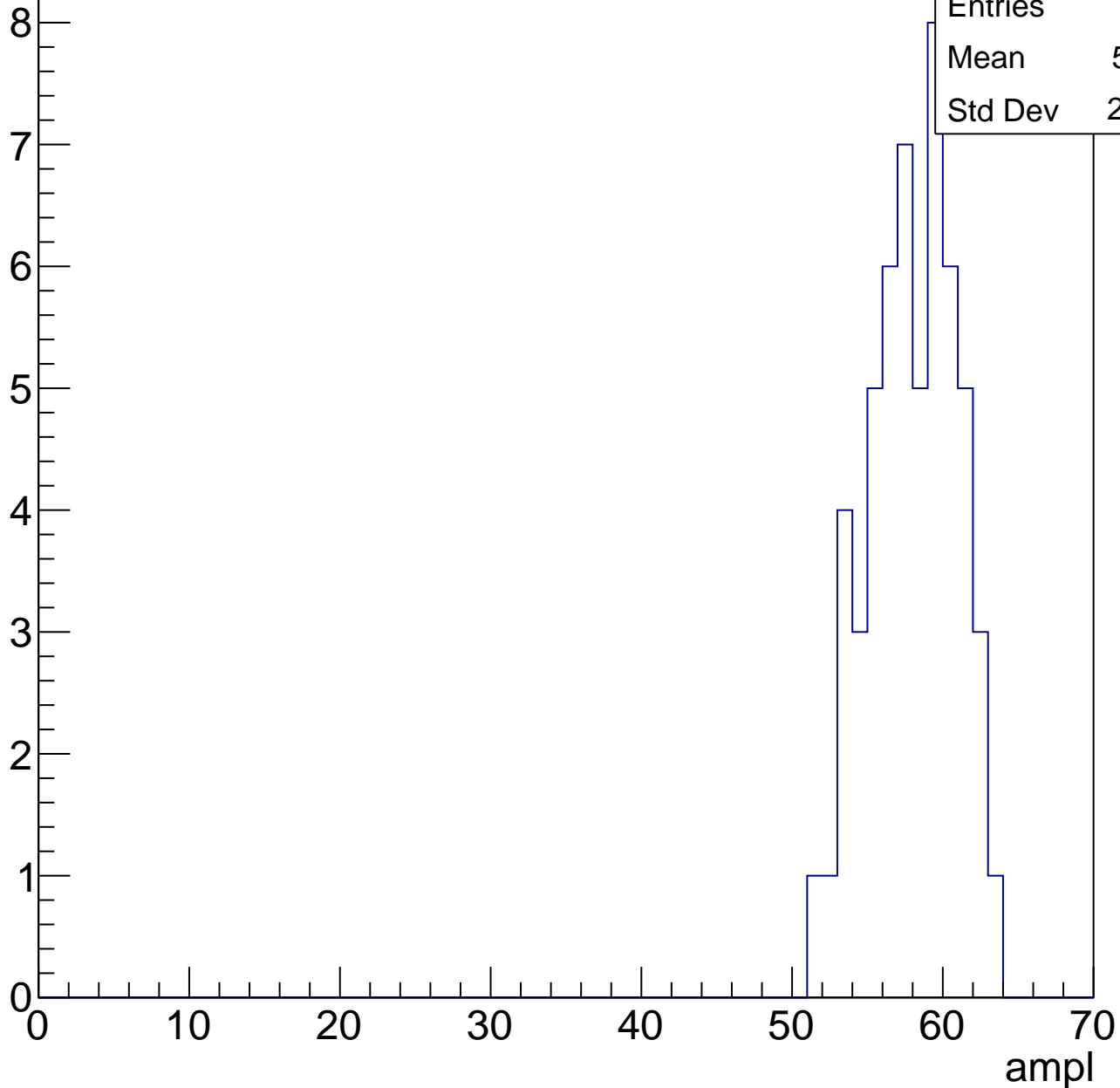
Entries	55
Mean	52.62
Std Dev	3.966



B1L103S, U12-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

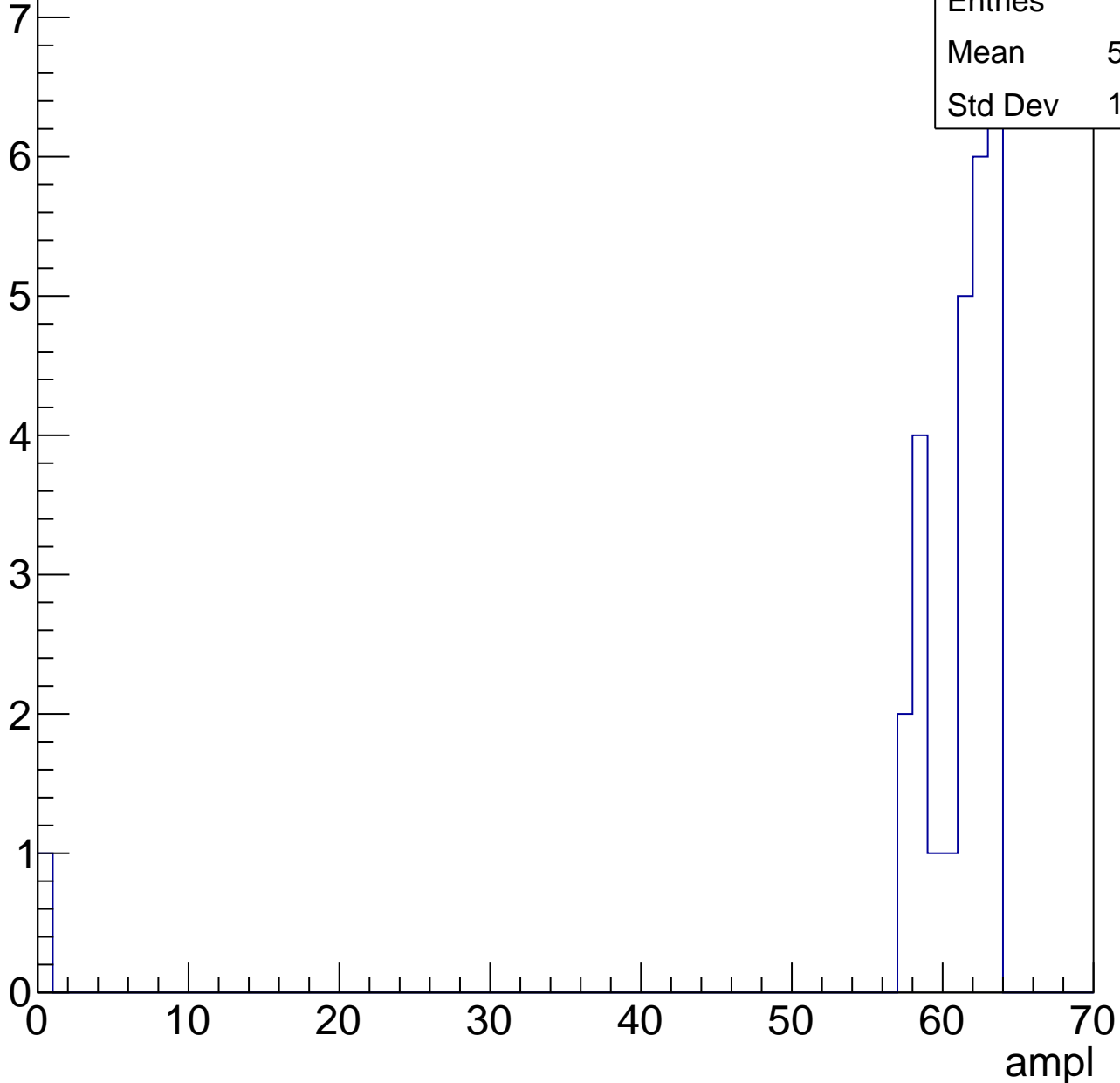


B1L103S, U12-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	58.63
Std Dev	11.67



B1L103S, U12-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	63
Std Dev	0

B1L103S, U12-ch93, adc0

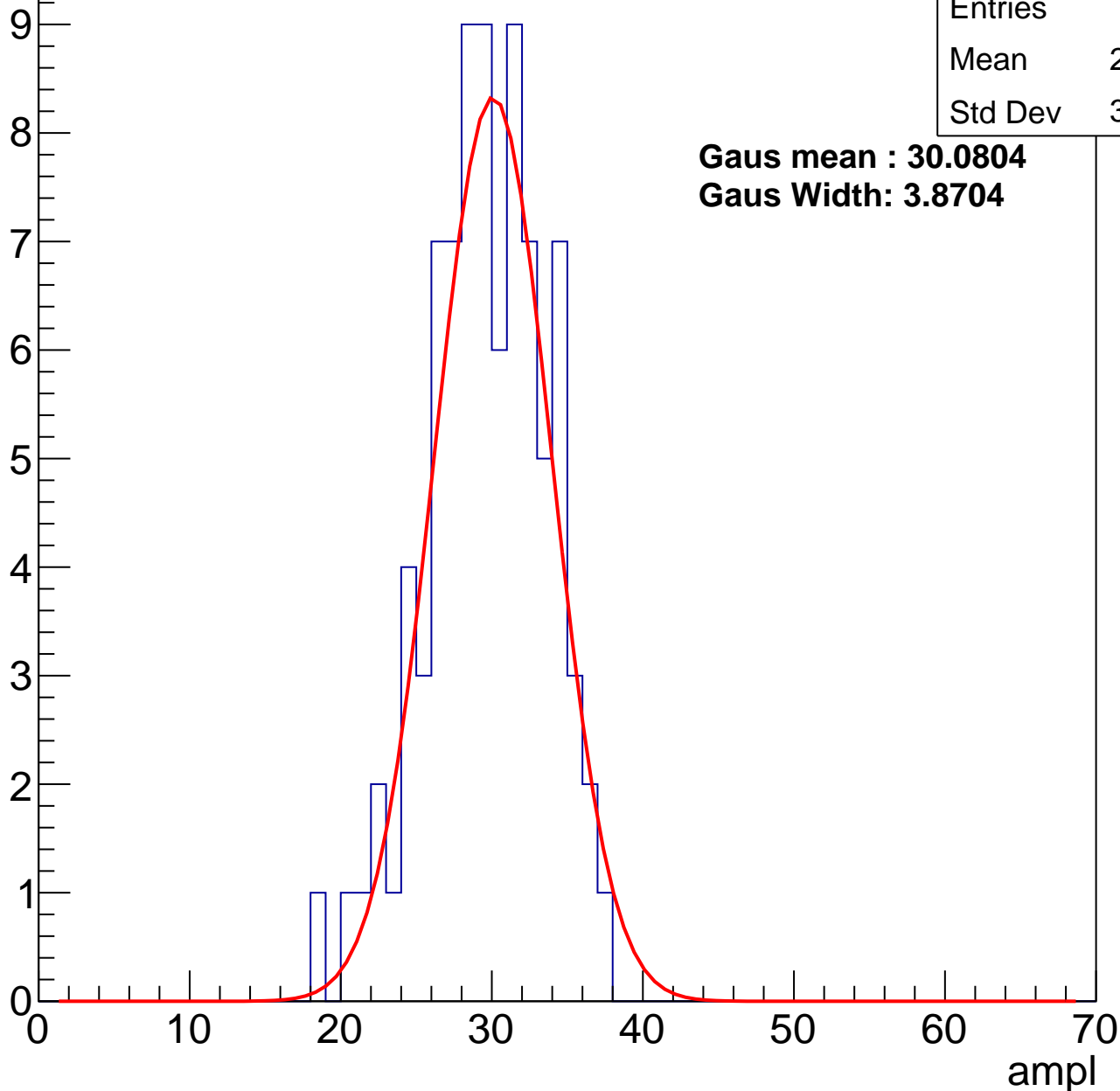
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	29.19
Std Dev	3.873

Gaus mean : 30.0804

Gaus Width: 3.8704



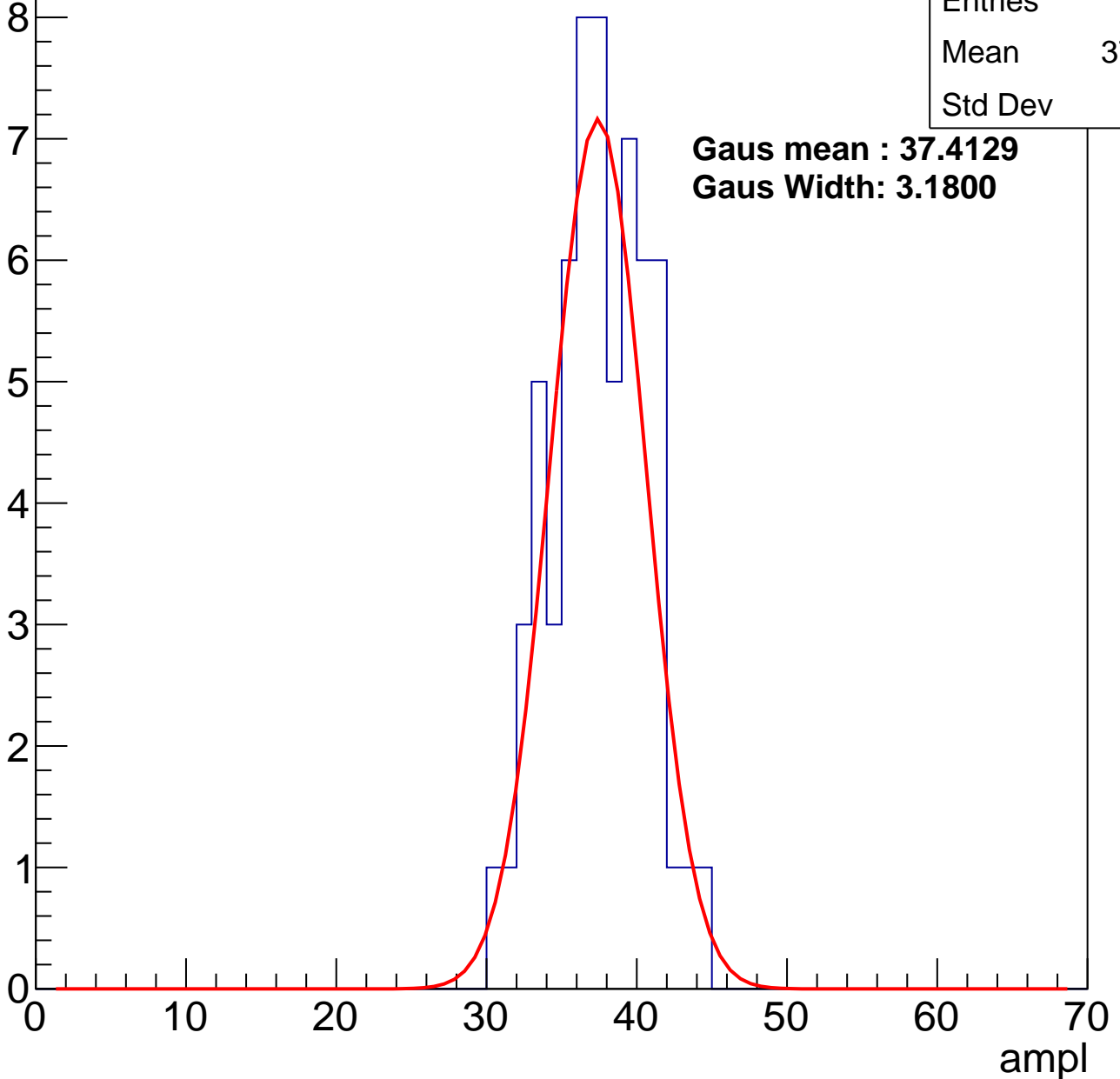
B1L103S, U12-ch93, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	37.03
Std Dev	3.09

Gaus mean : 37.4129
Gaus Width: 3.1800



B1L103S, U12-ch93, adc2

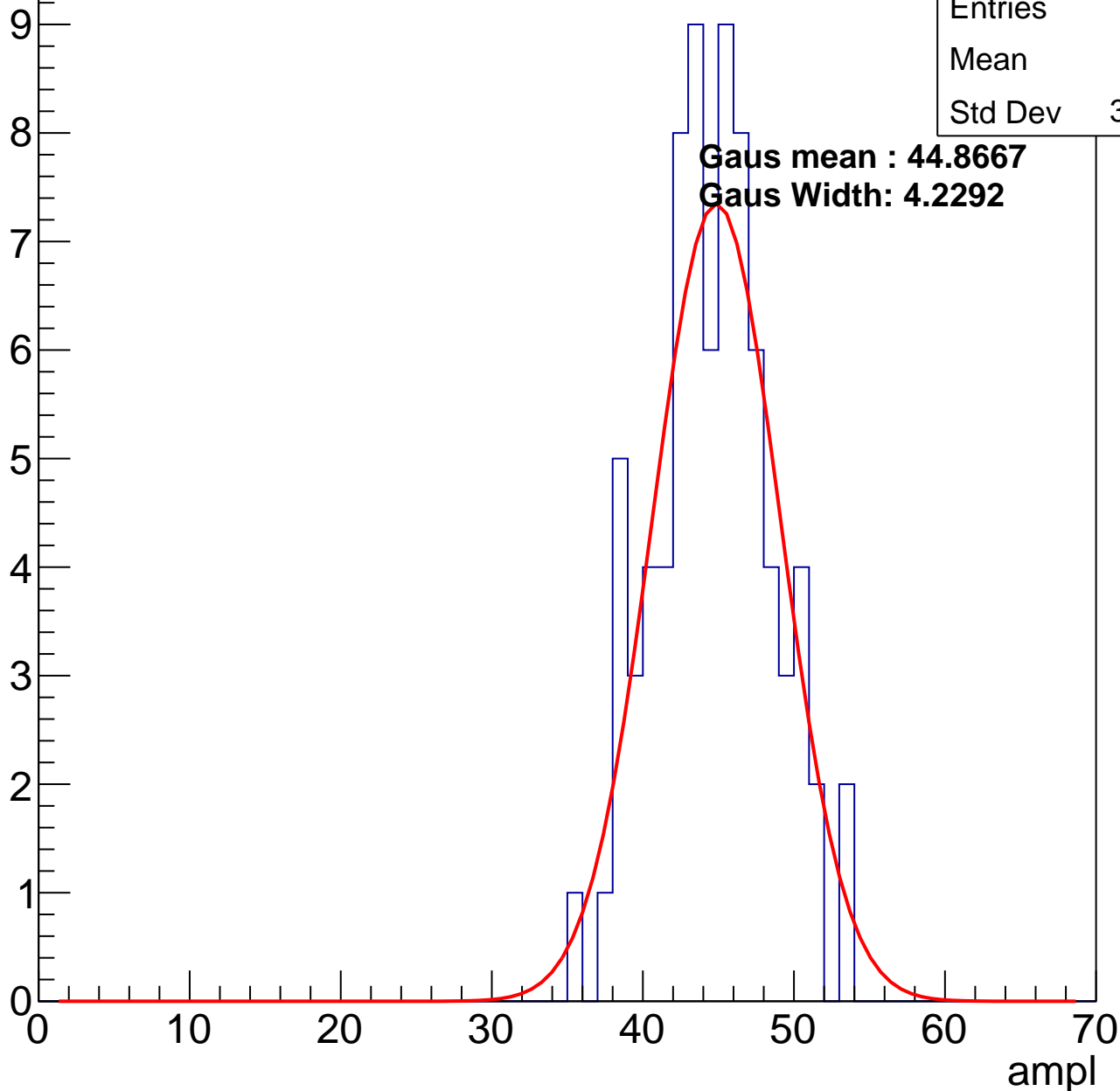
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	44.2
Std Dev	3.833

Gaus mean : 44.8667

Gaus Width: 4.2292

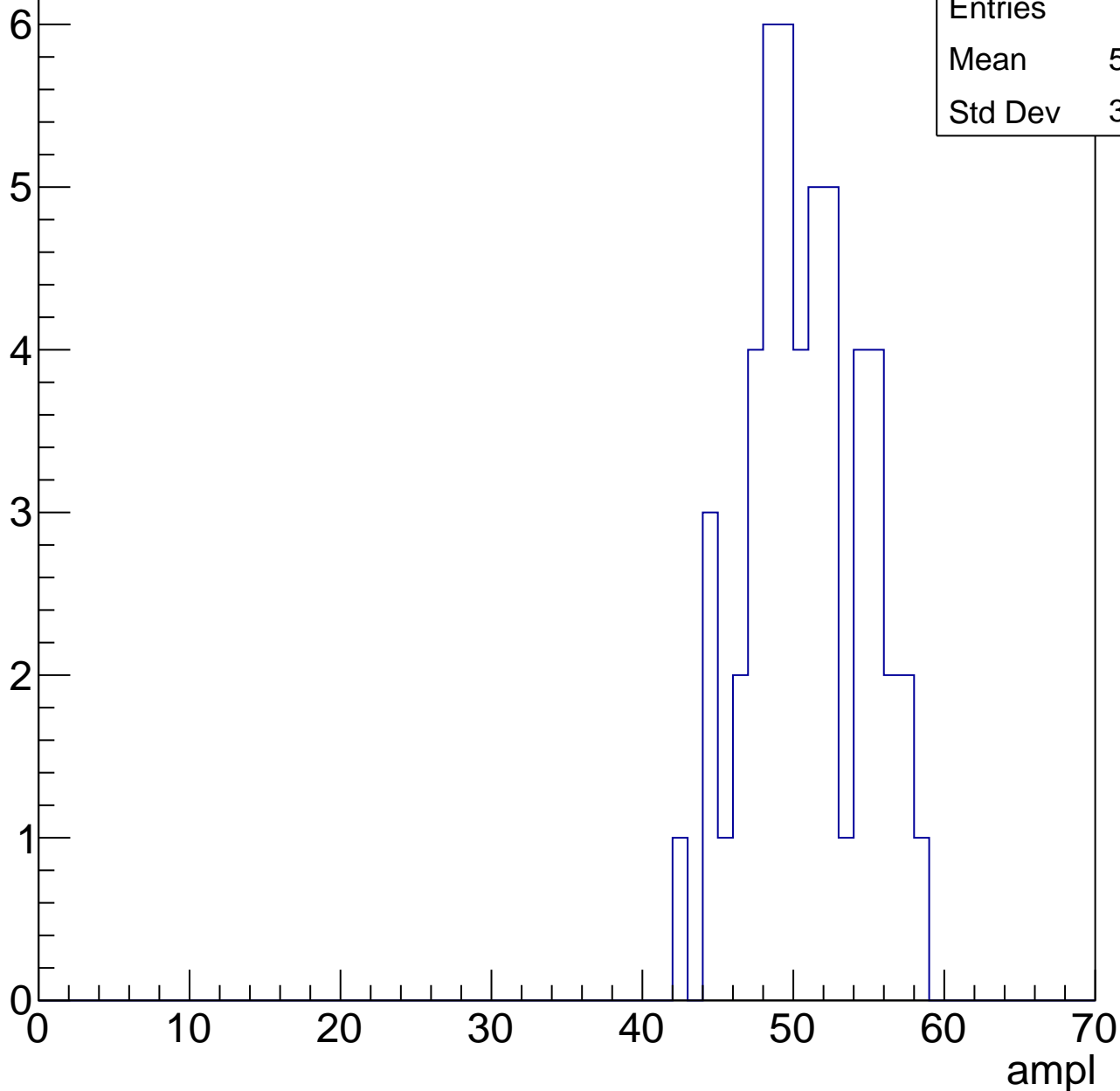


B1L103S, U12-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	50.37
Std Dev	3.757

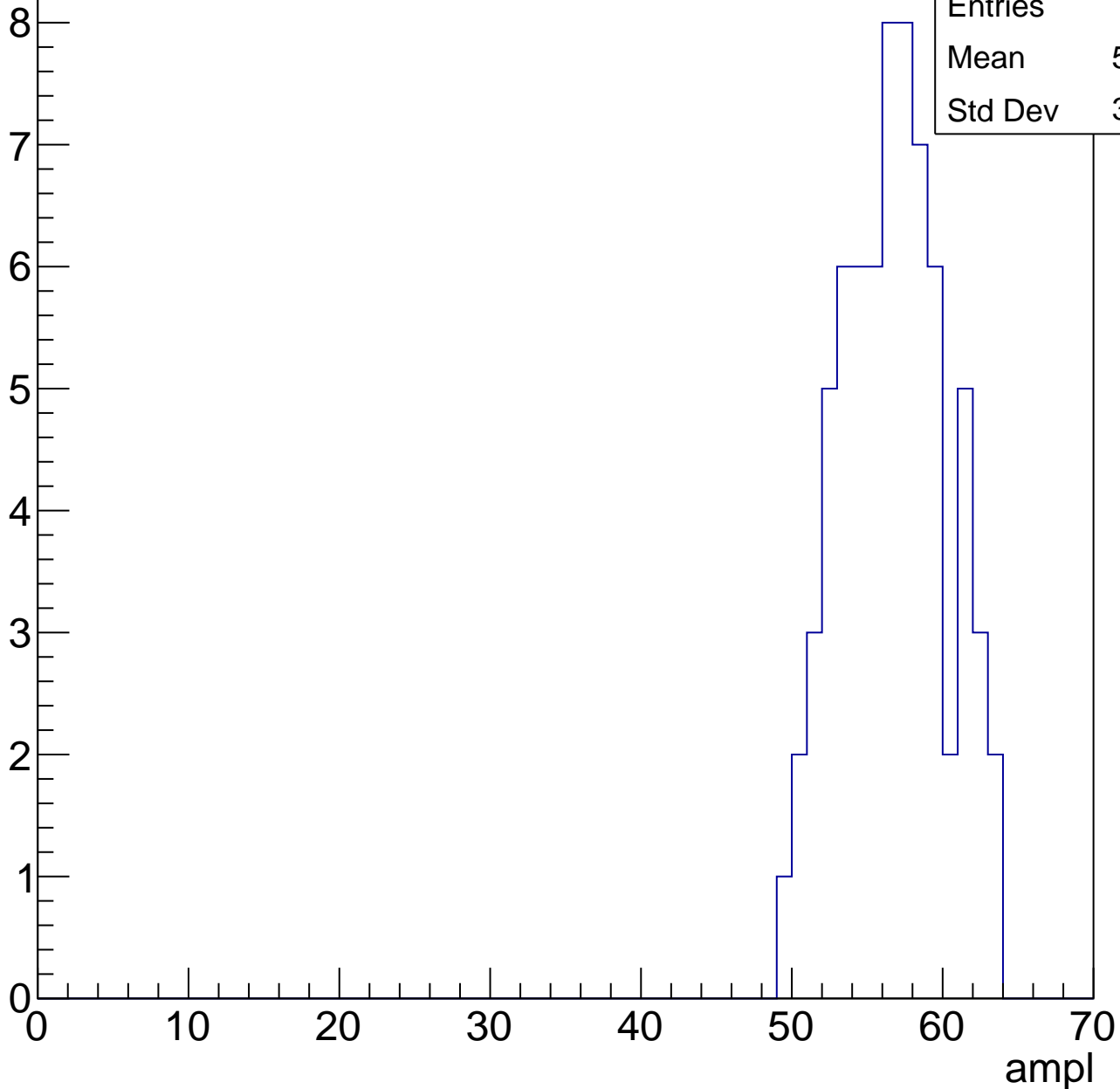


B1L103S, U12-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	56.21
Std Dev	3.401



B1L103S, U12-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

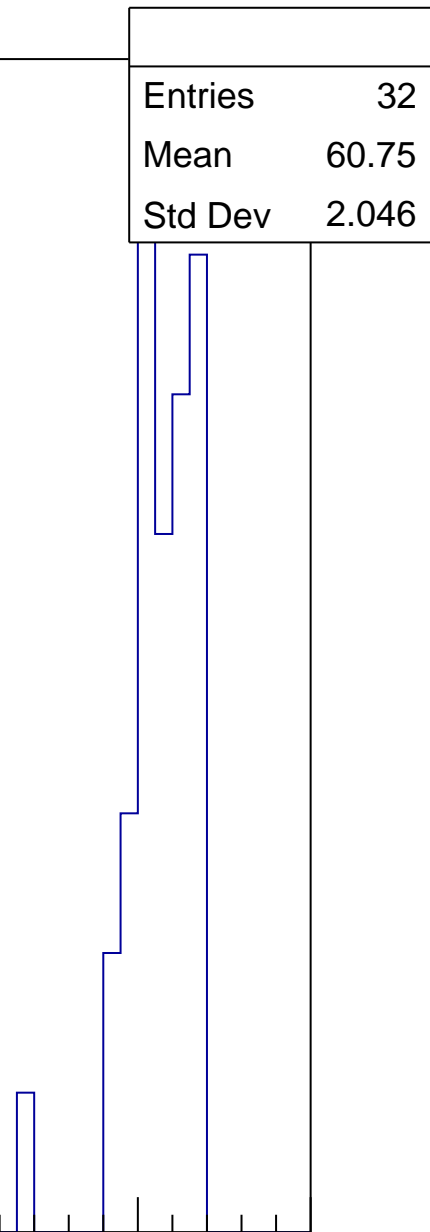
Entry

8
7
6
5
4
3
2
1
0

Entries	32
Mean	60.75
Std Dev	2.046

ampl

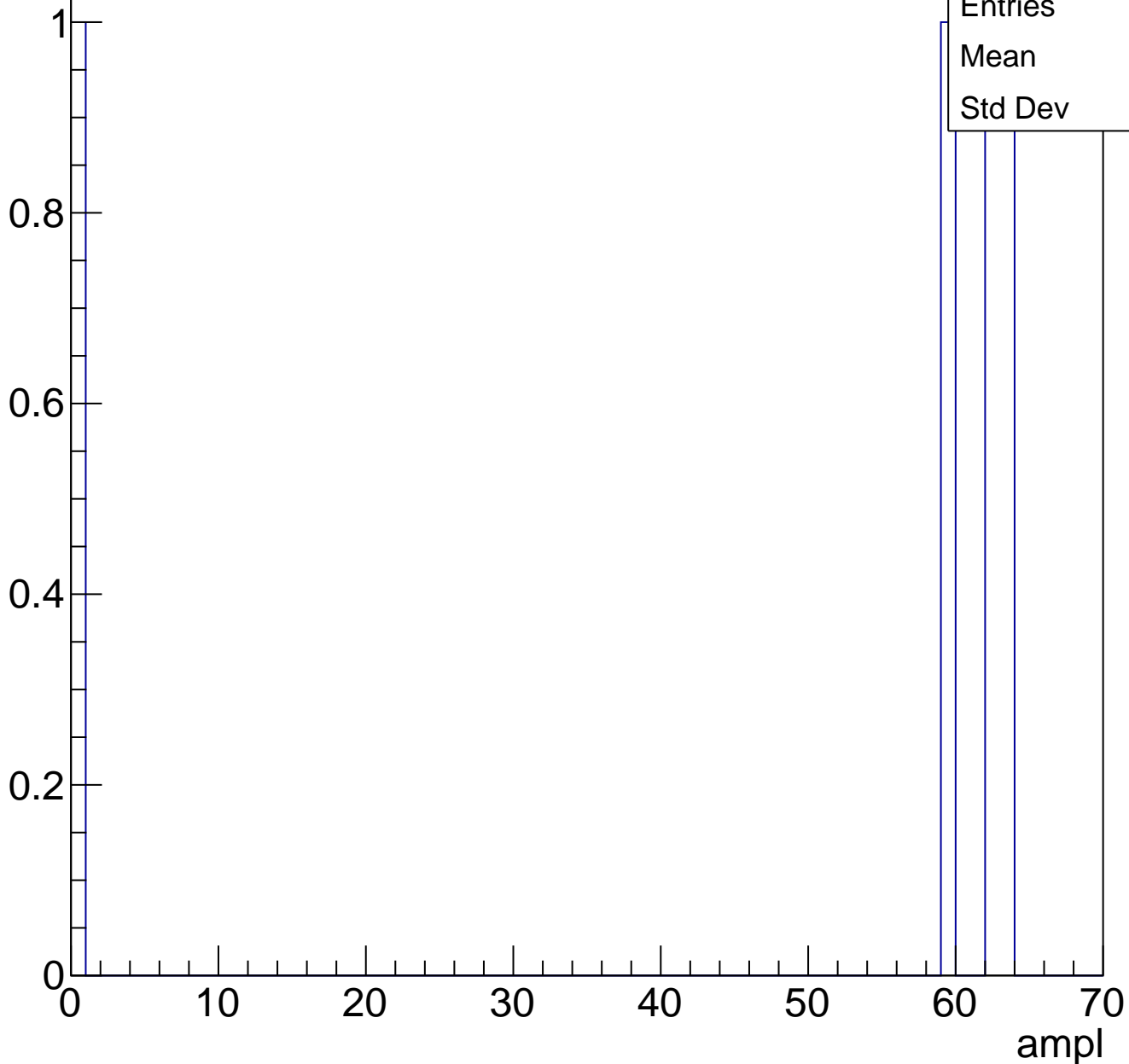
0 10 20 30 40 50 60 70



B1L103S, U12-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch94, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	28.84
Std Dev	3.829

Gaus mean : 29.6573

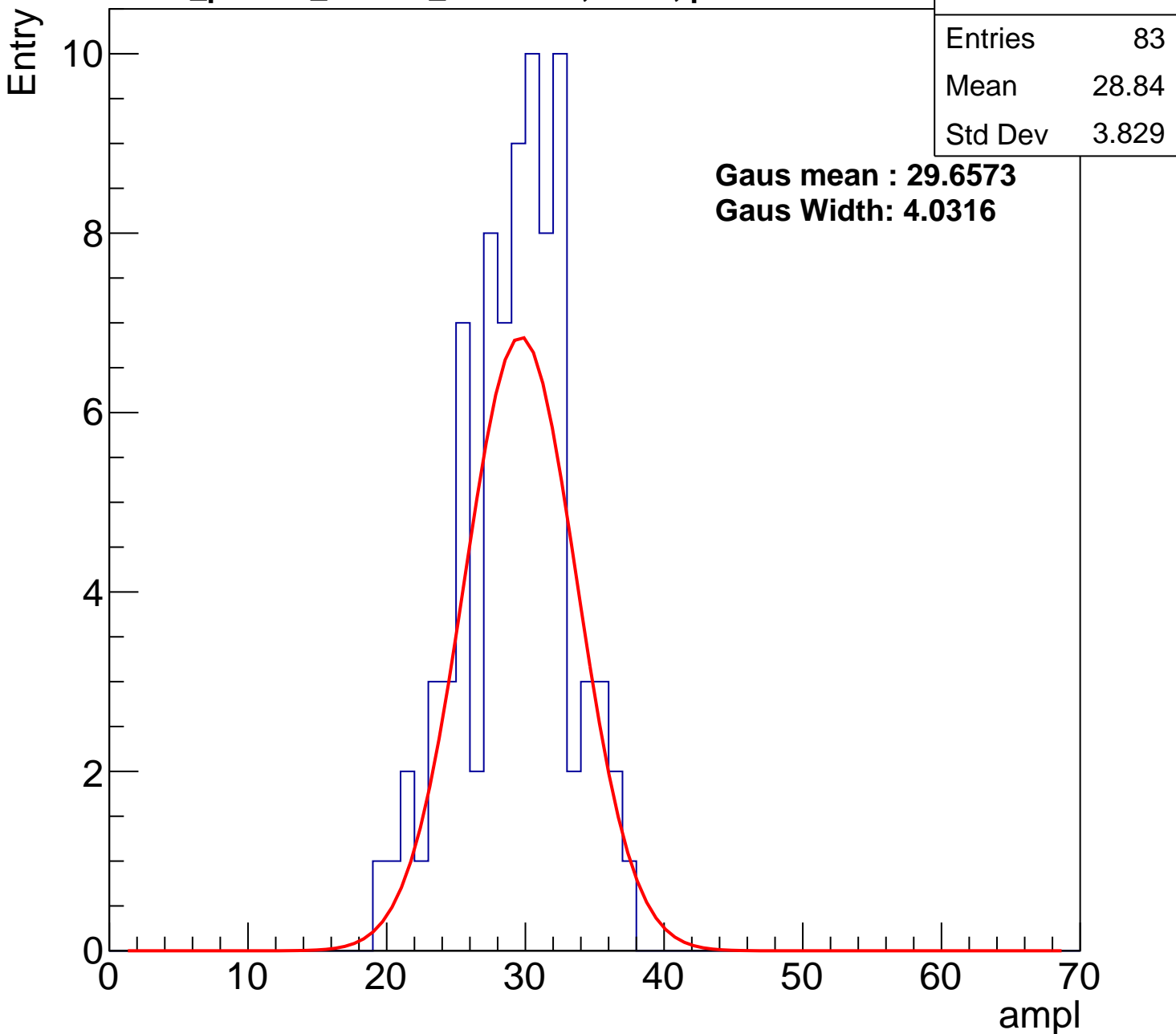
Gaus Width: 4.0316

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch94, adc1

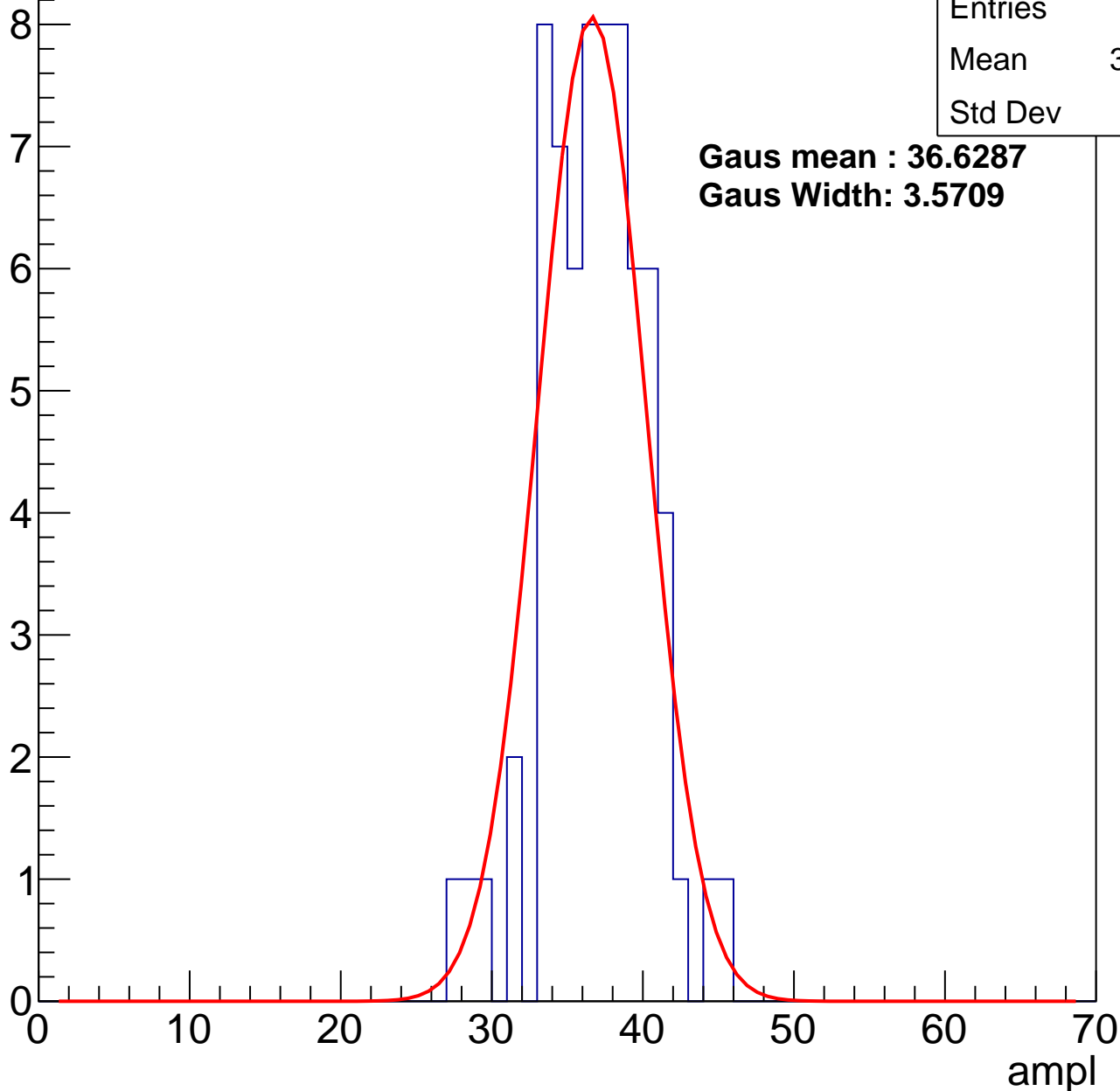
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	36.45
Std Dev	3.42

Gaus mean : 36.6287

Gaus Width: 3.5709



B1L103S, U12-ch94, adc2

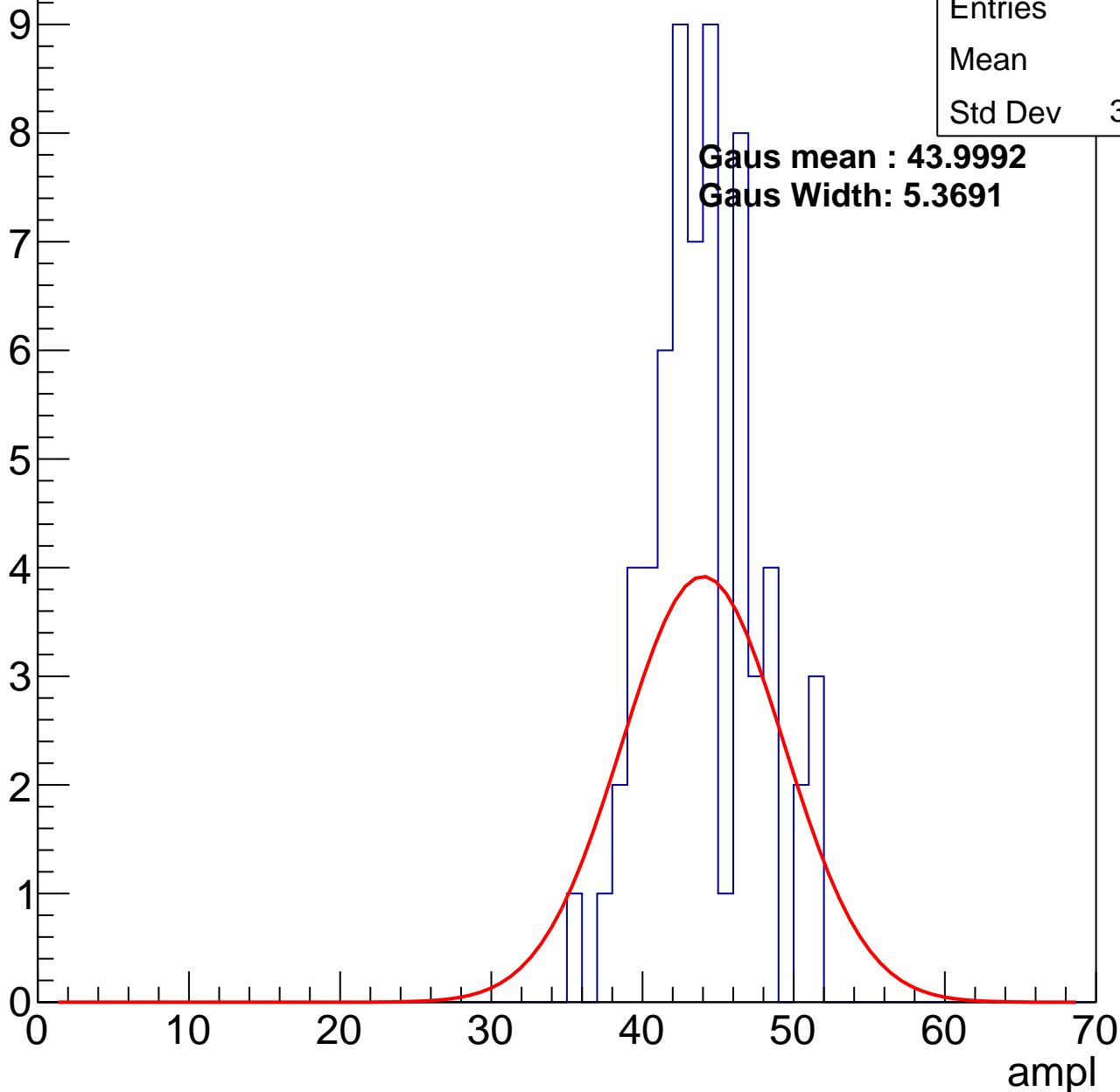
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.5
Std Dev	3.513

Gaus mean : 43.9992

Gaus Width: 5.3691

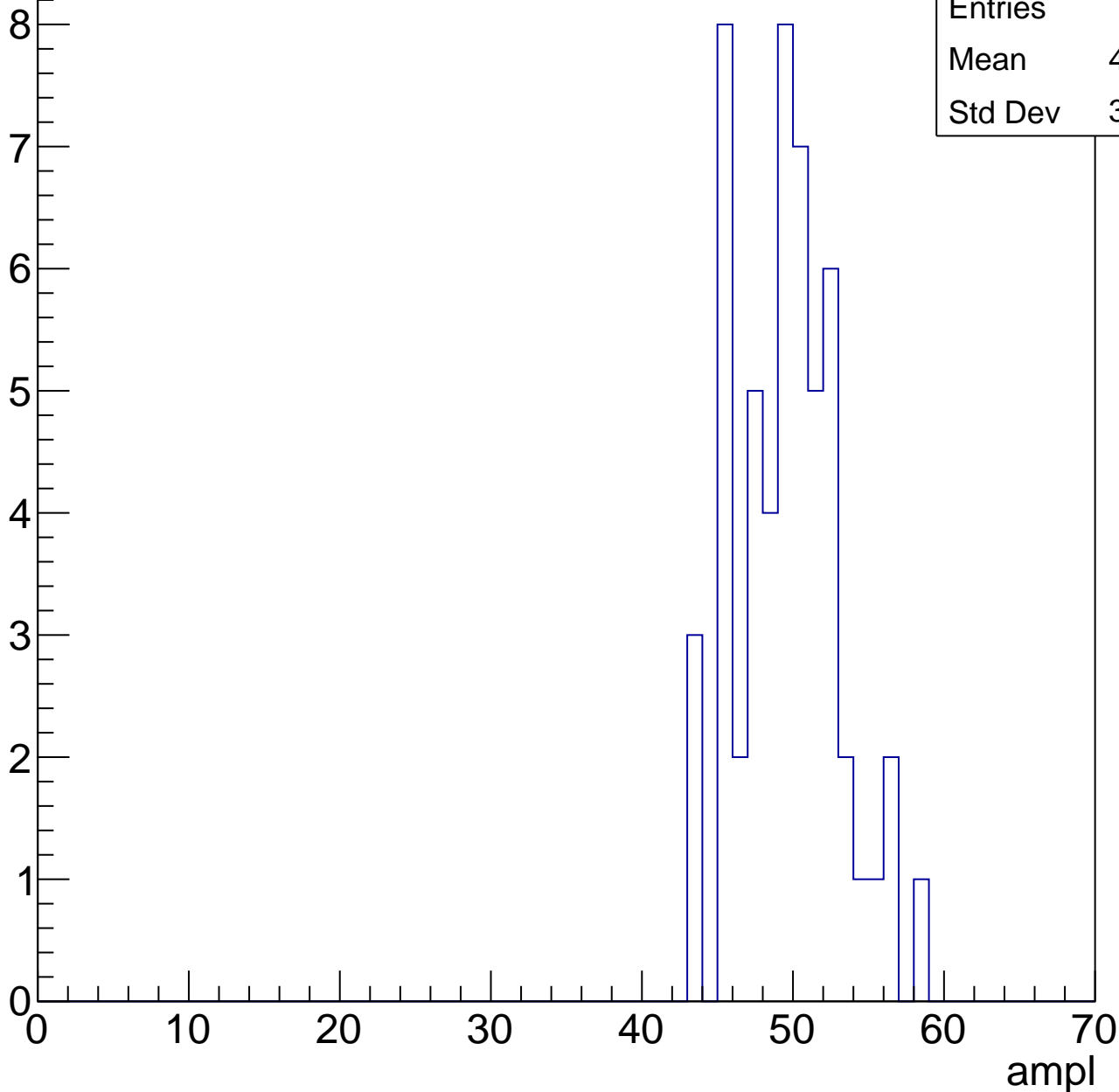


B1L103S, U12-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49.13
Std Dev	3.385

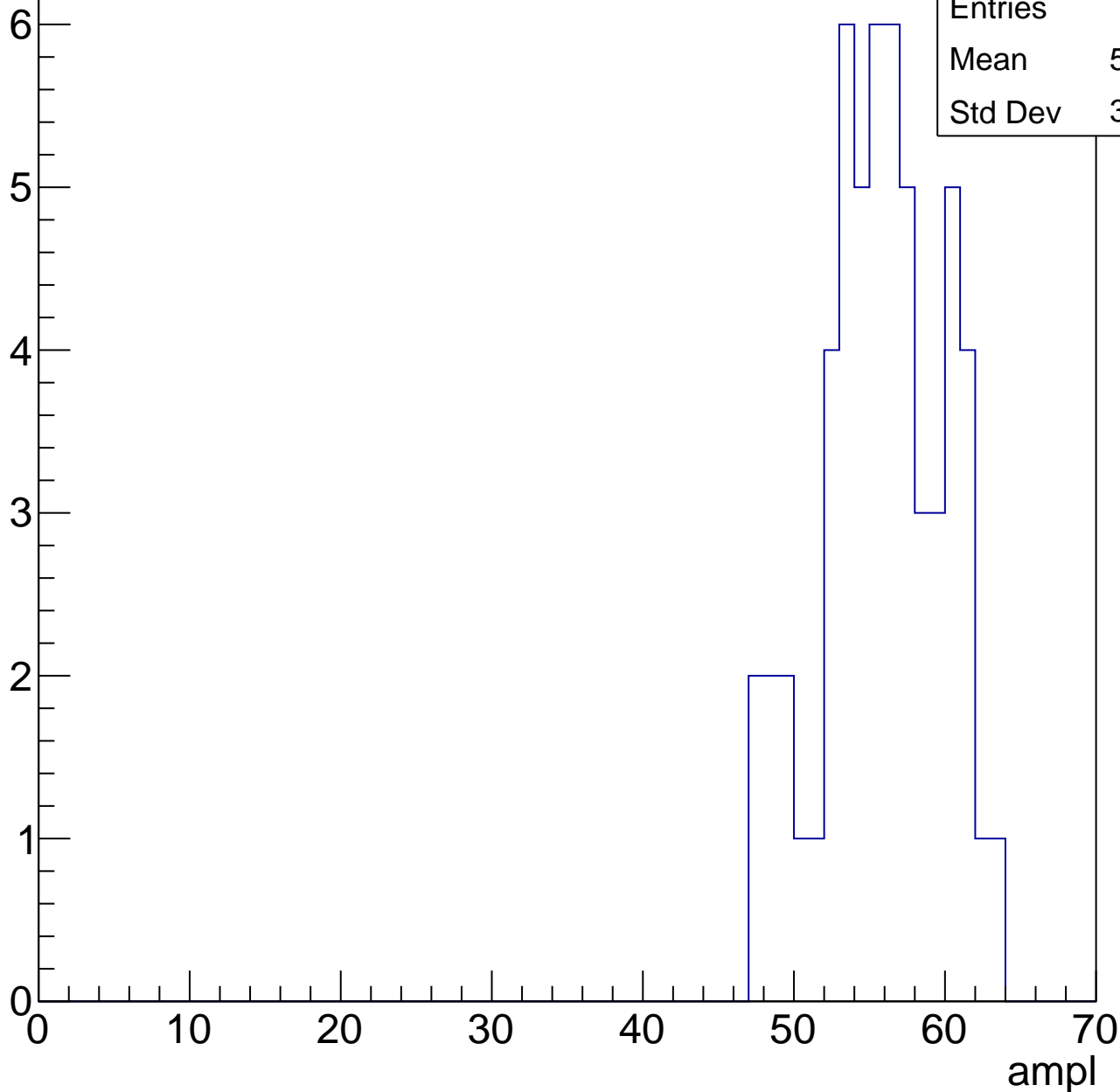


B1L103S, U12-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

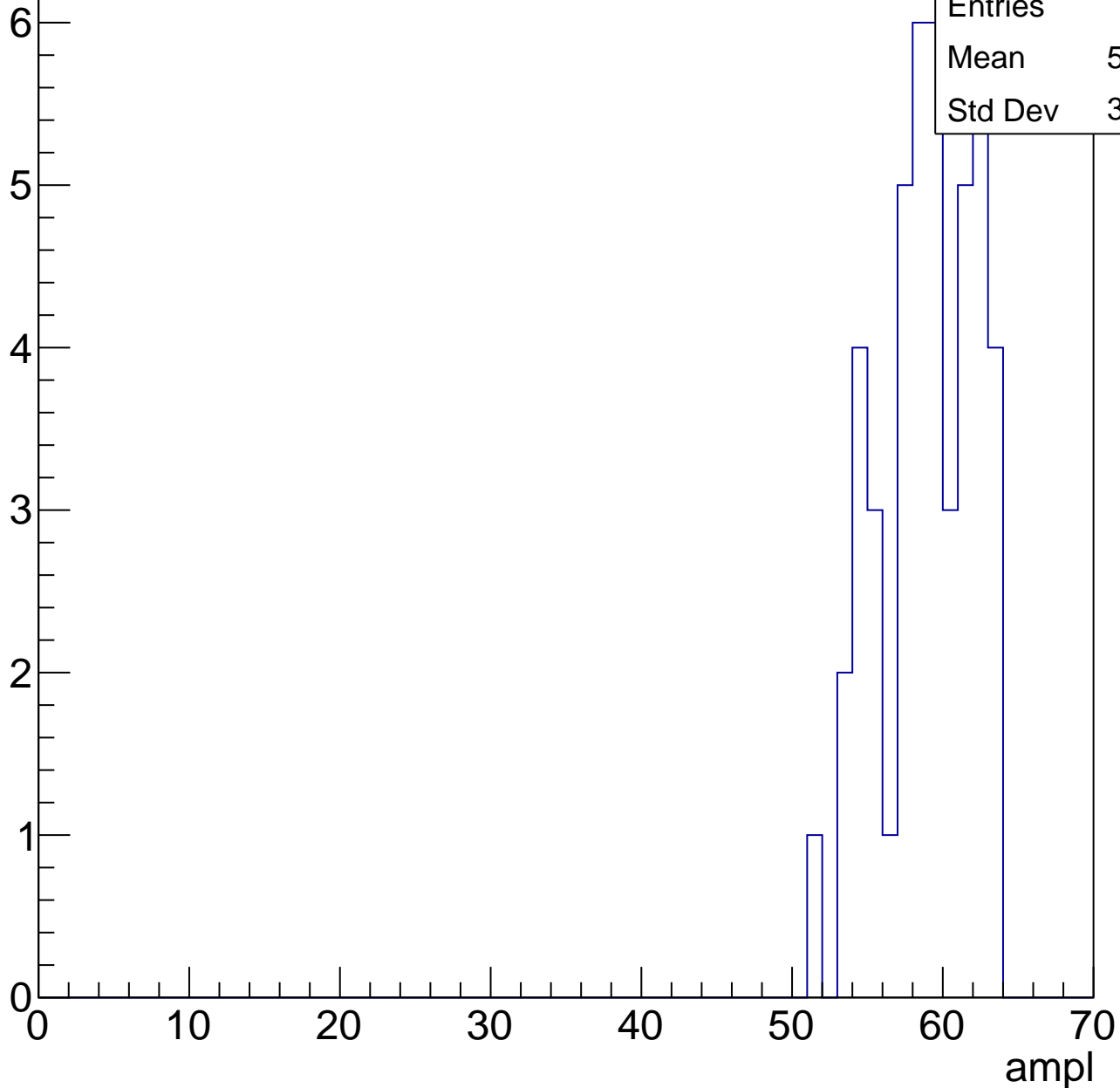
Entries	57
Mean	55.37
Std Dev	3.928



B1L103S, U12-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

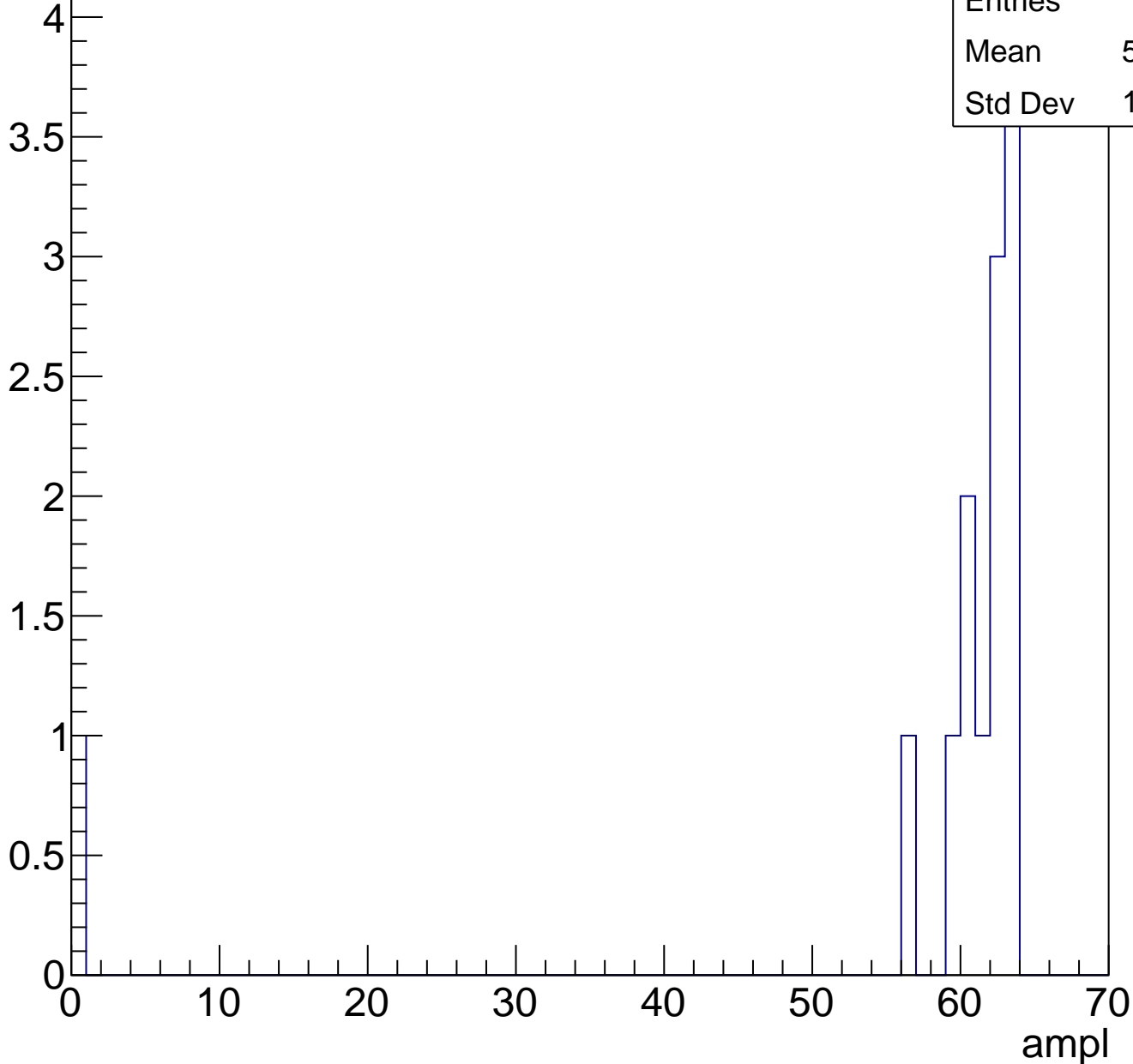
Entry



B1L103S, U12-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch95, adc0

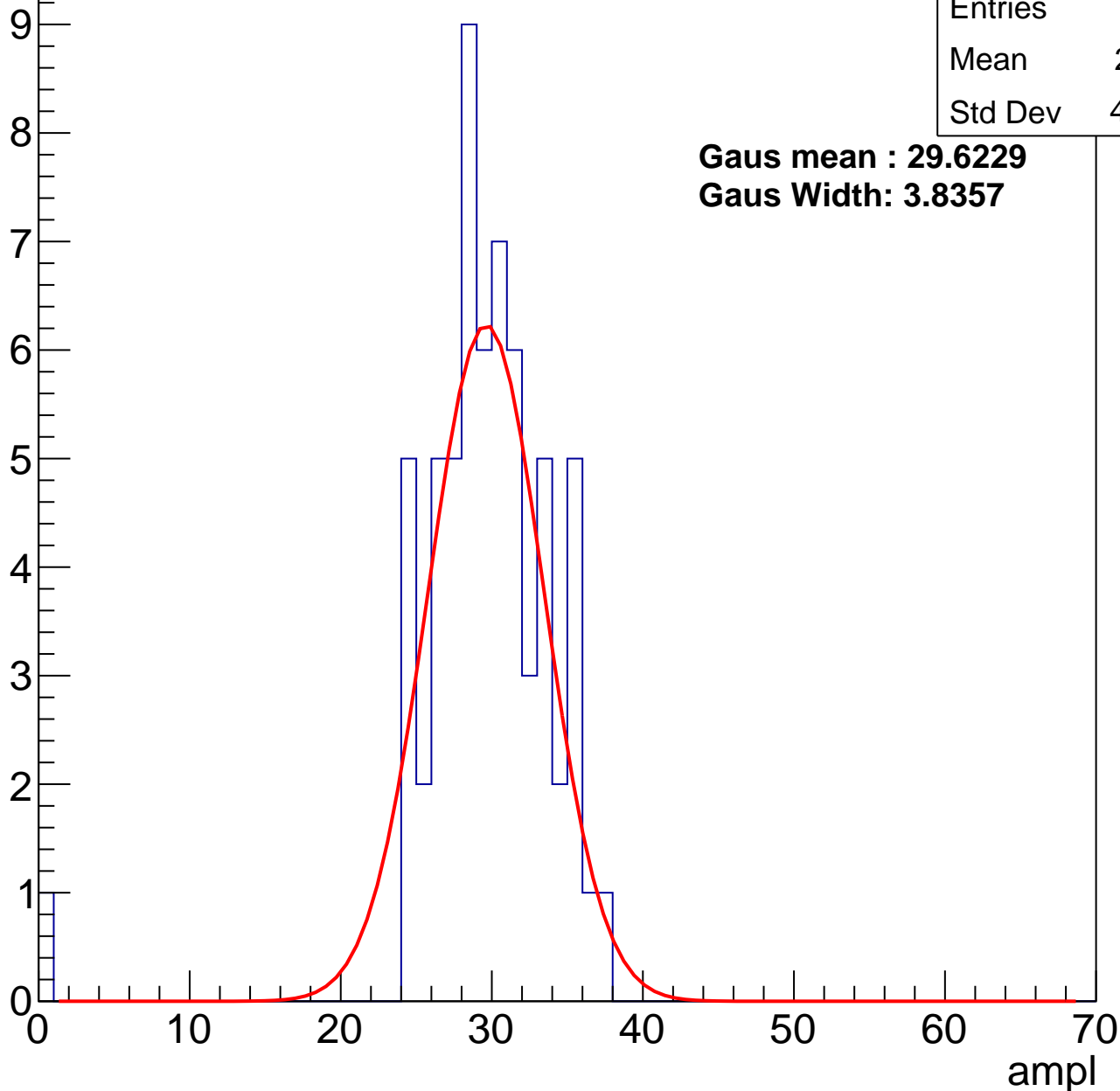
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.11
Std Dev	4.964

Gaus mean : 29.6229

Gaus Width: 3.8357



B1L103S, U12-ch95, adc1

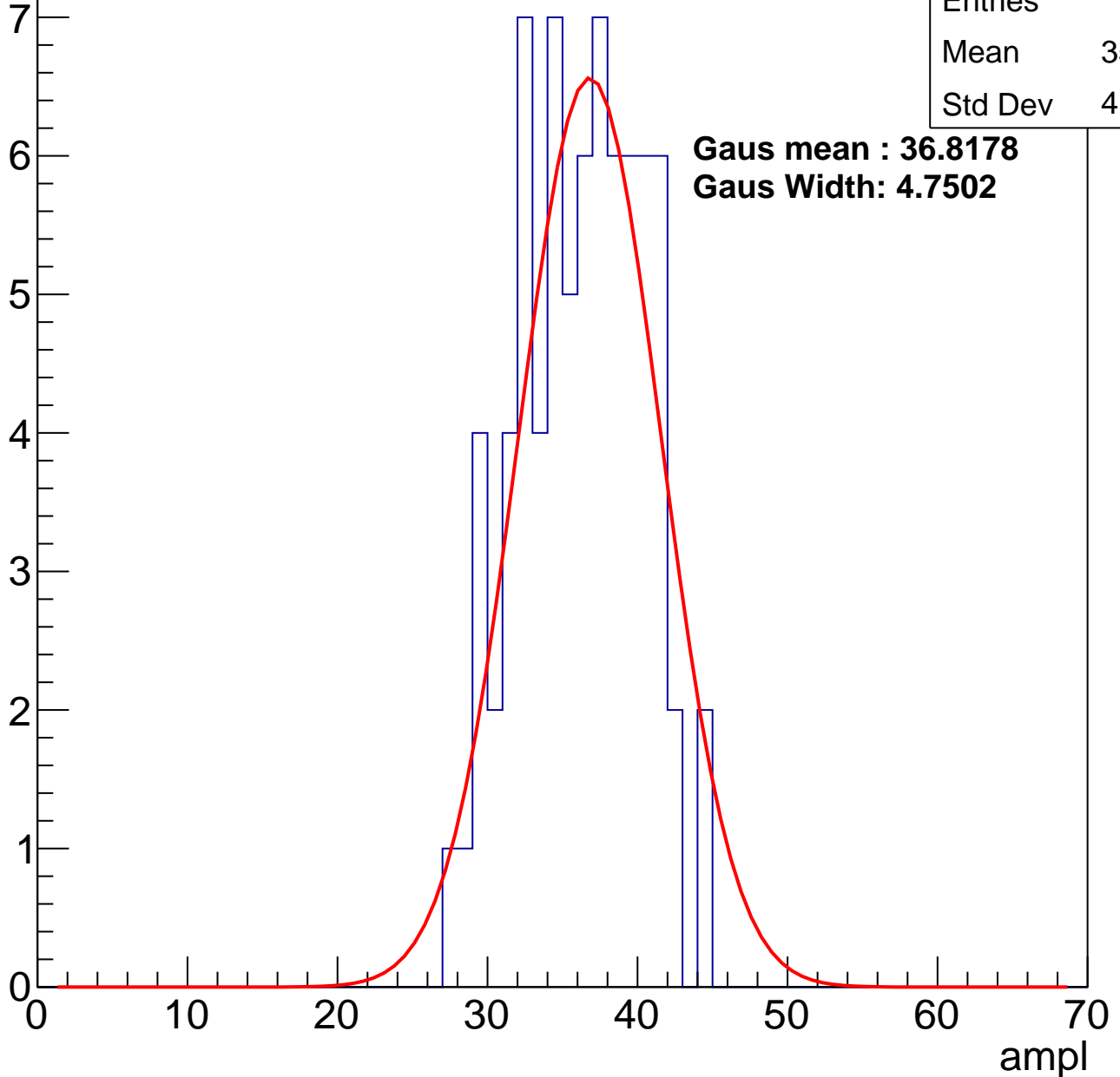
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.78
Std Dev	4.009

Gaus mean : 36.8178

Gaus Width: 4.7502



B1L103S, U12-ch95, adc2

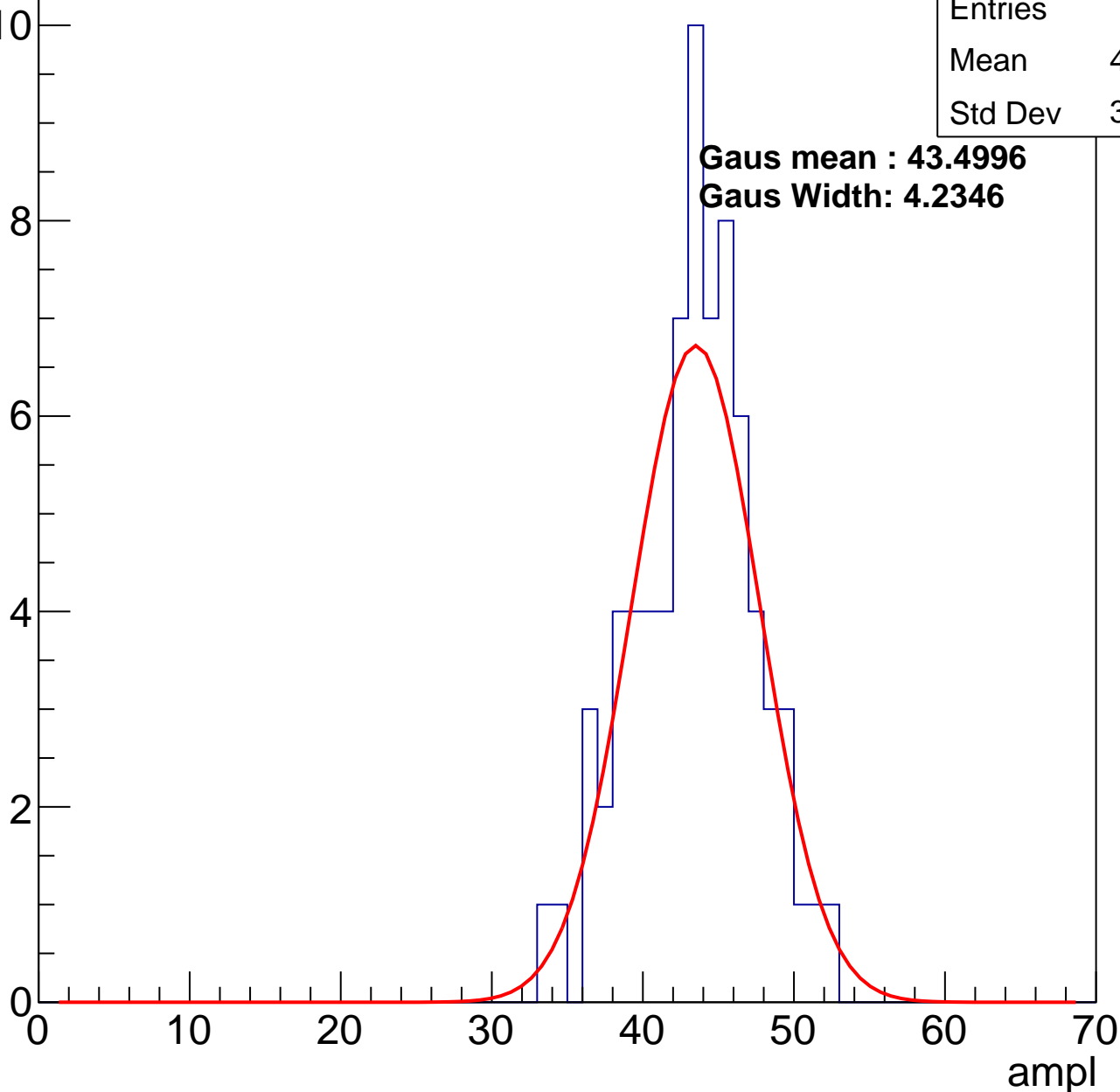
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.99
Std Dev	3.954

Gaus mean : 43.4996

Gaus Width: 4.2346

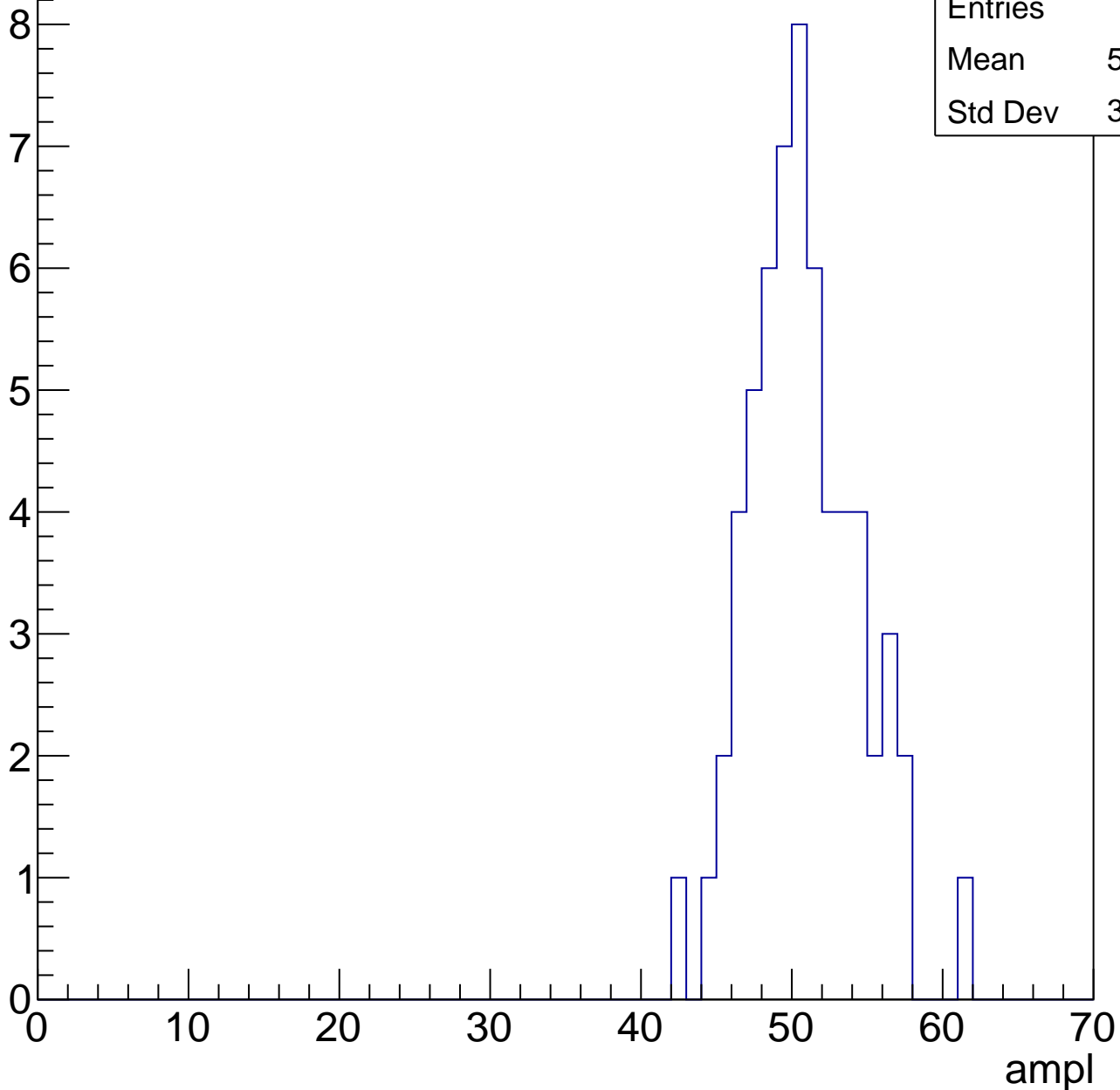


B1L103S, U12-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	50.35
Std Dev	3.609

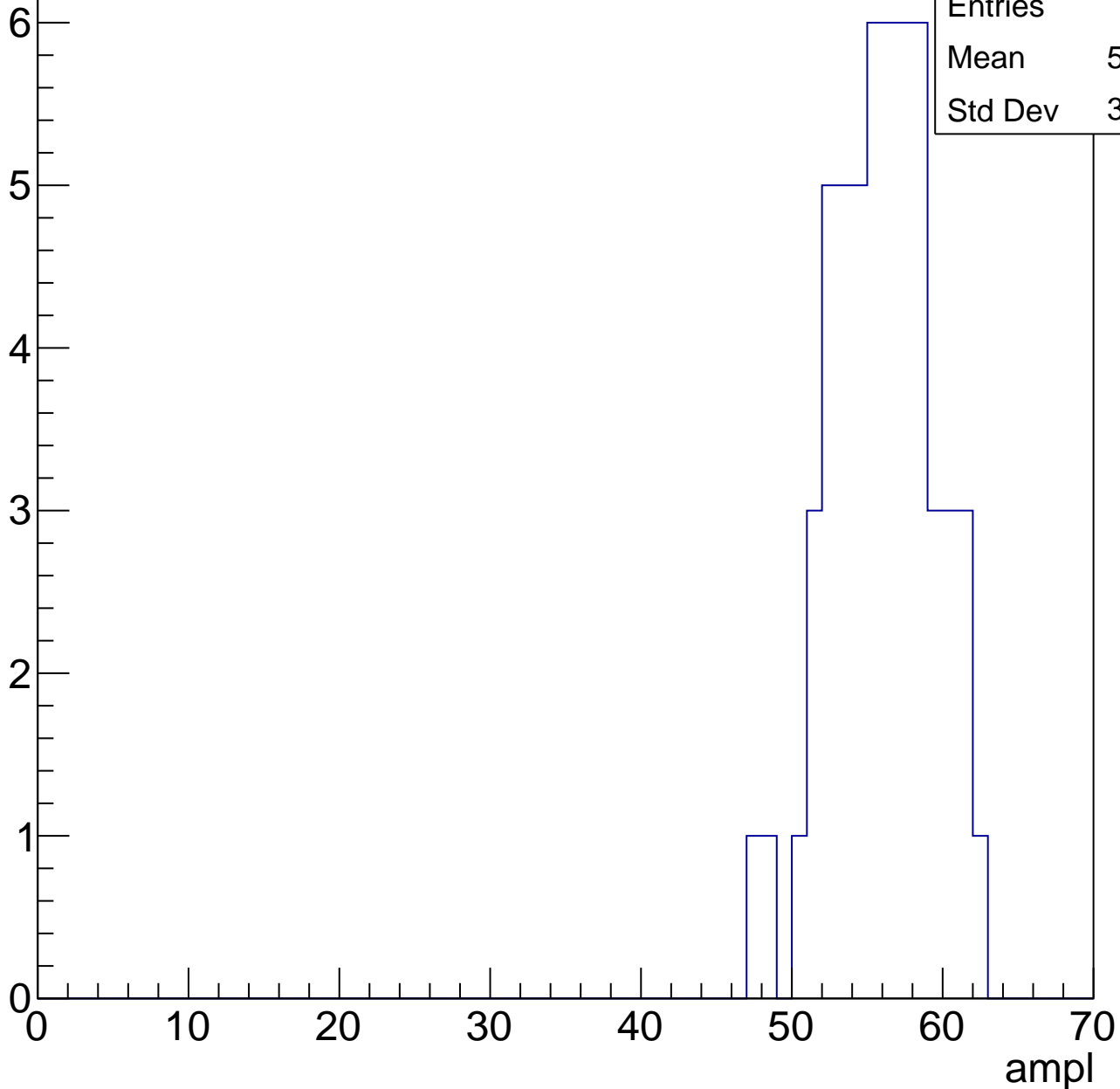


B1L103S, U12-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.47
Std Dev	3.324

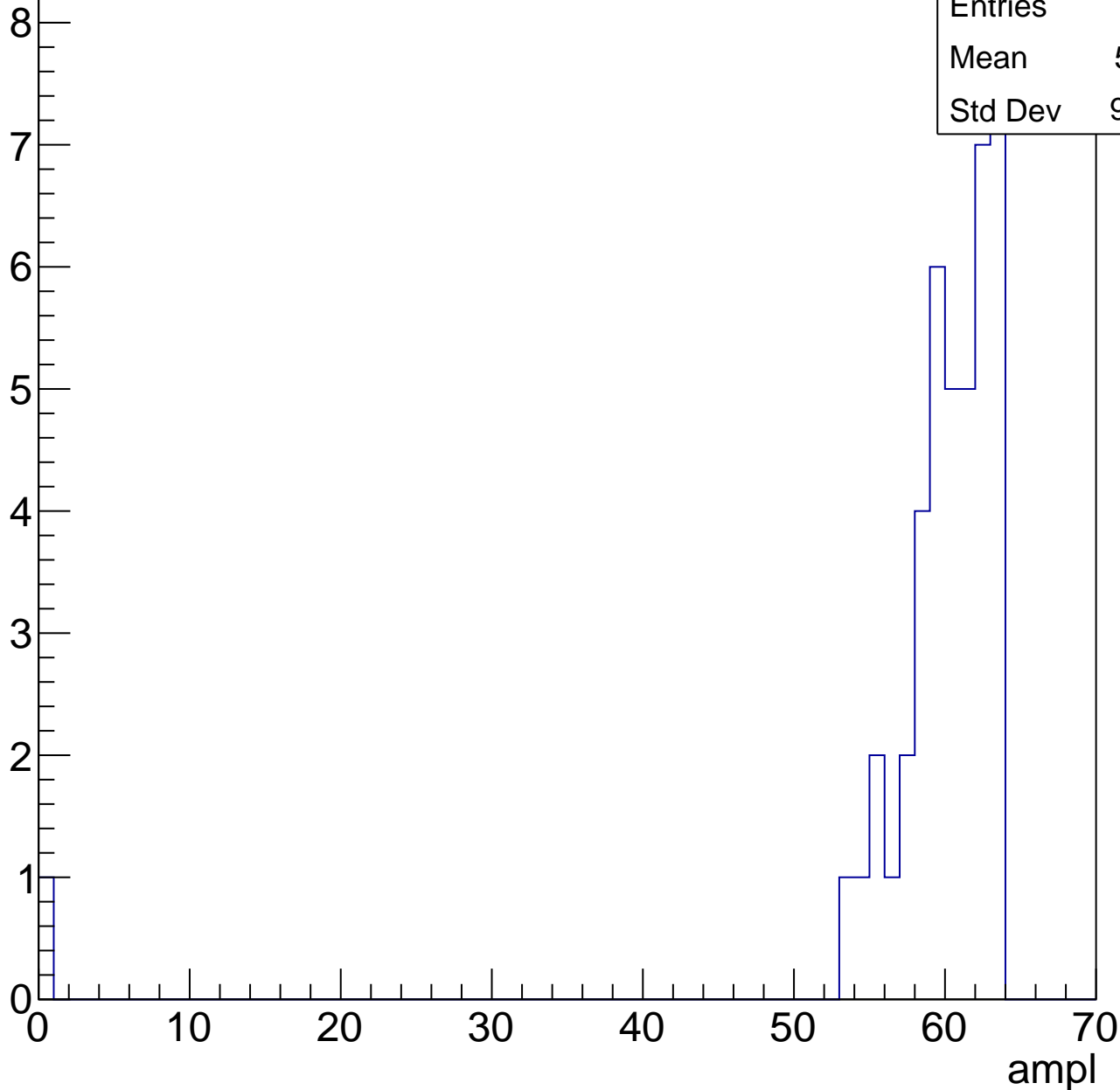


B1L103S, U12-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	58.51
Std Dev	9.402



B1L103S, U12-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	61.25
Std Dev	1.392

ampl

0 10 20 30 40 50 60 70

B1L103S, U12-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch96, adc0

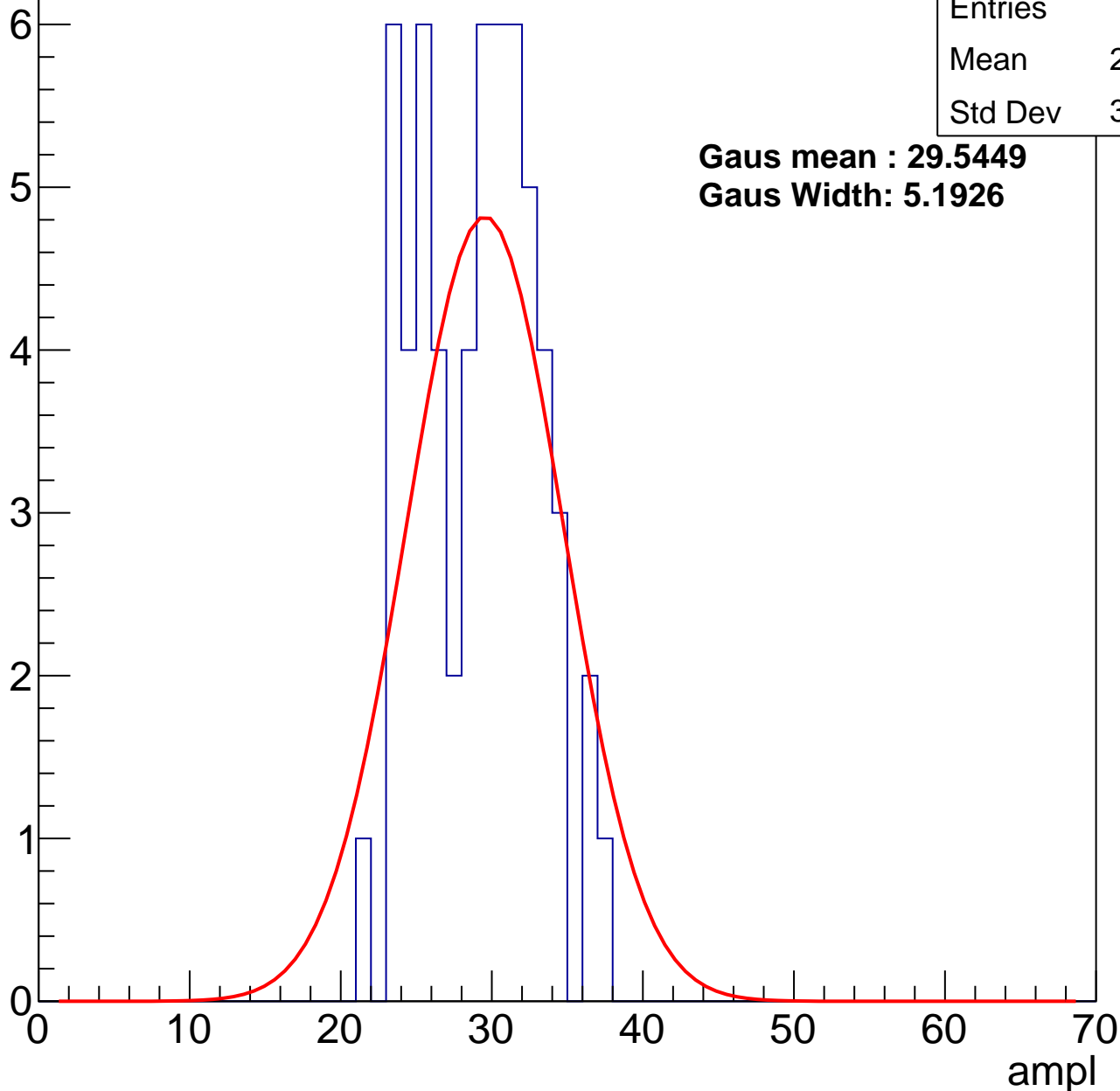
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.63
Std Dev	3.856

Gaus mean : 29.5449

Gaus Width: 5.1926



B1L103S, U12-ch96, adc1

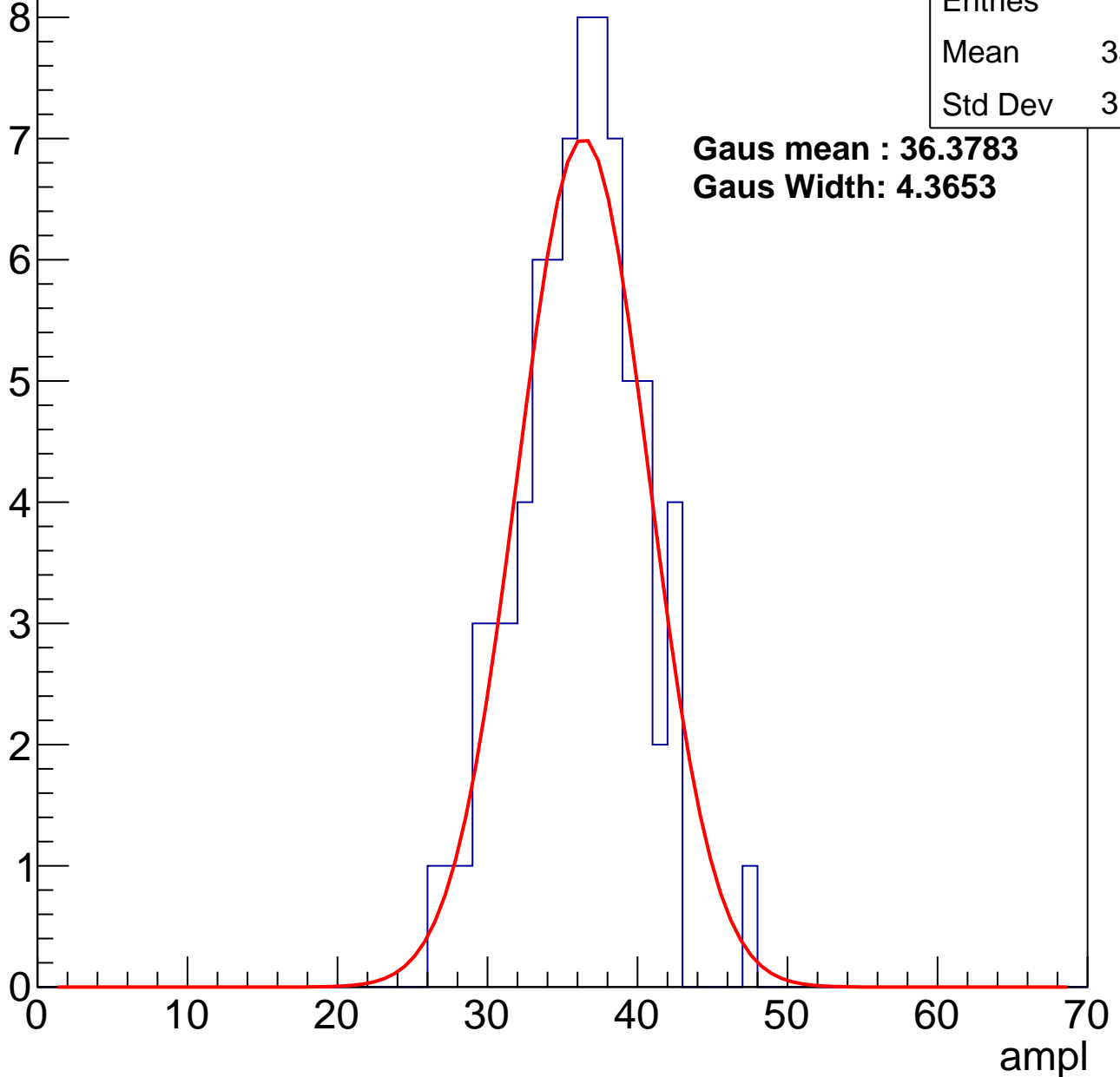
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	35.57
Std Dev	3.977

Gaus mean : 36.3783

Gaus Width: 4.3653

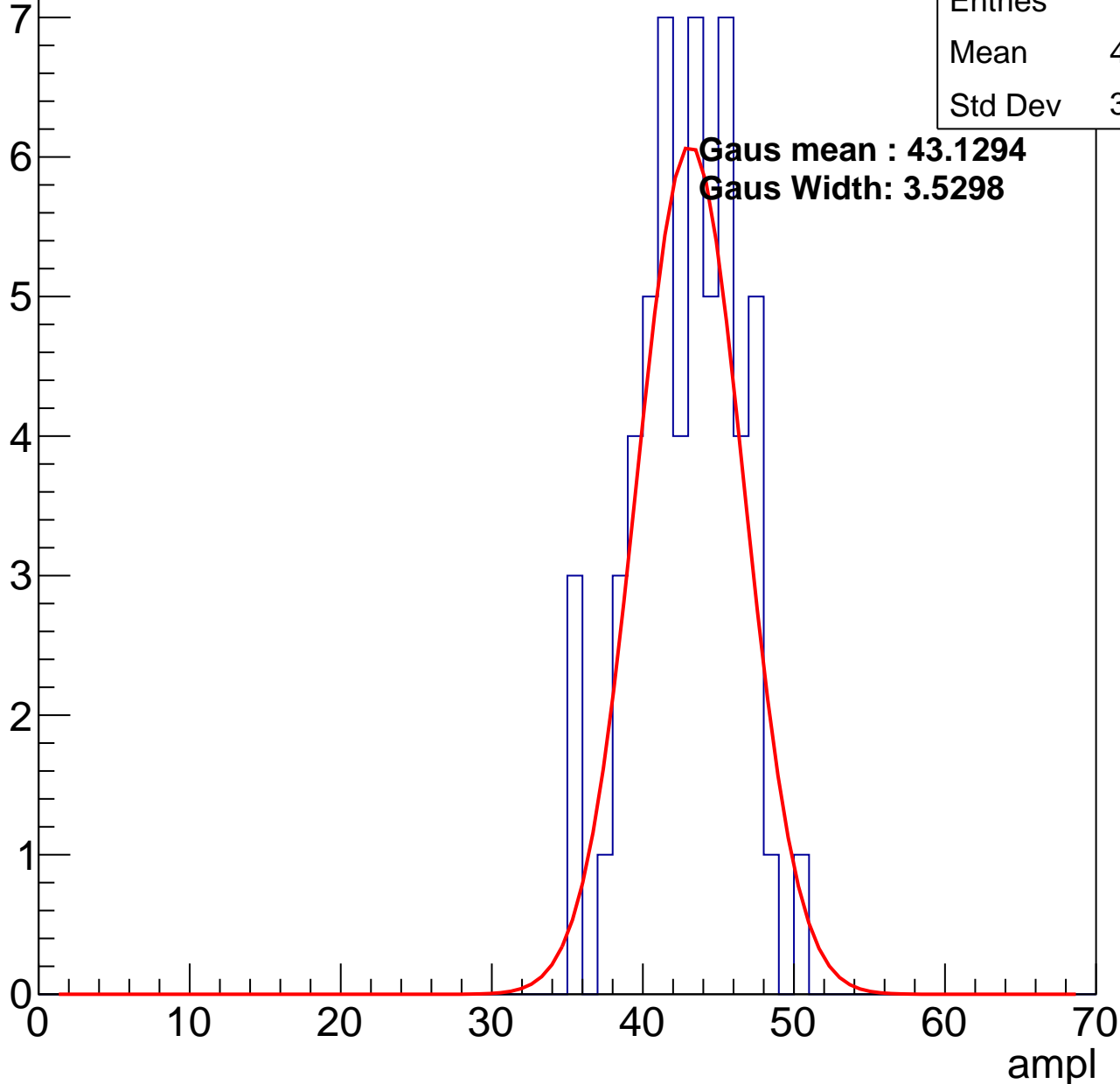


B1L103S, U12-ch96, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.46
Std Dev	3.377

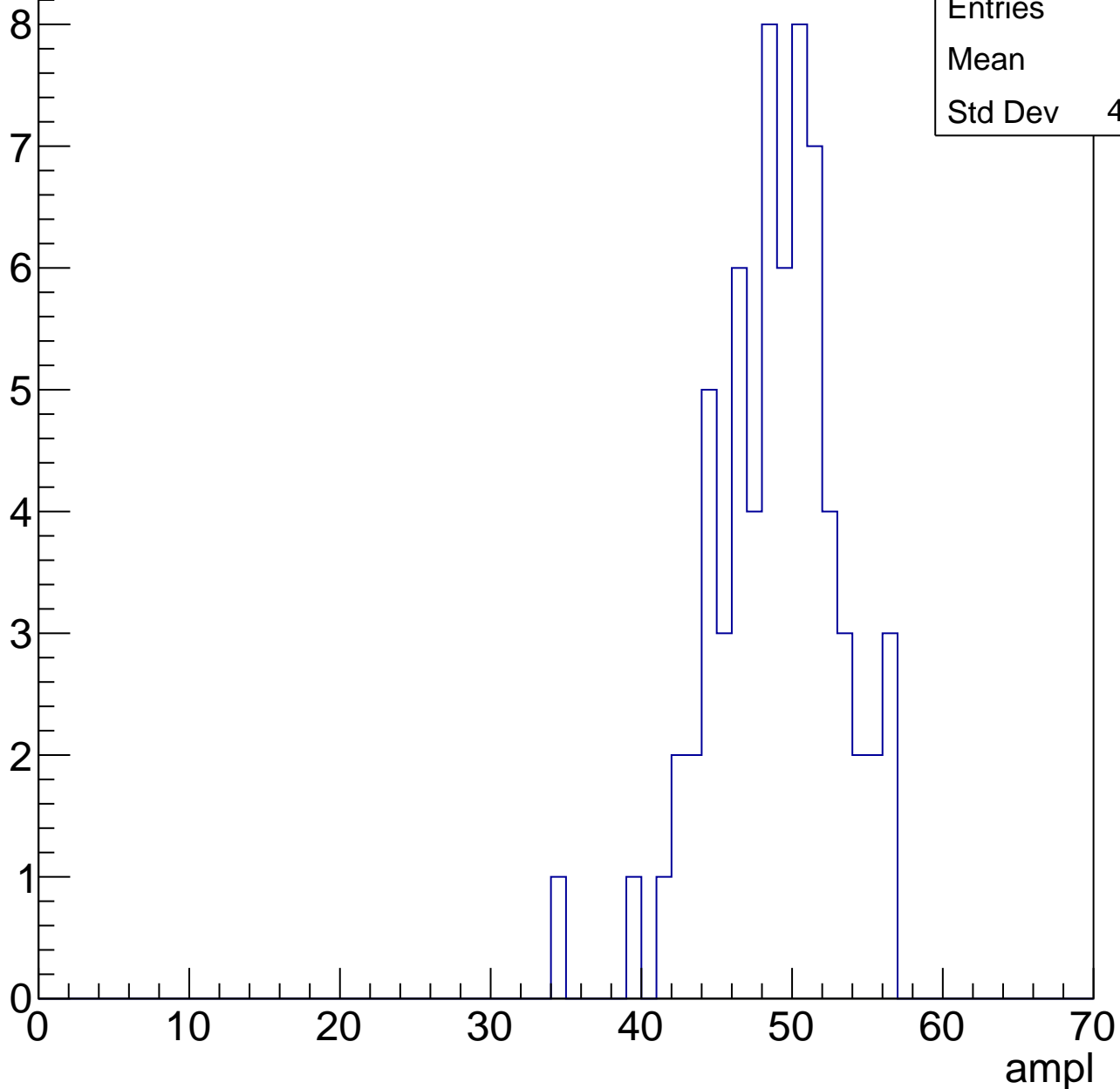


B1L103S, U12-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

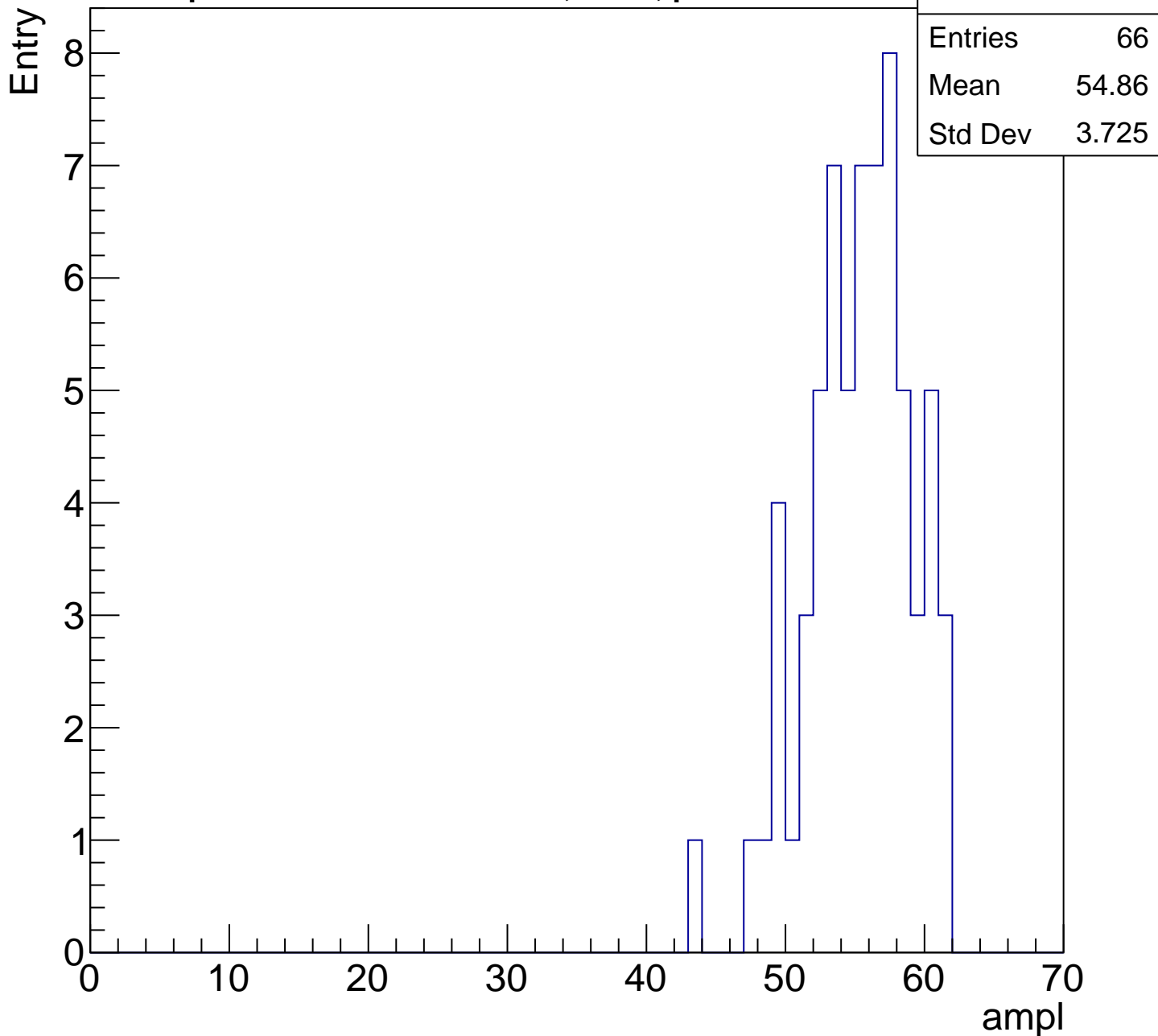
Entry

Entries	68
Mean	48.4
Std Dev	4.156



B1L103S, U12-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

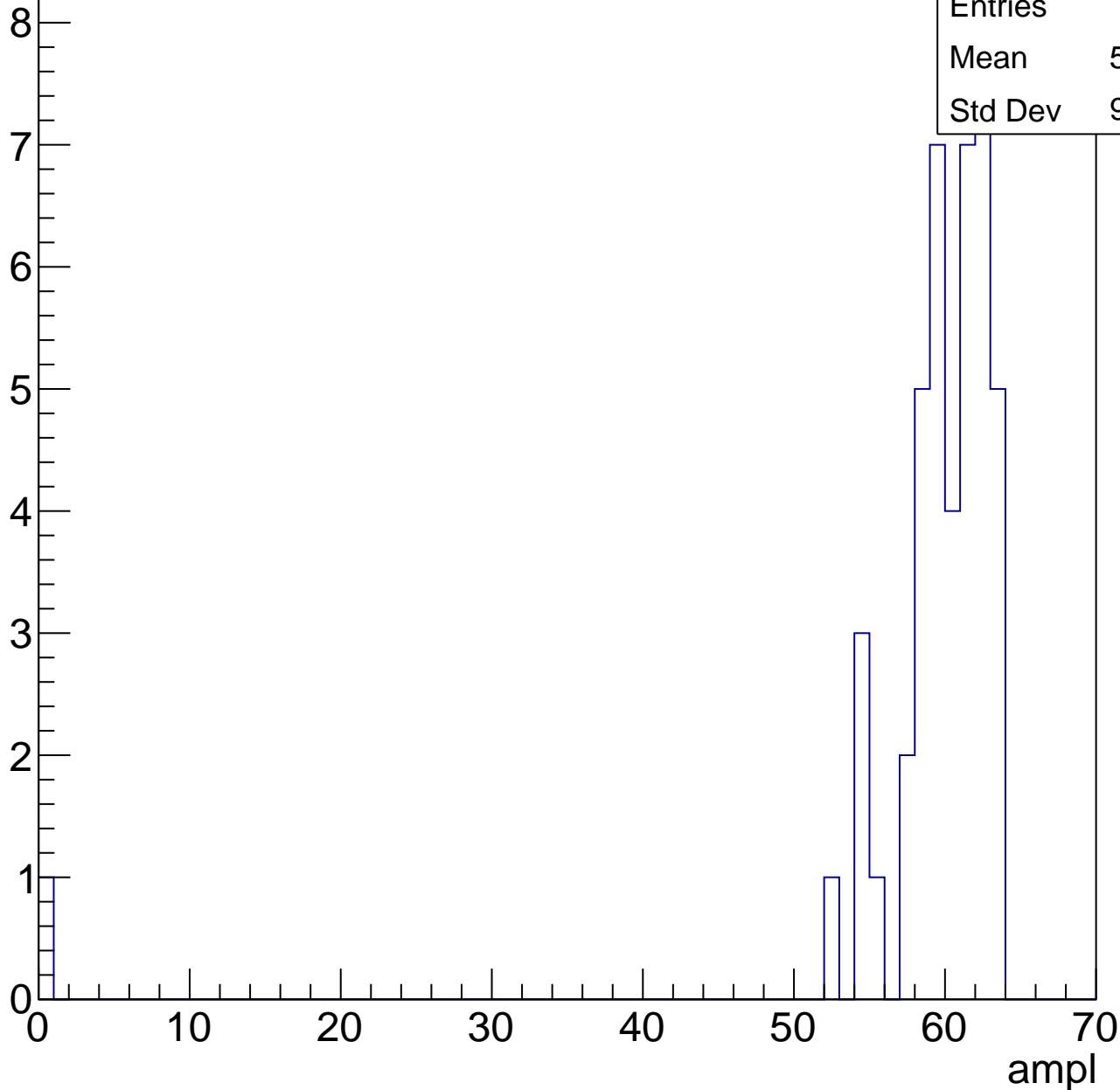


B1L103S, U12-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.27
Std Dev	9.287



B1L103S, U12-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

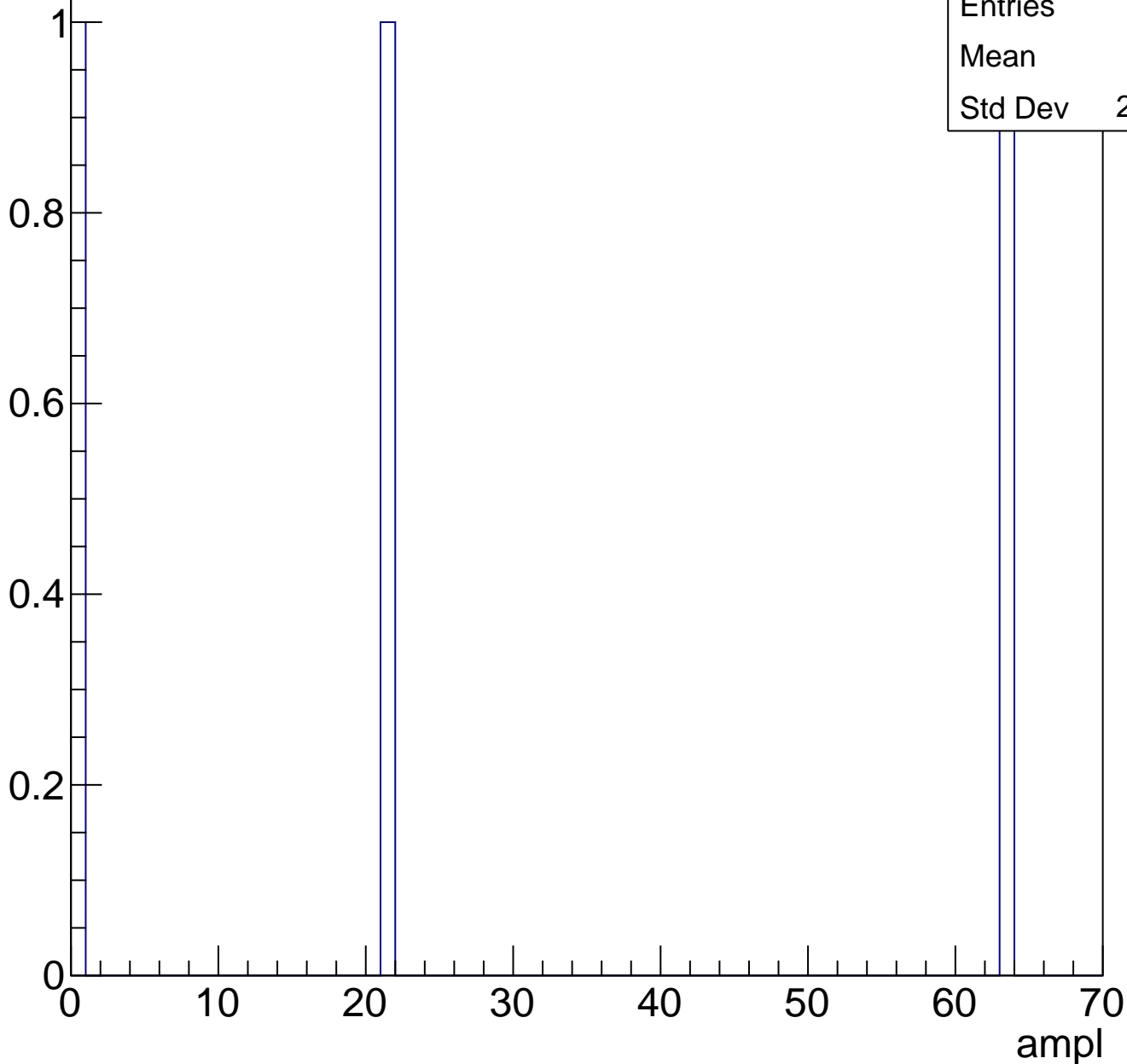
Entry



B1L103S, U12-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch97, adc0

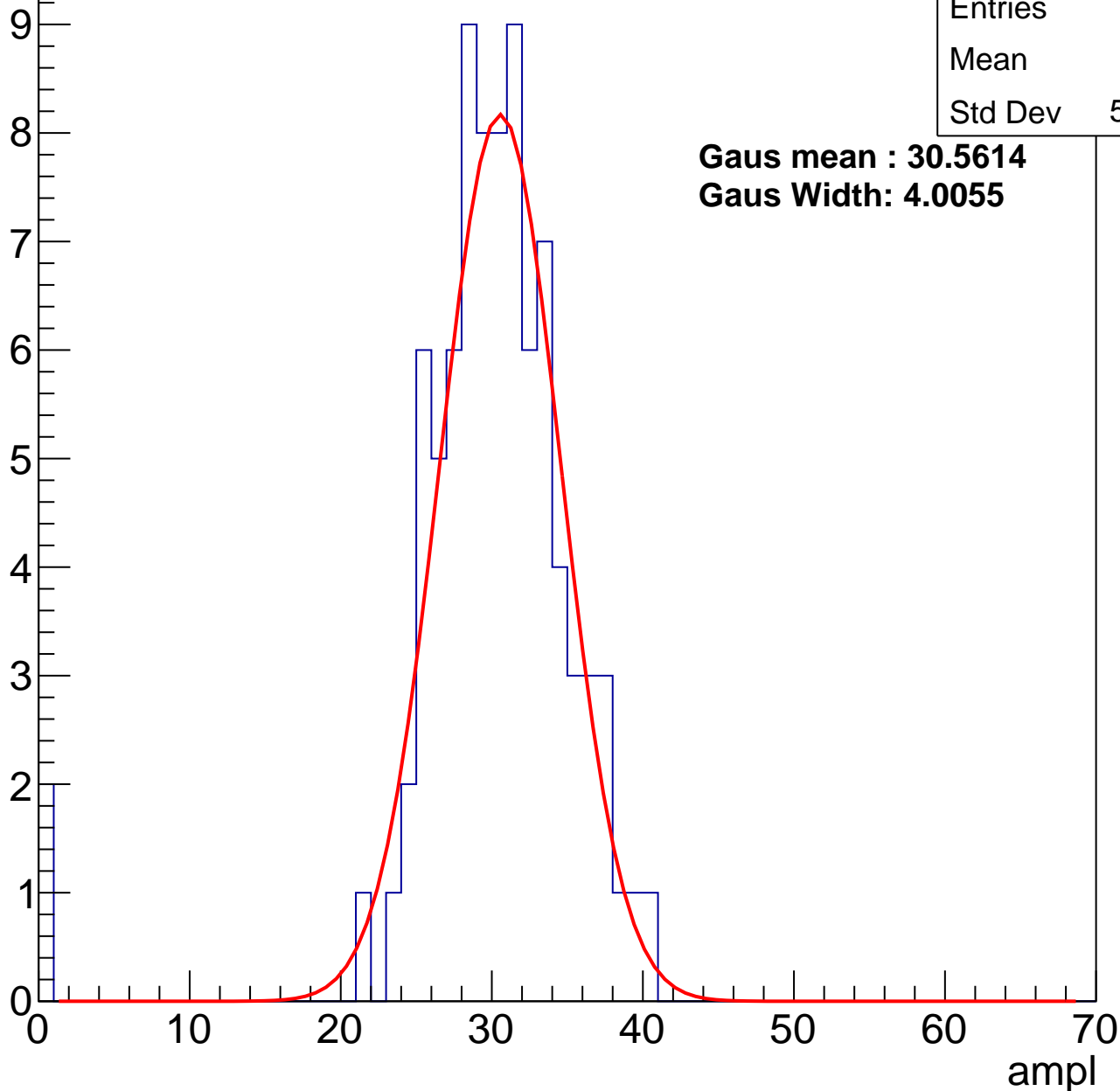
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	29.5
Std Dev	5.949

Gaus mean : 30.5614

Gaus Width: 4.0055



B1L103S, U12-ch97, adc1

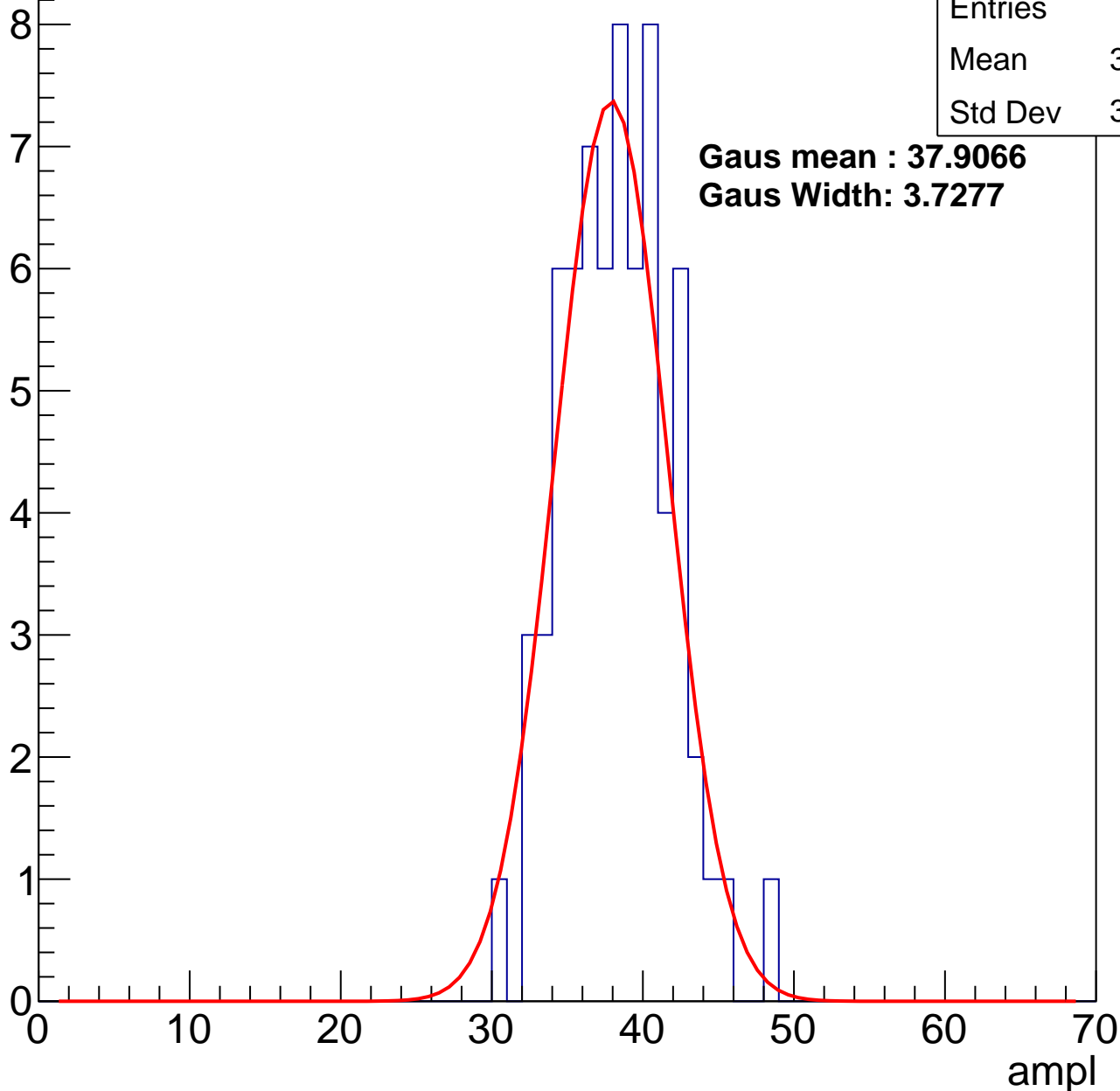
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.83
Std Dev	3.476

Gaus mean : 37.9066

Gaus Width: 3.7277



B1L103S, U12-ch97, adc2

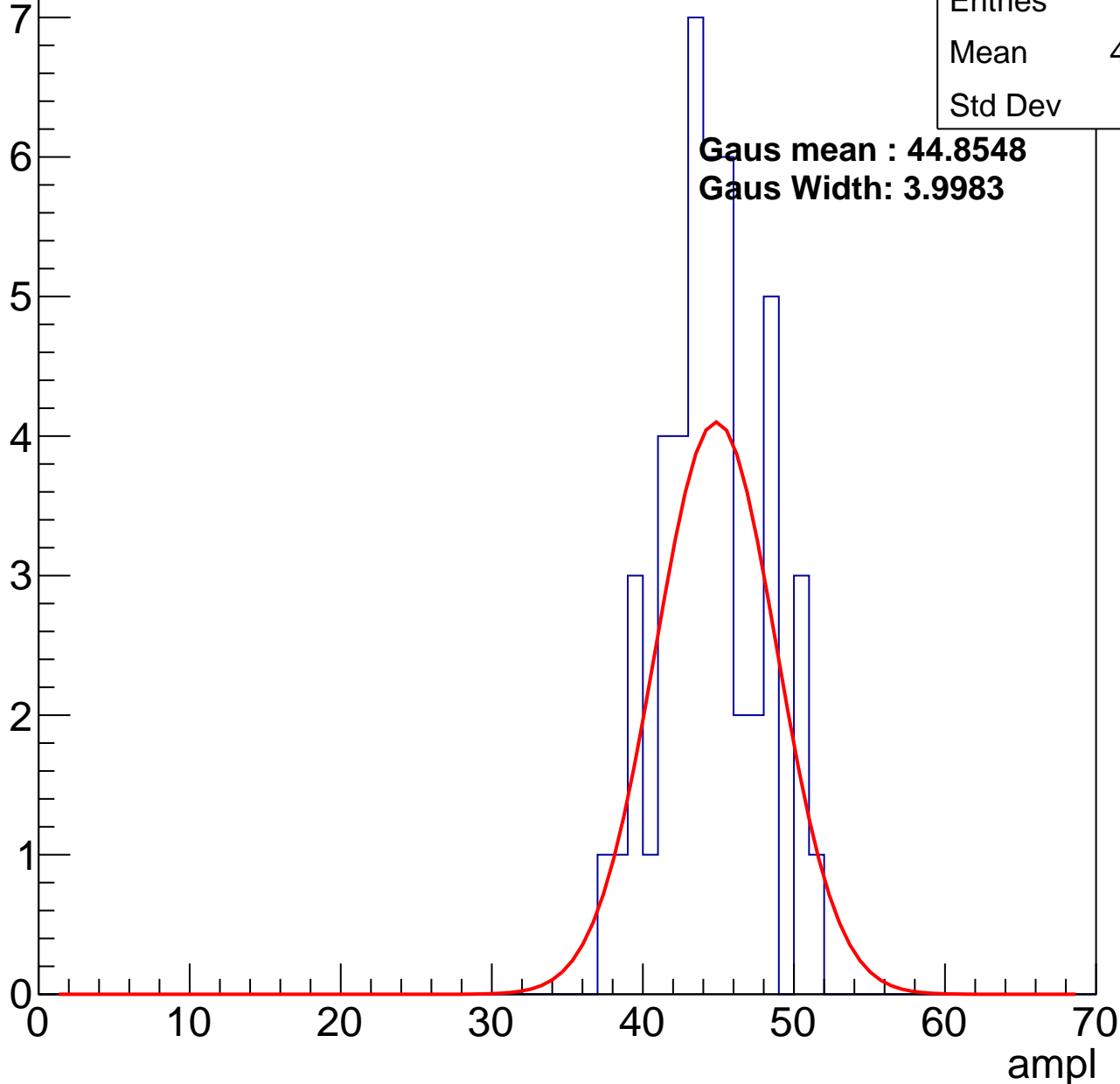
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	44.04
Std Dev	3.31

Gaus mean : 44.8548

Gaus Width: 3.9983



B1L103S, U12-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	50.08
Std Dev	3.826

Entry

10

8

6

4

2

0

0

10

20

30

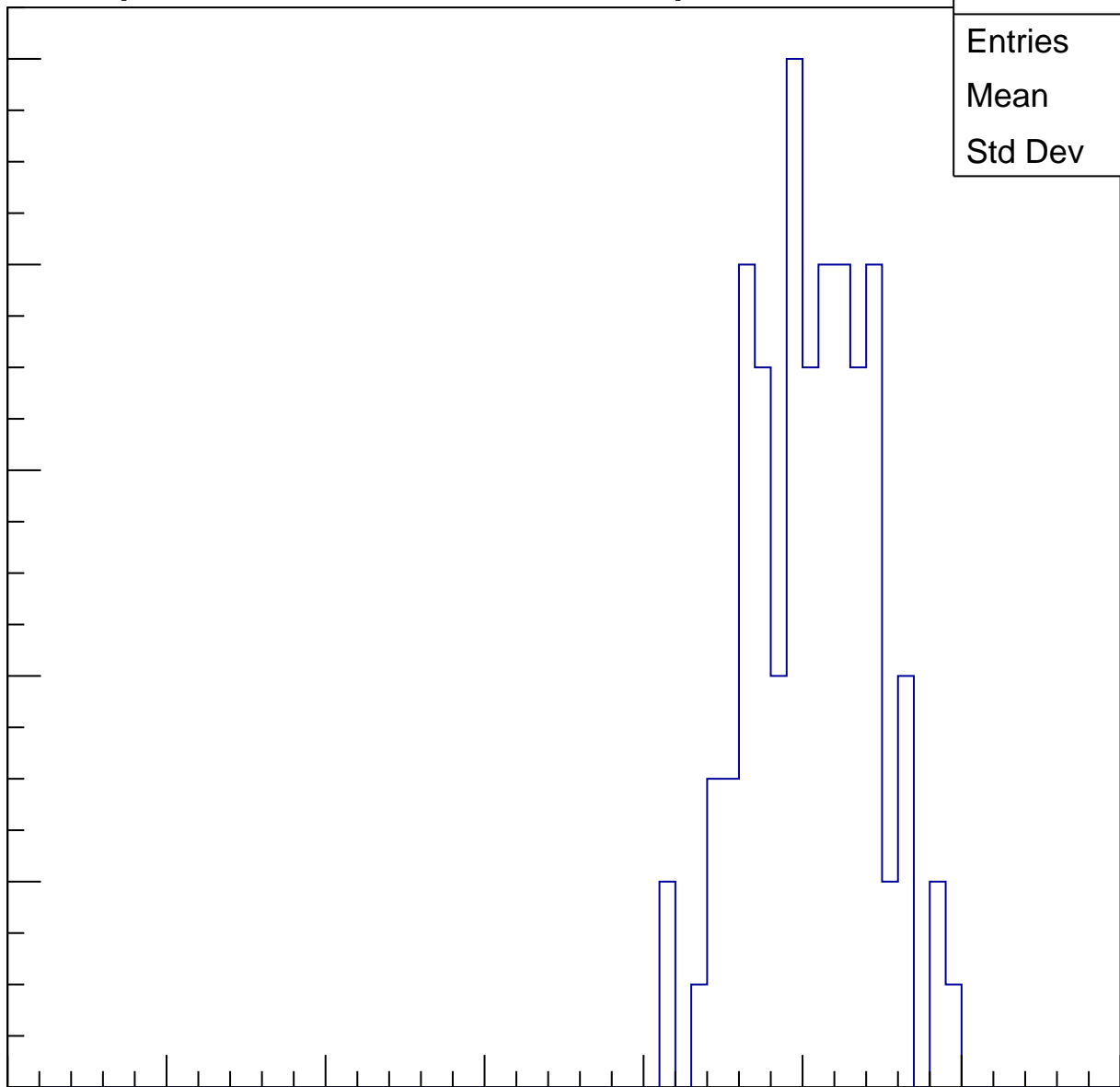
40

50

60

70

ampl

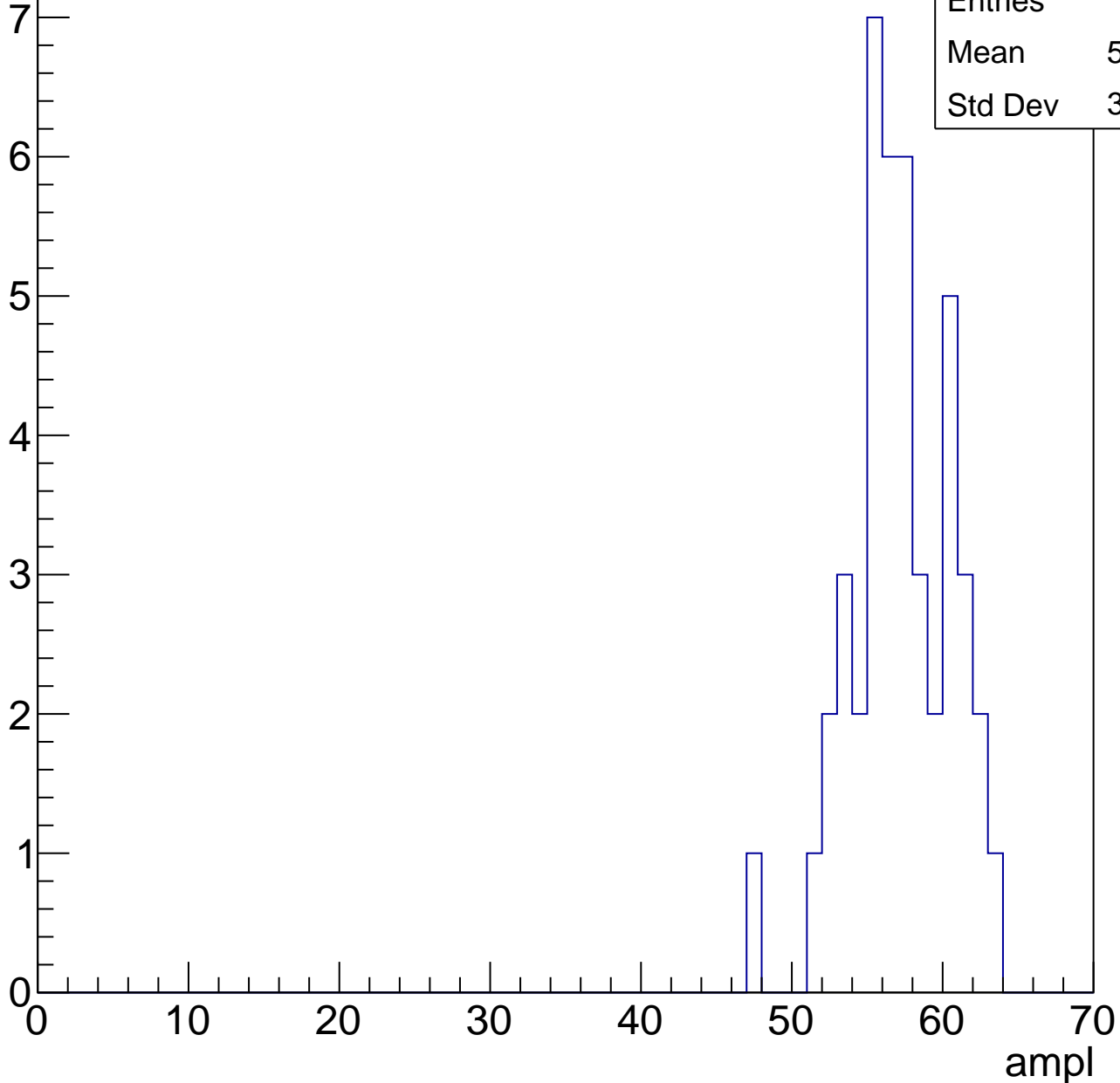


B1L103S, U12-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	56.68
Std Dev	3.274

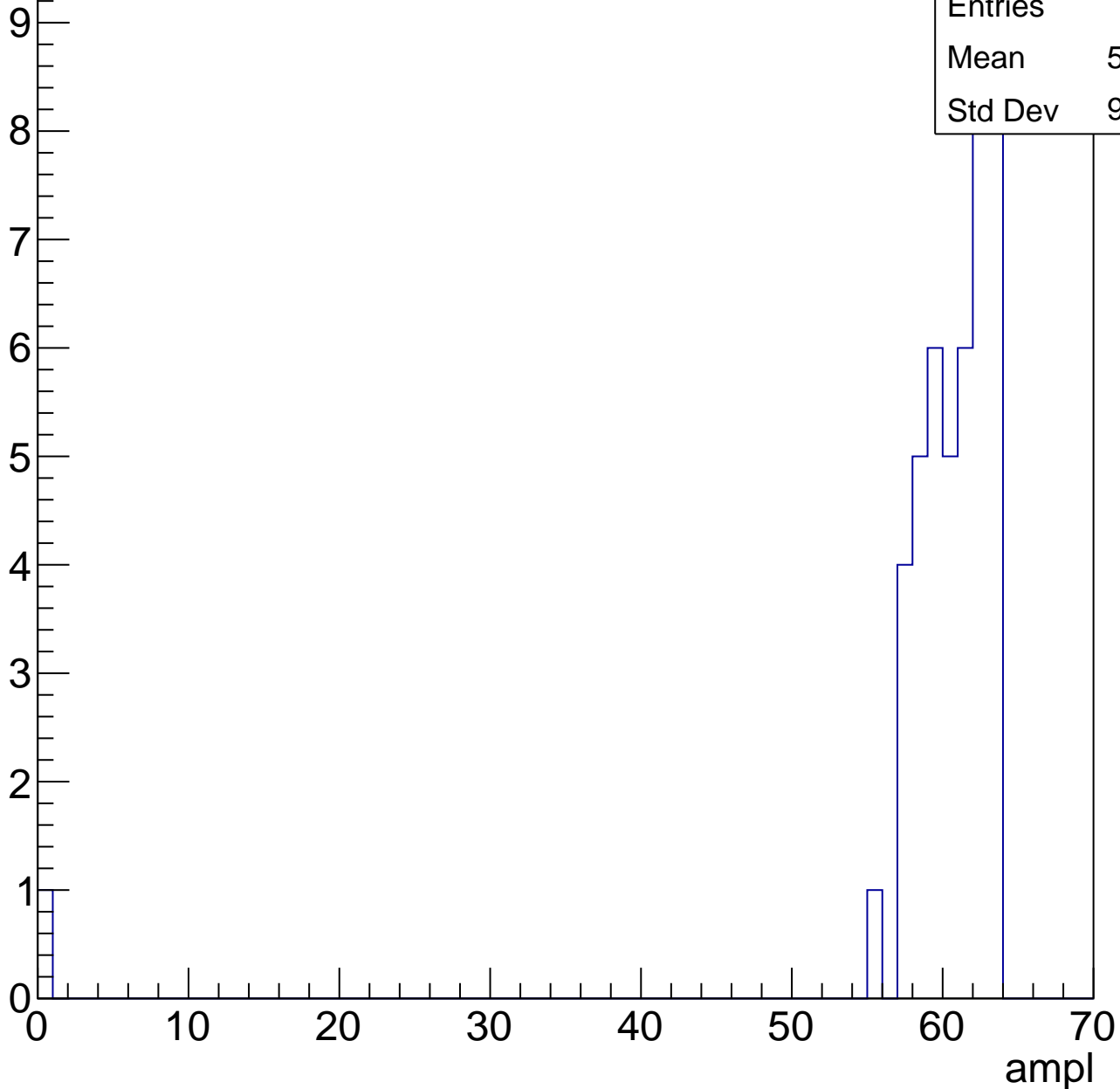


B1L103S, U12-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

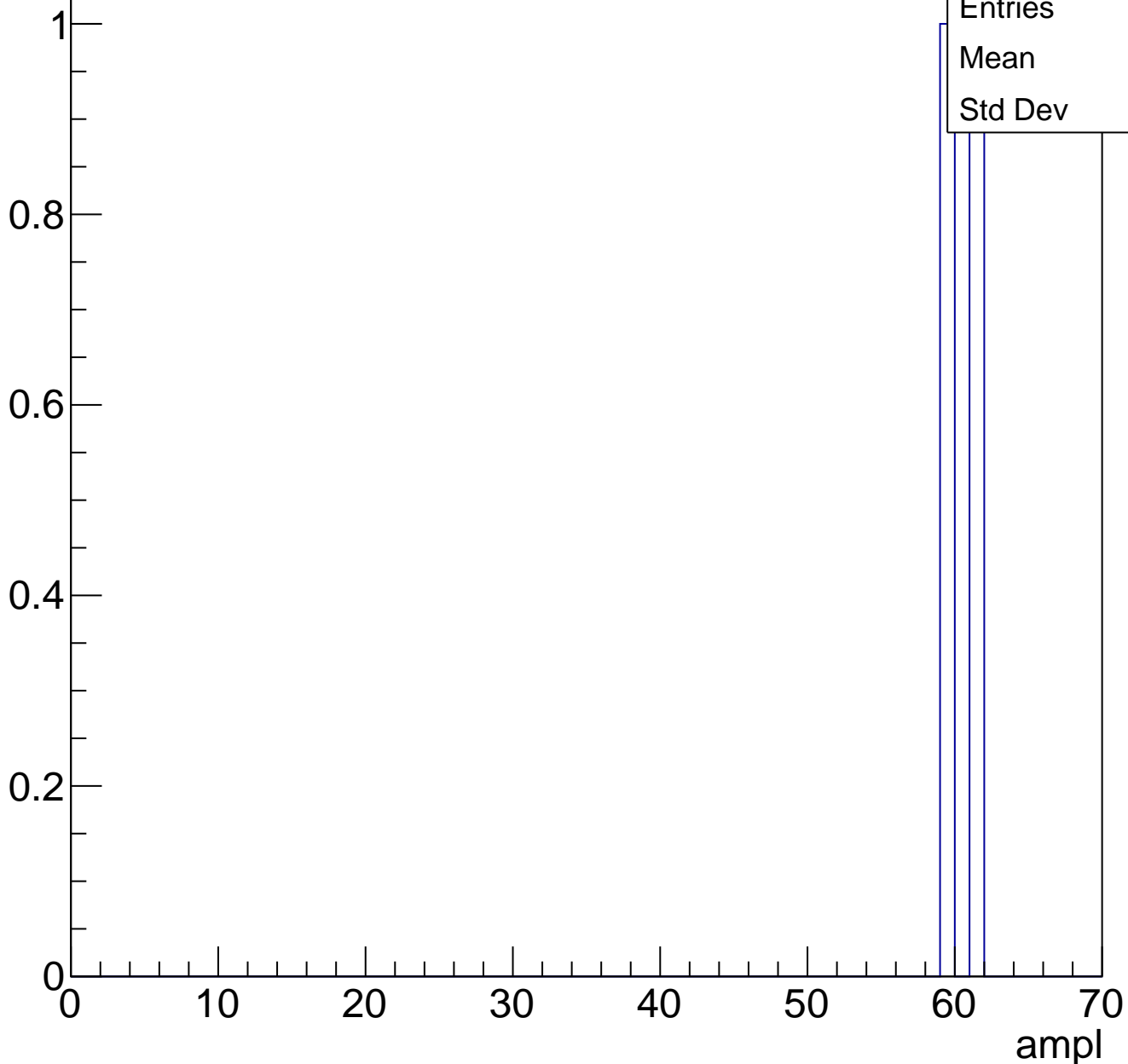
Entries	45
Mean	59.02
Std Dev	9.144



B1L103S, U12-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch98, adc0

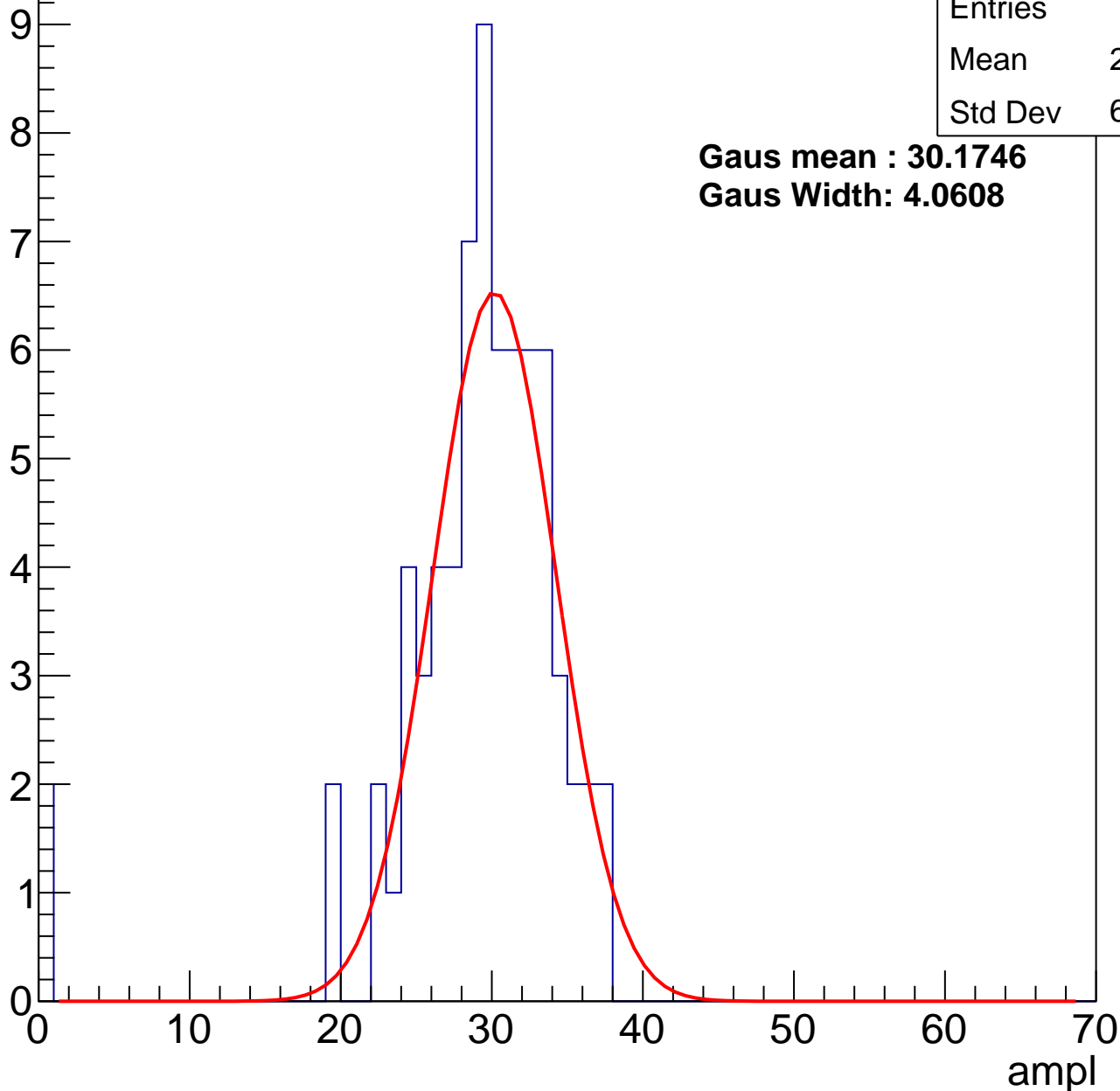
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	28.44
Std Dev	6.227

Gaus mean : 30.1746

Gaus Width: 4.0608



B1L103S, U12-ch98, adc1

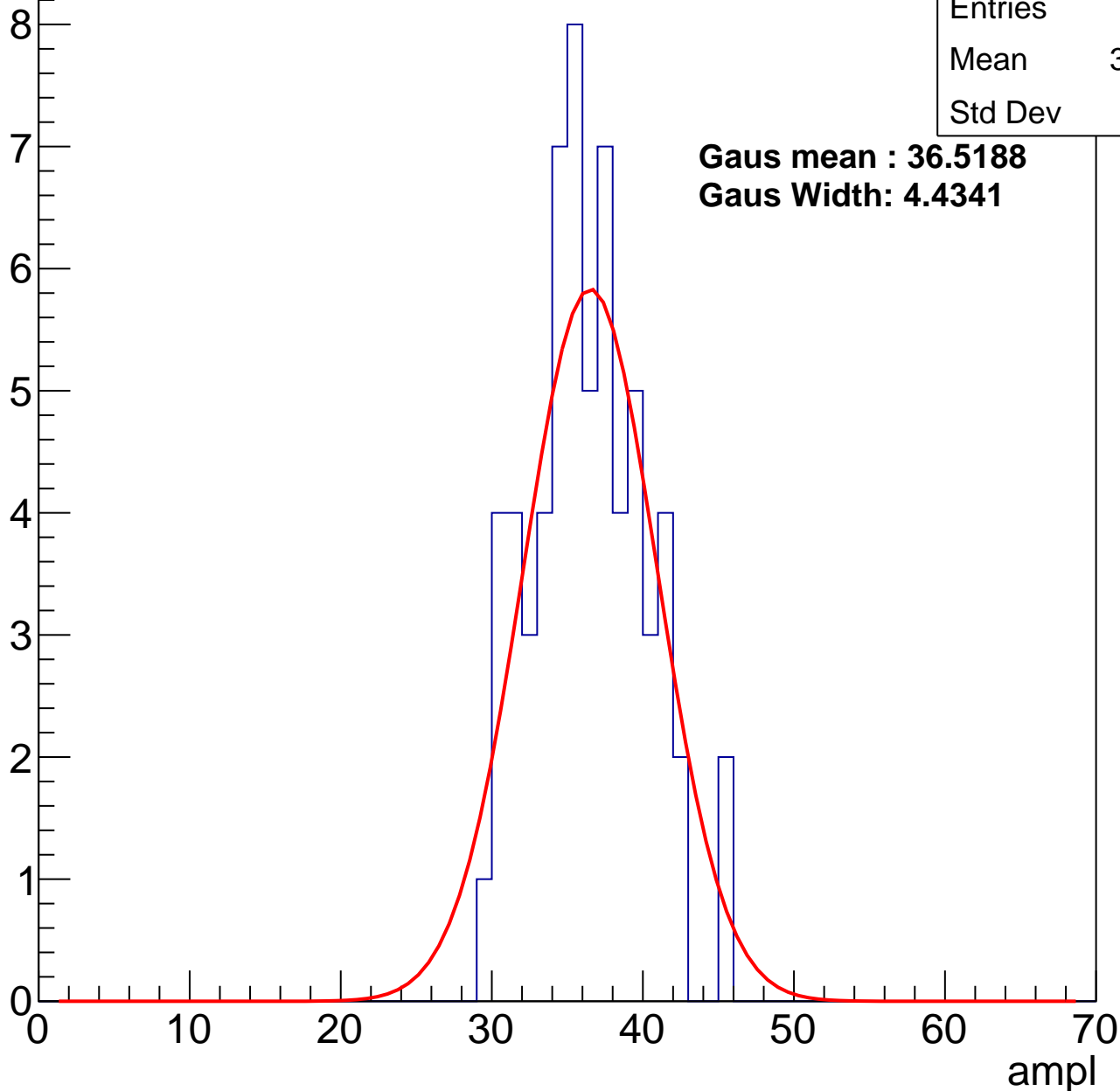
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.92
Std Dev	3.7

Gaus mean : 36.5188

Gaus Width: 4.4341



B1L103S, U12-ch98, adc2

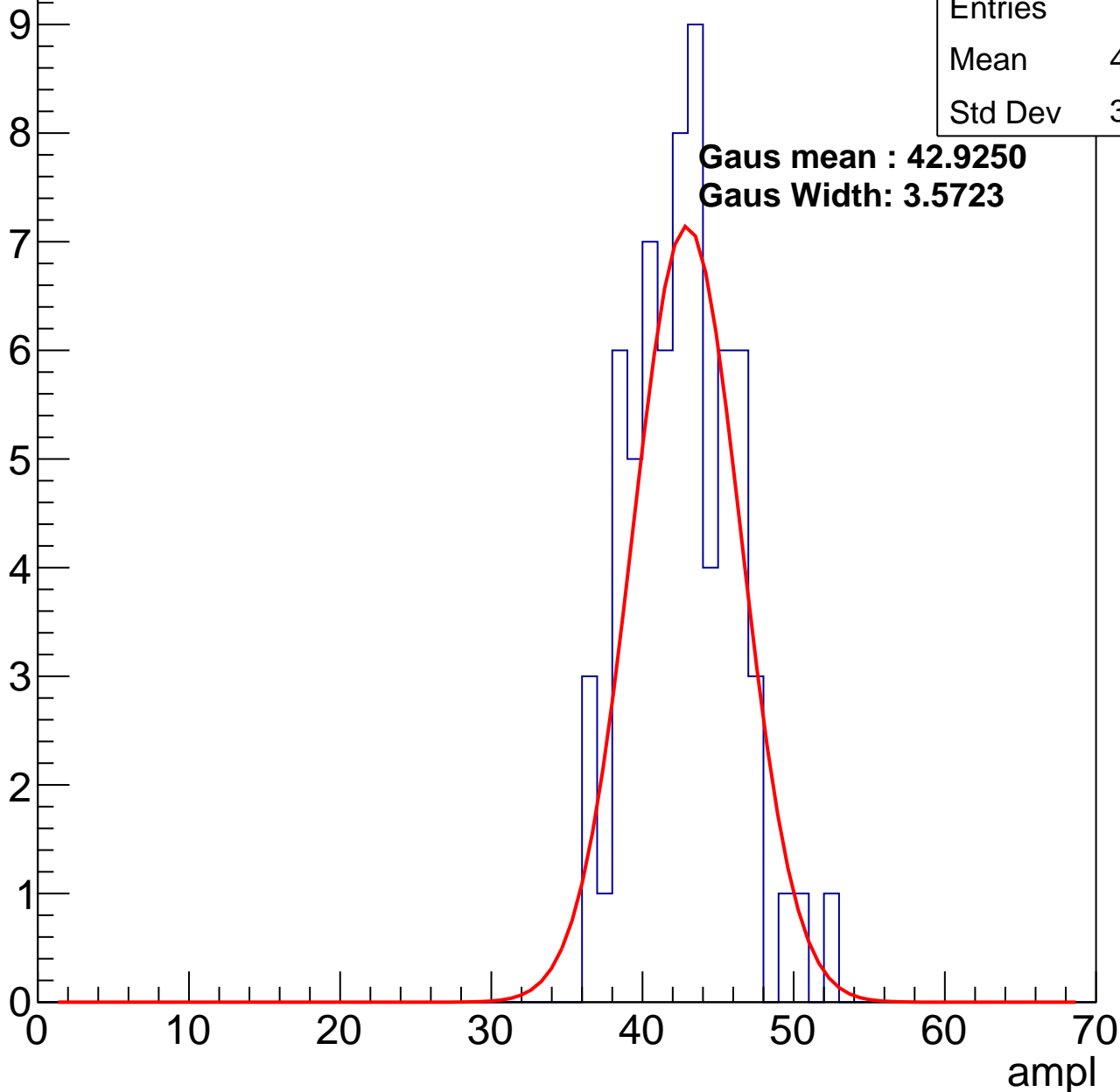
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.25
Std Dev	3.383

Gaus mean : 42.9250

Gaus Width: 3.5723

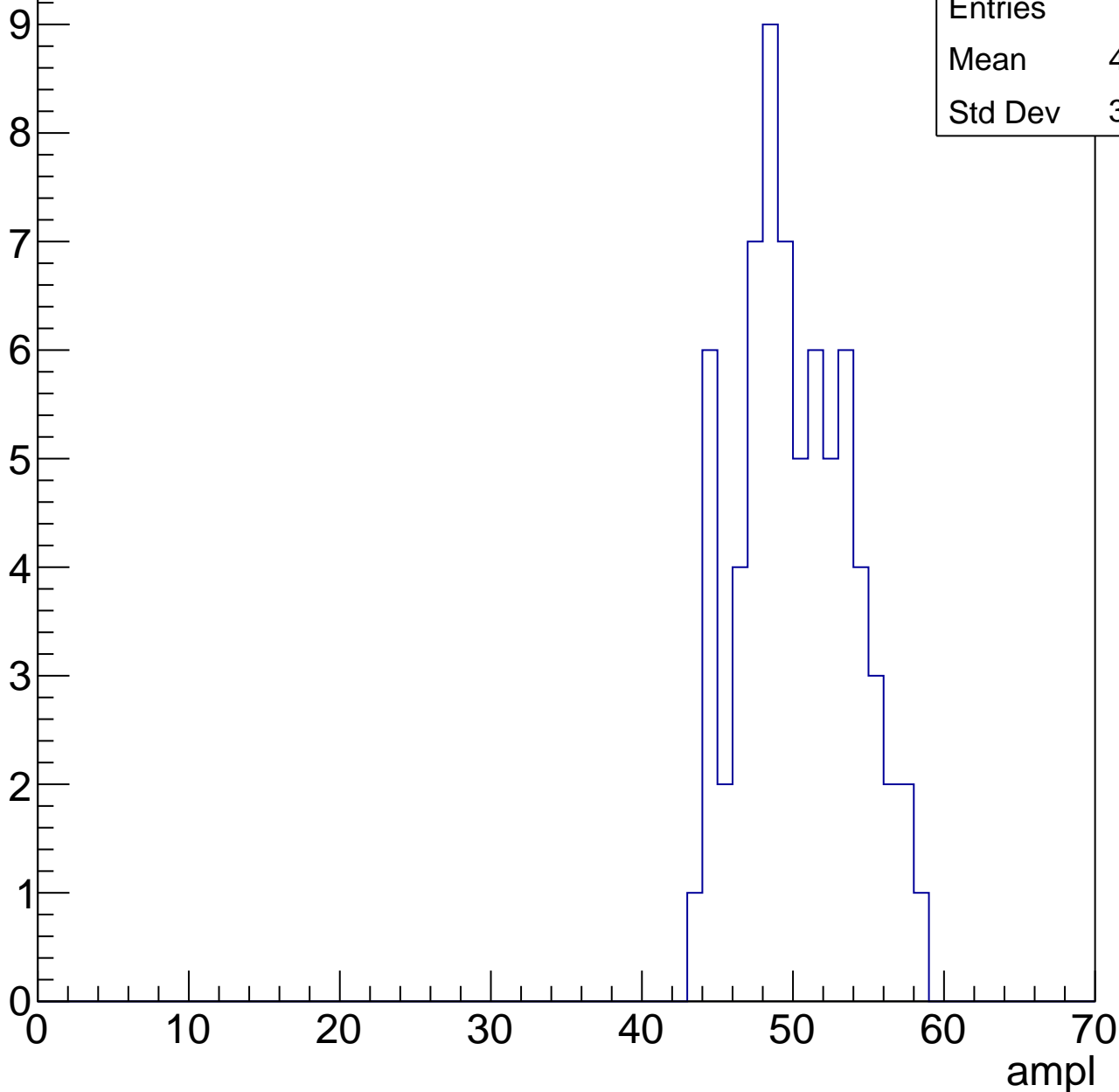


B1L103S, U12-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.77
Std Dev	3.653

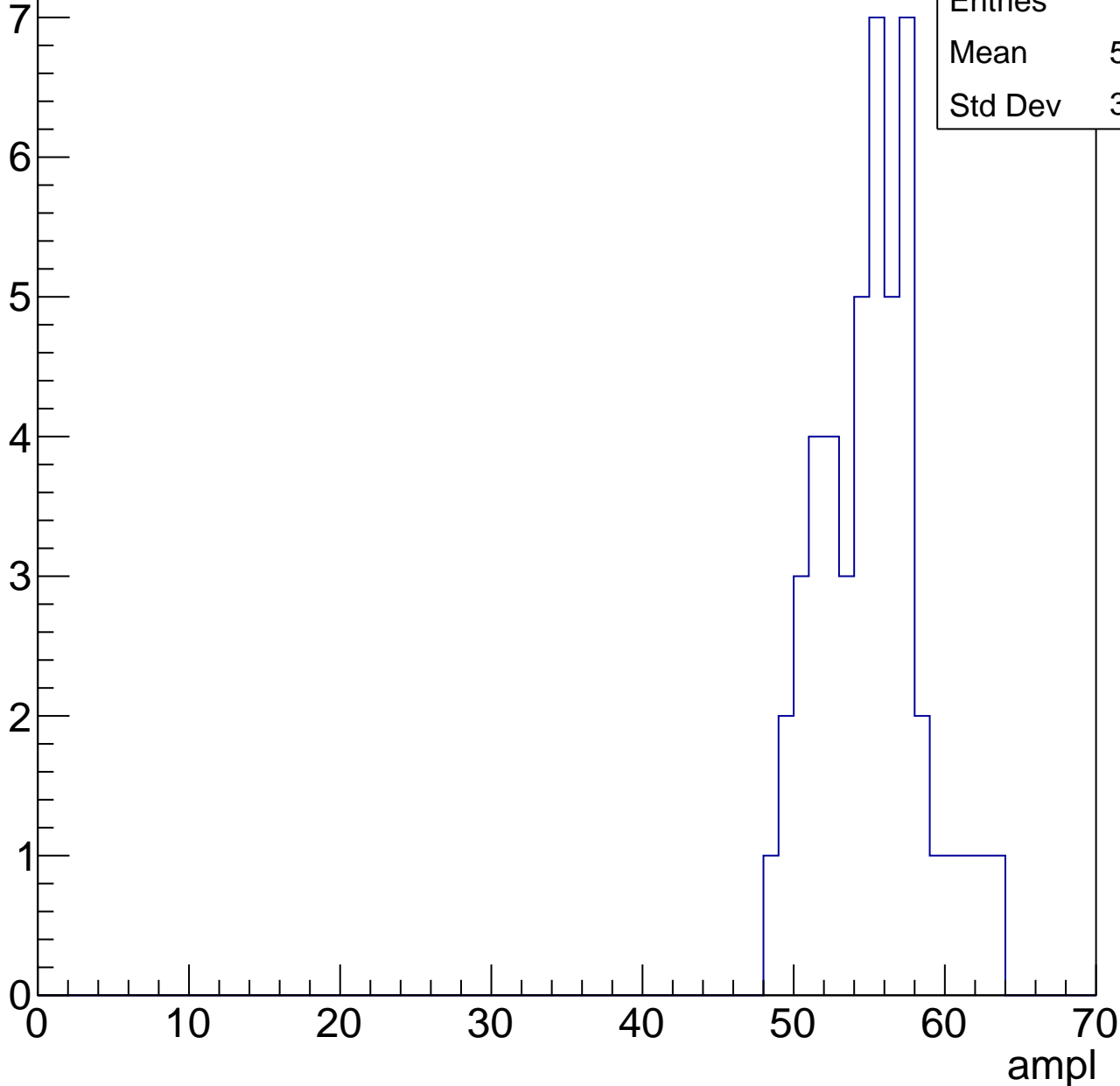


B1L103S, U12-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

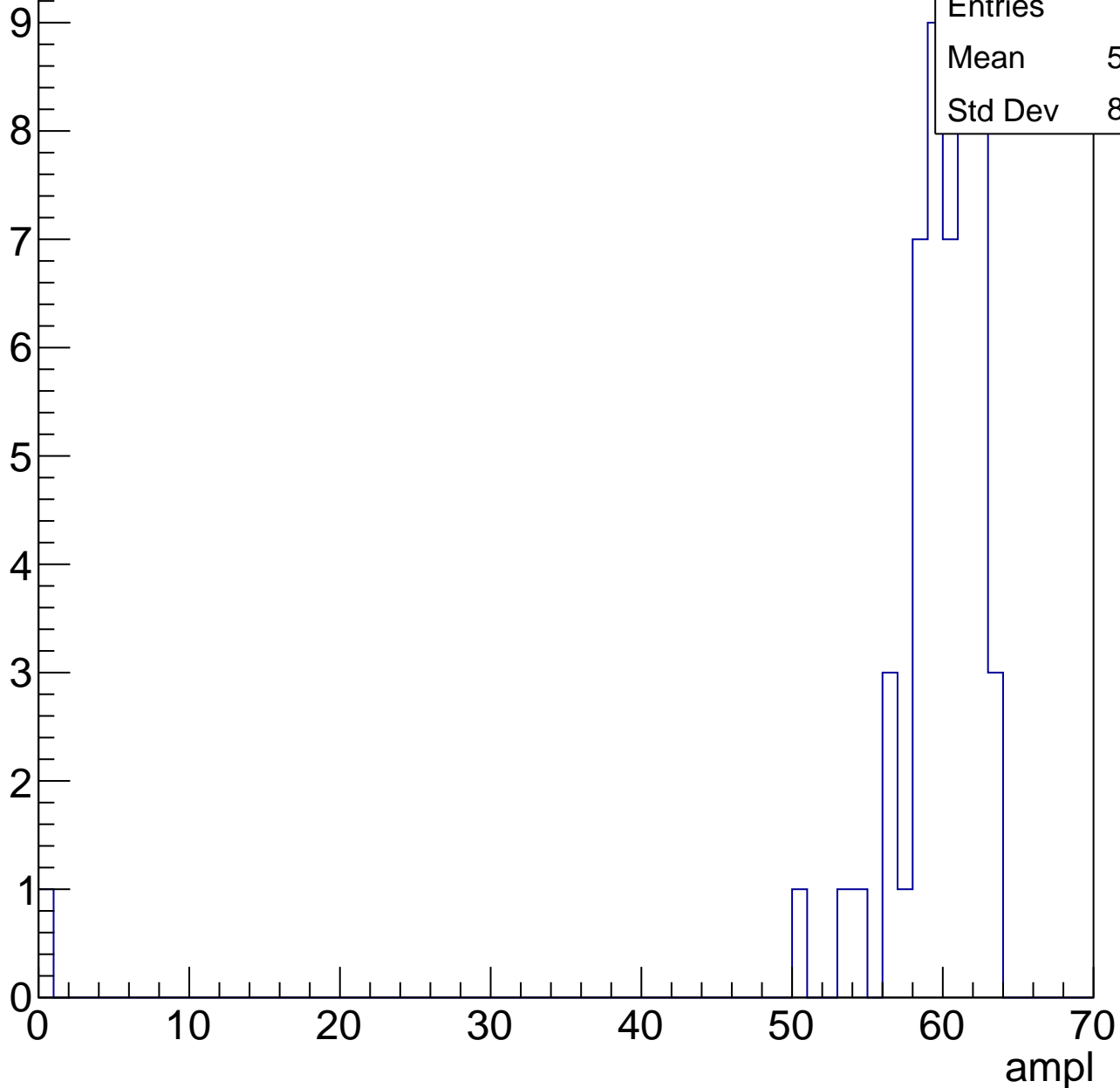
Entries	48
Mean	54.62
Std Dev	3.377



B1L103S, U12-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

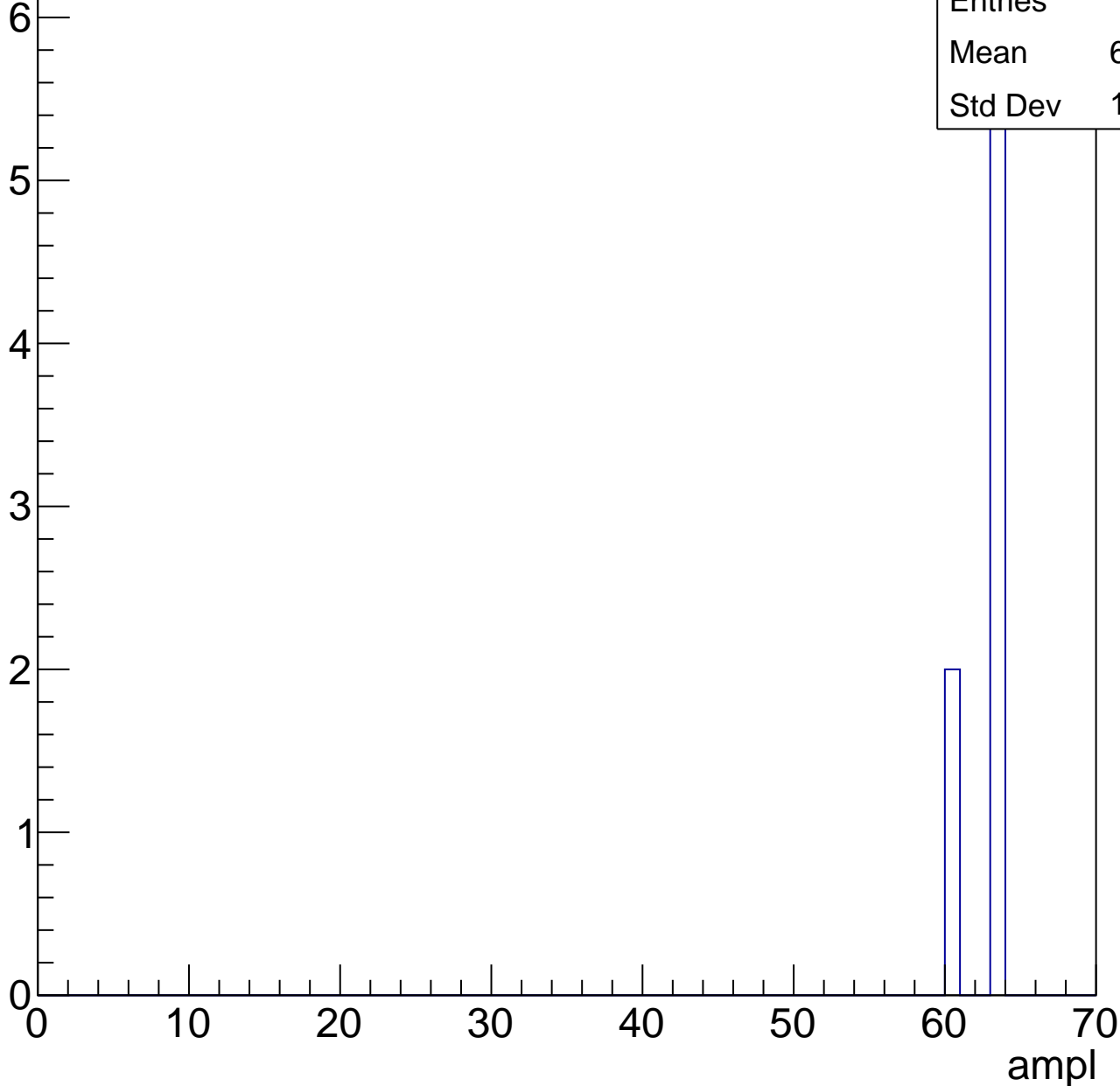


B1L103S, U12-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	8
Mean	62.25
Std Dev	1.299



B1L103S, U12-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch99, adc0

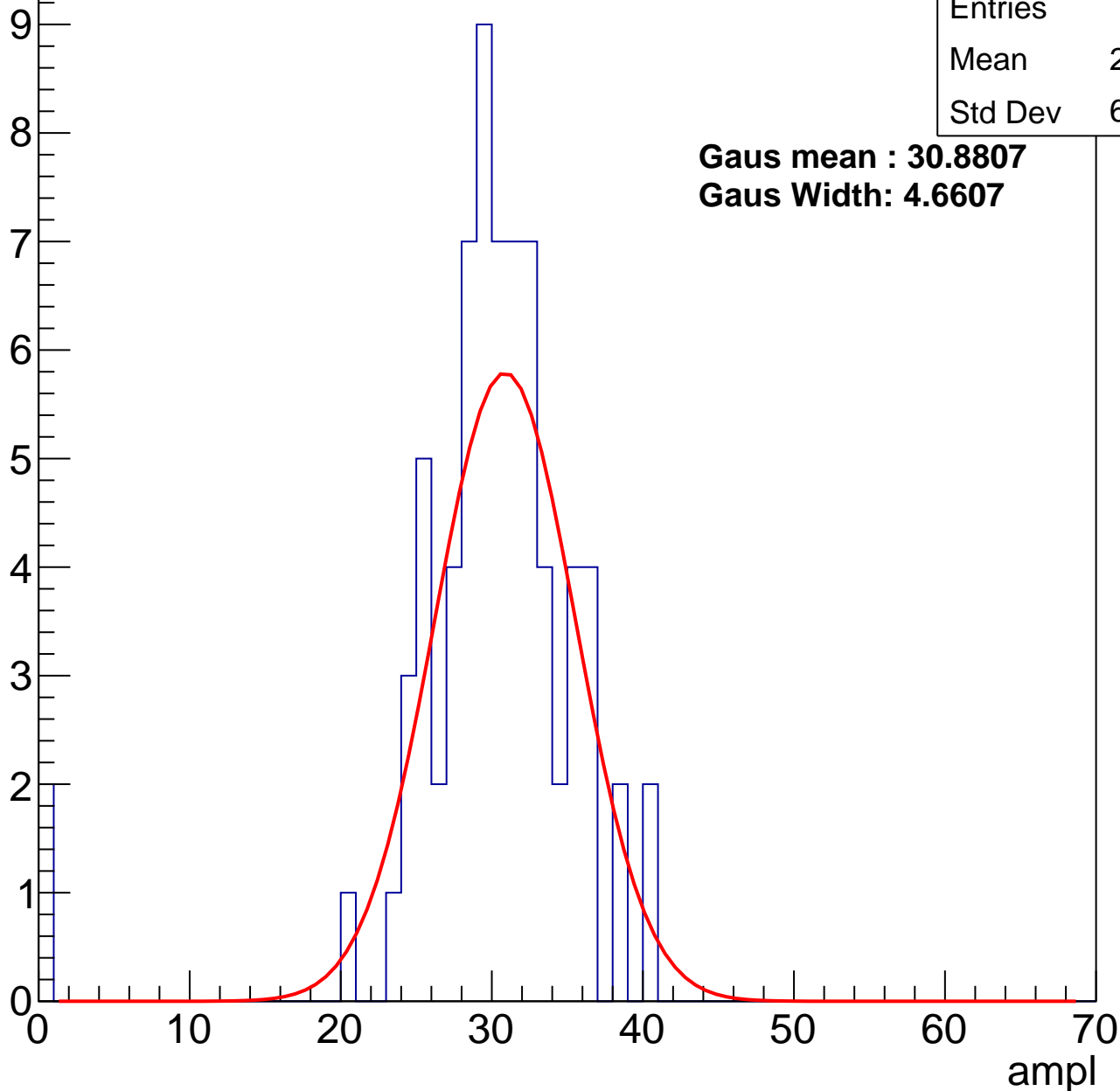
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.42
Std Dev	6.344

Gaus mean : 30.8807

Gaus Width: 4.6607



B1L103S, U12-ch99, adc1

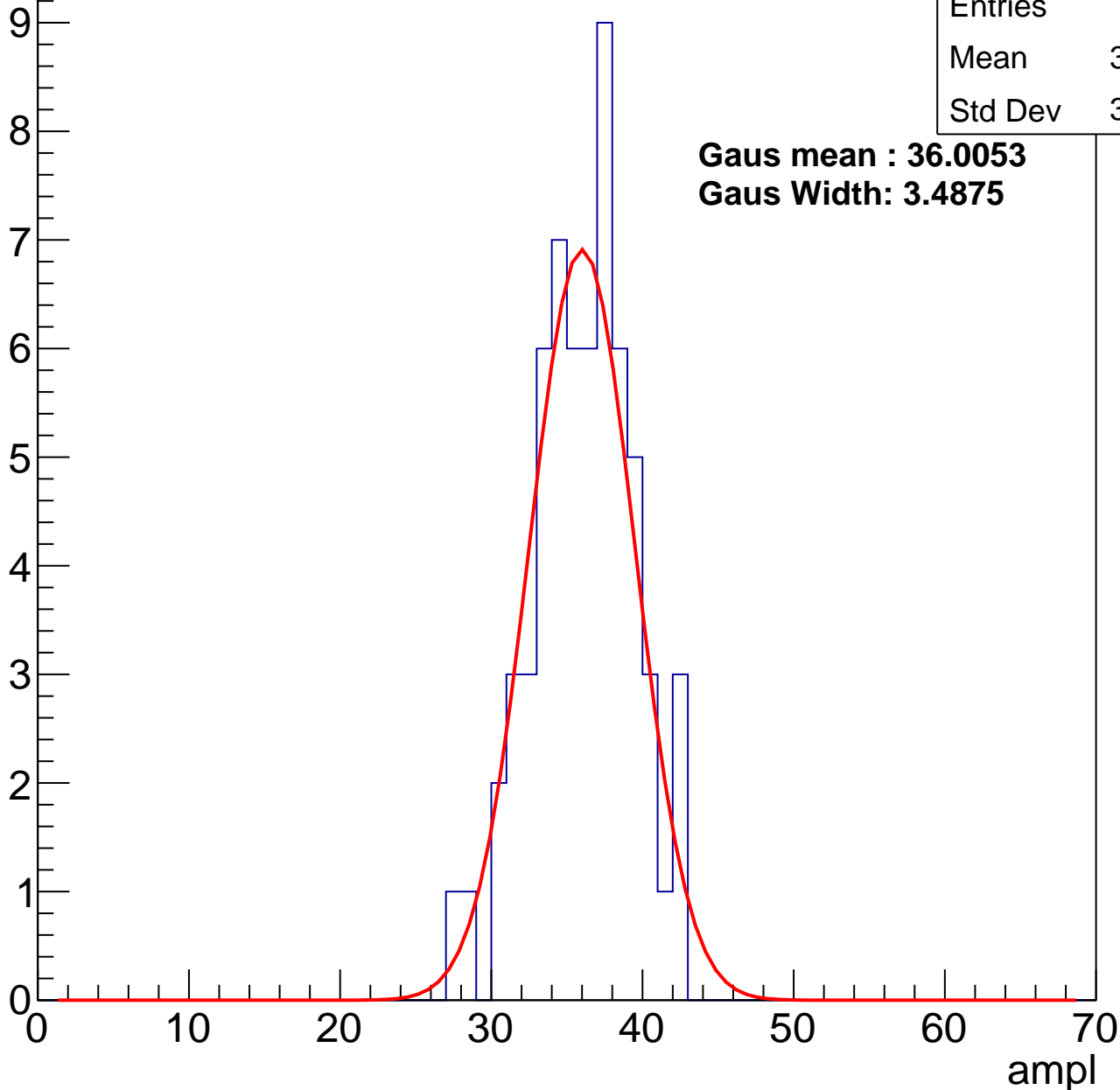
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.63
Std Dev	3.308

Gaus mean : 36.0053

Gaus Width: 3.4875



B1L103S, U12-ch99, adc2

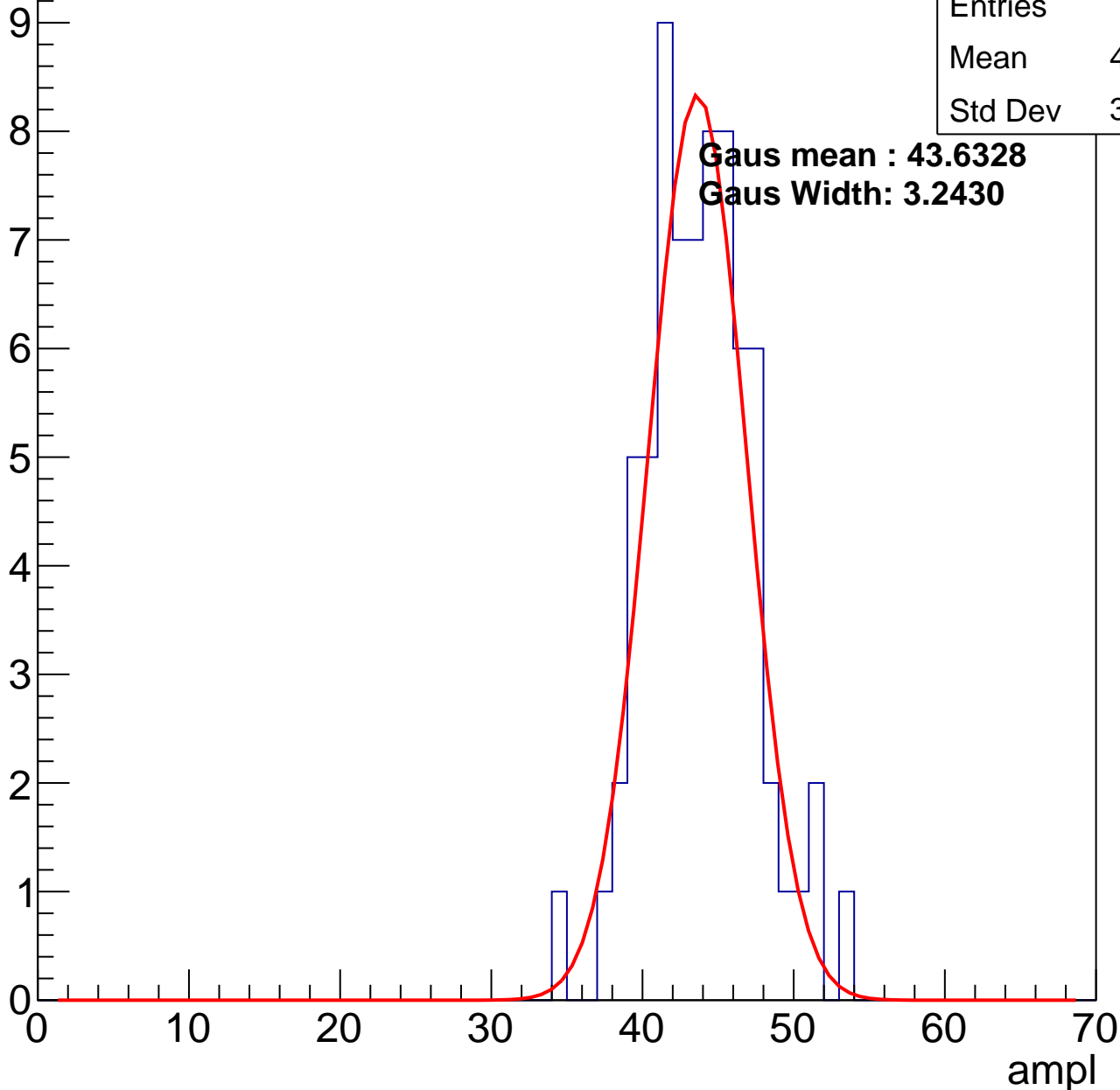
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.42
Std Dev	3.487

Gaus mean : 43.6328

Gaus Width: 3.2430

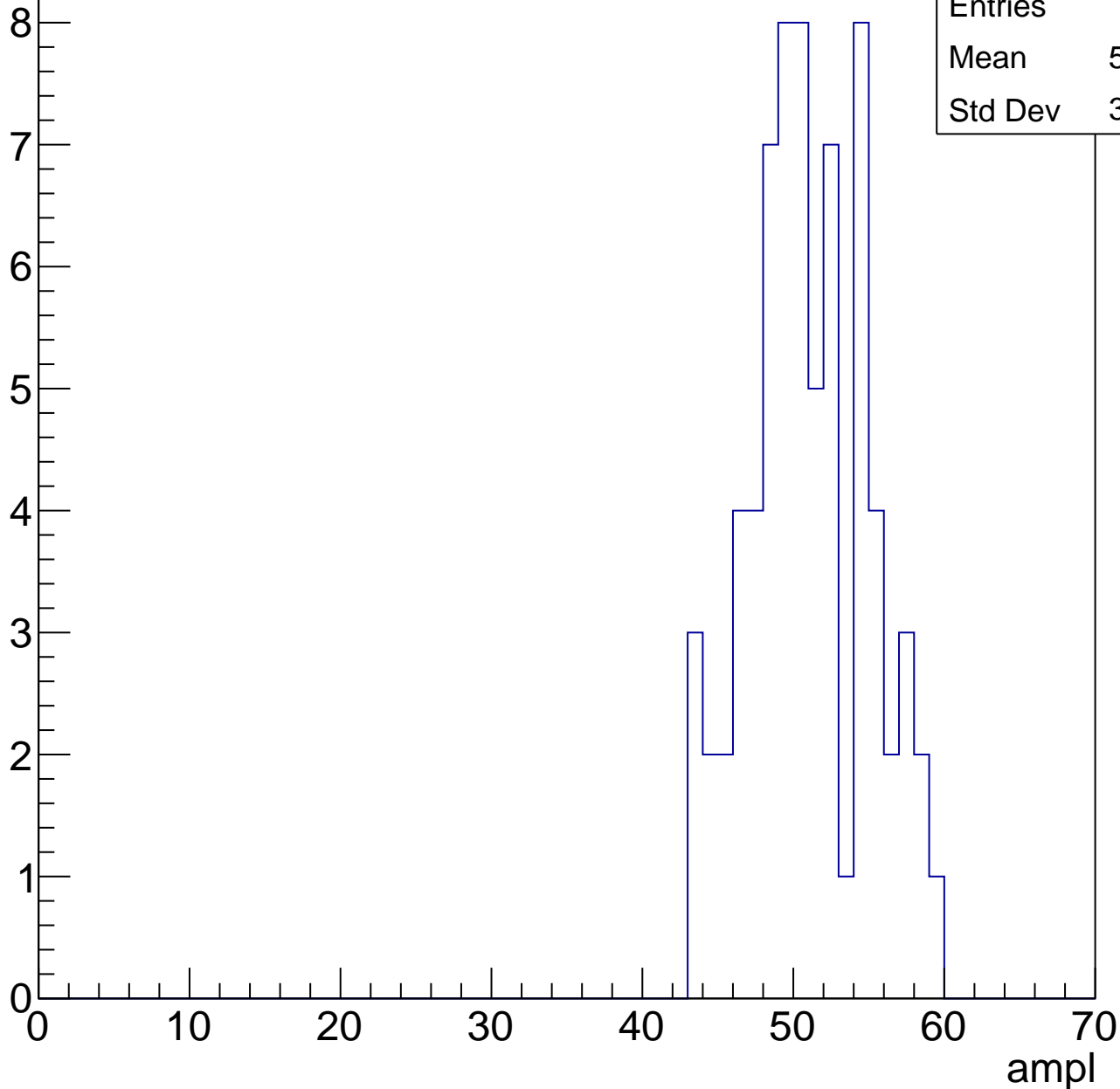


B1L103S, U12-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.55
Std Dev	3.903

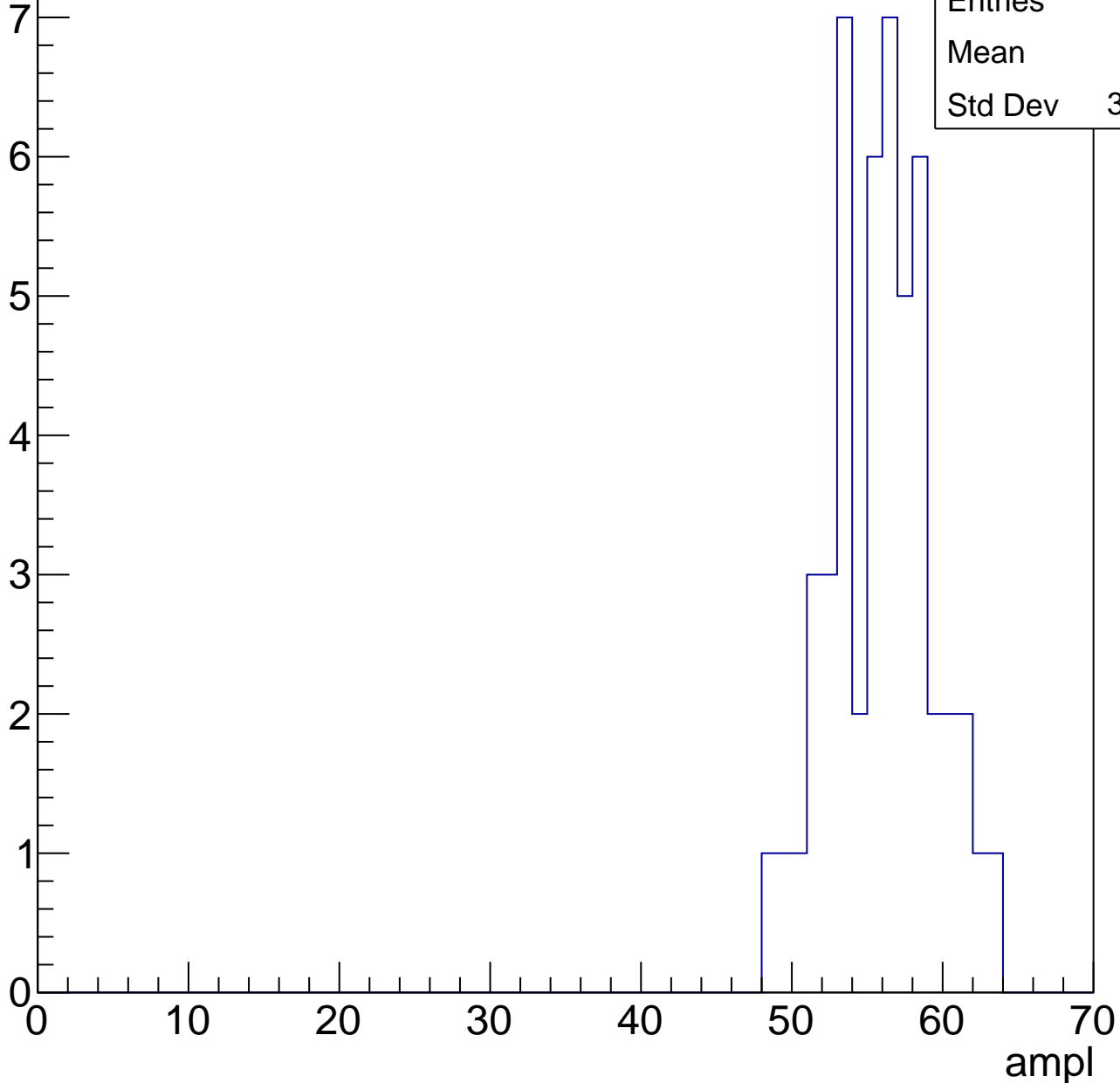


B1L103S, U12-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.5
Std Dev	3.318

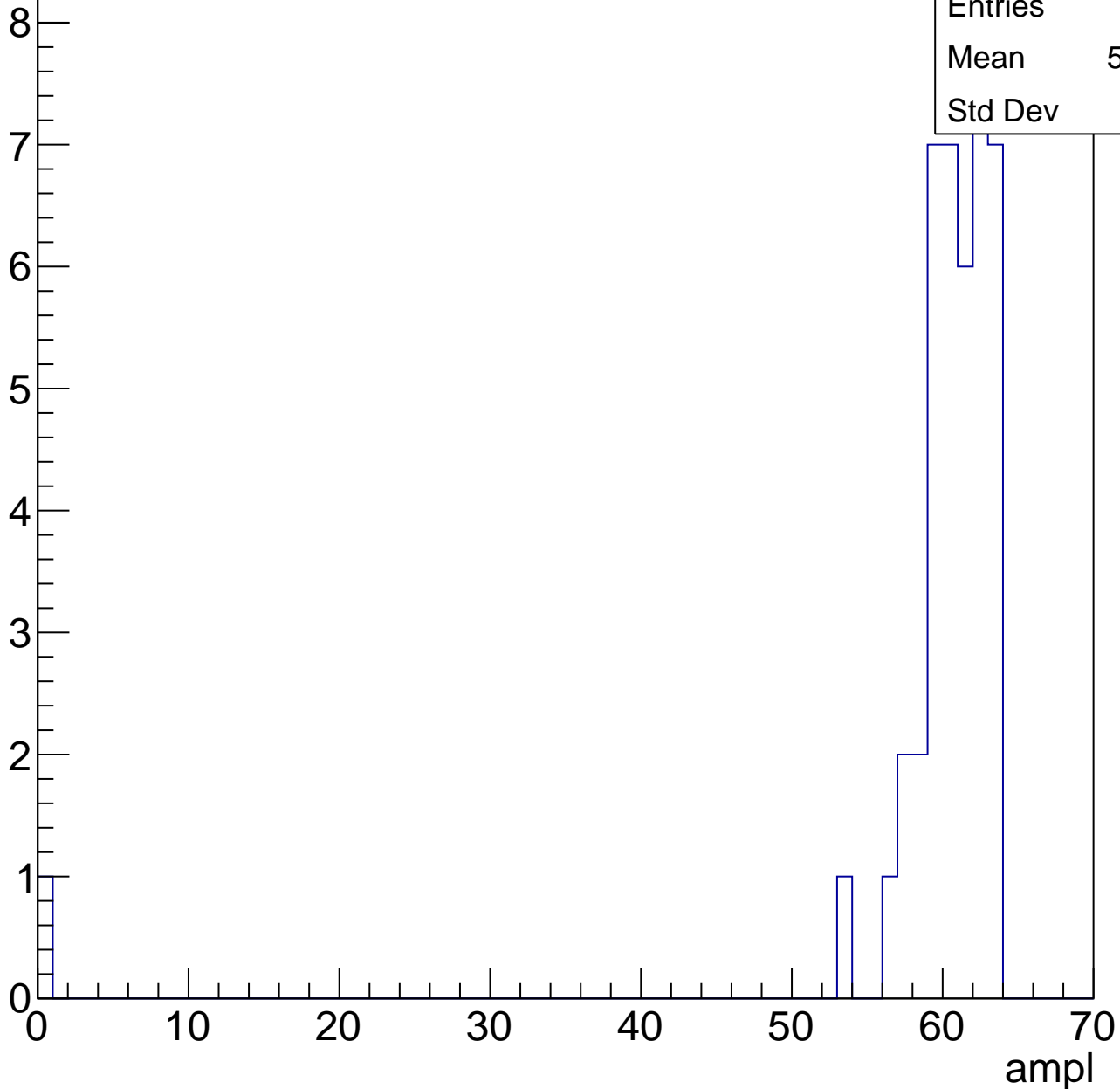


B1L103S, U12-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.93
Std Dev	9.45



B1L103S, U12-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

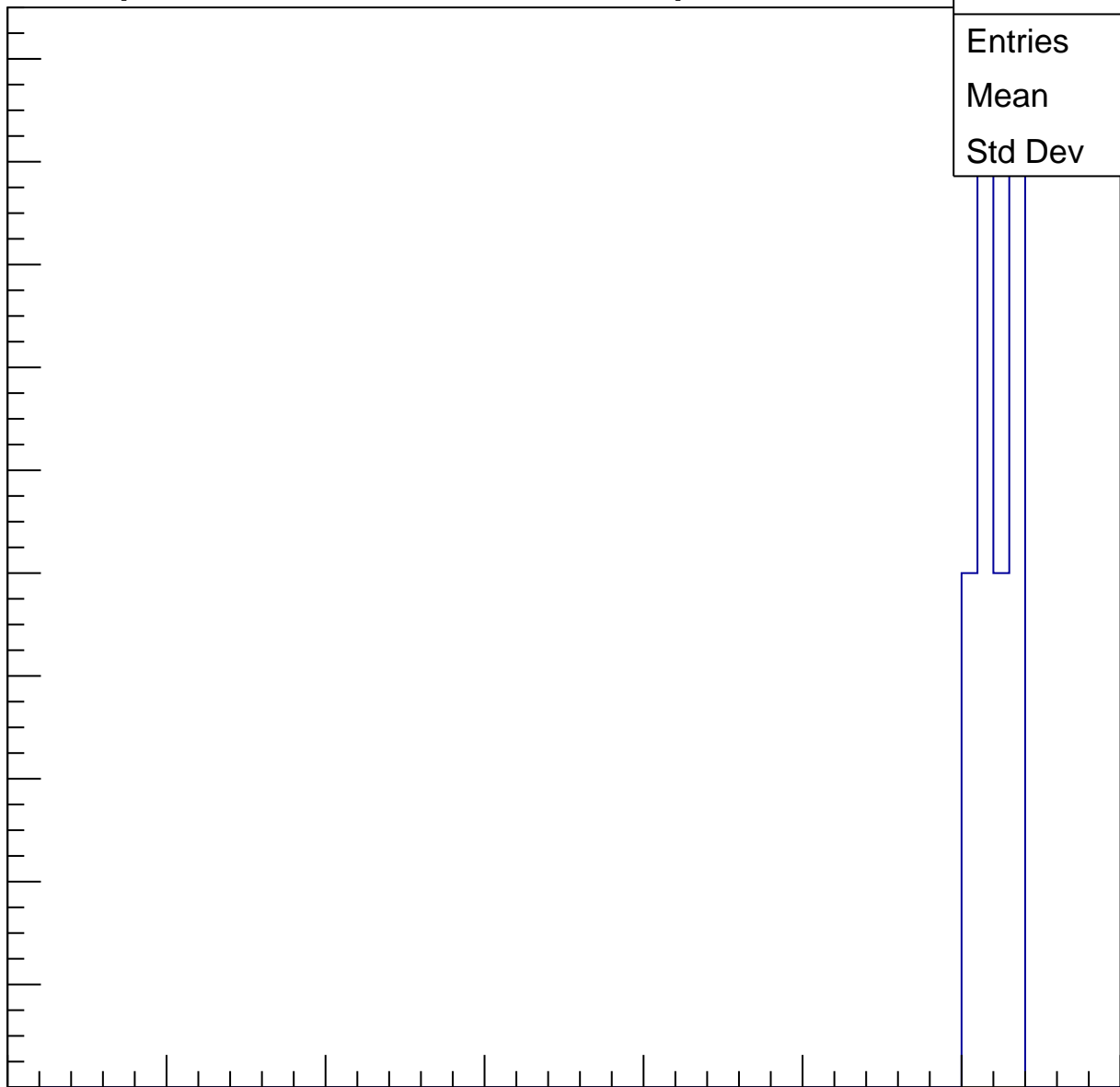
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.67
Std Dev	1.106

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch100, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	84
Mean	30.19
Std Dev	5.266

Gaus mean : 30.7608

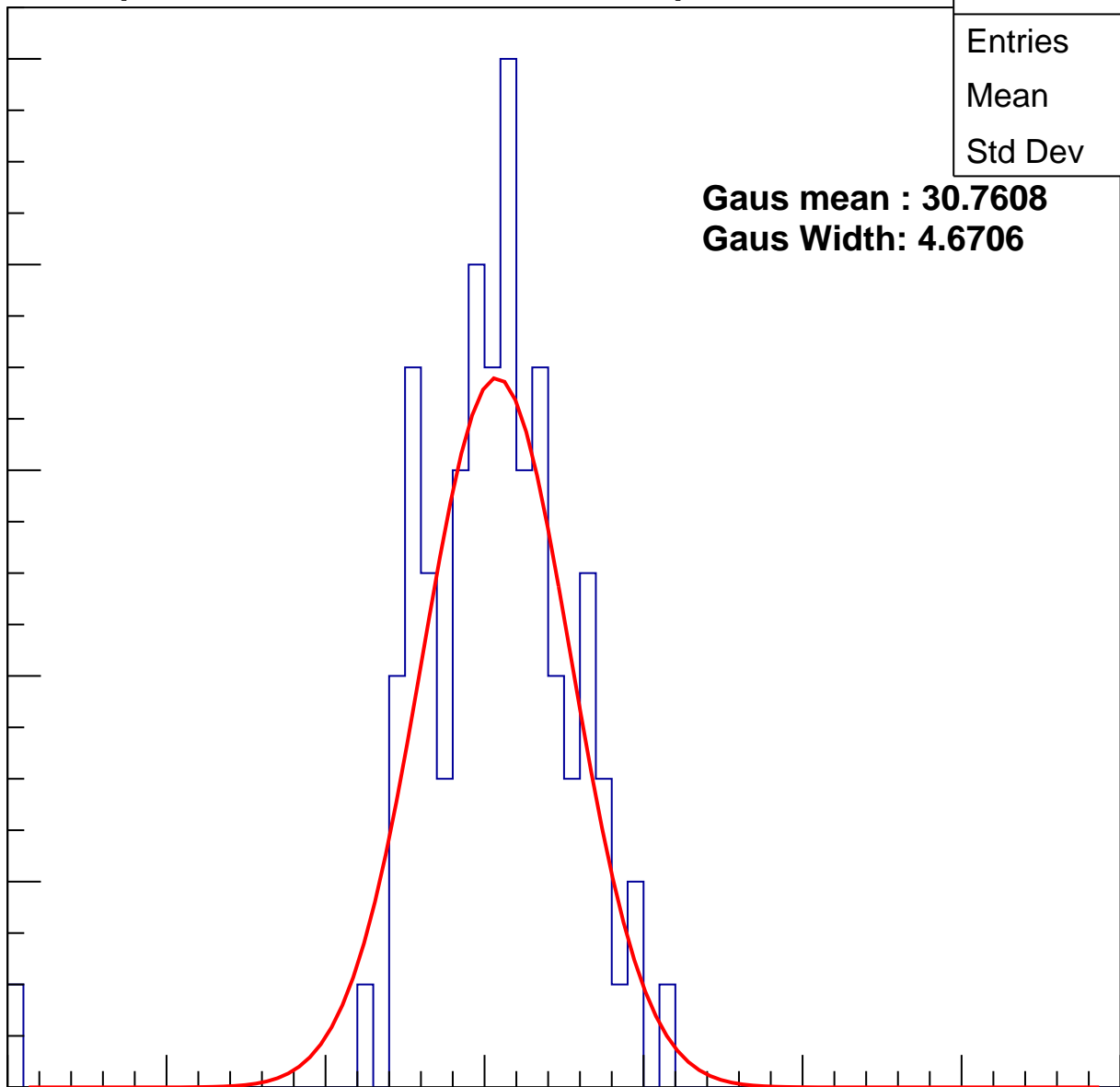
Gaus Width: 4.6706

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch100, adc1

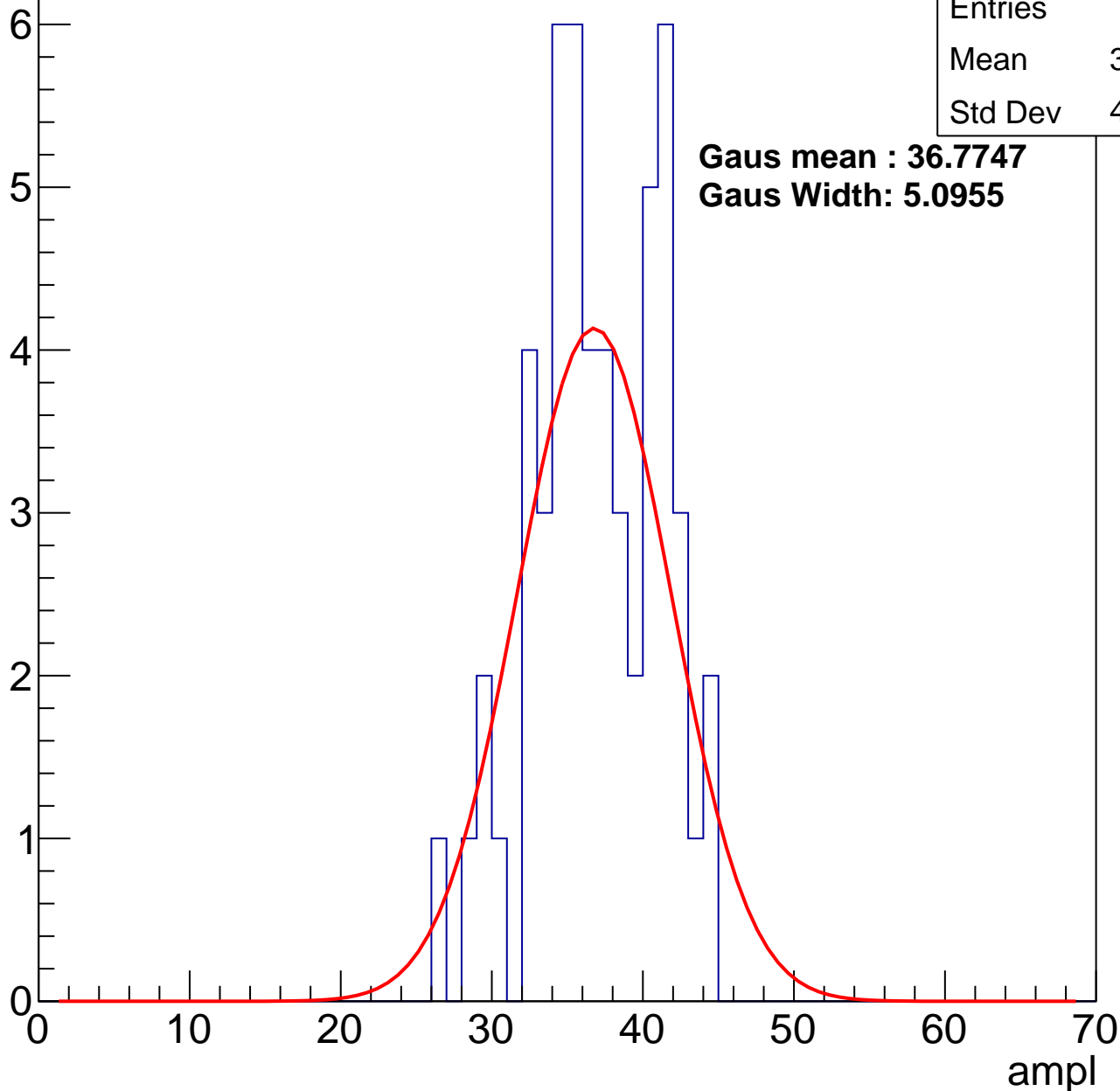
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.48
Std Dev	4.206

Gaus mean : 36.7747

Gaus Width: 5.0955



B1L103S, U12-ch100, adc2

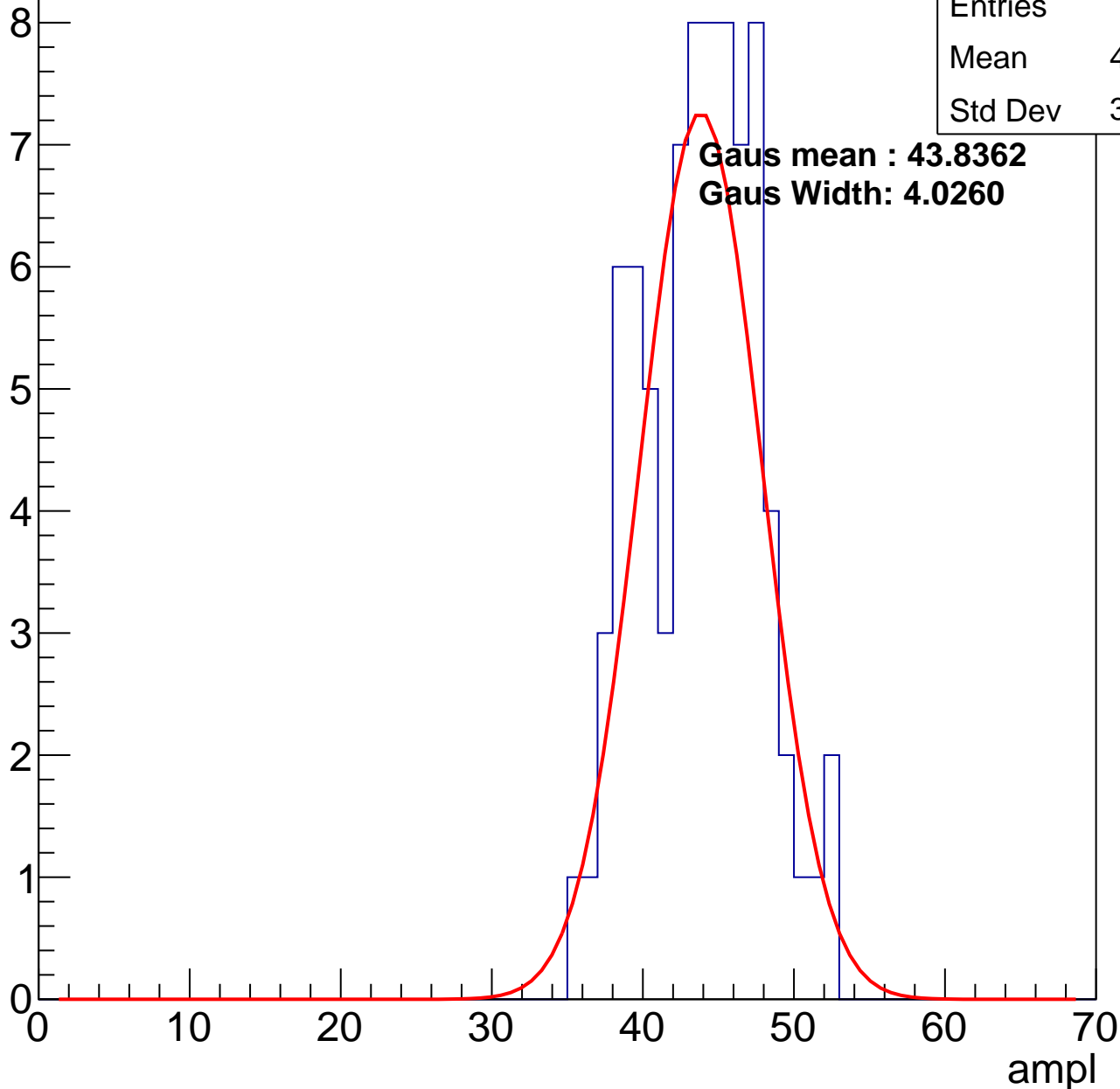
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	43.33
Std Dev	3.849

Gaus mean : 43.8362

Gaus Width: 4.0260

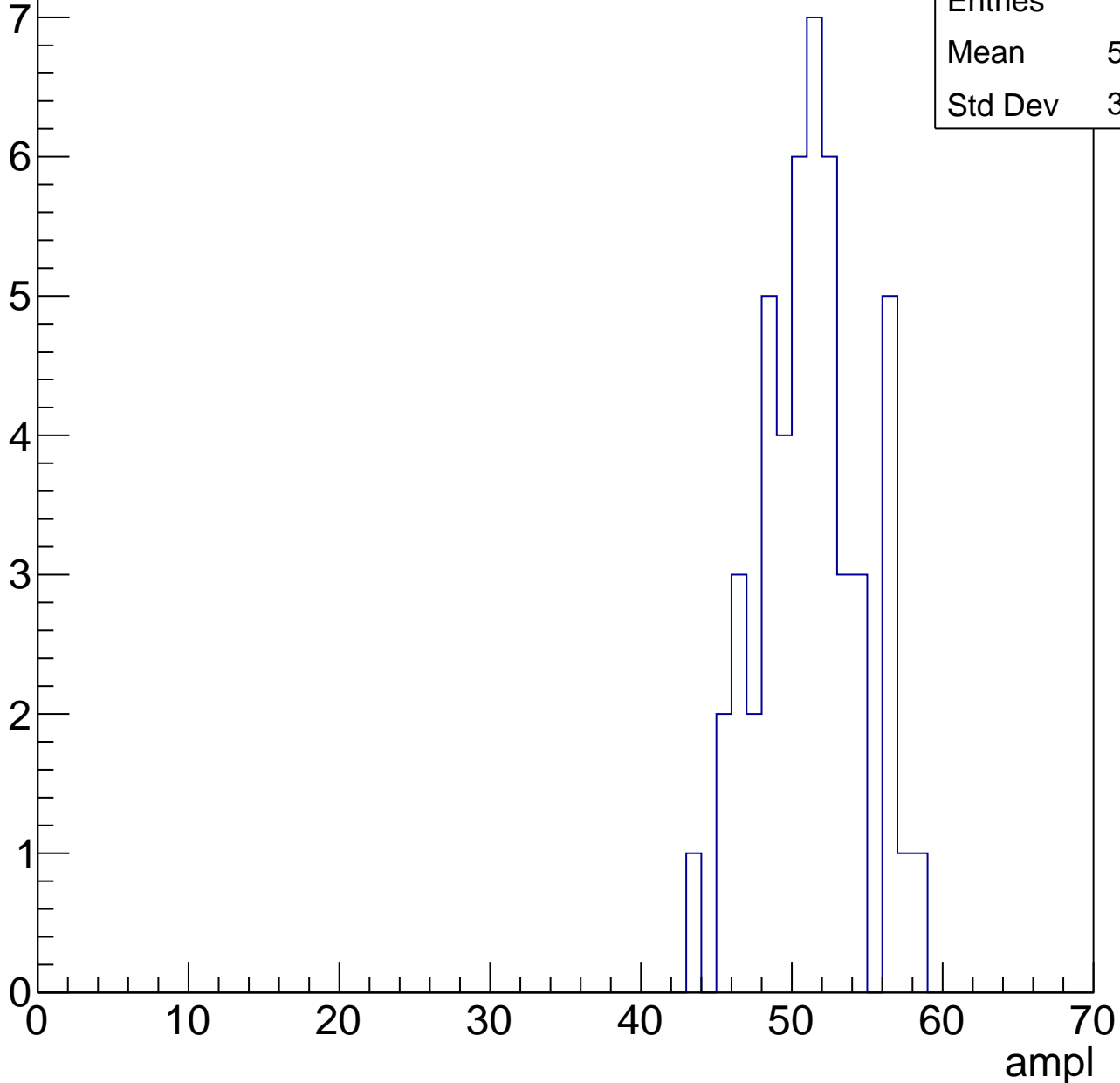


B1L103S, U12-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

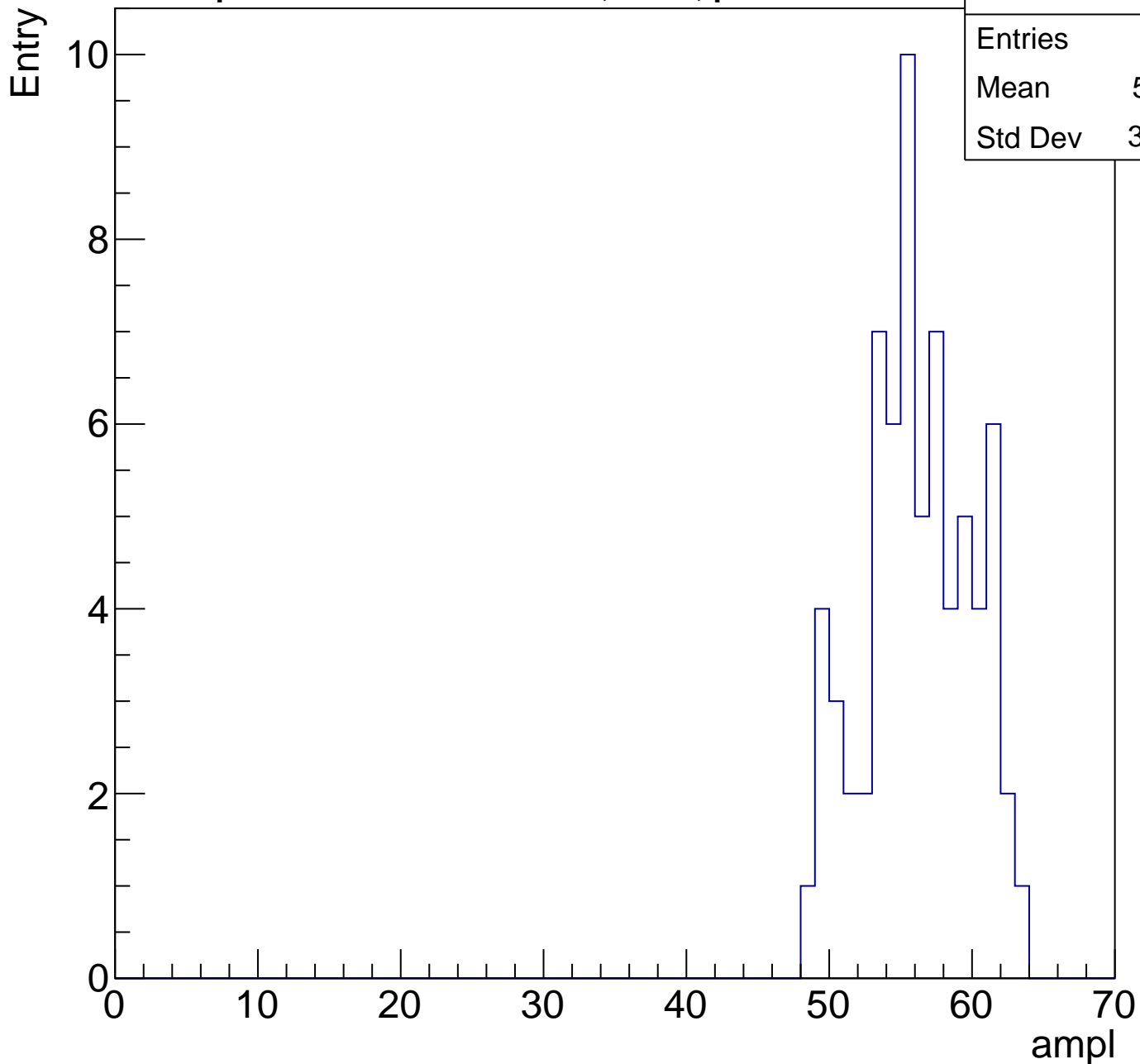
Entries	49
Mean	50.73
Std Dev	3.385



B1L103S, U12-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	55.71
Std Dev	3.699

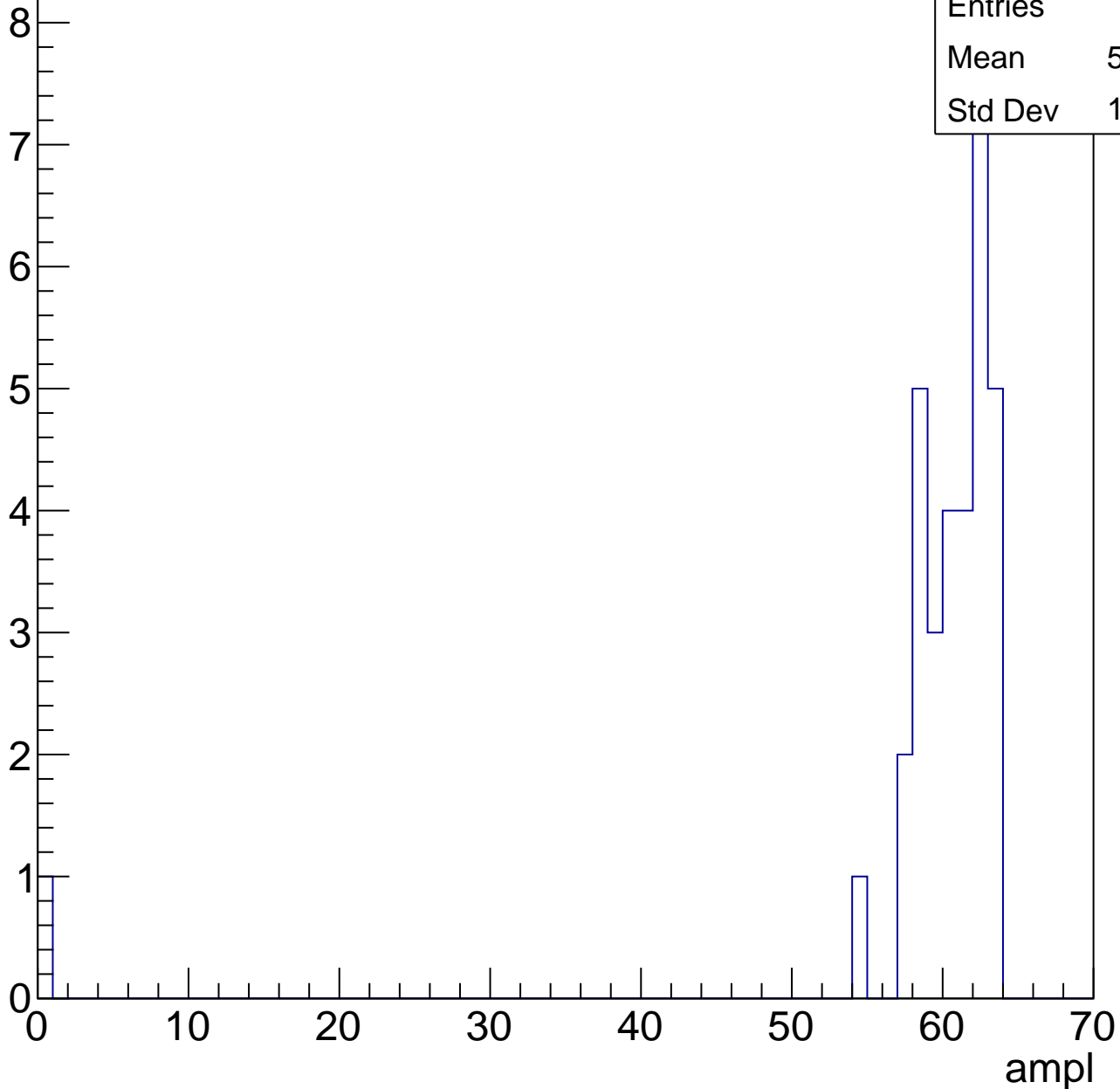


B1L103S, U12-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

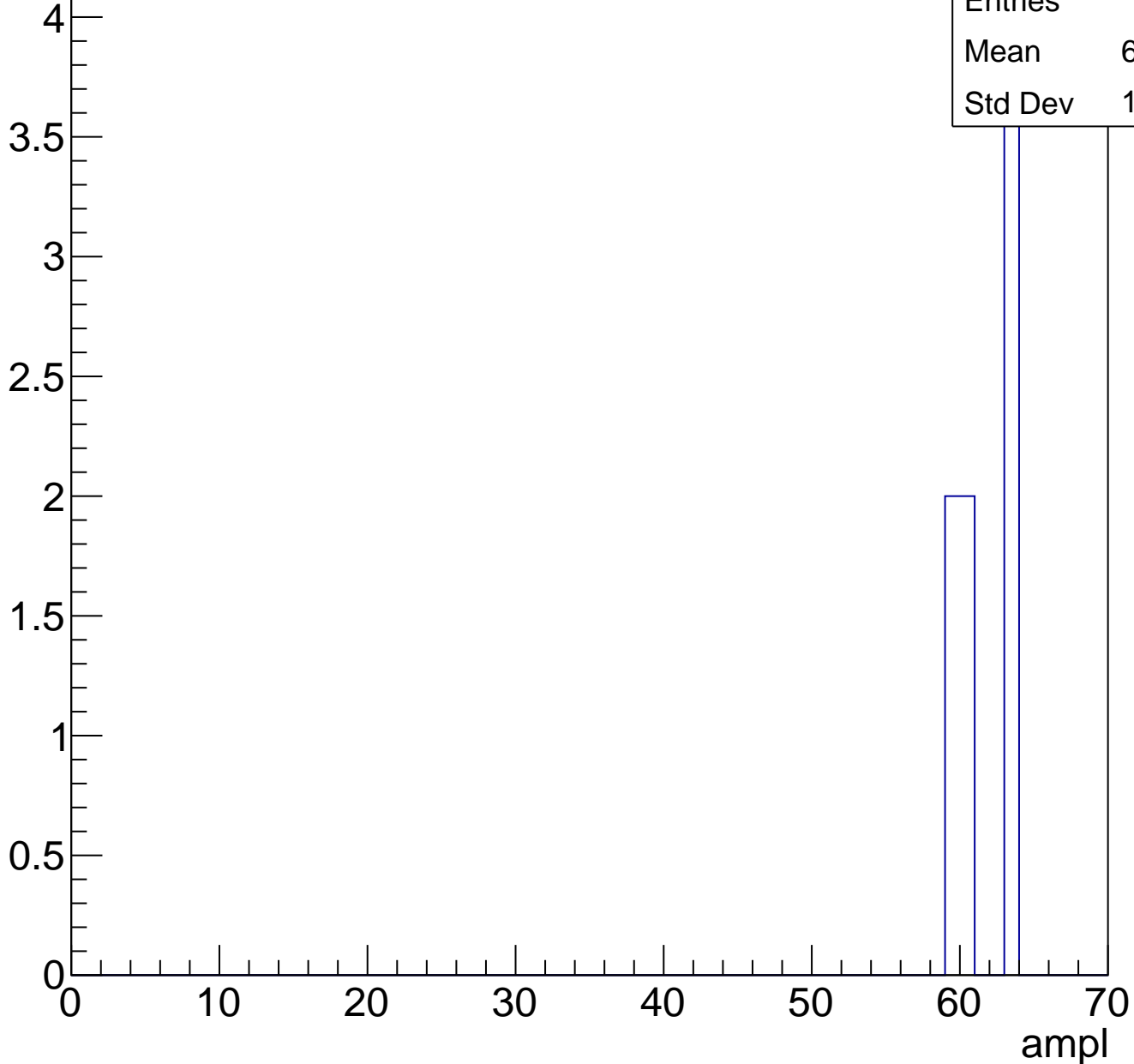
Entries	33
Mean	58.48
Std Dev	10.56



B1L103S, U12-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch101, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	28.86
Std Dev	5.947

Gaus mean : 30.8803

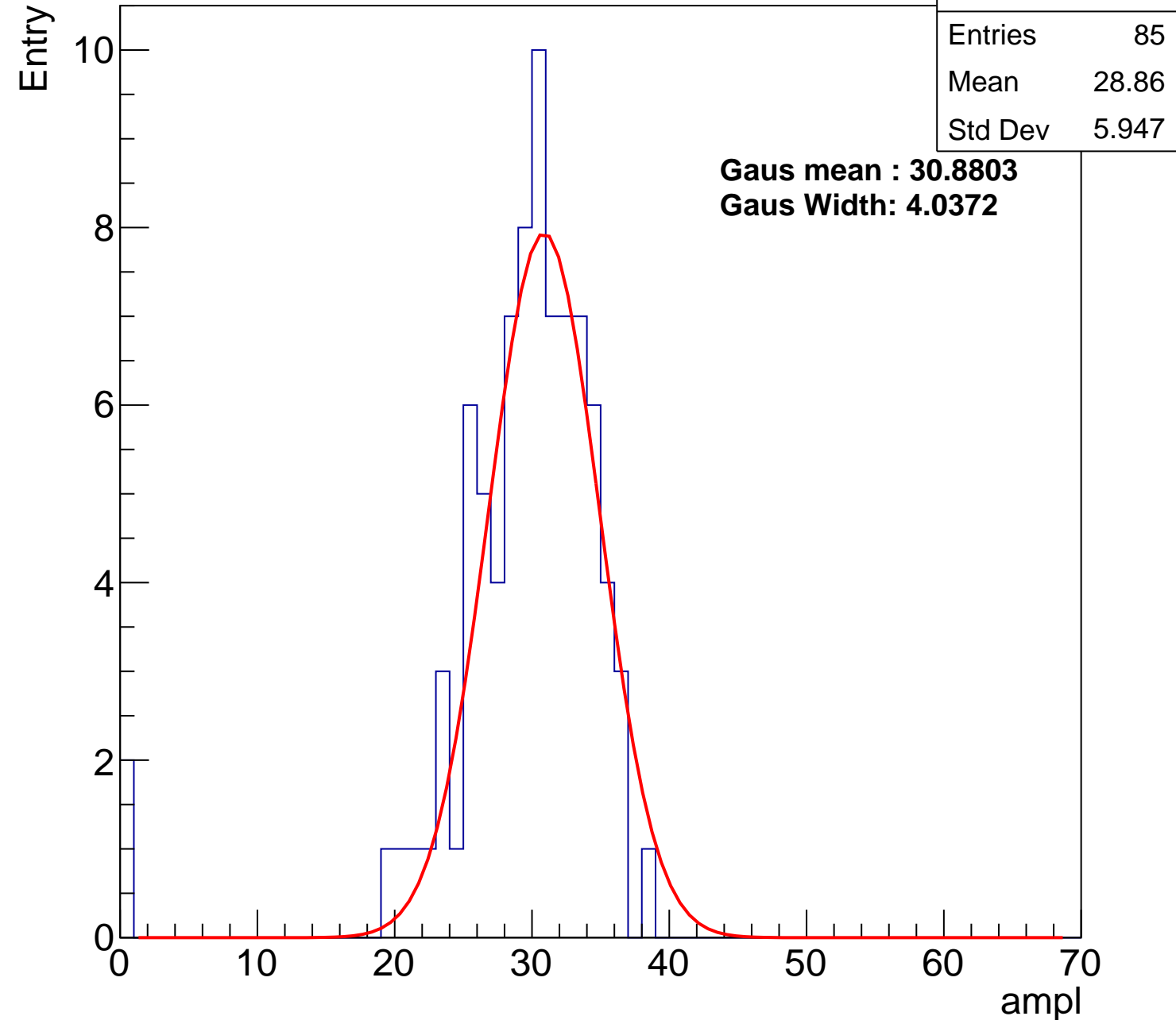
Gaus Width: 4.0372

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch101, adc1

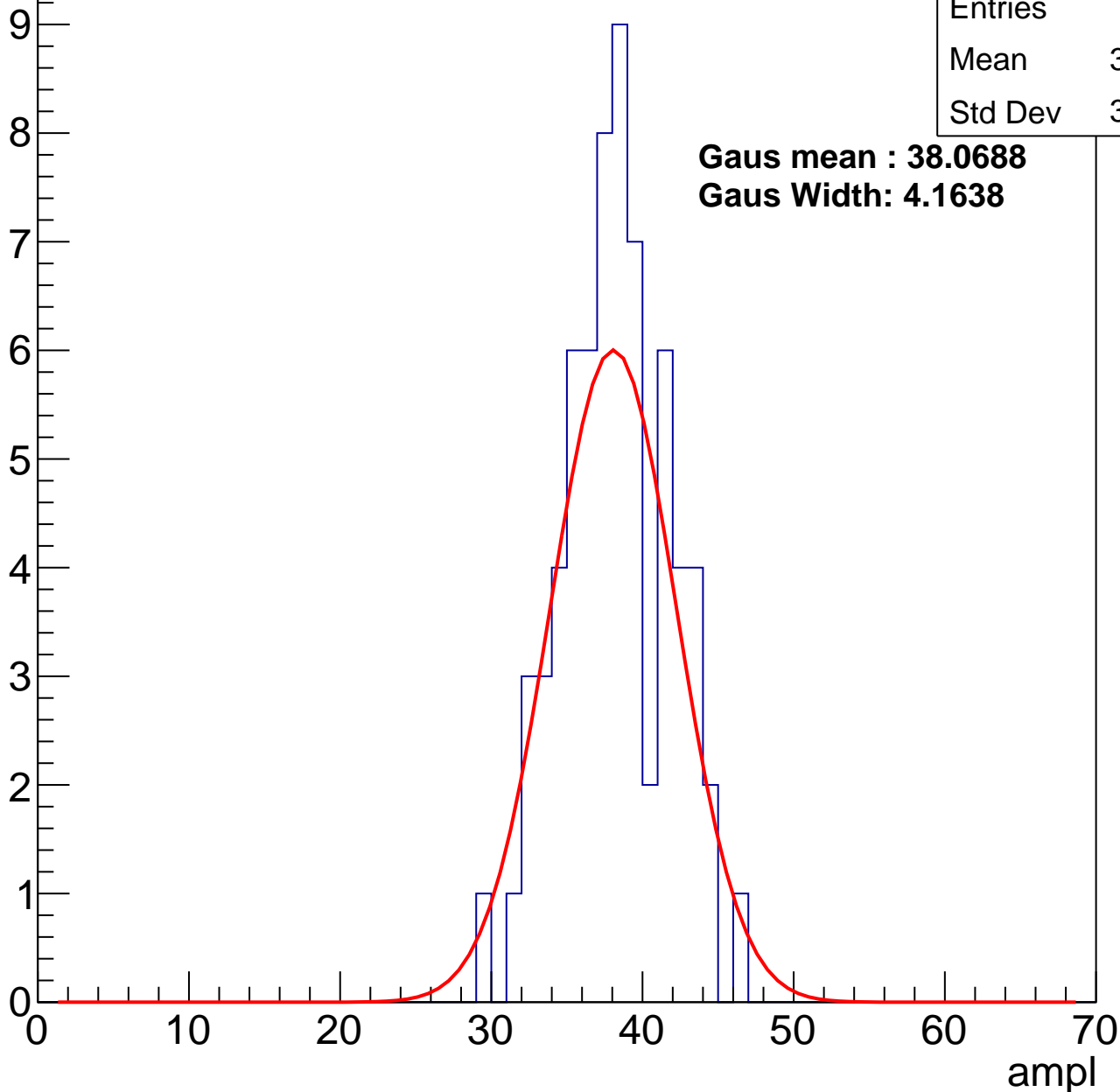
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37.73
Std Dev	3.514

Gaus mean : 38.0688

Gaus Width: 4.1638



B1L103S, U12-ch101, adc2

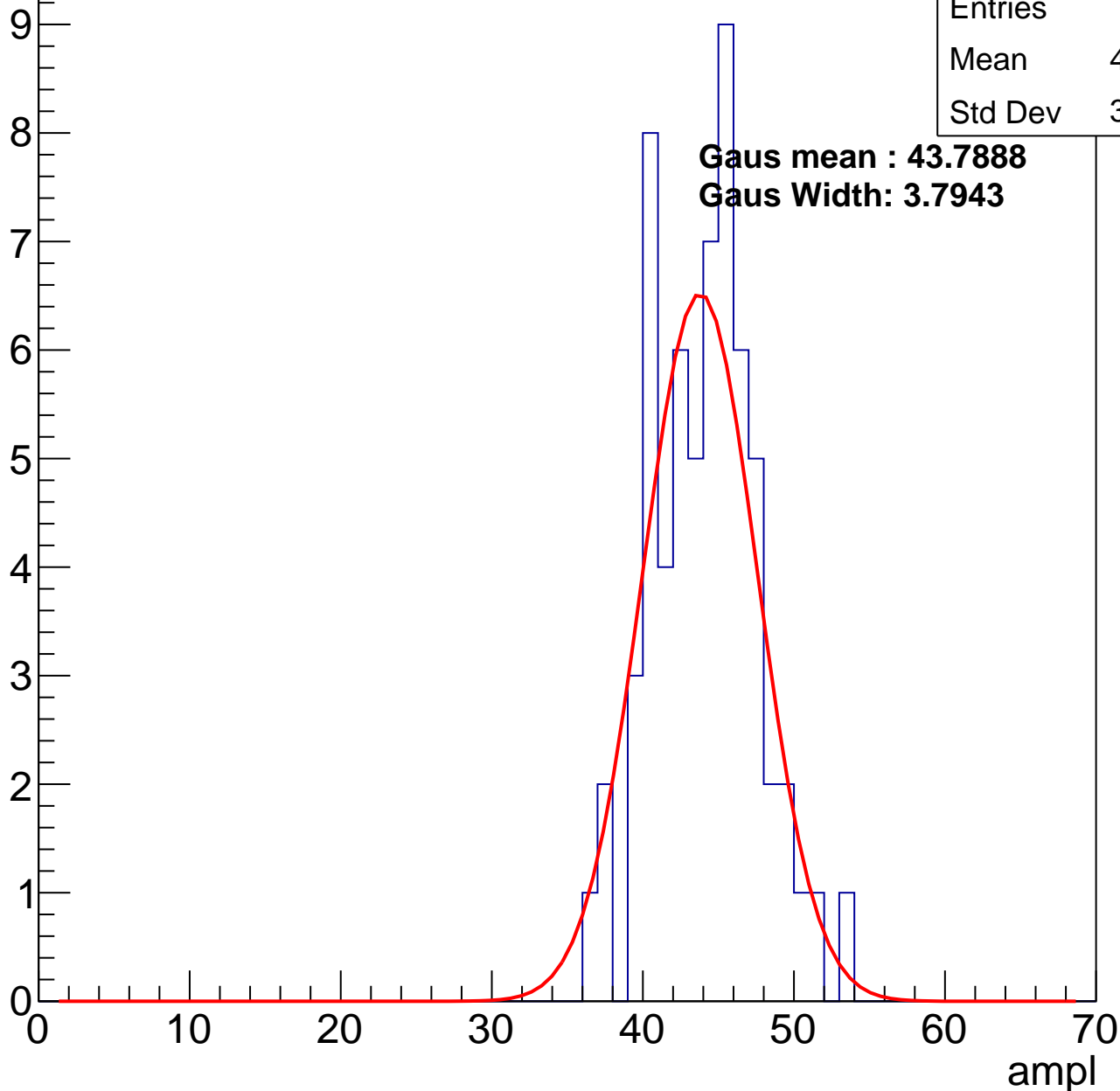
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	43.65
Std Dev	3.446

Gaus mean : 43.7888

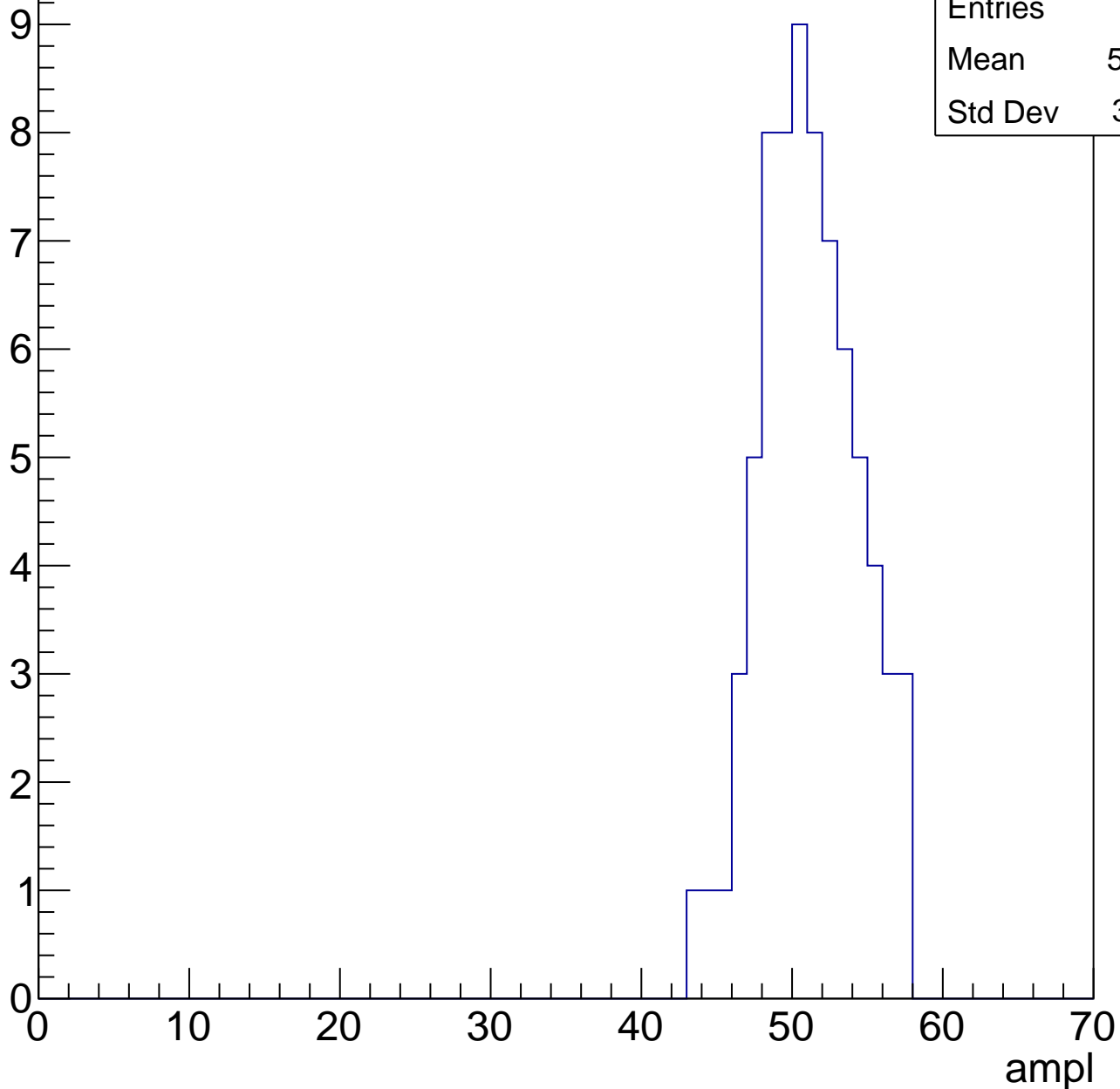
Gaus Width: 3.7943



B1L103S, U12-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



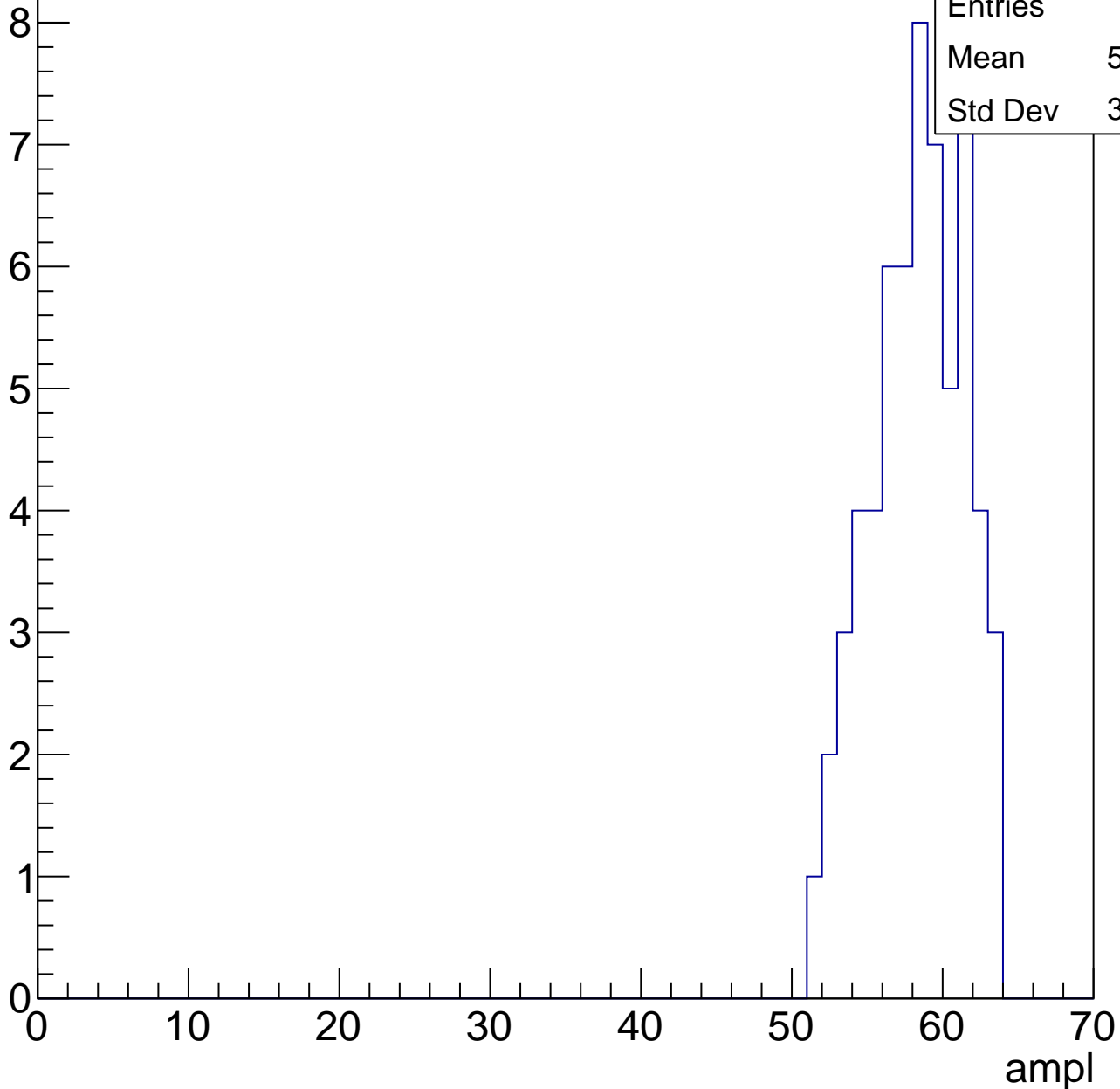
Entries	72
Mean	50.69
Std Dev	3.191

B1L103S, U12-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	57.87
Std Dev	3.038

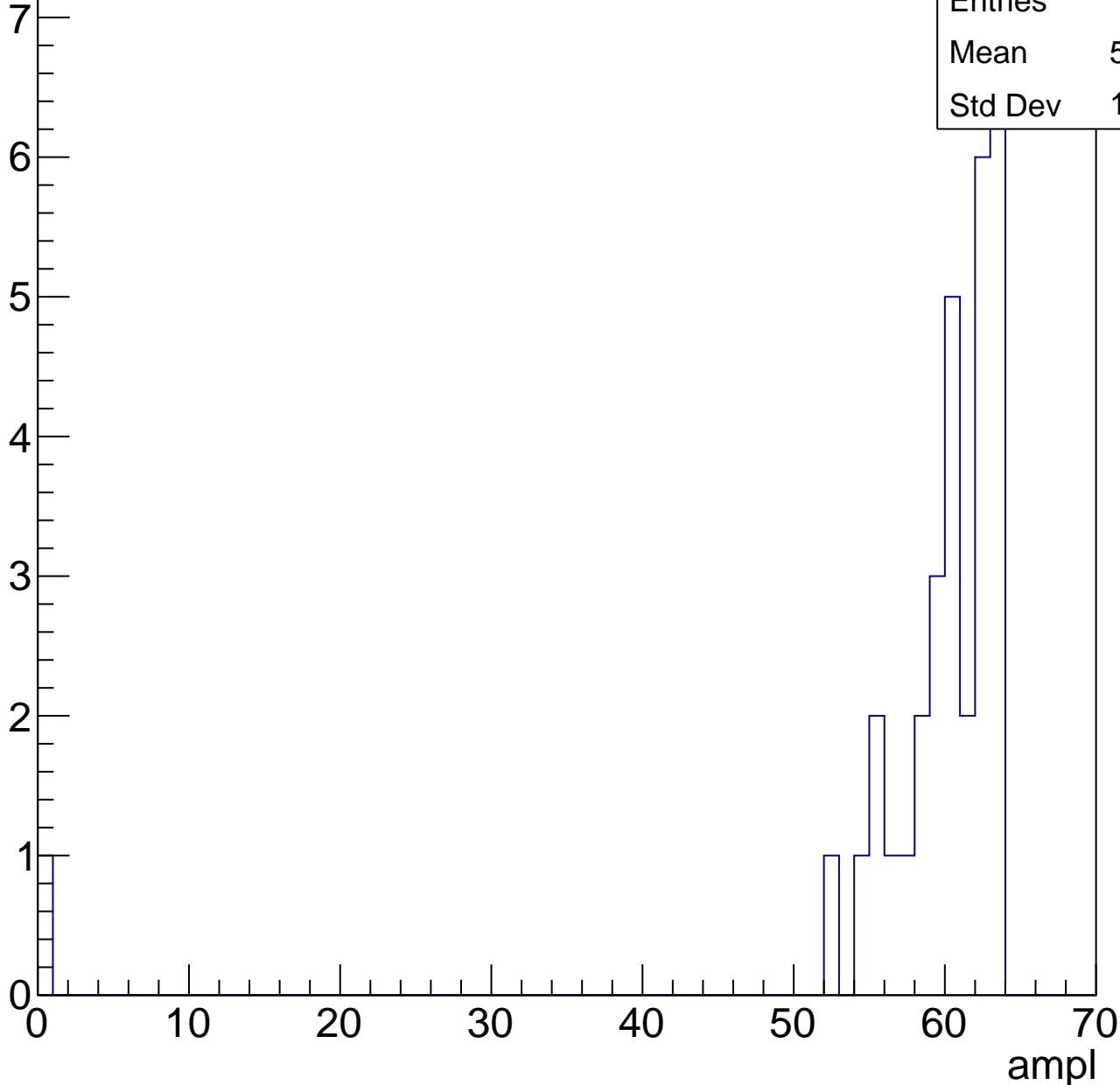


B1L103S, U12-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	32
Mean	58.03
Std Dev	10.83



B1L103S, U12-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch102, adc0

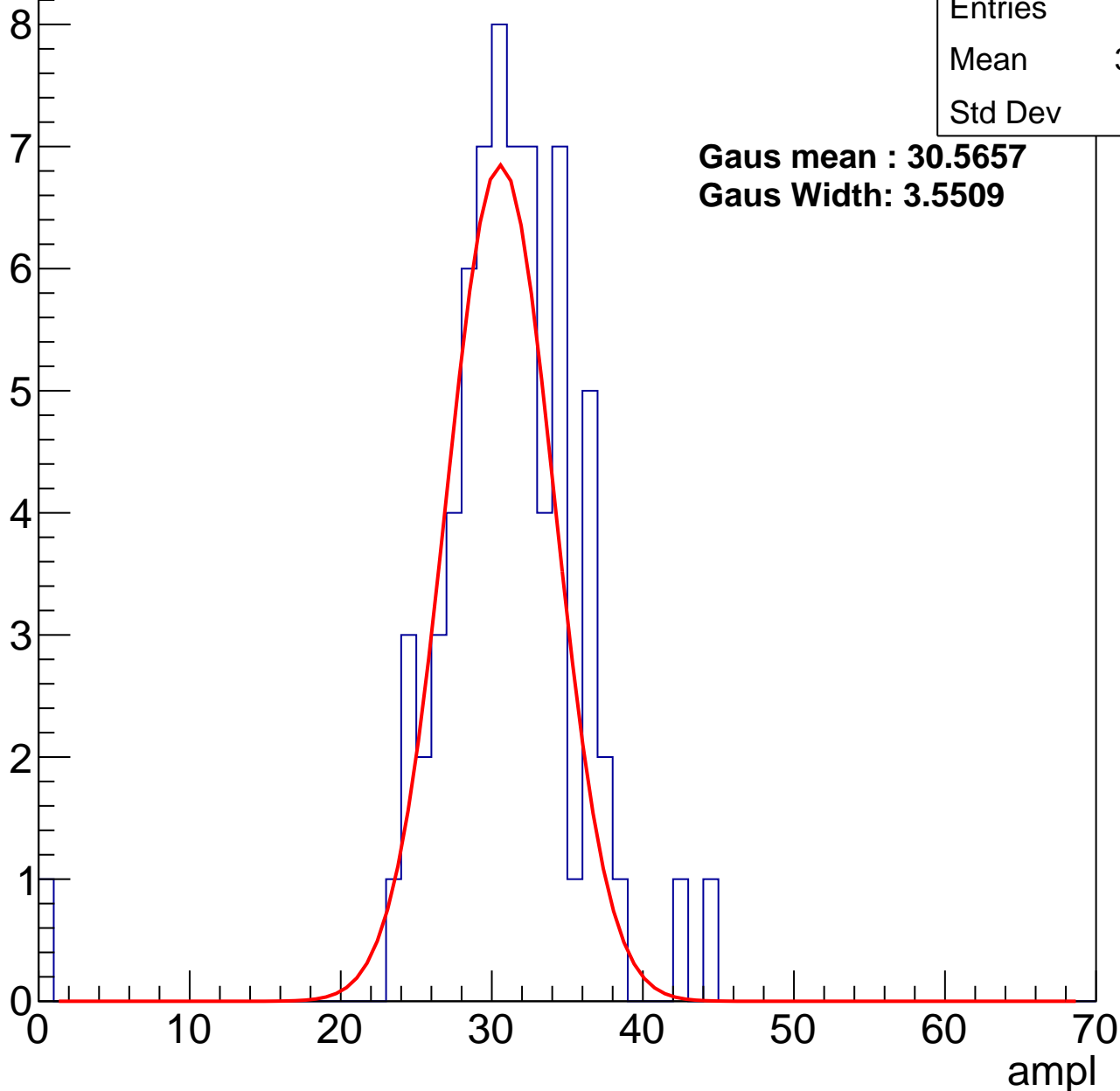
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.51
Std Dev	5.43

Gaus mean : 30.5657

Gaus Width: 3.5509



B1L103S, U12-ch102, adc1

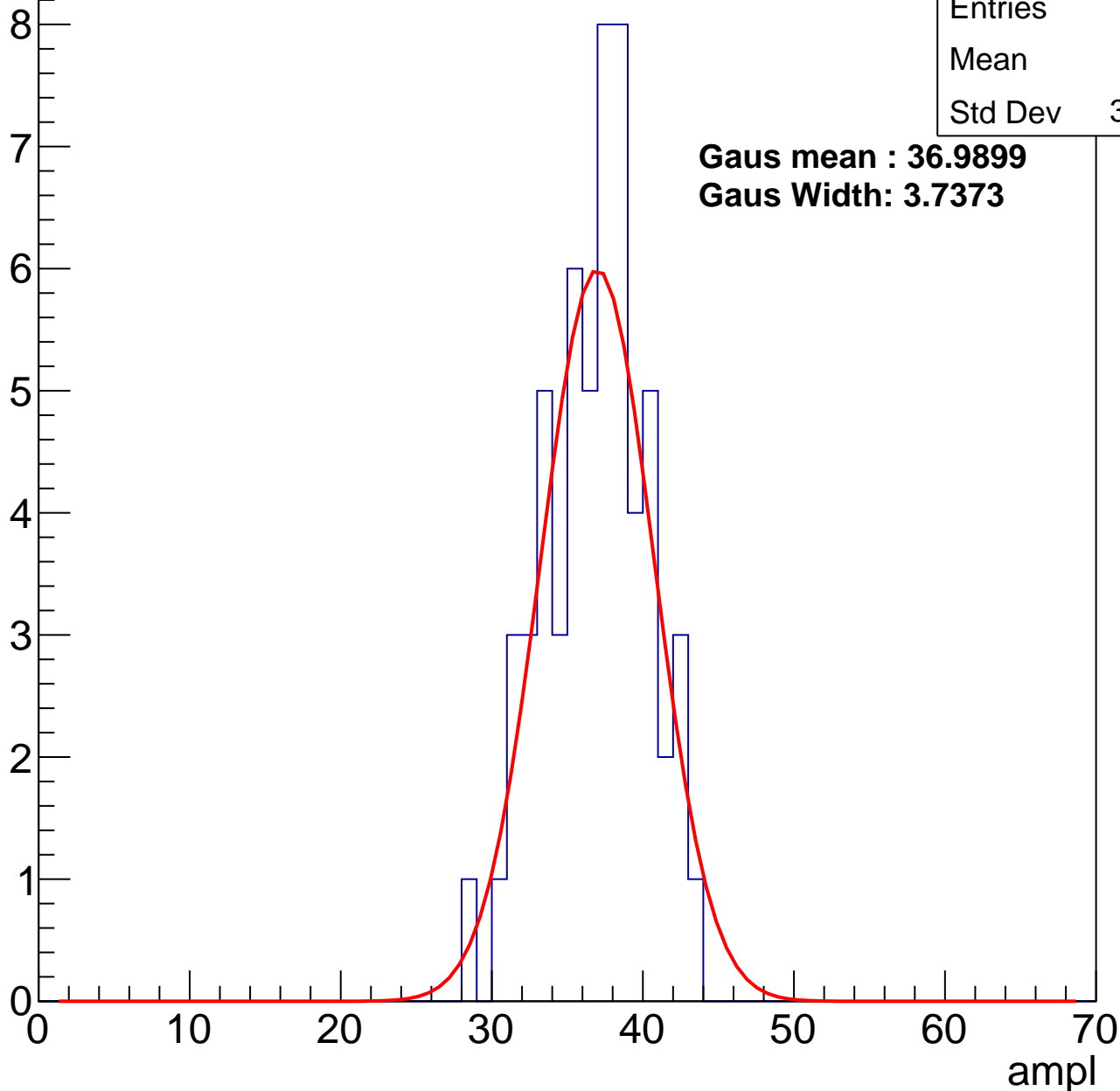
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.4
Std Dev	3.322

Gaus mean : 36.9899

Gaus Width: 3.7373



B1L103S, U12-ch102, adc2

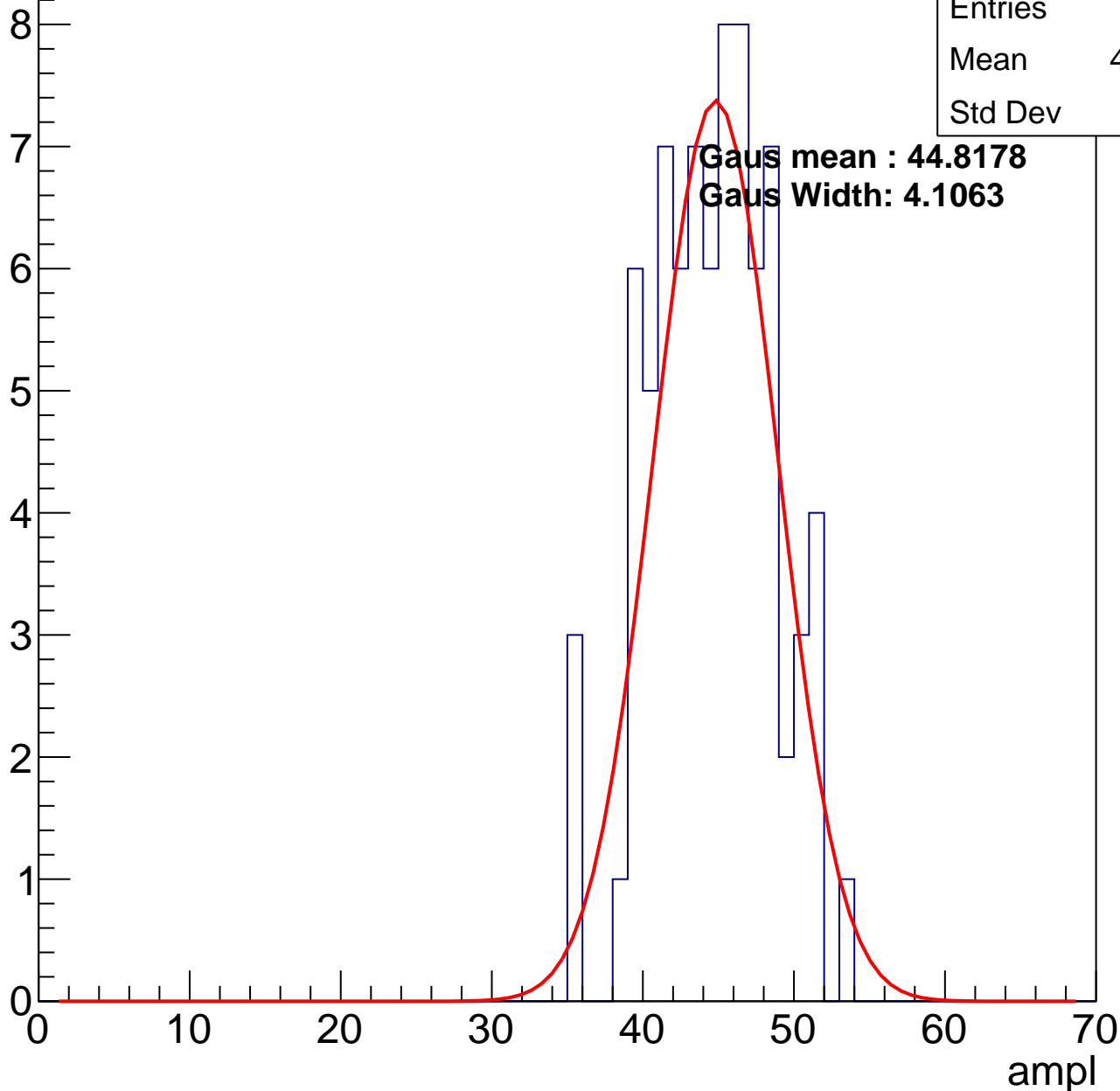
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	44.15
Std Dev	3.94

Gaus mean : 44.8178

Gaus Width: 4.1063

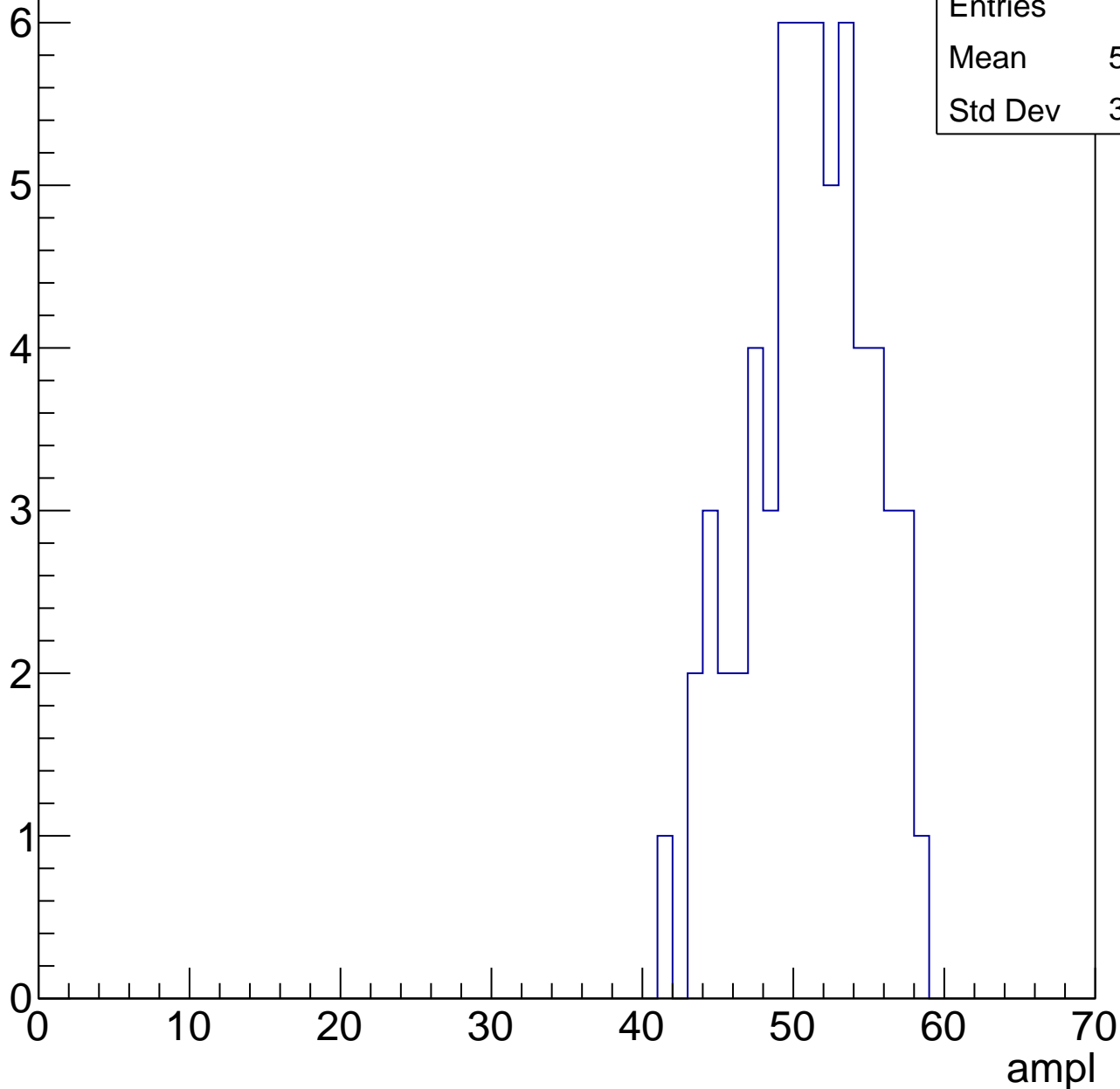


B1L103S, U12-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	50.56
Std Dev	3.982

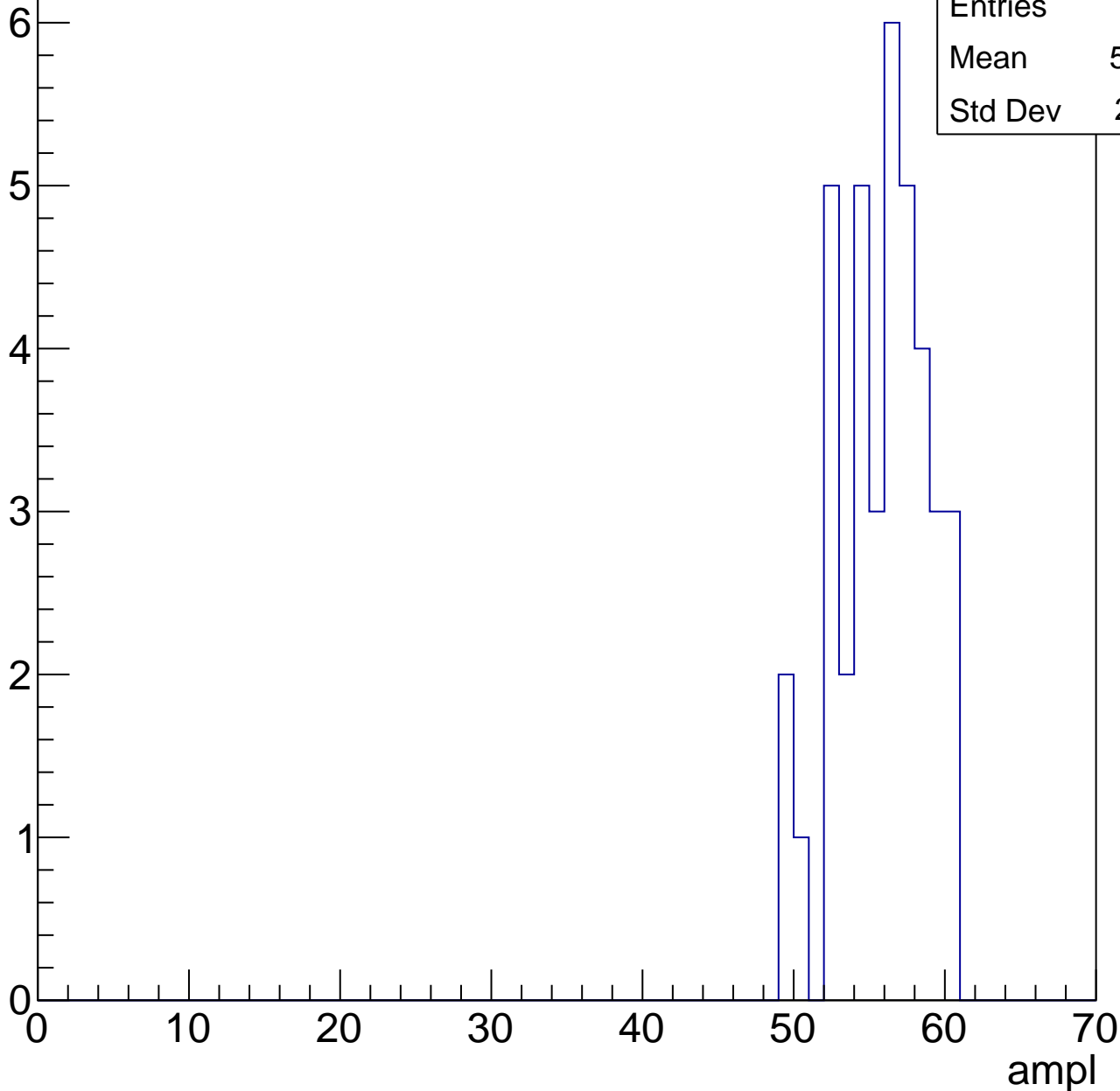


B1L103S, U12-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	55.36
Std Dev	2.931

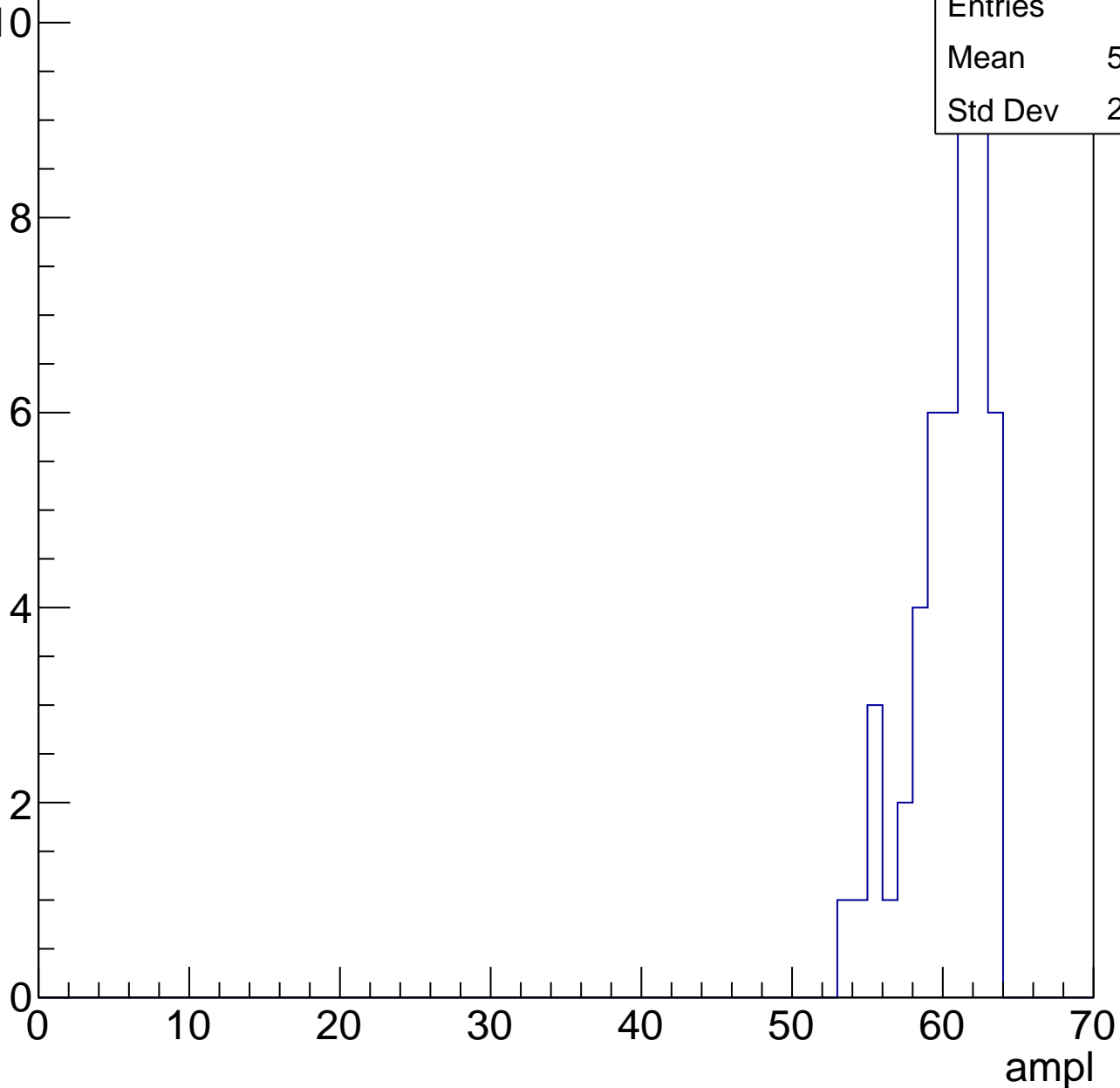


B1L103S, U12-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

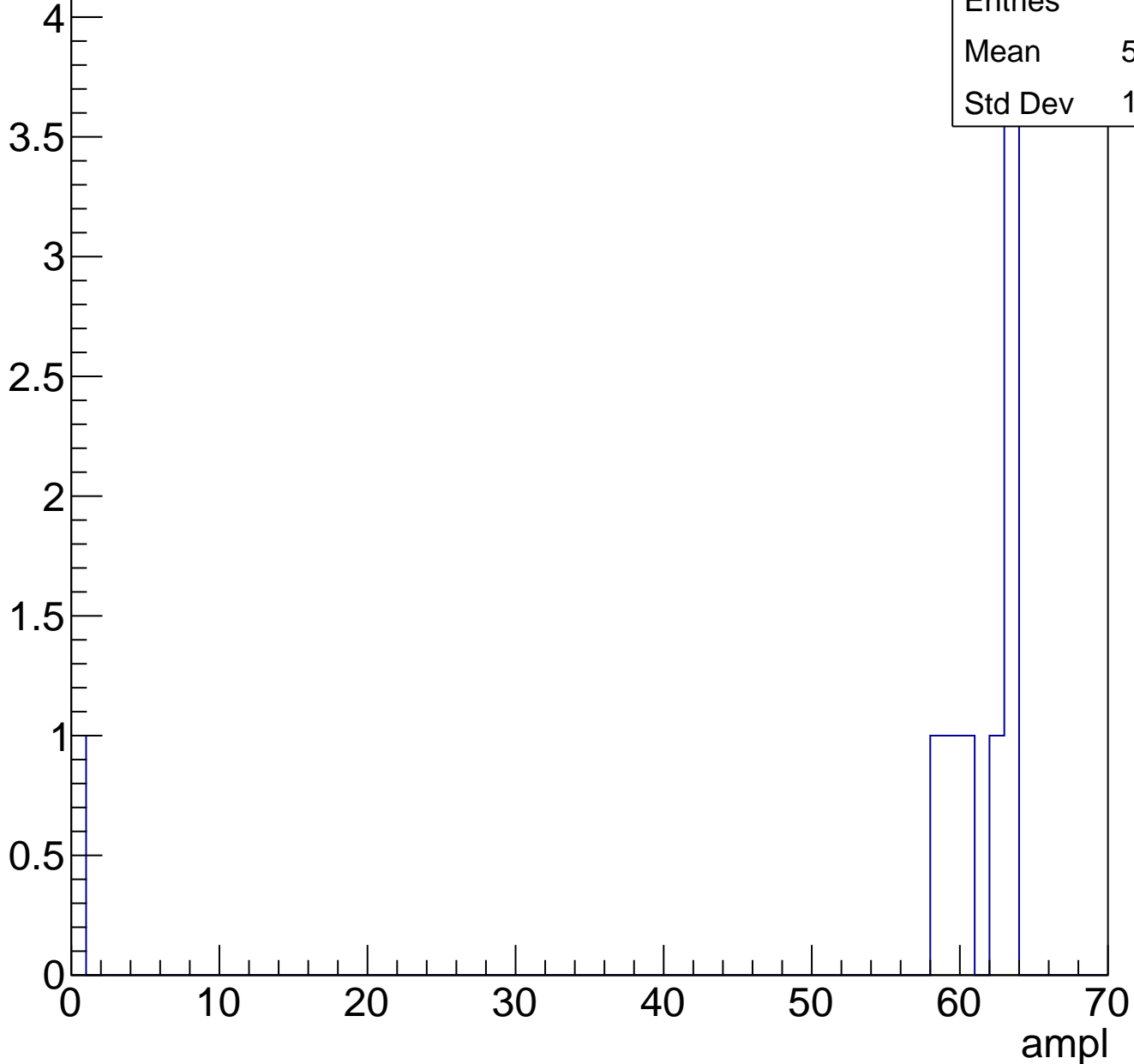
Entries	49
Mean	59.88
Std Dev	2.537



B1L103S, U12-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

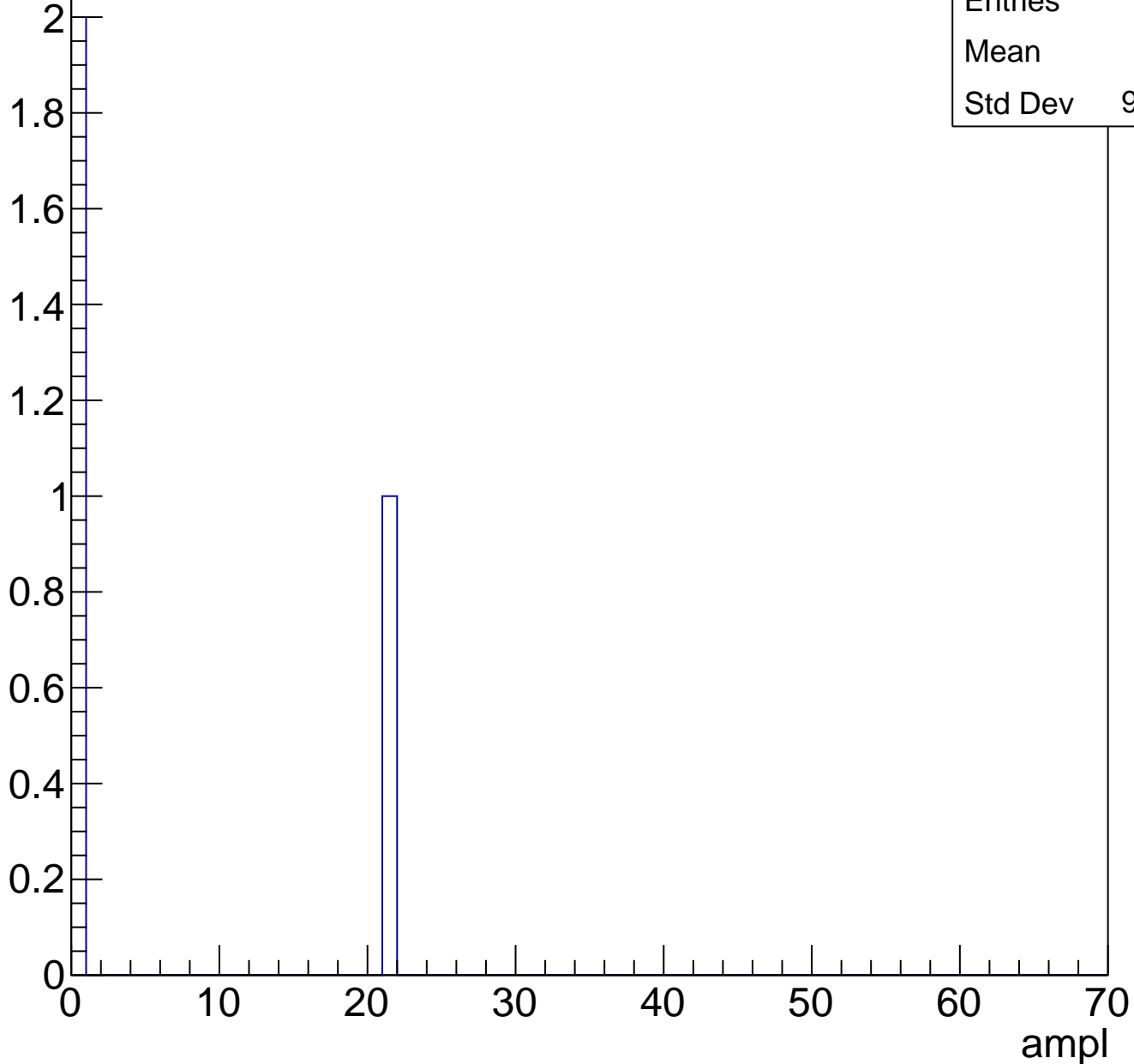
Entry



B1L103S, U12-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch103, adc0

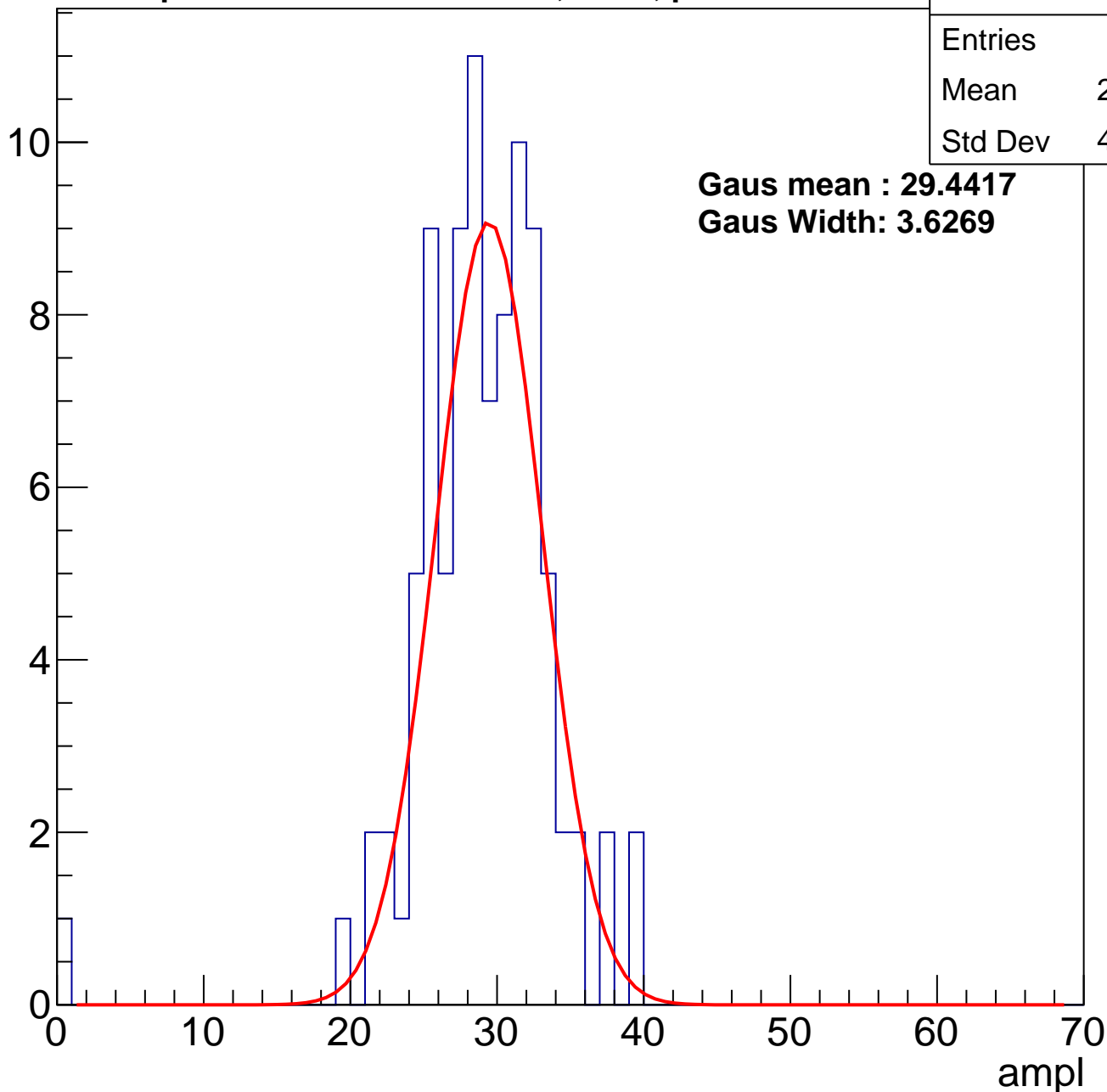
calib_packv5_042523_0143.root, FC#7, port C2

Entries	93
Mean	28.49
Std Dev	4.863

Gaus mean : 29.4417

Gaus Width: 3.6269

Entry



B1L103S, U12-ch103, adc1

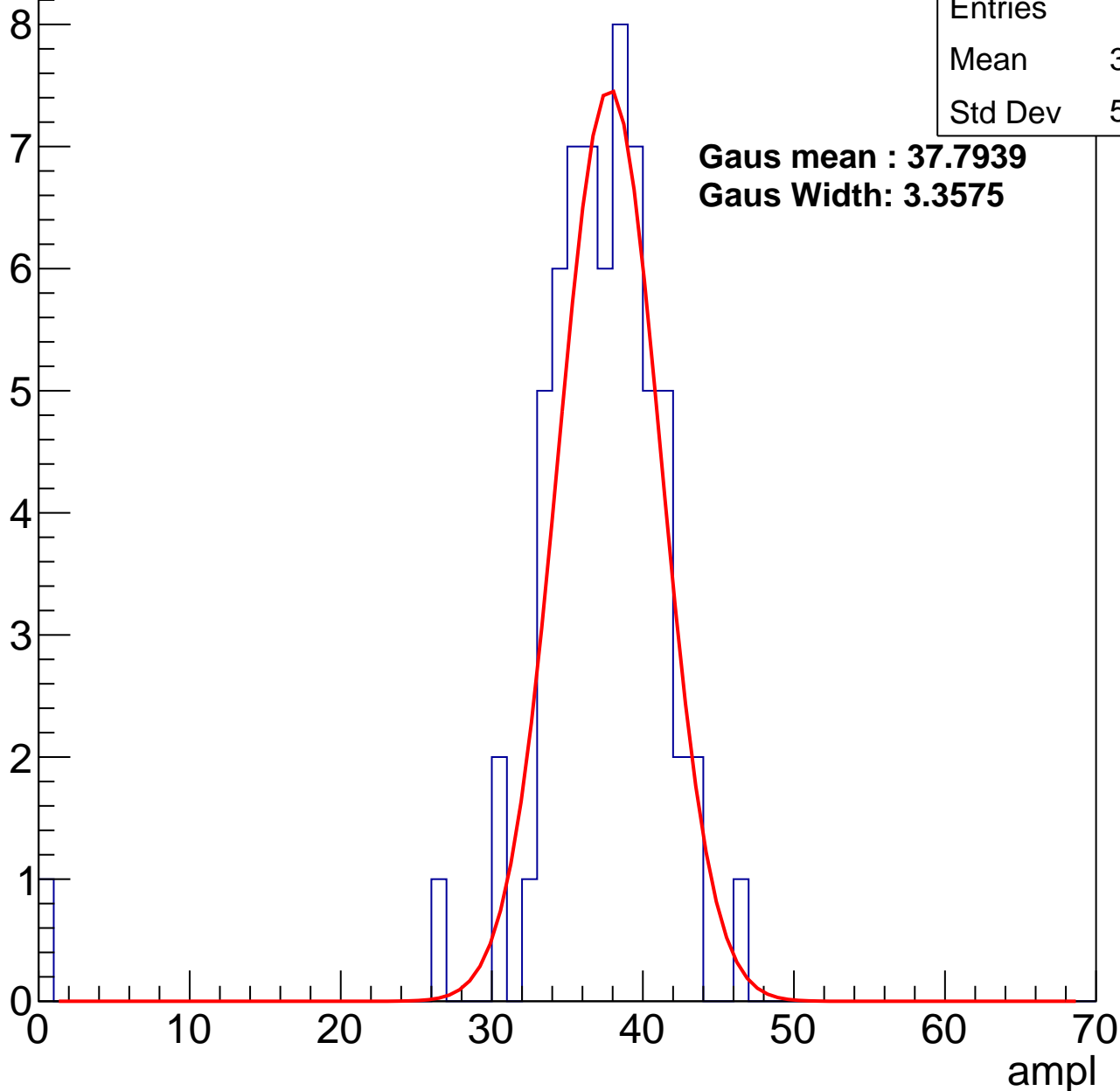
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.42
Std Dev	5.673

Gaus mean : 37.7939

Gaus Width: 3.3575



B1L103S, U12-ch103, adc2

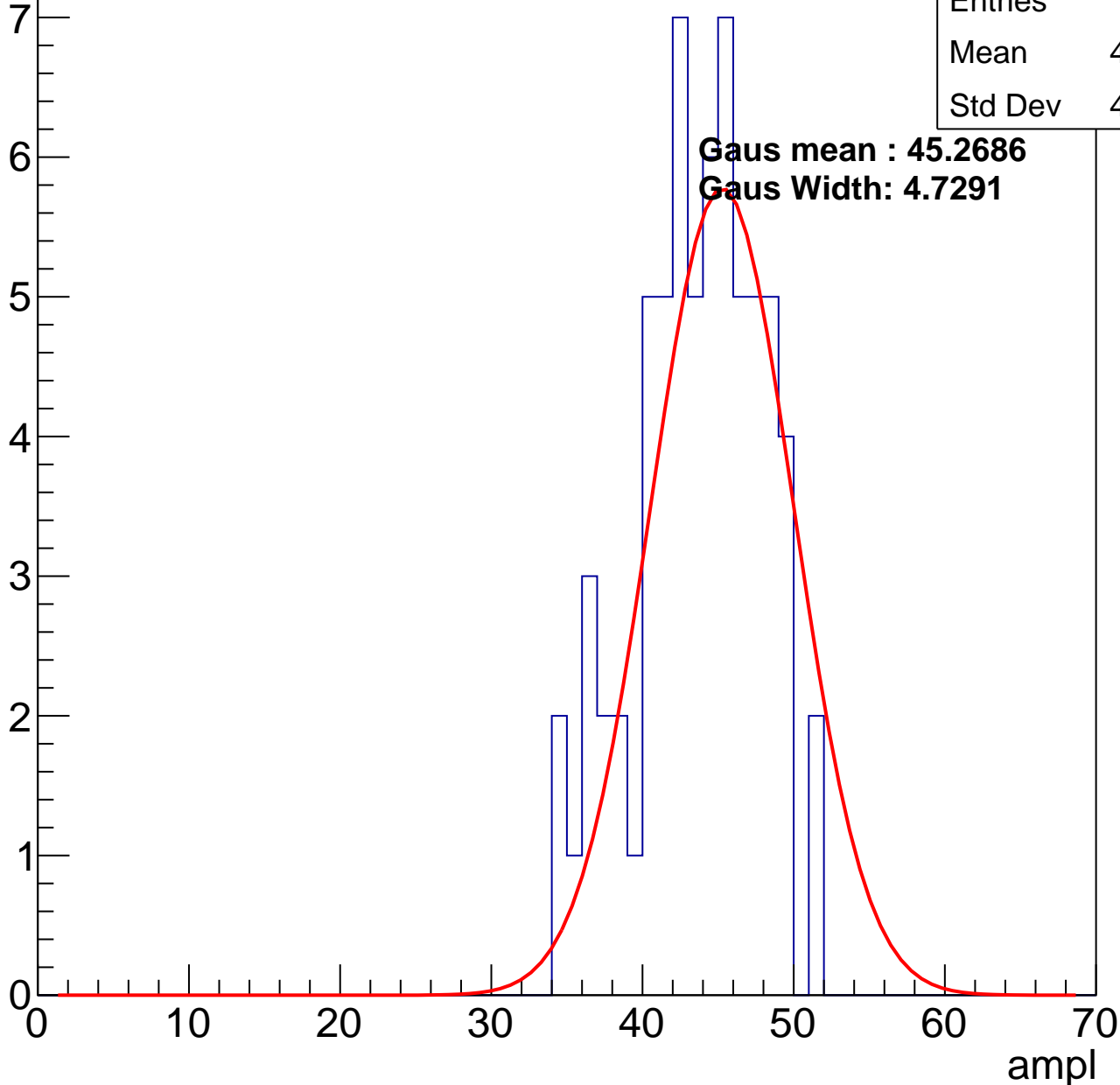
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.22
Std Dev	4.117

Gaus mean : 45.2686

Gaus Width: 4.7291

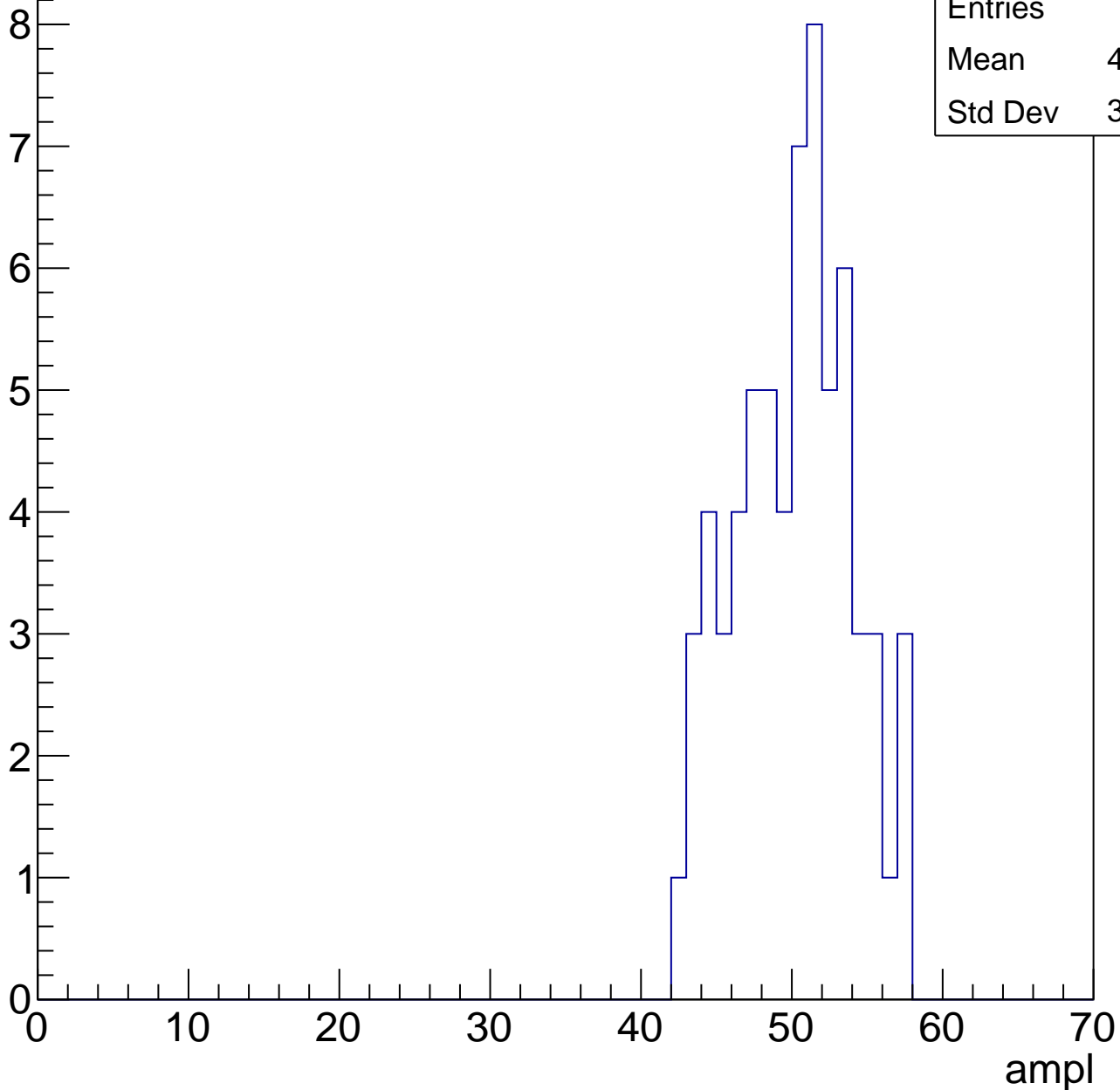


B1L103S, U12-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.65
Std Dev	3.809

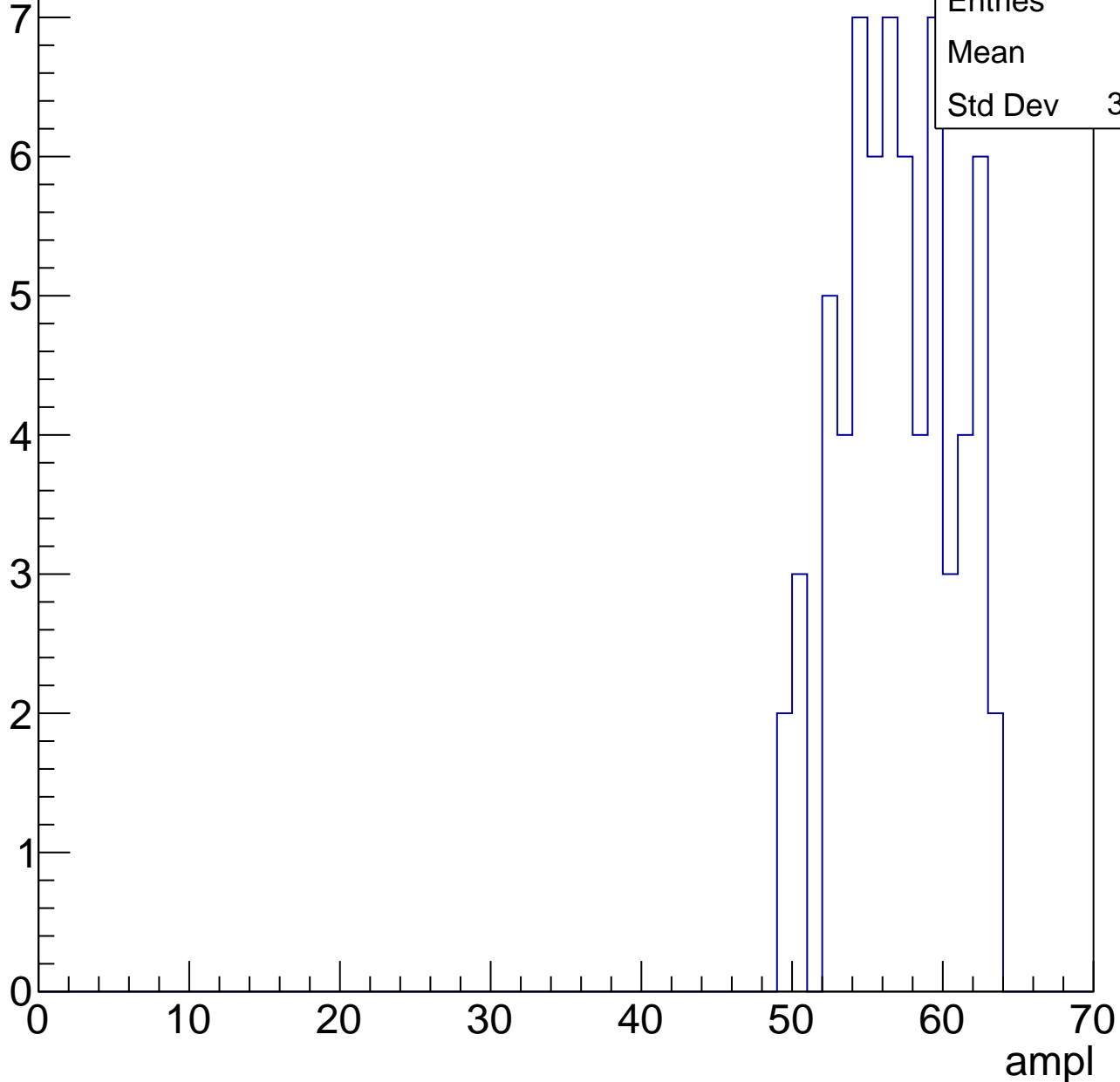


B1L103S, U12-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	56.5
Std Dev	3.665

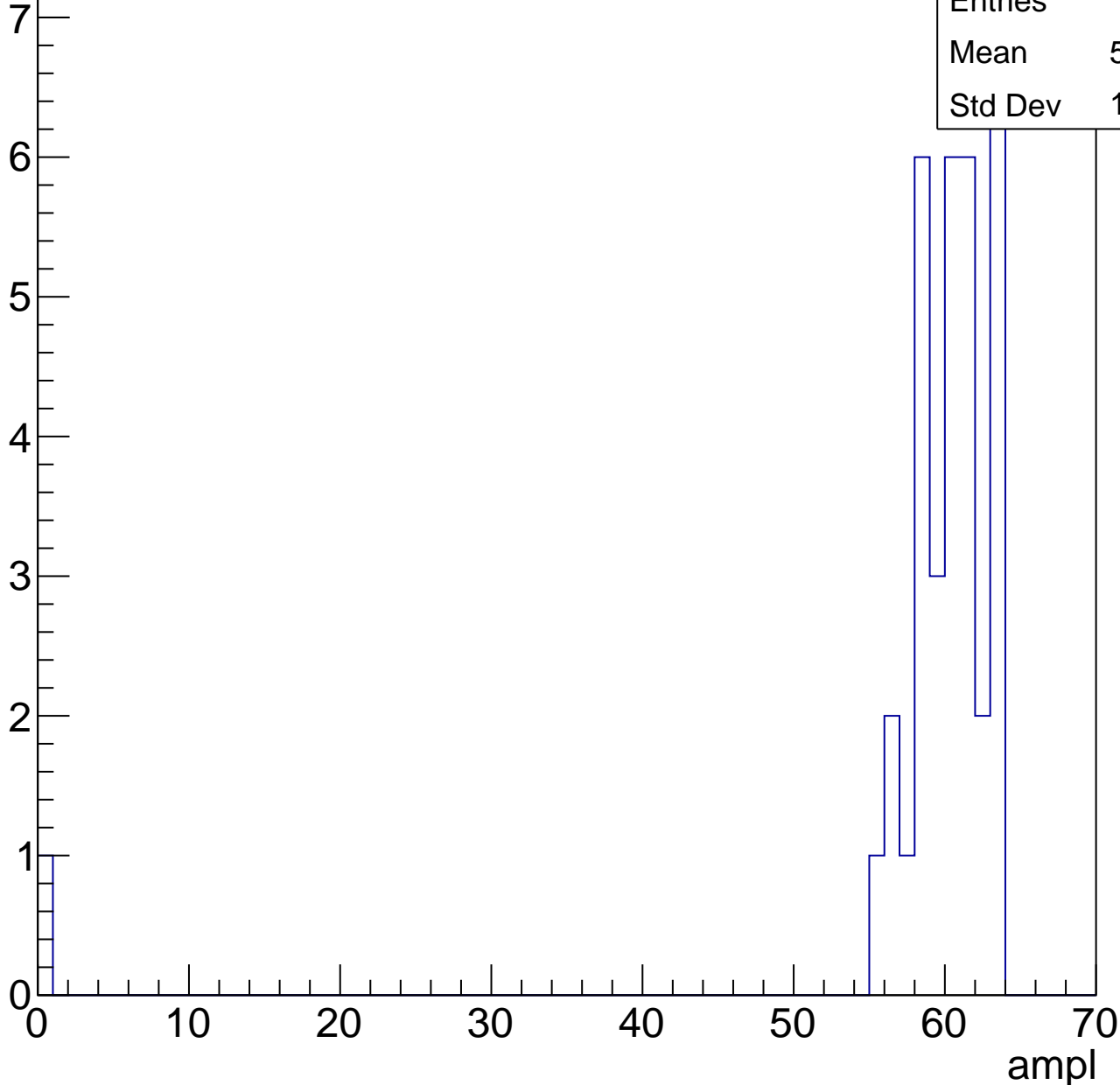


B1L103S, U12-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.29
Std Dev	10.24



B1L103S, U12-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.09

ampl

0 10 20 30 40 50 60 70

B1L103S, U12-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch104, adc0

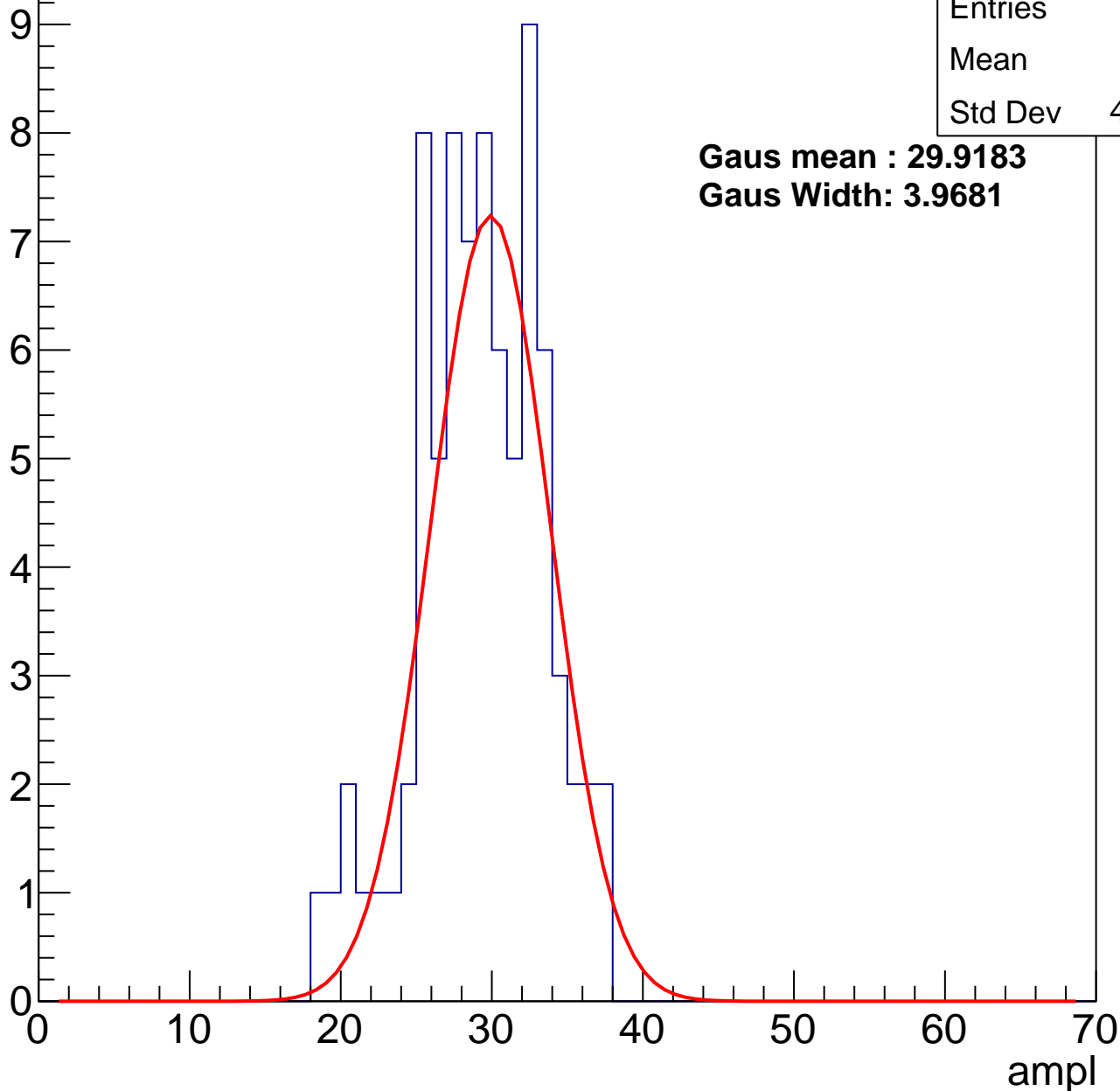
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	28.8
Std Dev	4.133

Gaus mean : 29.9183

Gaus Width: 3.9681



B1L103S, U12-ch104, adc1

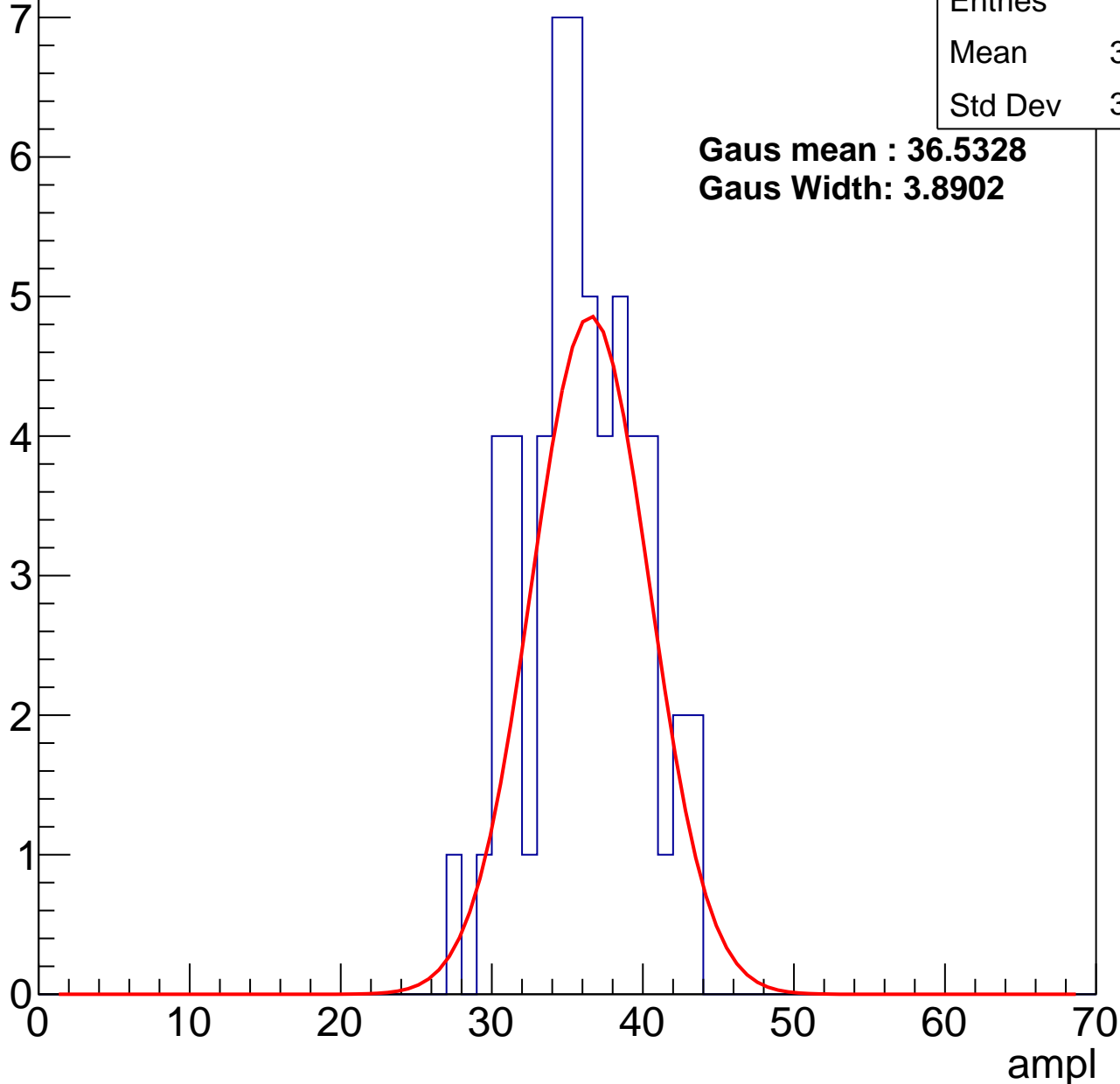
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	35.57
Std Dev	3.707

Gaus mean : 36.5328

Gaus Width: 3.8902



B1L103S, U12-ch104, adc2

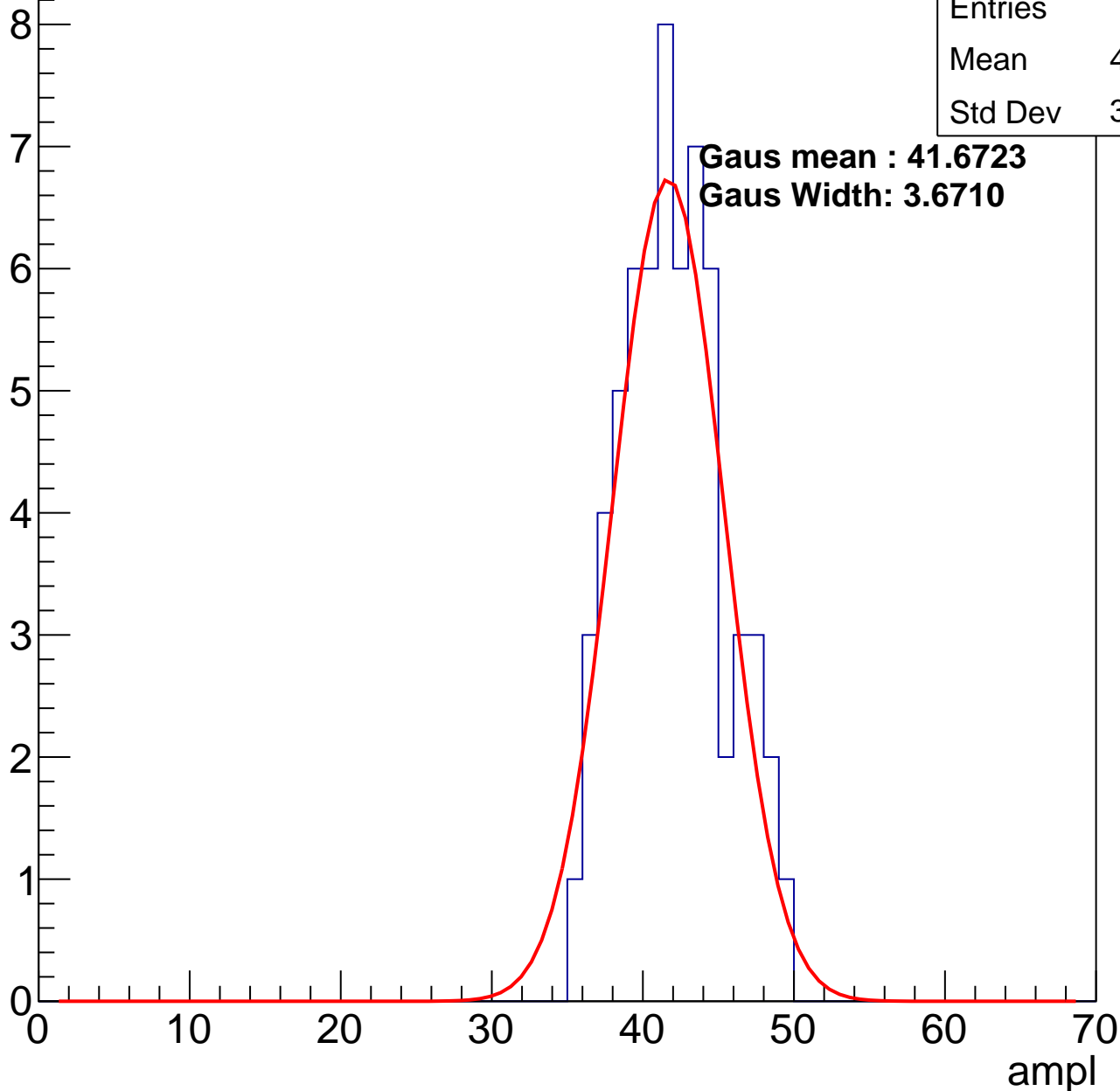
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.49
Std Dev	3.333

Gaus mean : 41.6723

Gaus Width: 3.6710

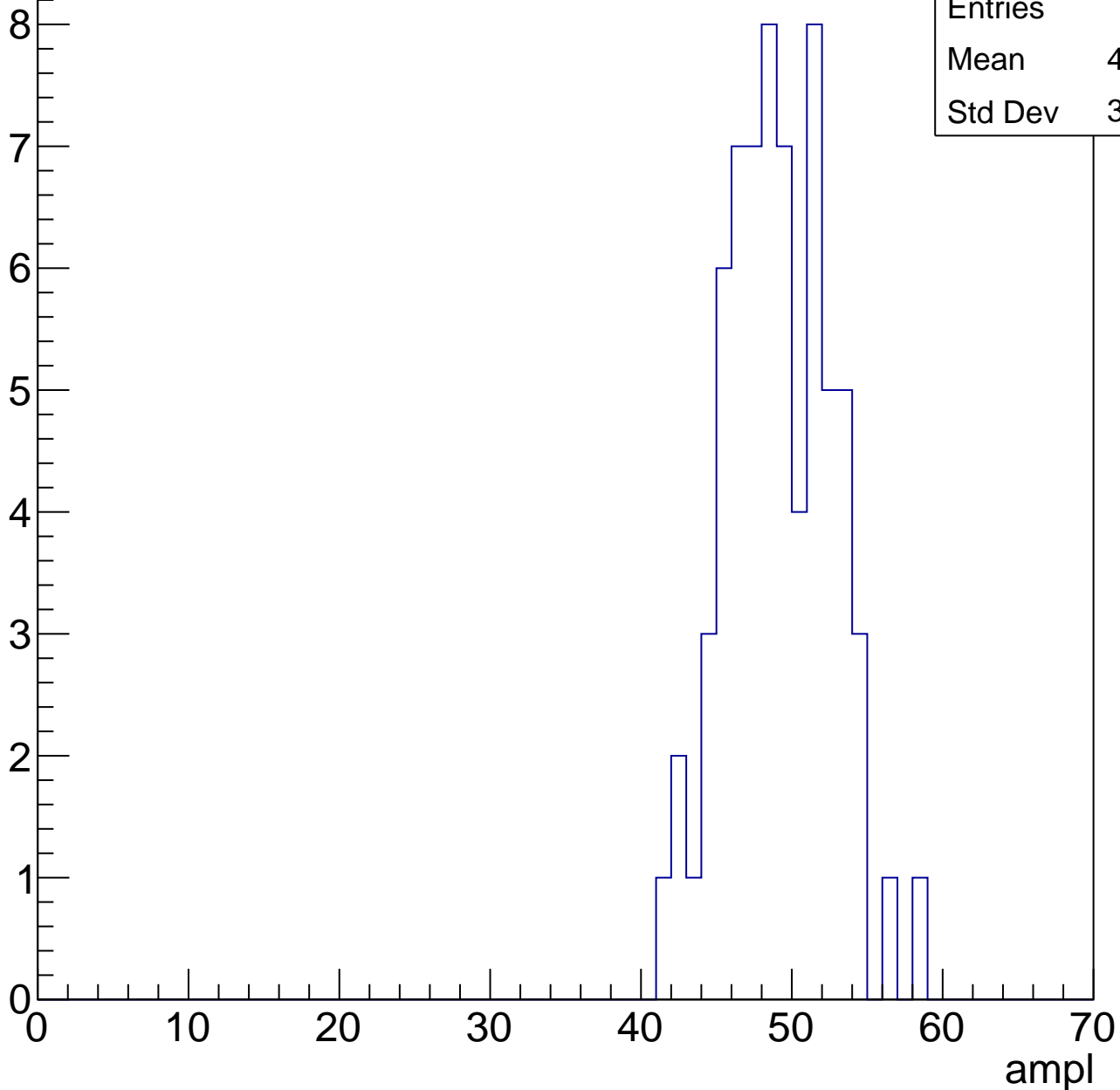


B1L103S, U12-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.65
Std Dev	3.459

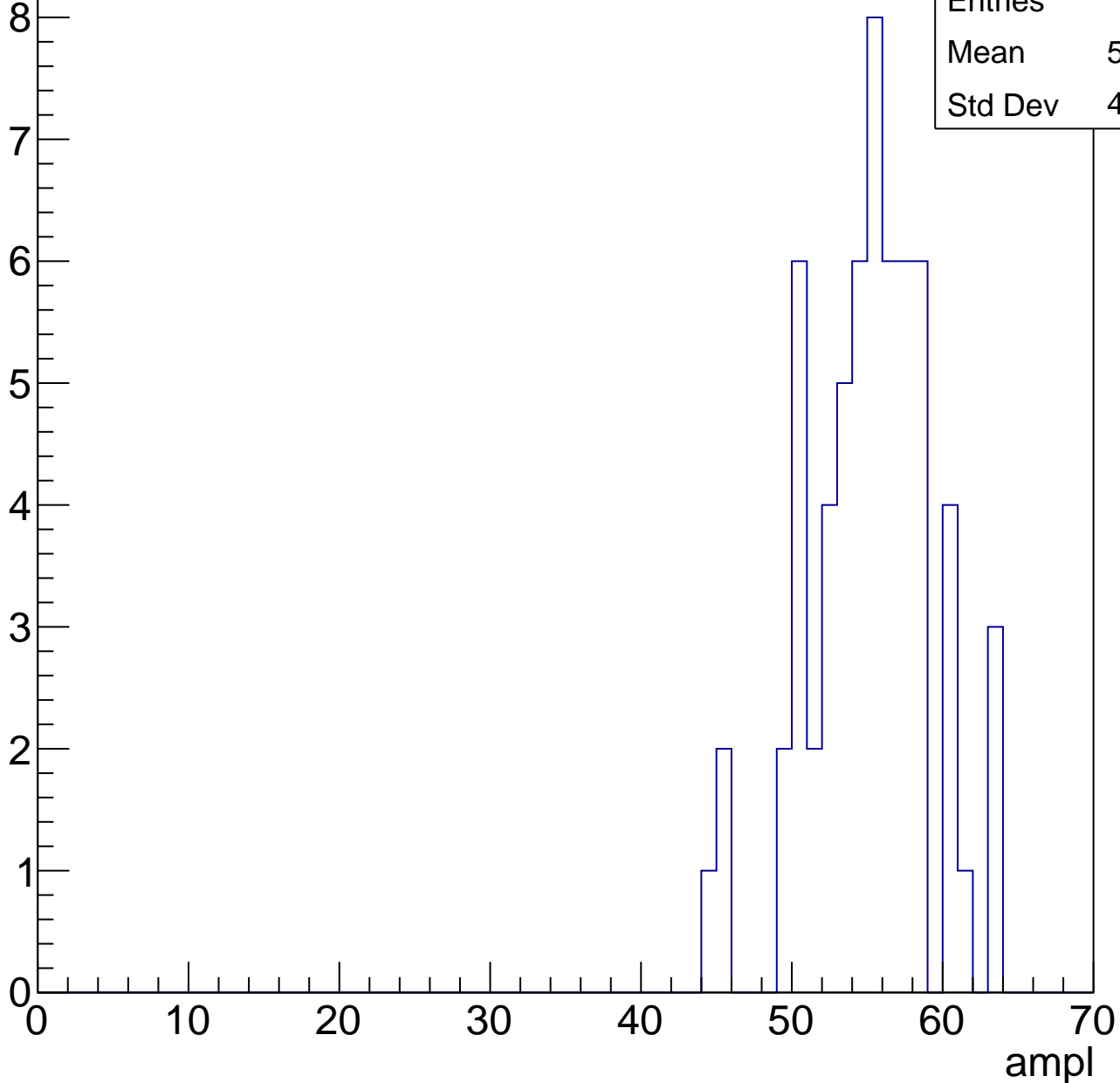


B1L103S, U12-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	54.63
Std Dev	4.097

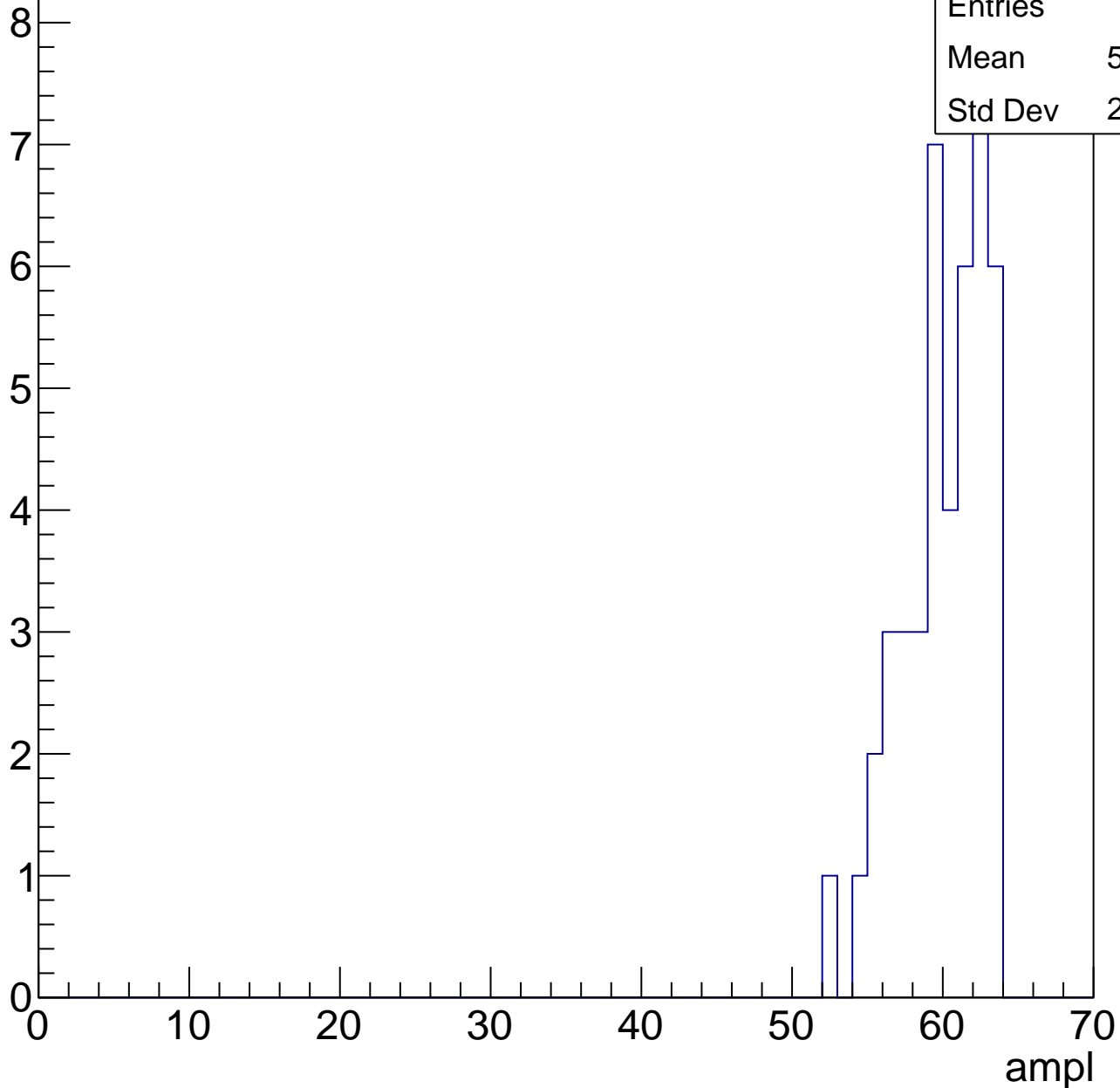


B1L103S, U12-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

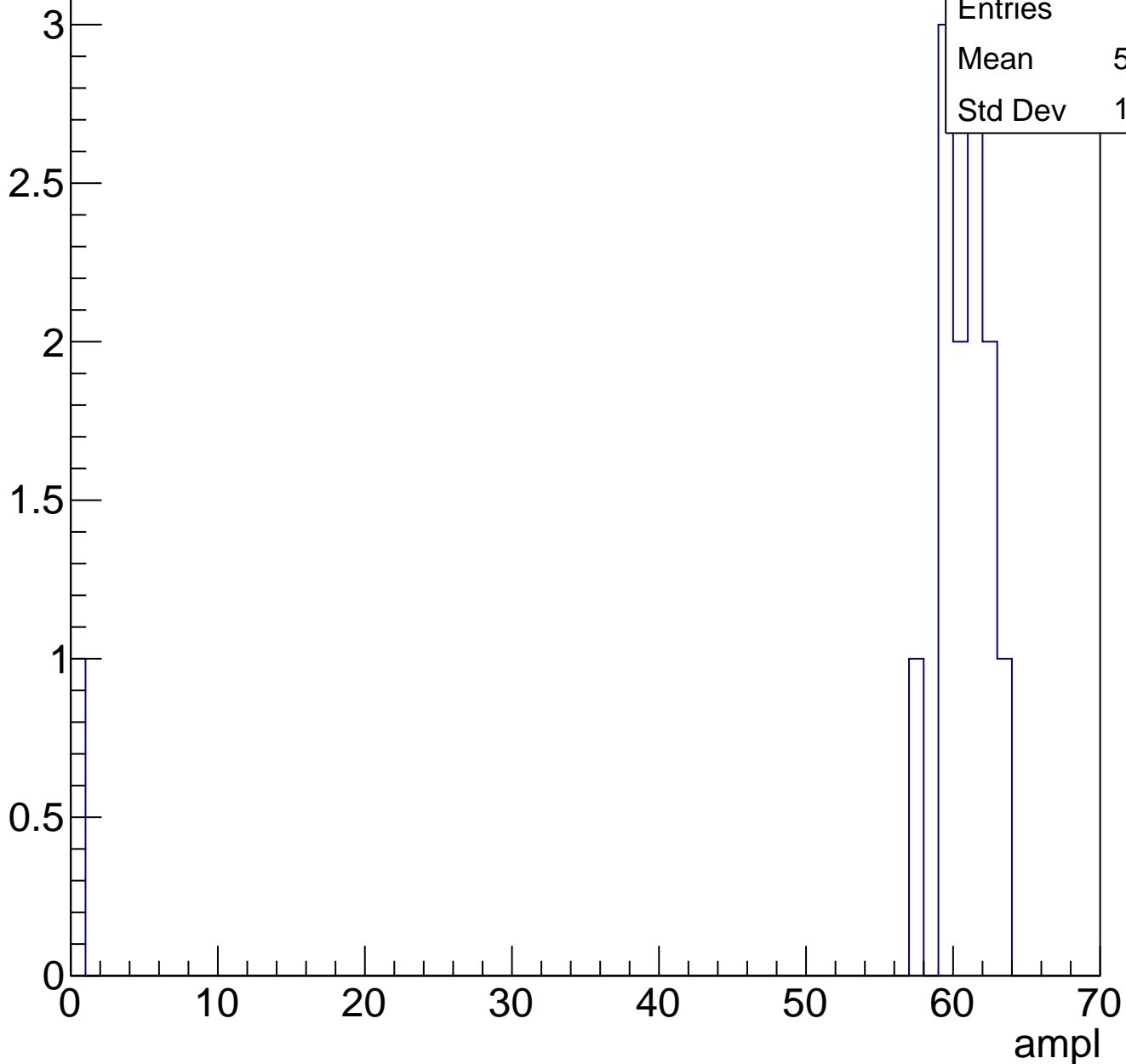
Entries	44
Mean	59.59
Std Dev	2.733



B1L103S, U12-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch105, adc0

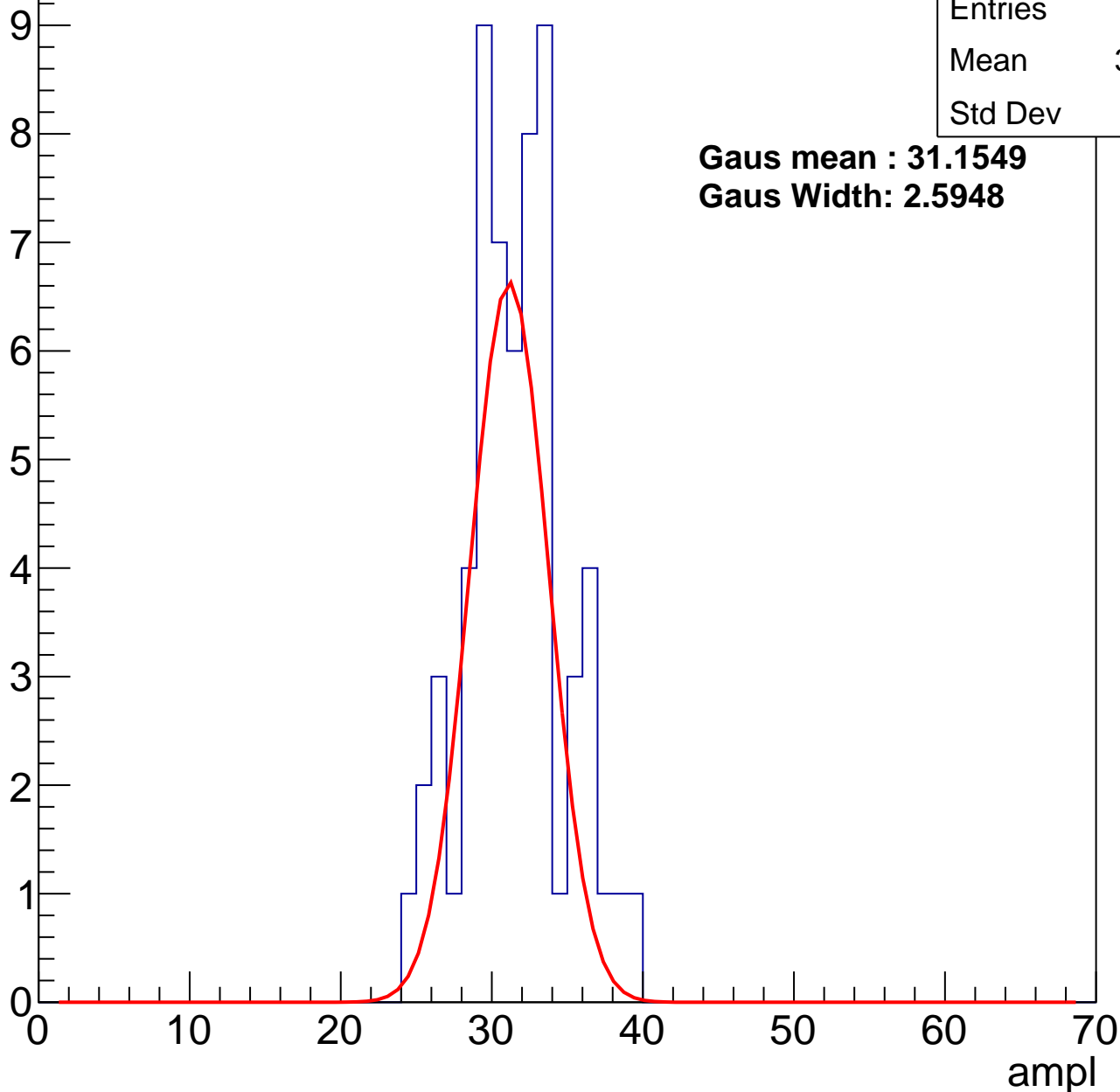
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	31.11
Std Dev	3.24

Gaus mean : 31.1549

Gaus Width: 2.5948



B1L103S, U12-ch105, adc1

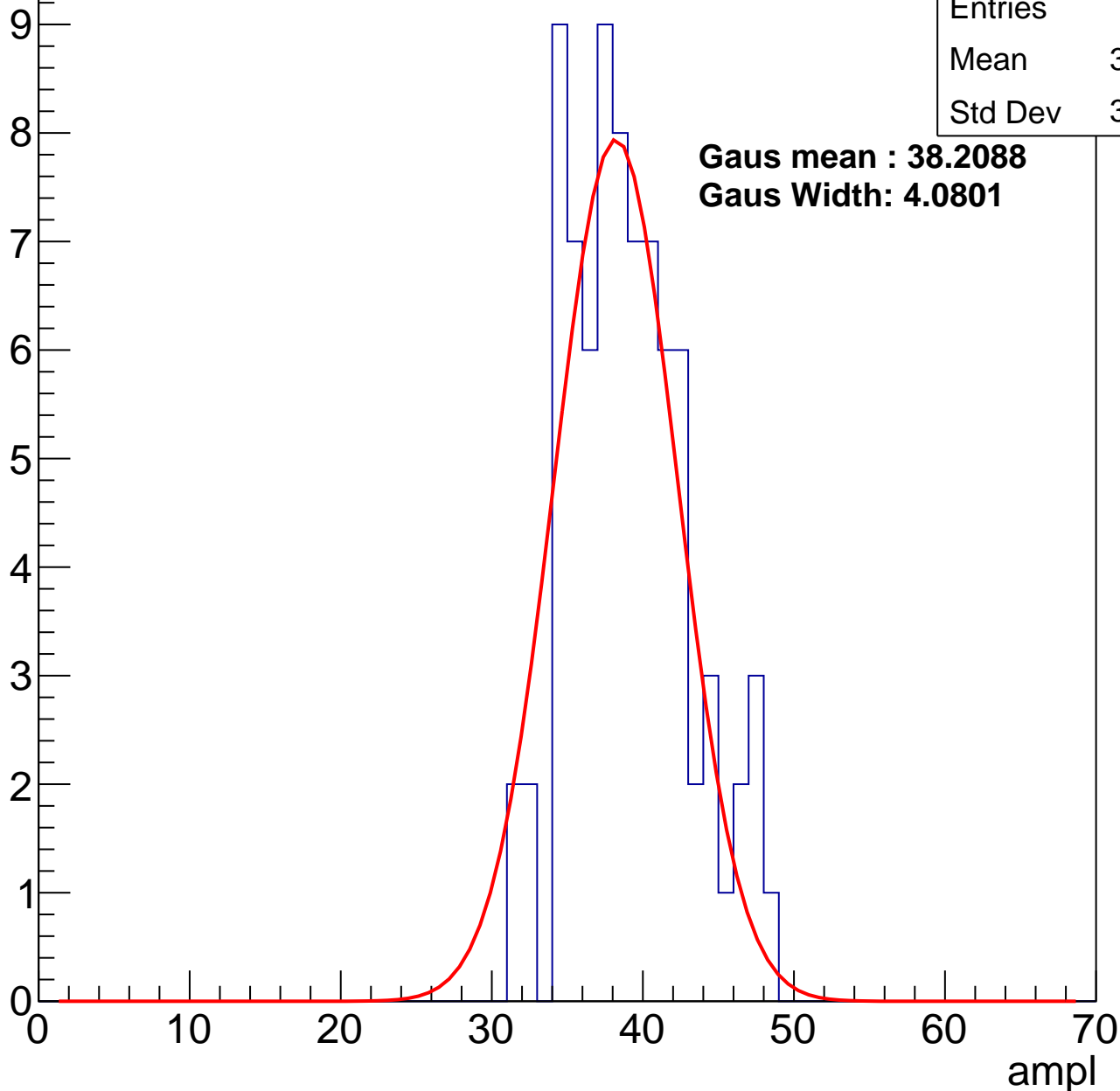
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	38.58
Std Dev	3.922

Gaus mean : 38.2088

Gaus Width: 4.0801



B1L103S, U12-ch105, adc2

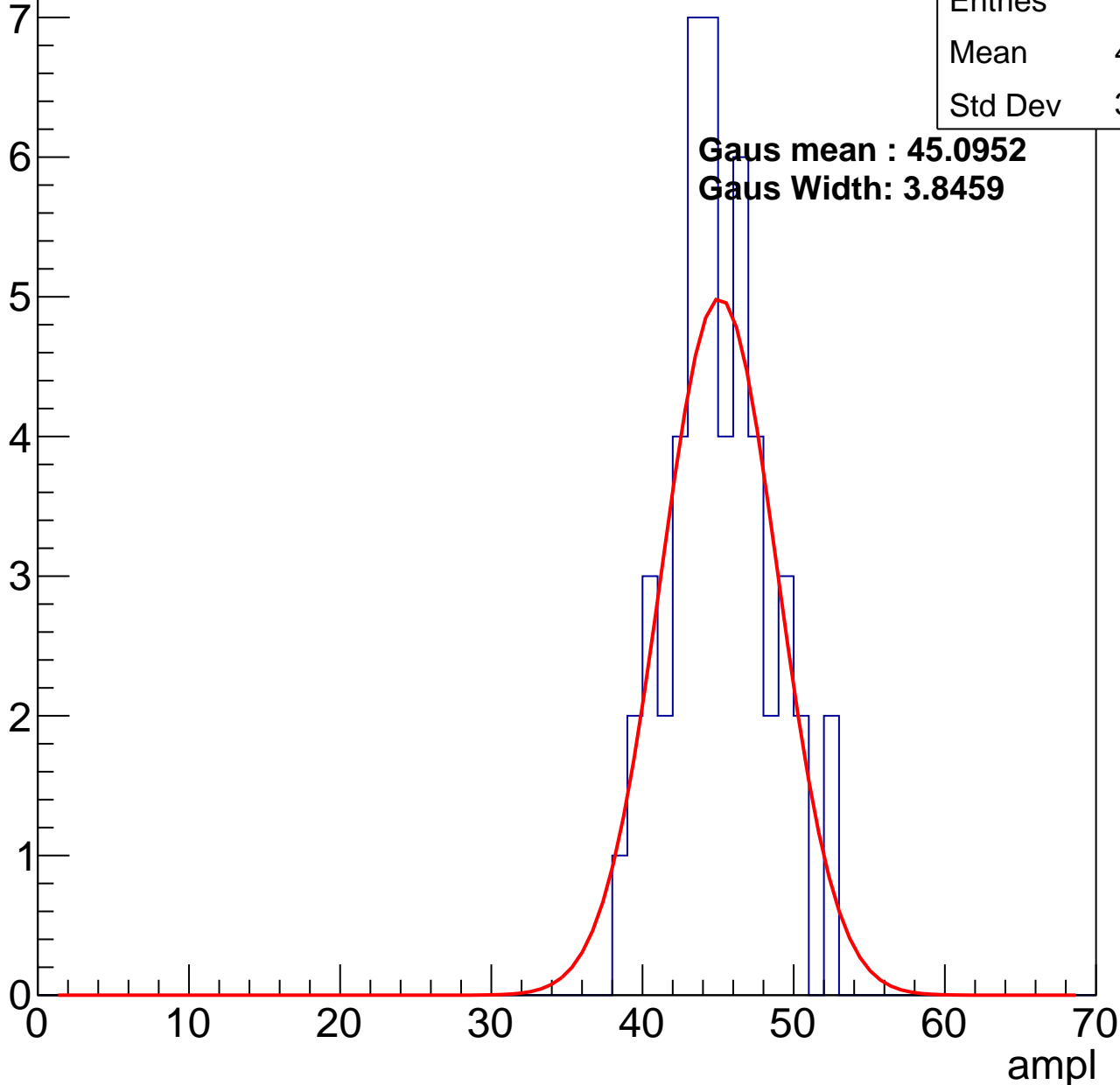
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	44.61
Std Dev	3.281

Gaus mean : 45.0952

Gaus Width: 3.8459

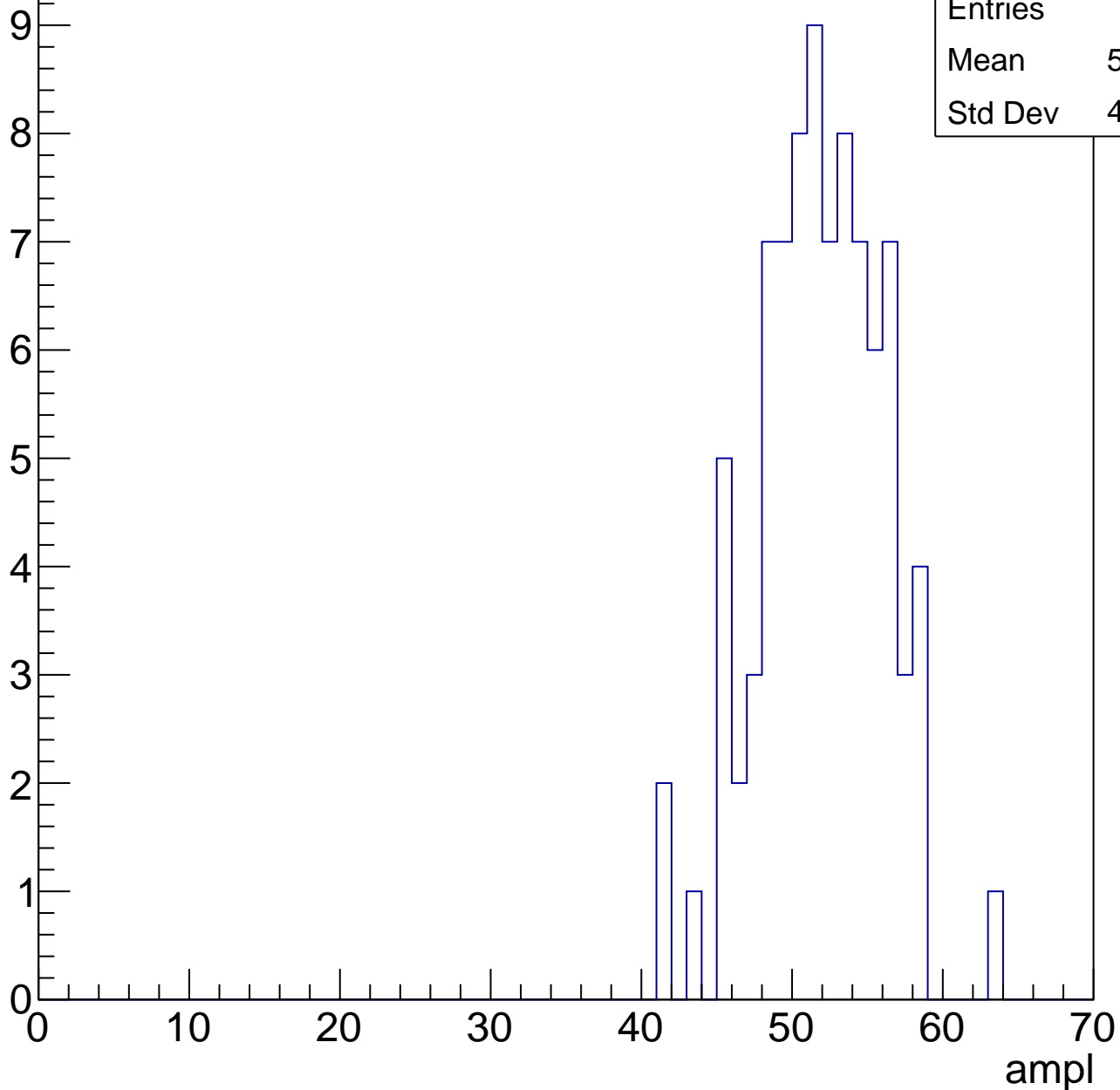


B1L103S, U12-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	51.44
Std Dev	4.076

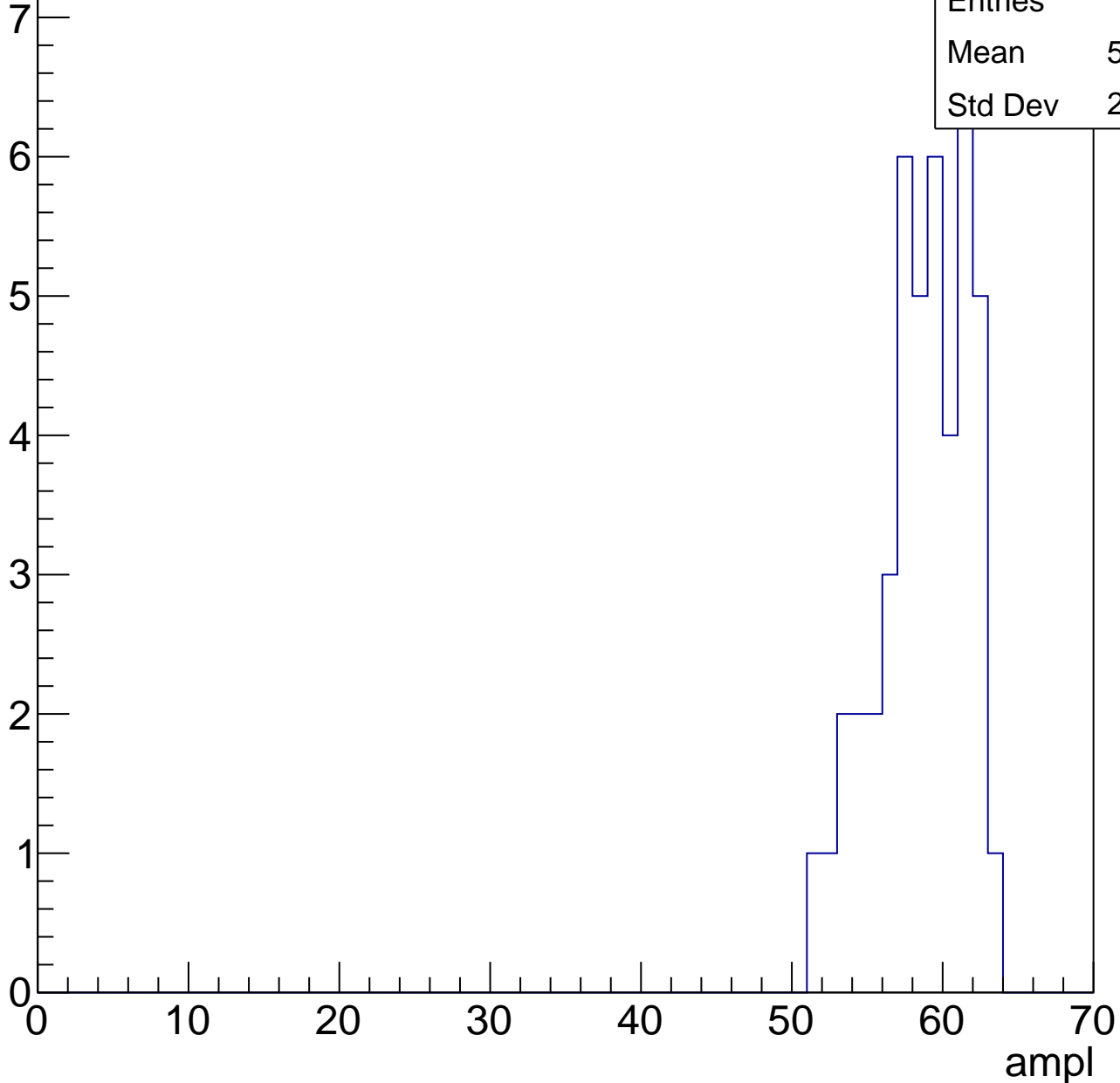


B1L103S, U12-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.24
Std Dev	2.945

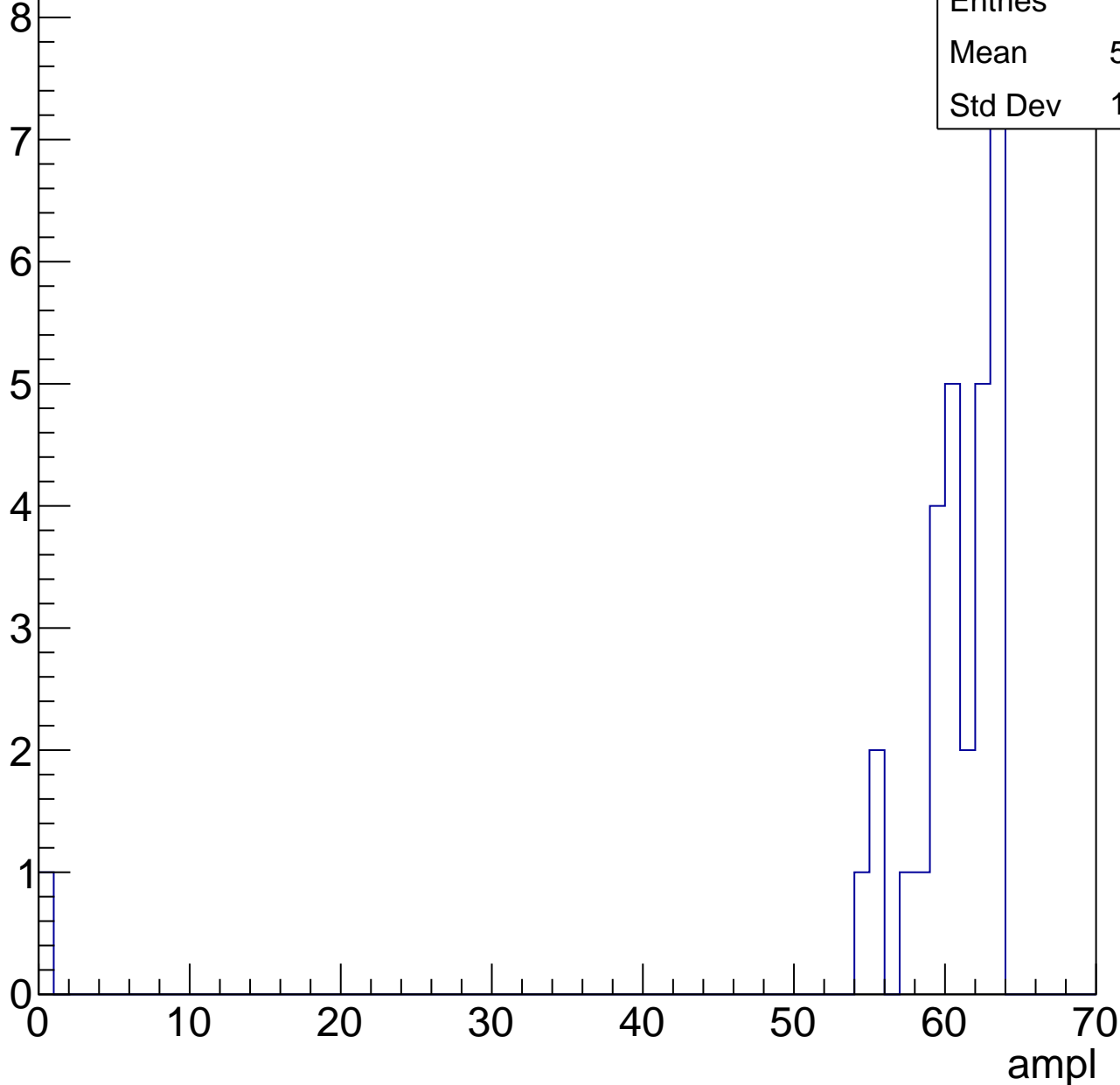


B1L103S, U12-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

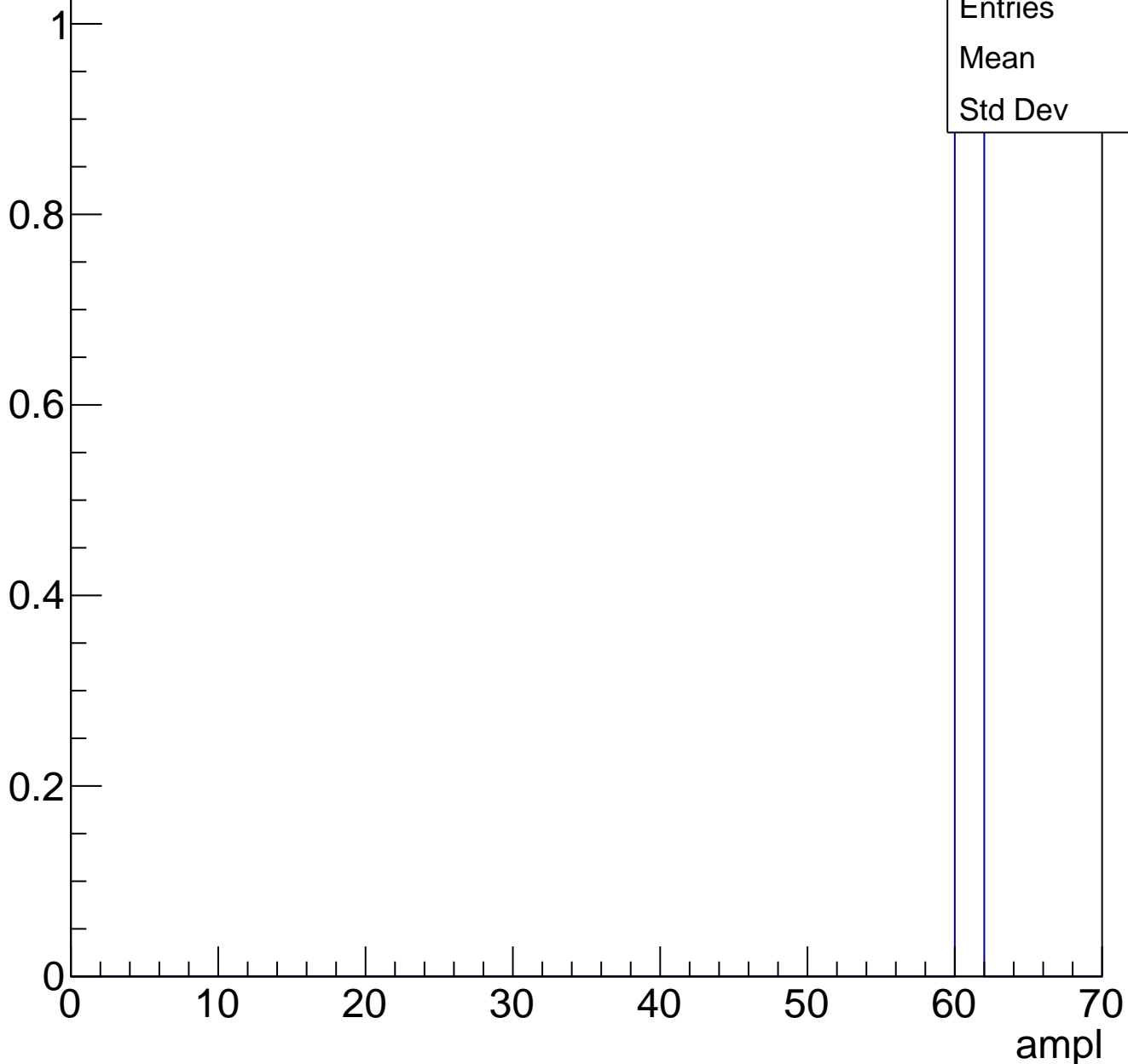
Entries	30
Mean	58.37
Std Dev	11.13



B1L103S, U12-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U12-ch106, adc0

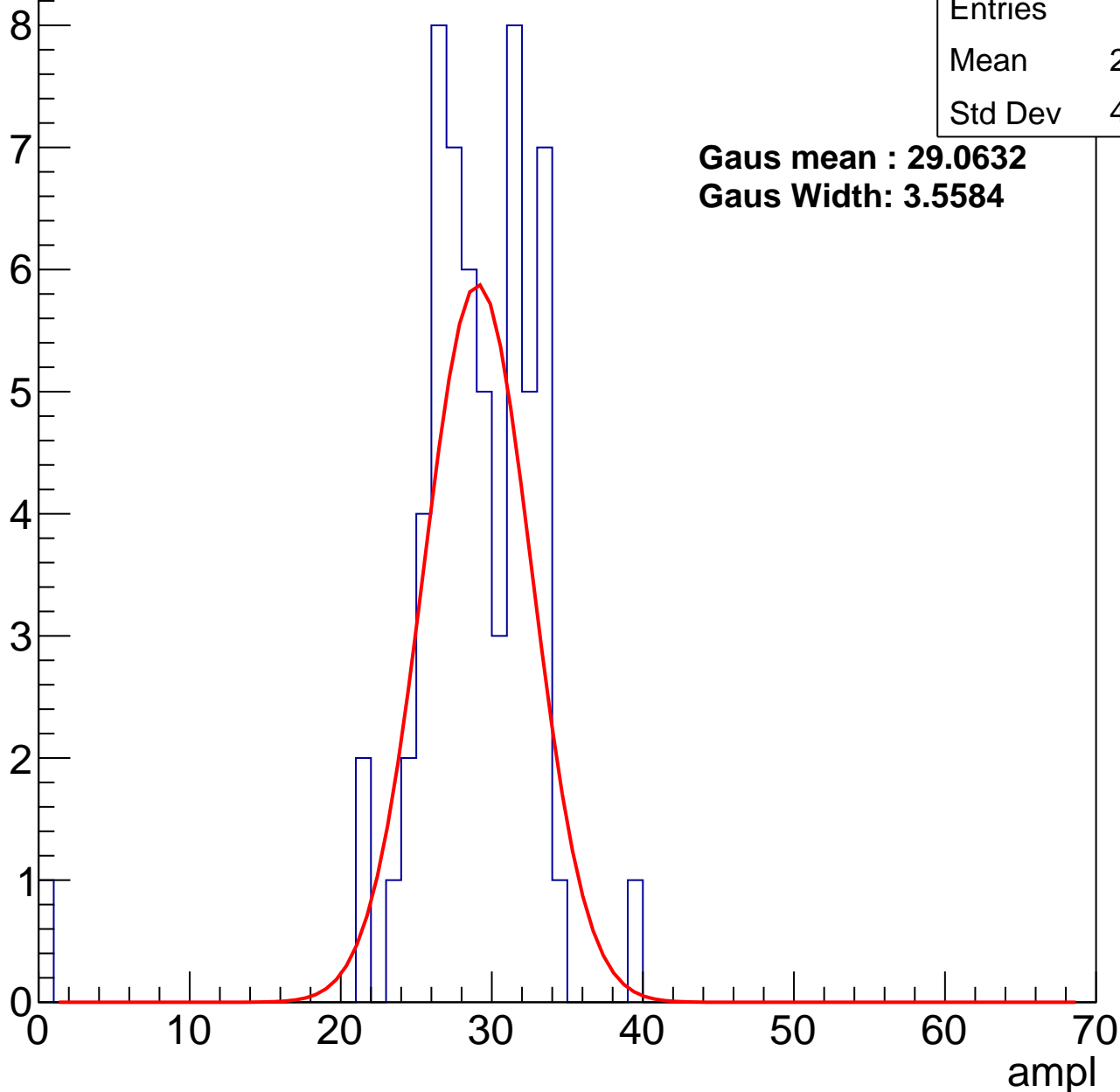
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	28.28
Std Dev	4.976

Gaus mean : 29.0632

Gaus Width: 3.5584



B1L103S, U12-ch106, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	36.59
Std Dev	3.846

Gaus mean : 37.1262

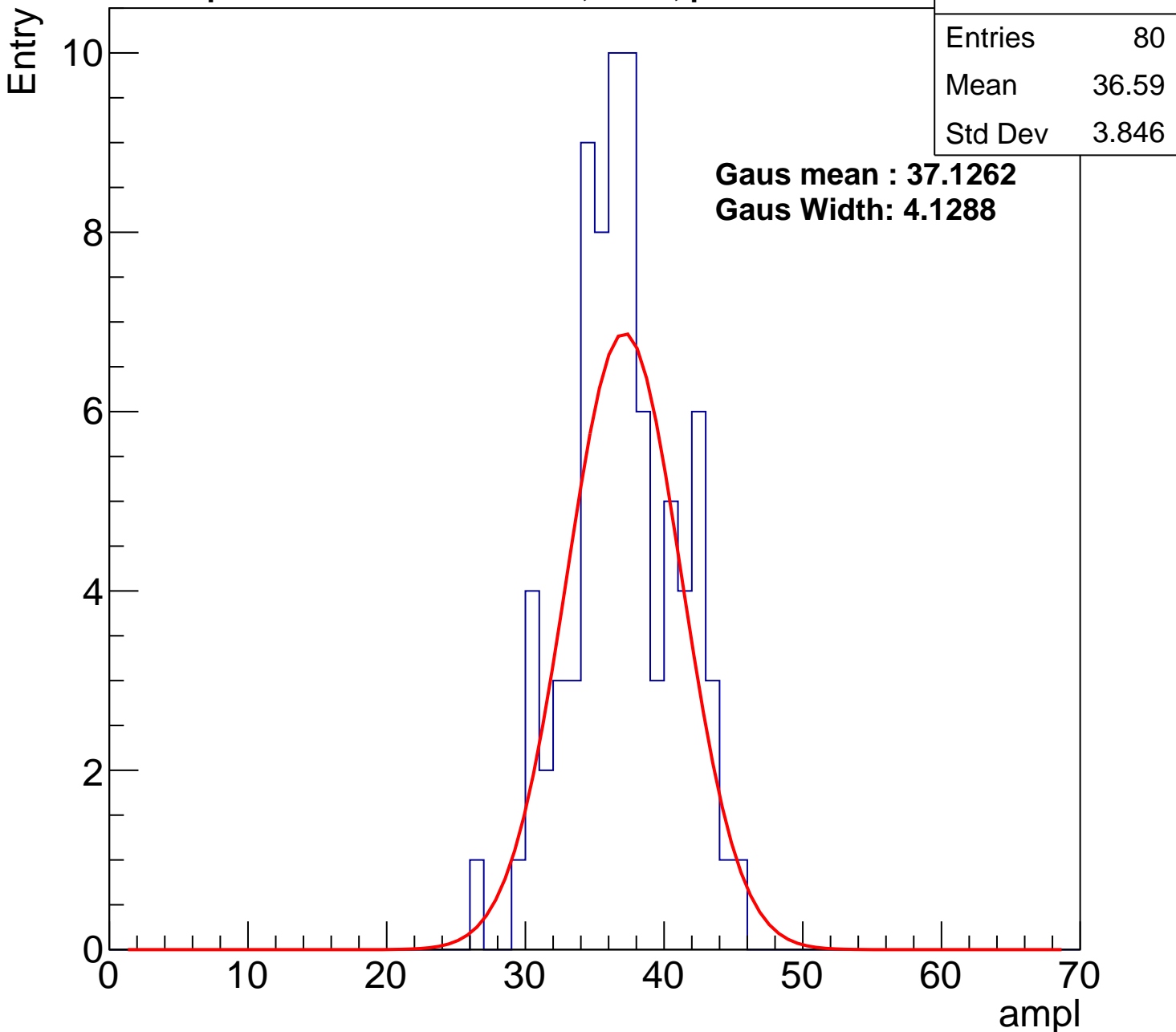
Gaus Width: 4.1288

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch106, adc2

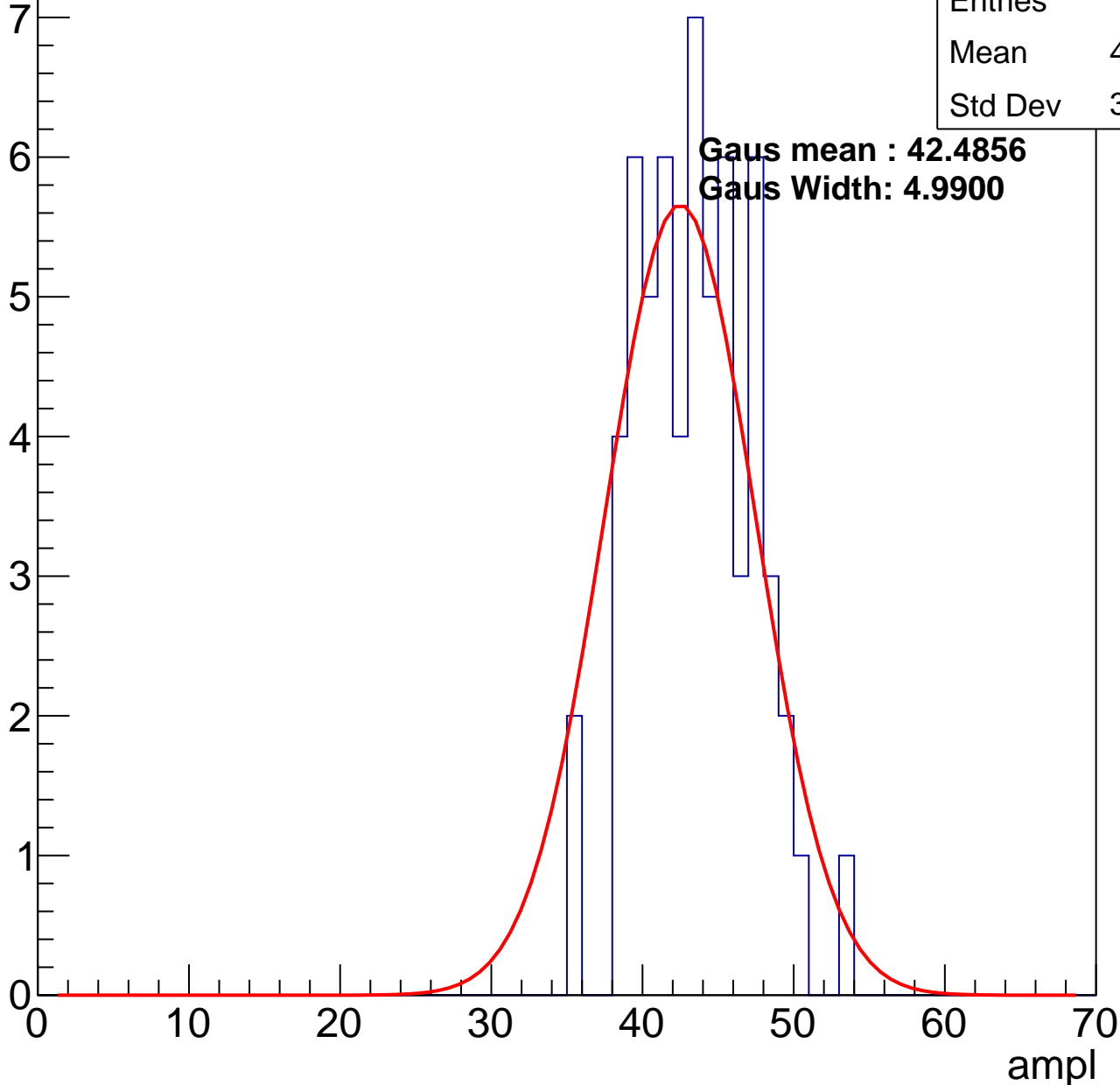
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	43.05
Std Dev	3.722

Gaus mean : 42.4856

Gaus Width: 4.9900

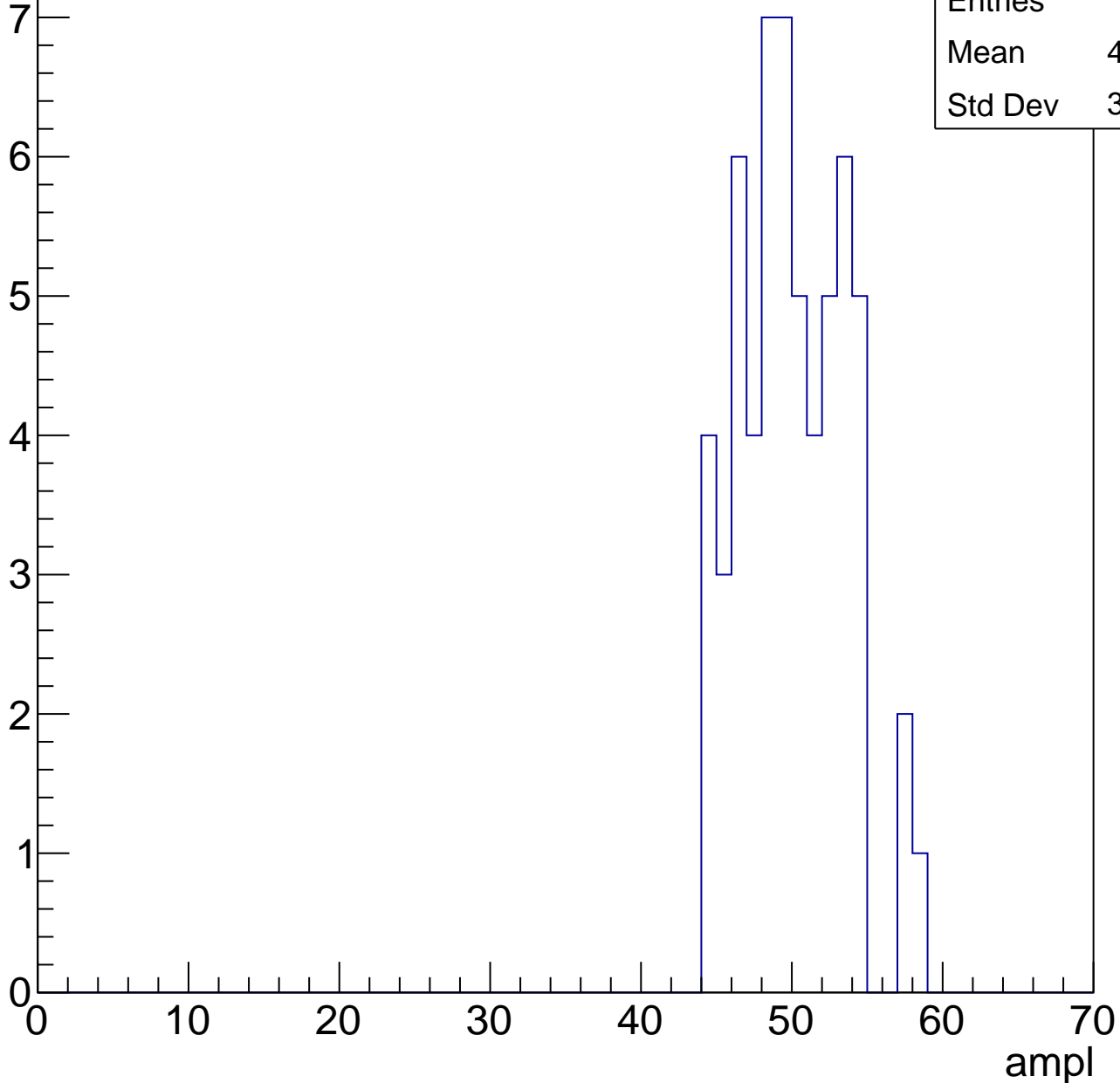


B1L103S, U12-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.63
Std Dev	3.439

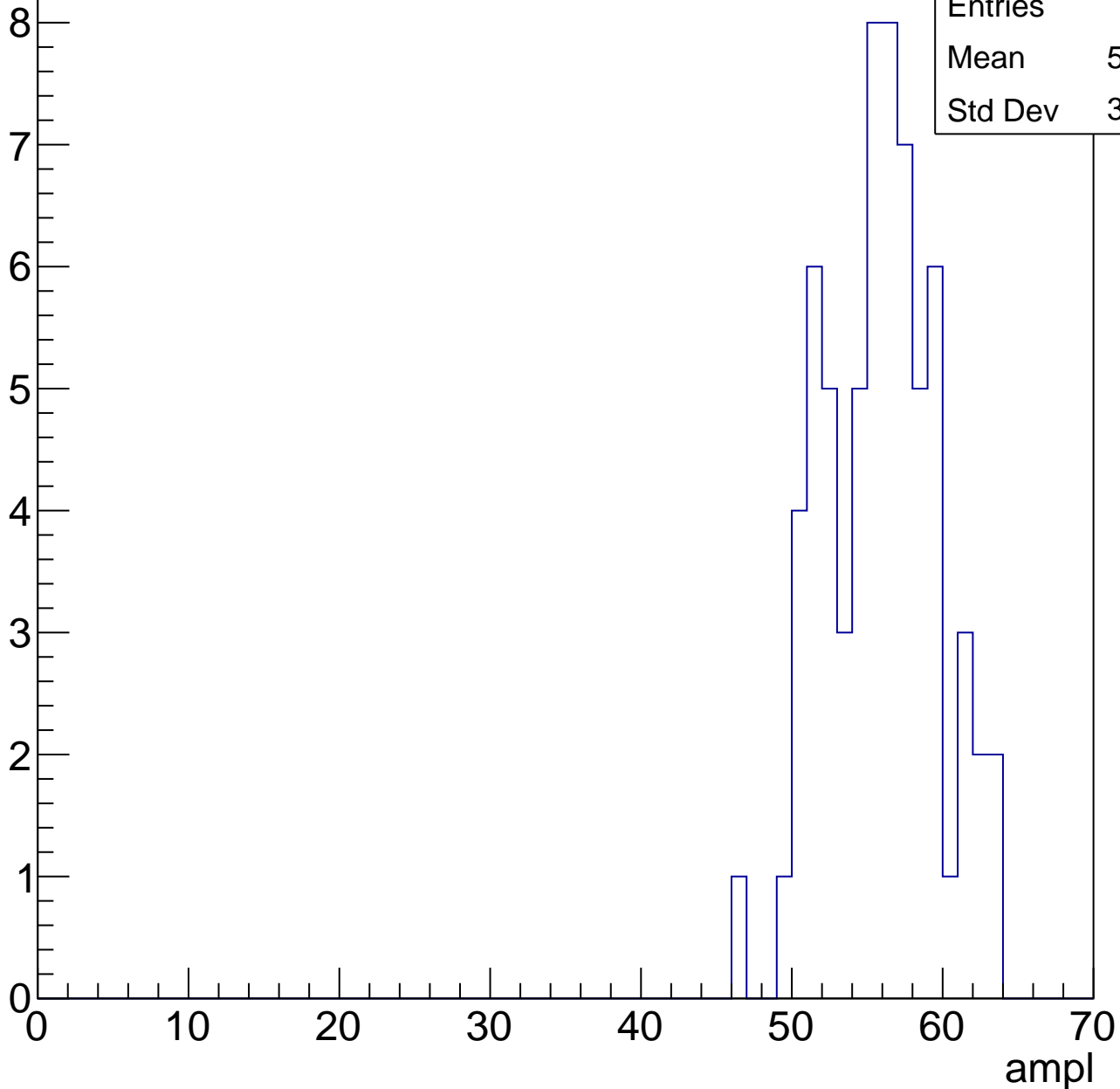


B1L103S, U12-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	55.43
Std Dev	3.662

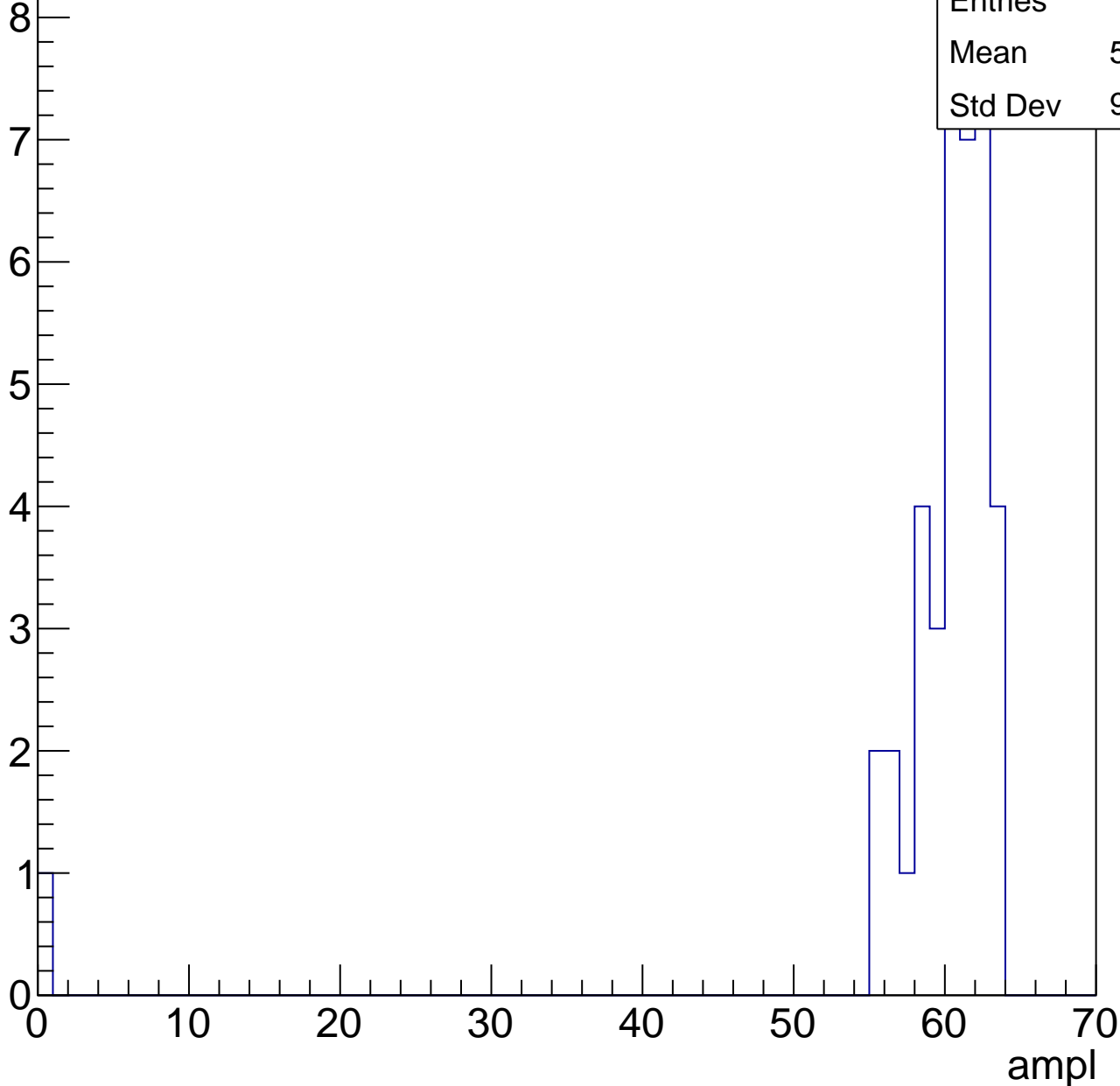


B1L103S, U12-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

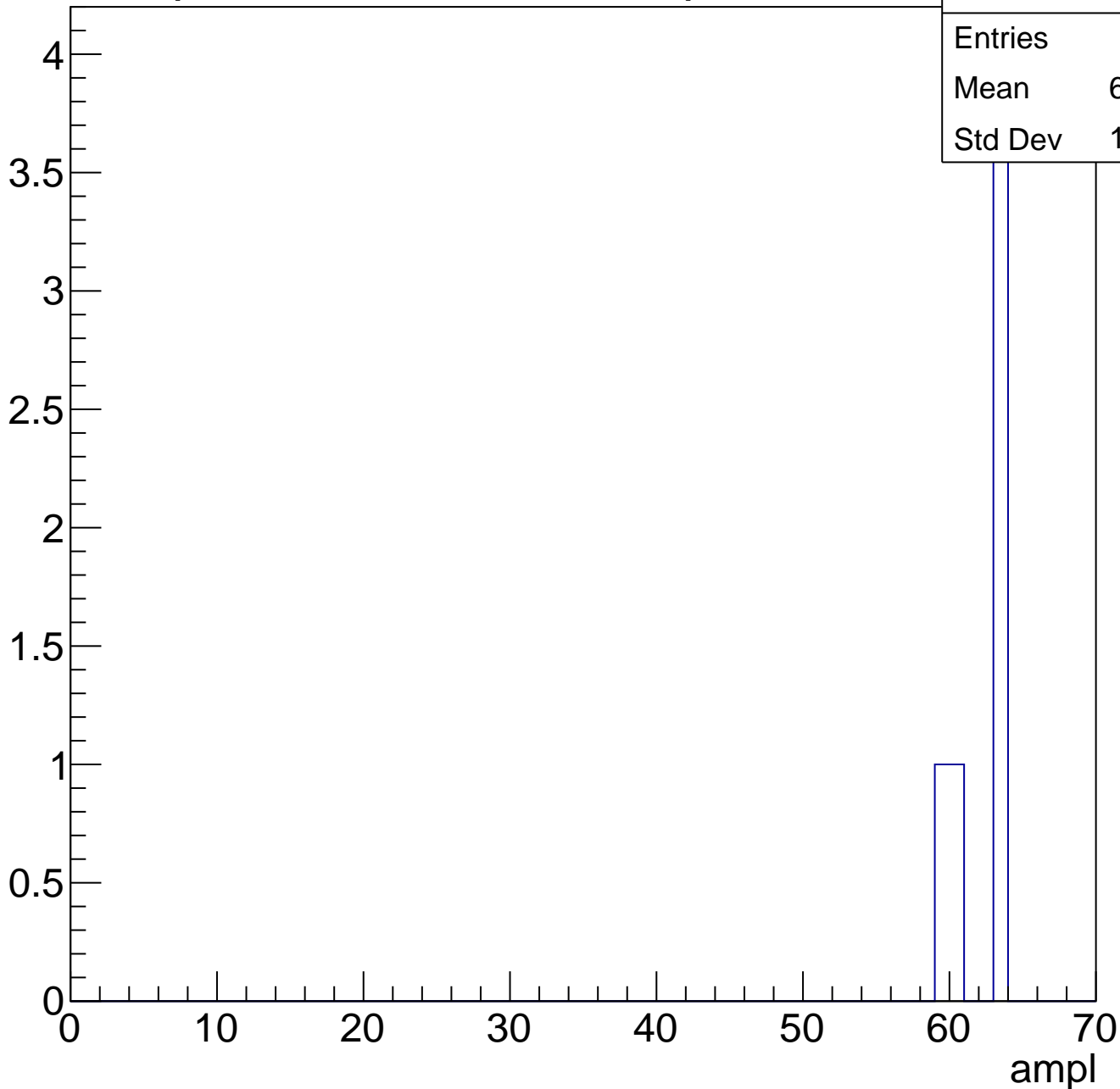
Entries	40
Mean	58.58
Std Dev	9.623



B1L103S, U12-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch107, adc0

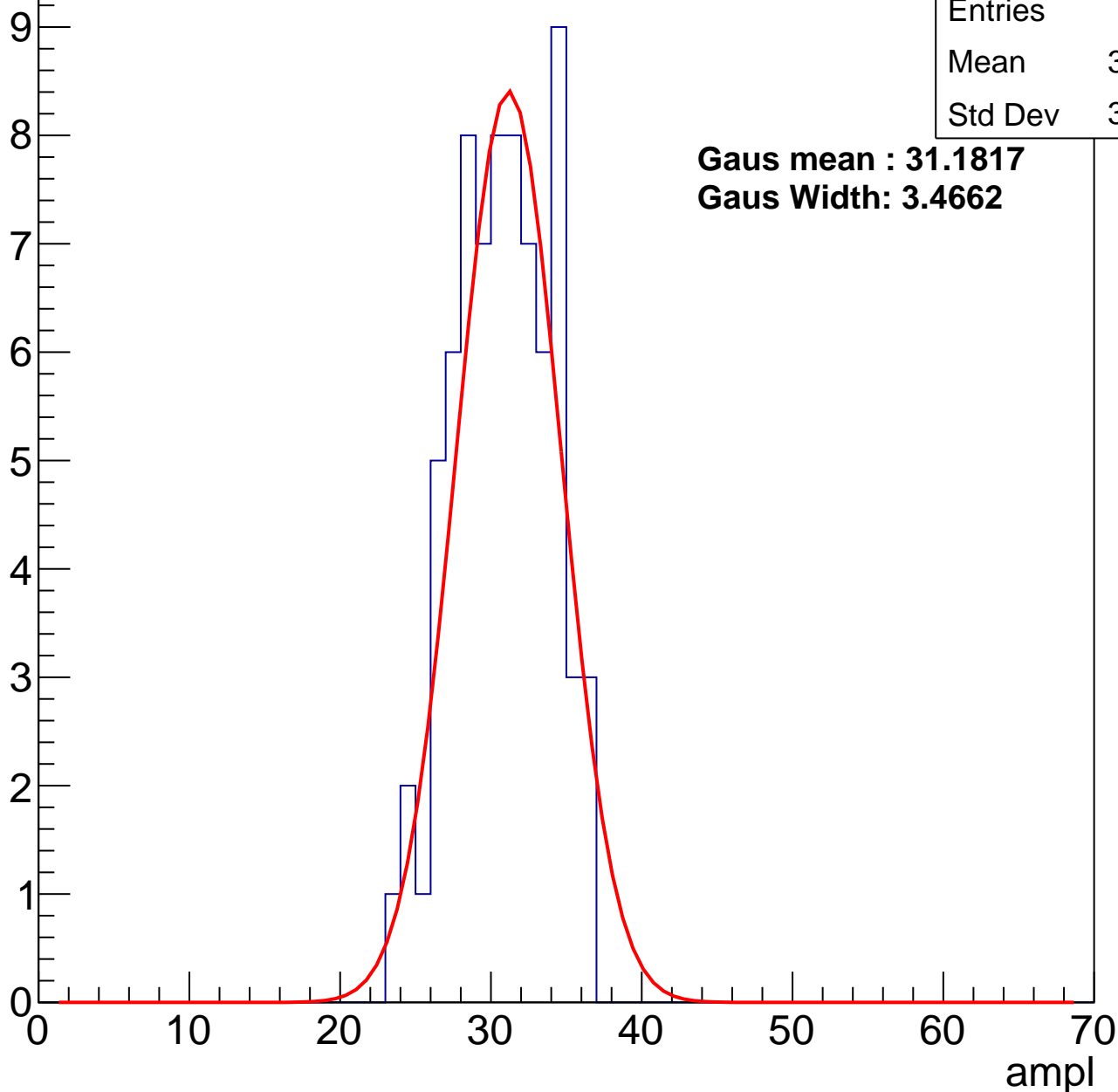
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.32
Std Dev	3.137

Gaus mean : 31.1817

Gaus Width: 3.4662



B1L103S, U12-ch107, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	37.53
Std Dev	3.694

Gaus mean : 38.6588

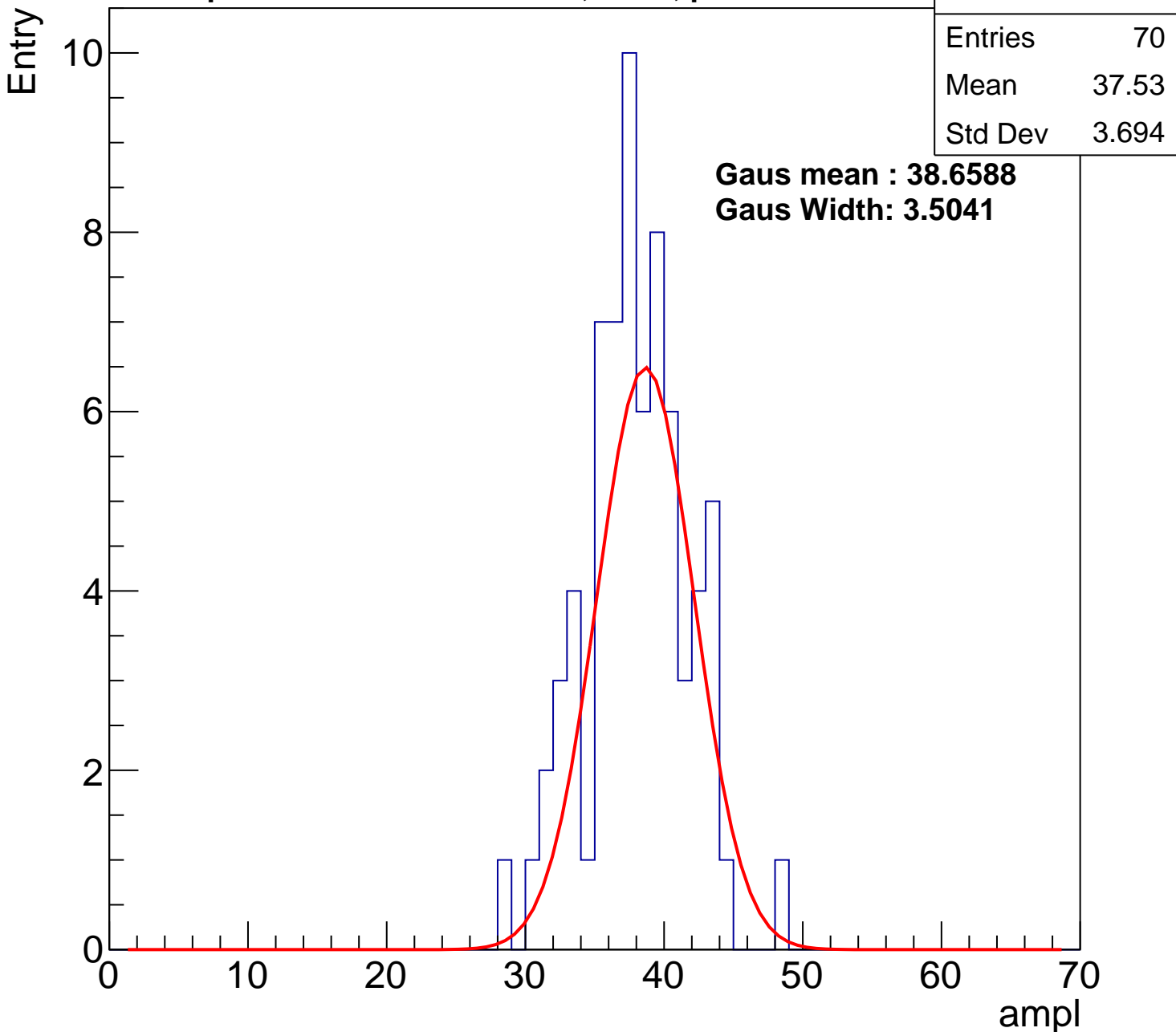
Gaus Width: 3.5041

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch107, adc2

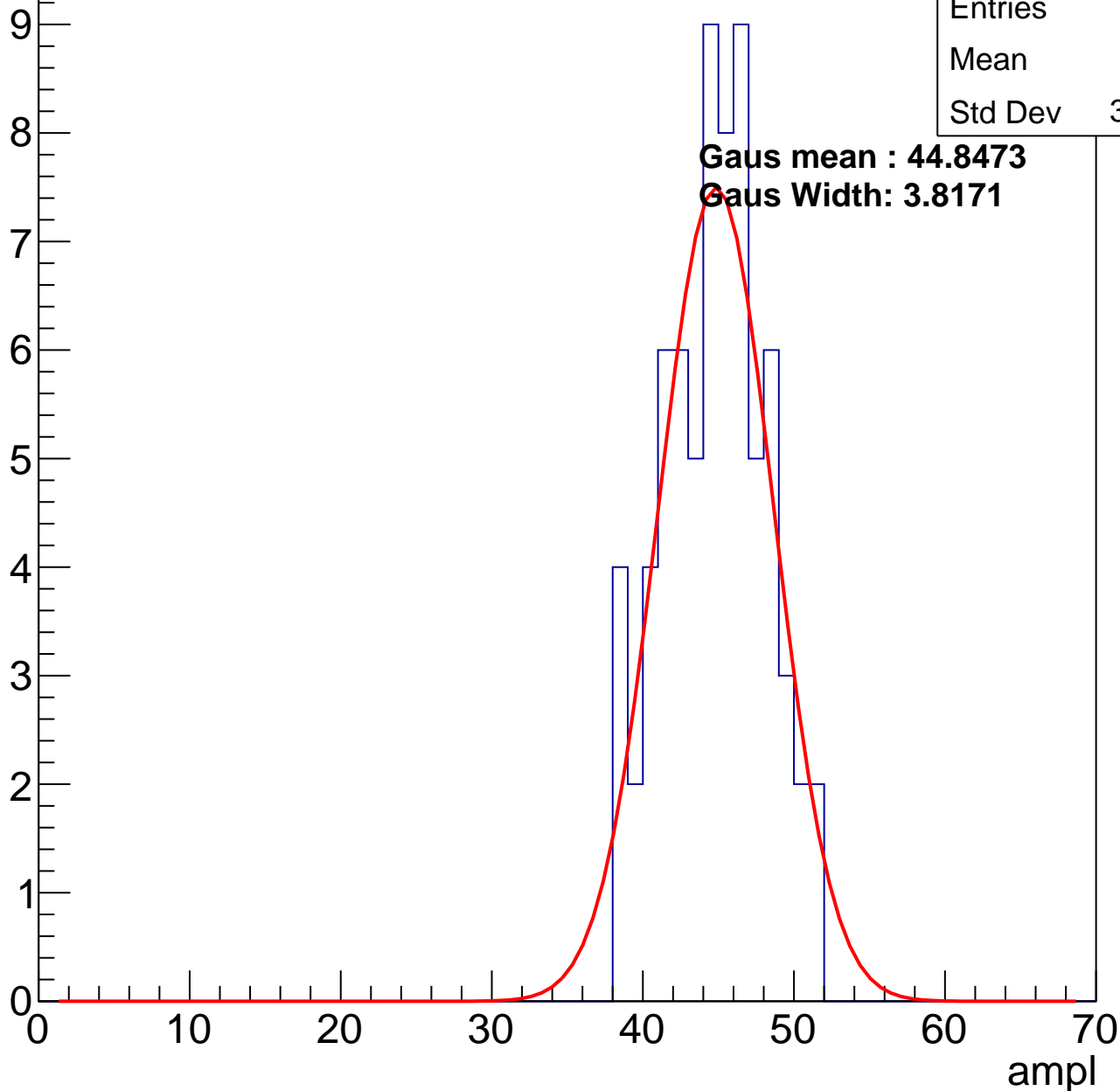
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	44.3
Std Dev	3.282

Gaus mean : 44.8473

Gaus Width: 3.8171

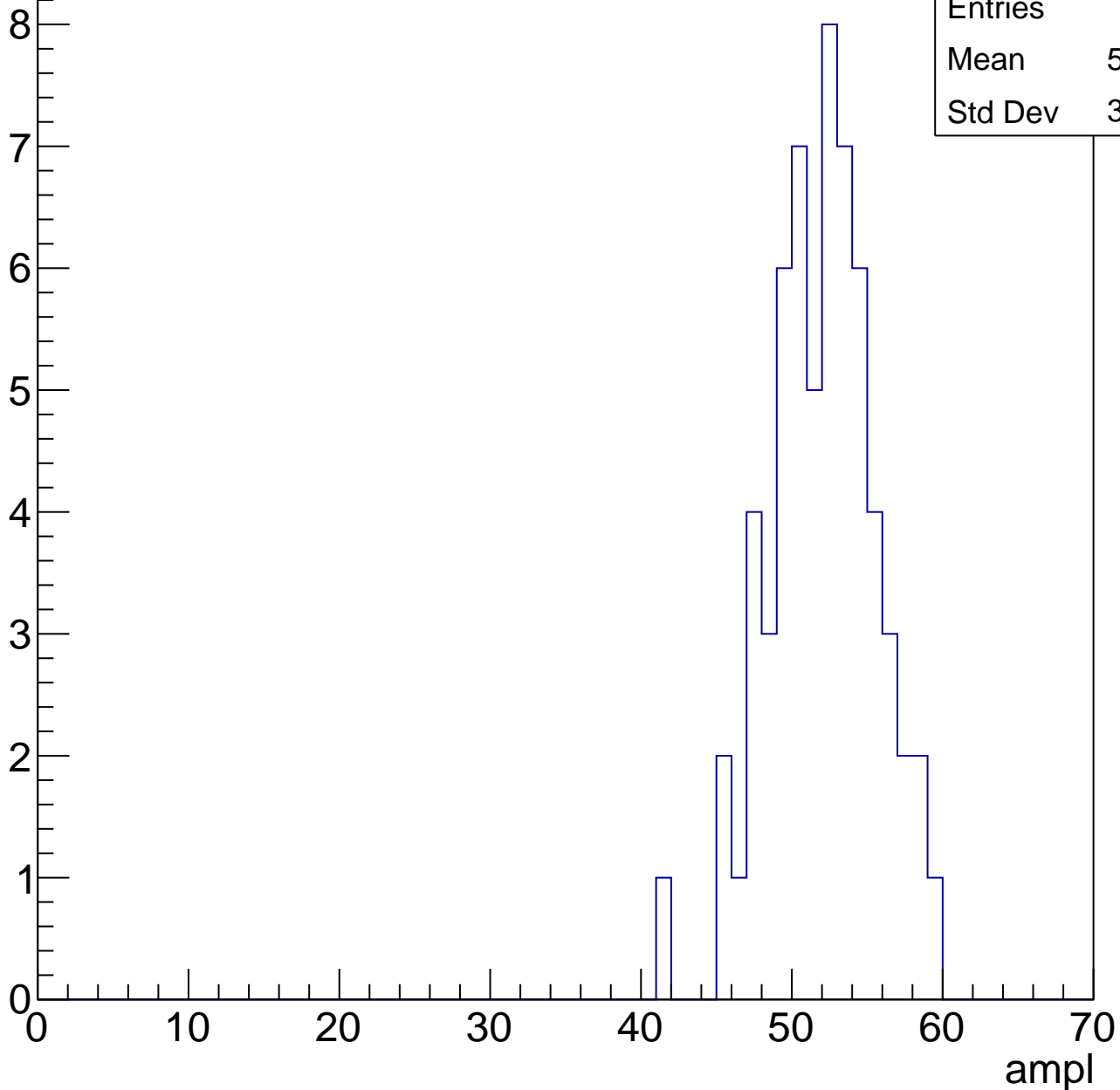


B1L103S, U12-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

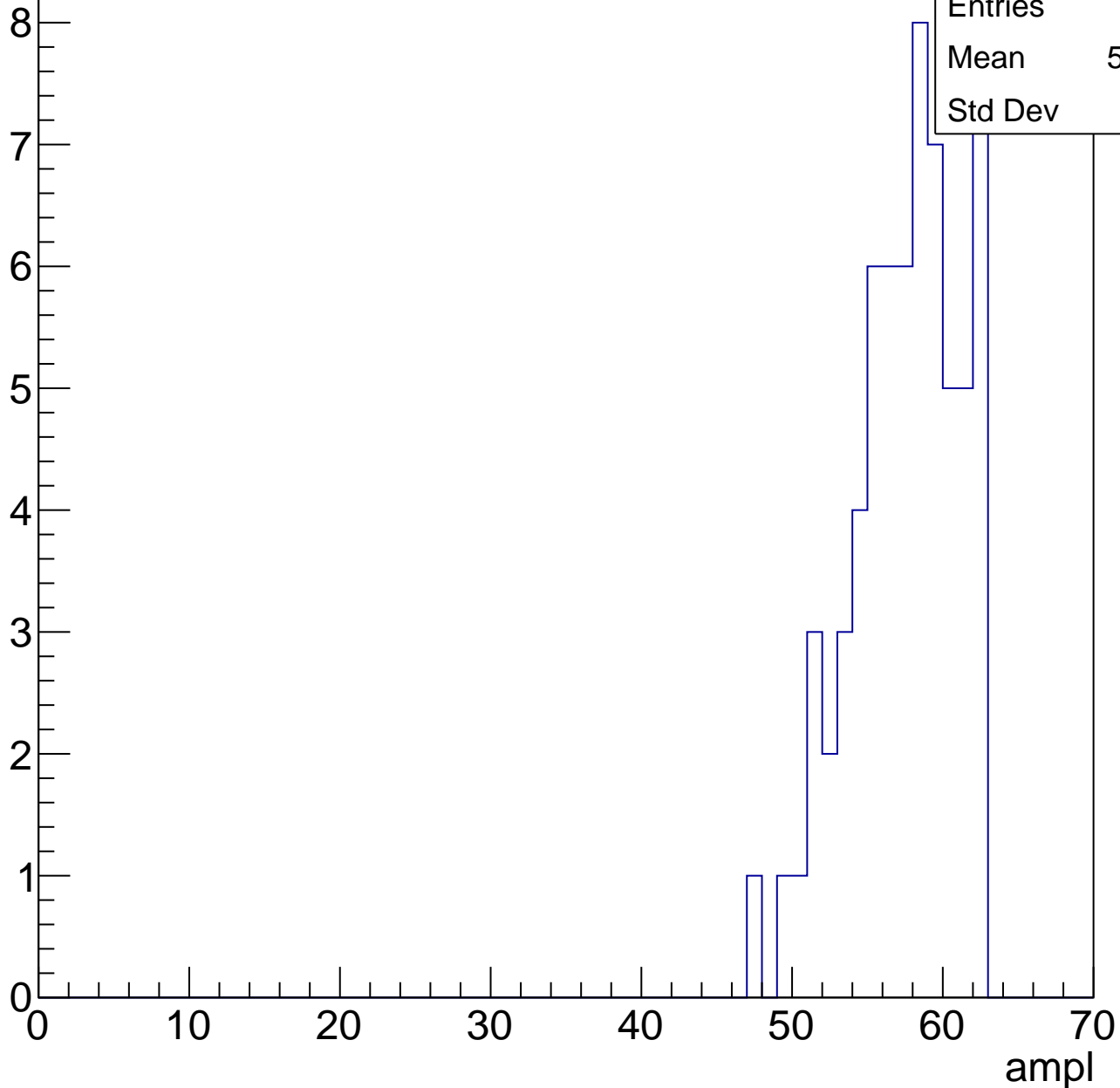
Entries	62
Mean	51.55
Std Dev	3.509



B1L103S, U12-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



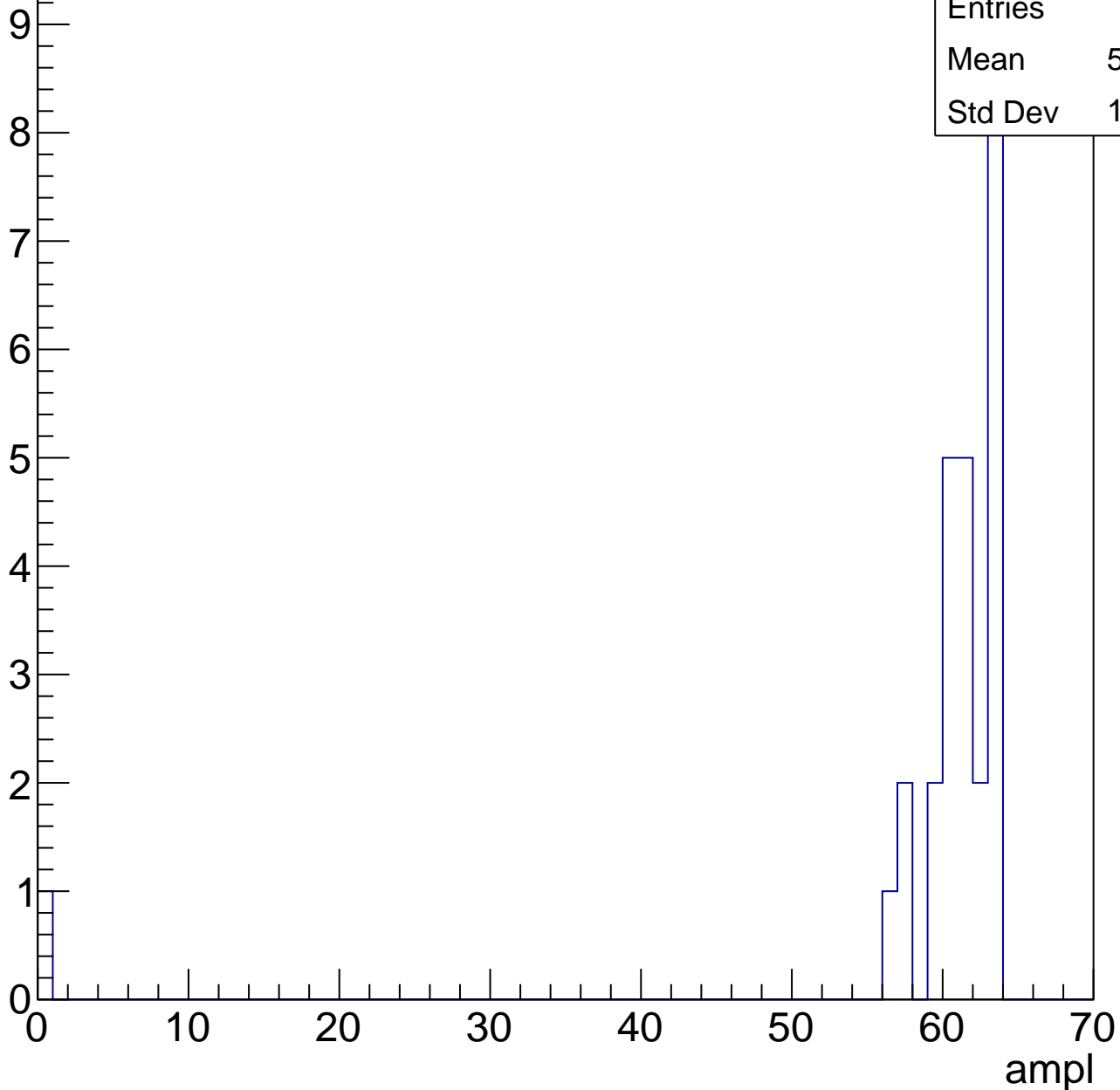
Entries	66
Mean	57.03
Std Dev	3.58

B1L103S, U12-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	58.67
Std Dev	11.68



B1L103S, U12-ch107, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch108, adc0

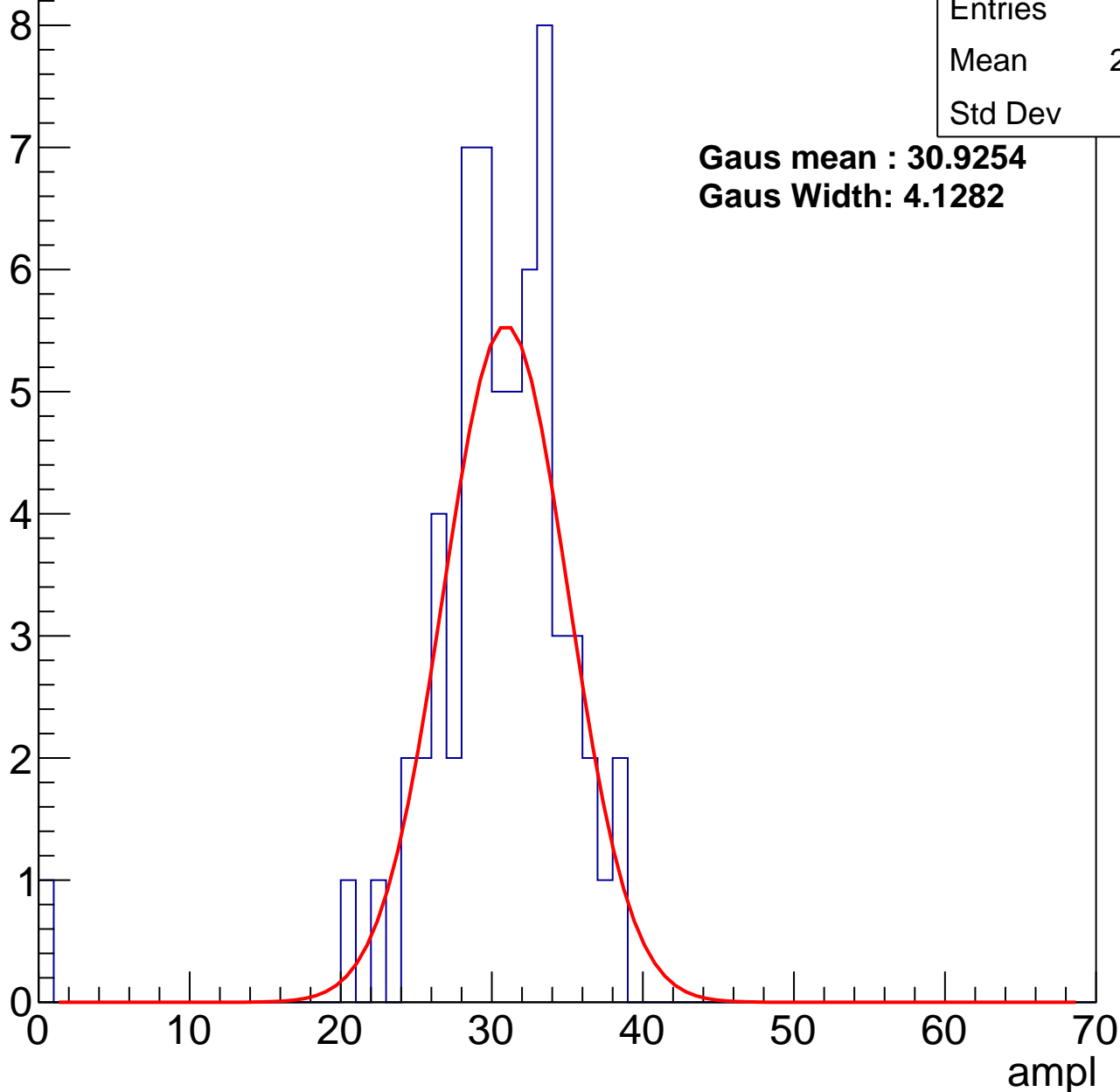
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.84
Std Dev	5.35

Gaus mean : 30.9254

Gaus Width: 4.1282



B1L103S, U12-ch108, adc1

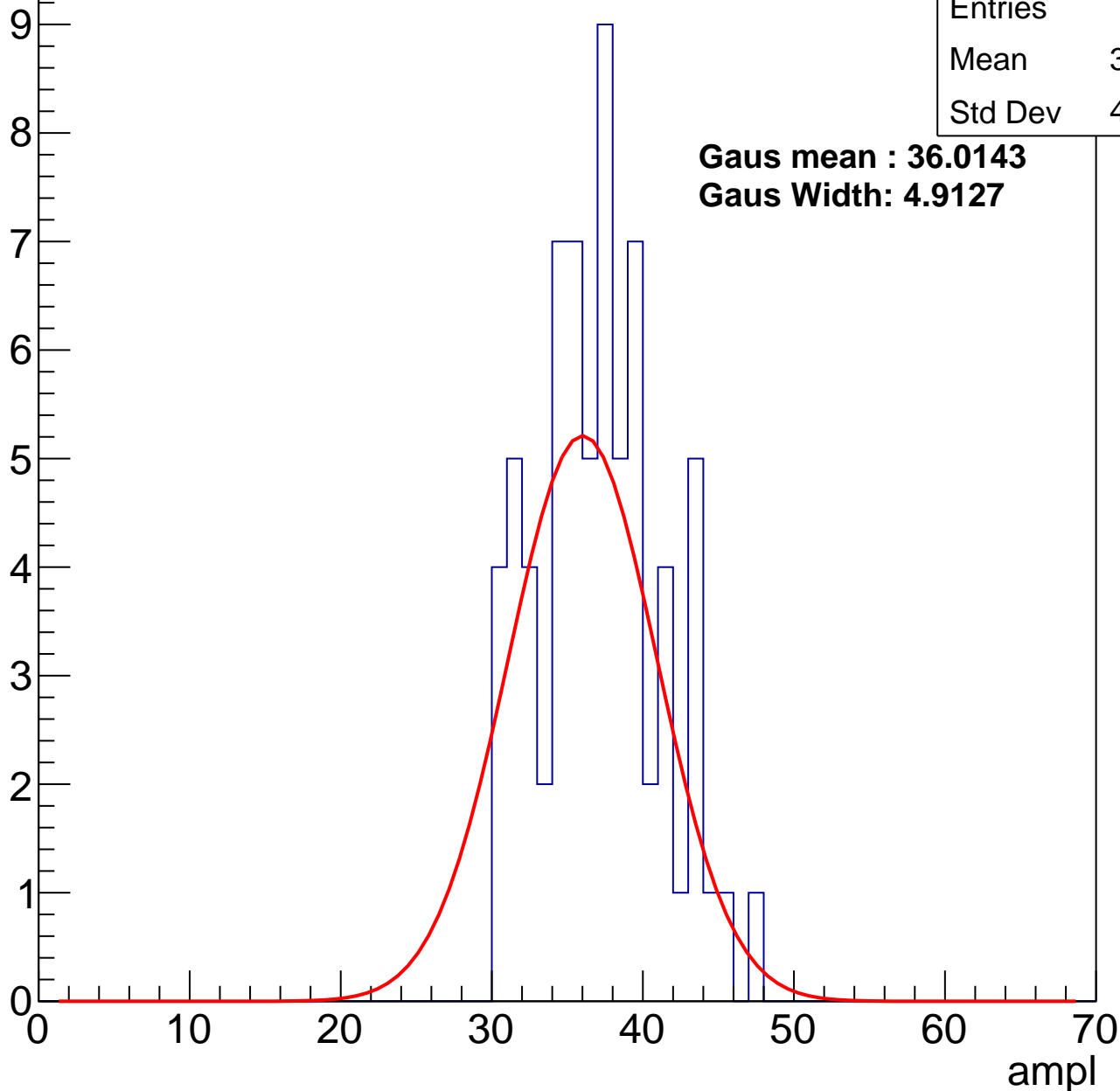
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	36.64
Std Dev	4.014

Gaus mean : 36.0143

Gaus Width: 4.9127



B1L103S, U12-ch108, adc2

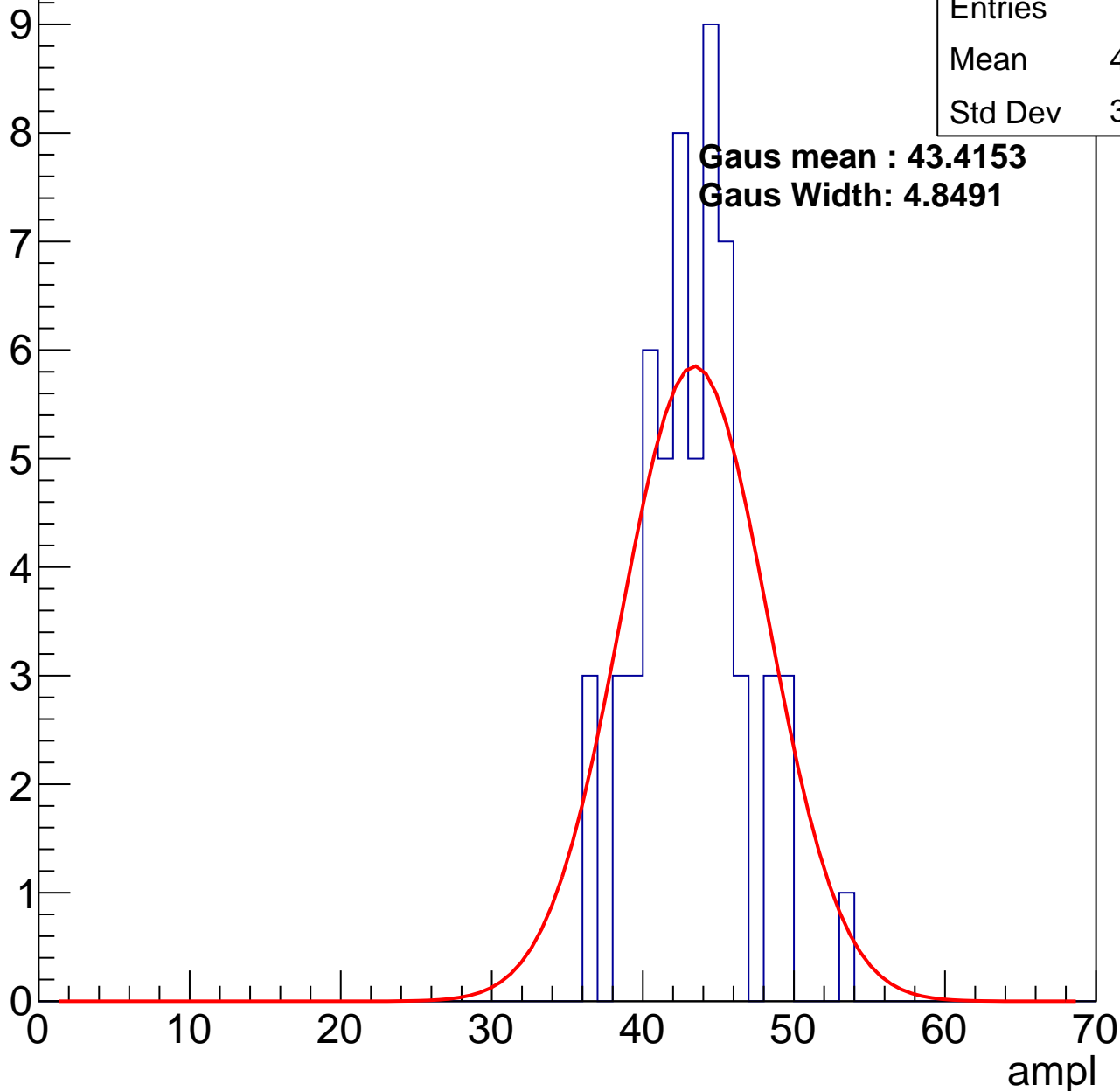
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.85
Std Dev	3.439

Gaus mean : 43.4153

Gaus Width: 4.8491

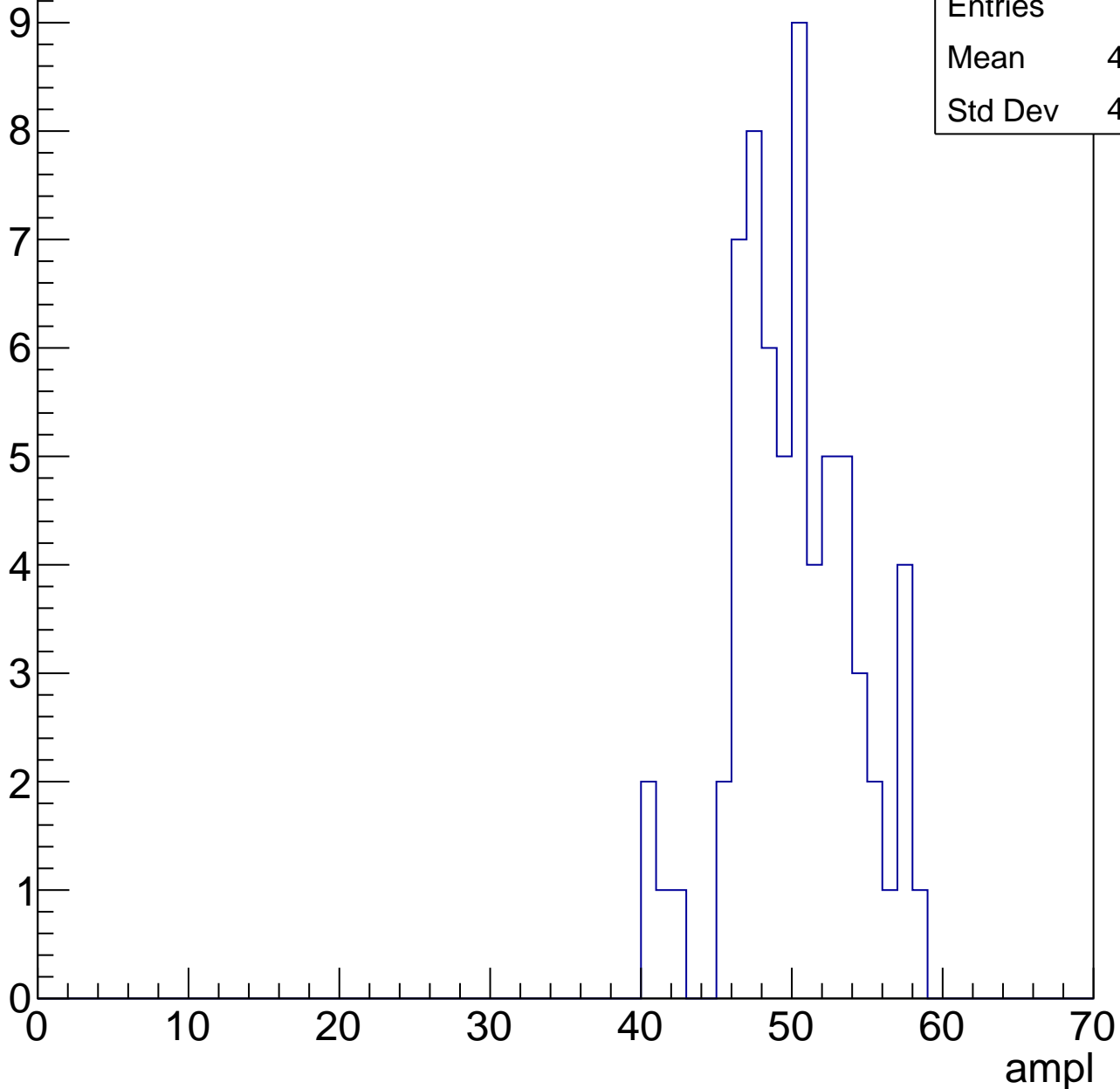


B1L103S, U12-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.65
Std Dev	4.013

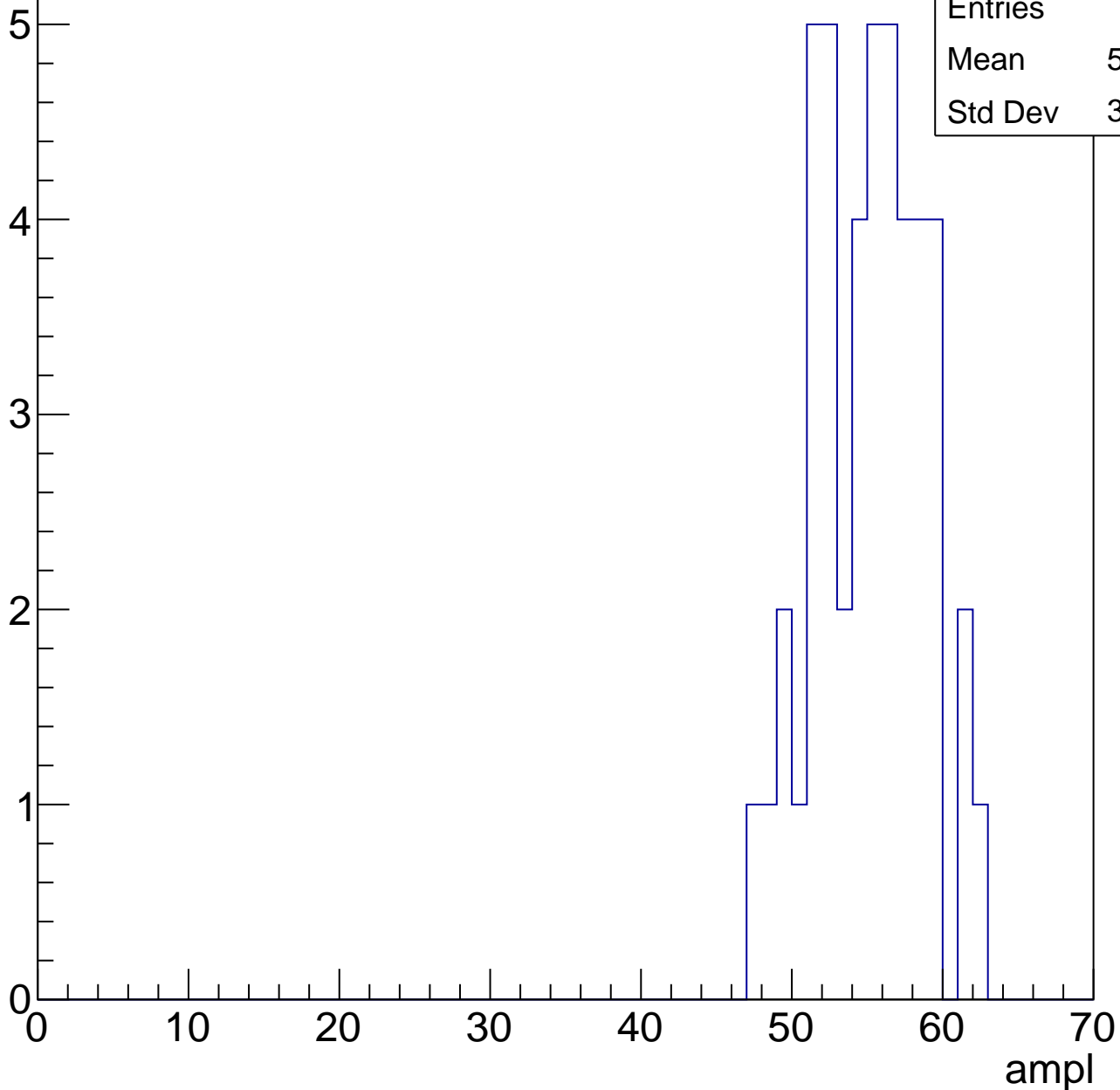


B1L103S, U12-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	54.67
Std Dev	3.563

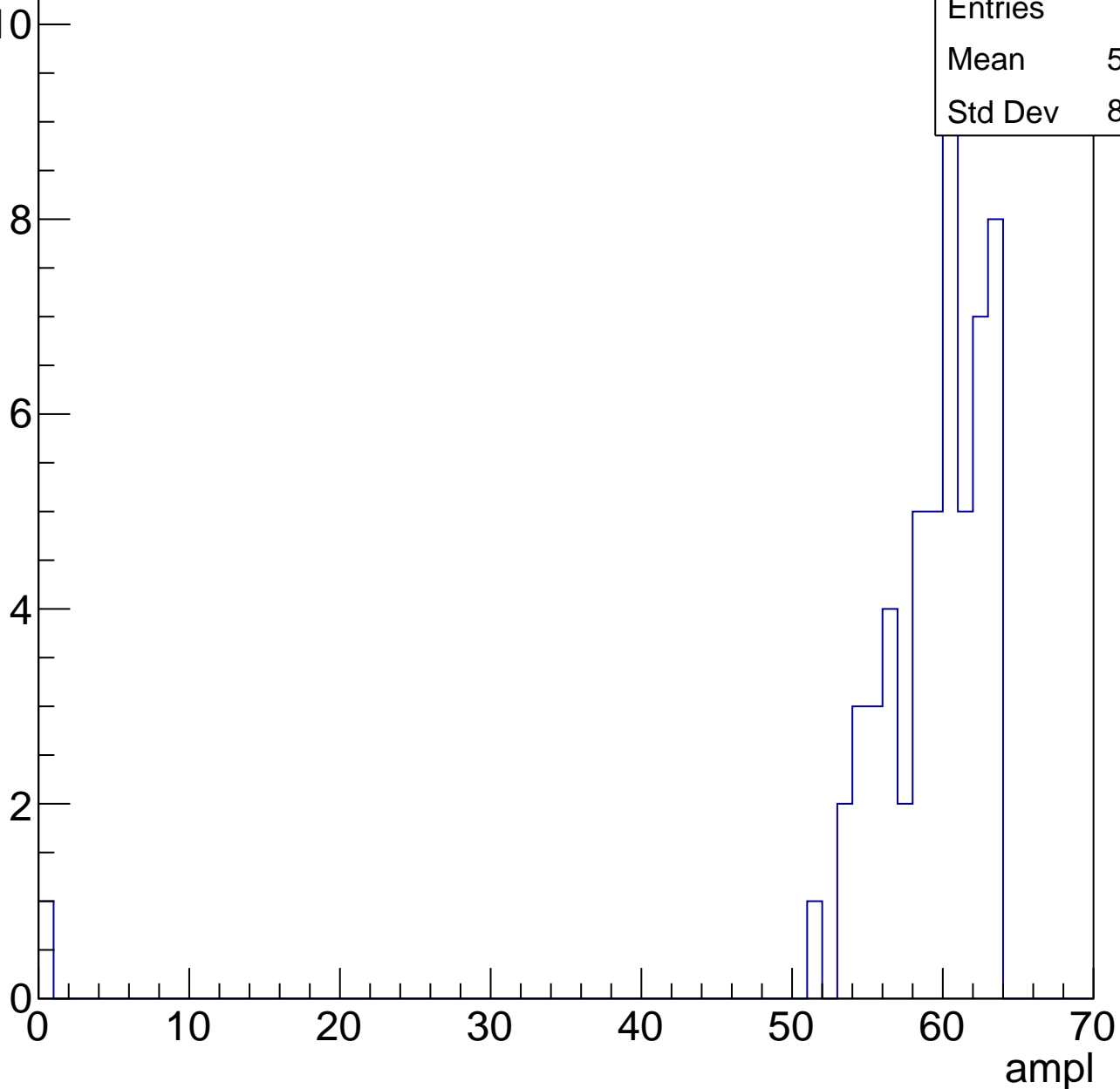


B1L103S, U12-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

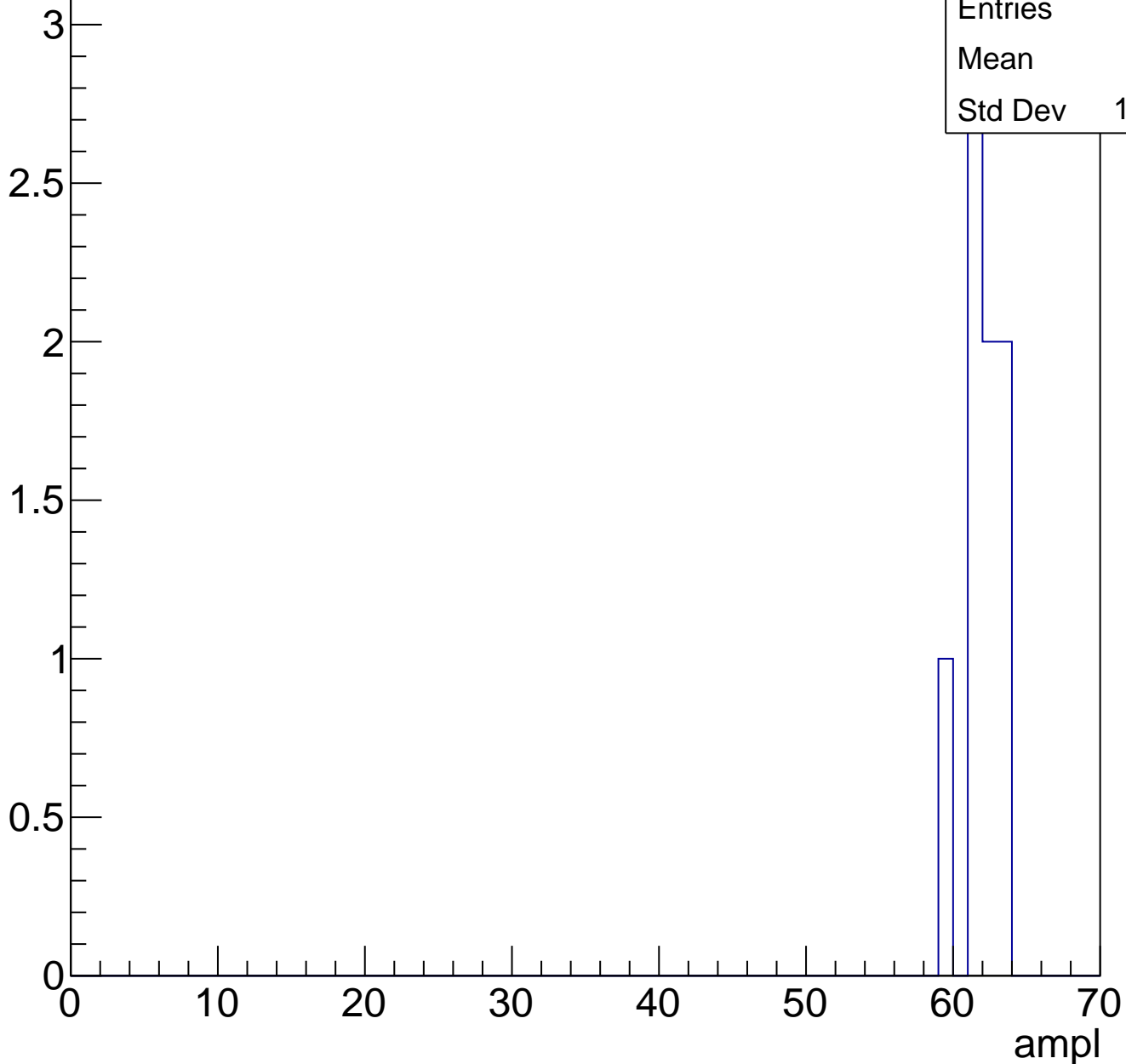
Entries	56
Mean	58.04
Std Dev	8.407



B1L103S, U12-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U12-ch109, adc0

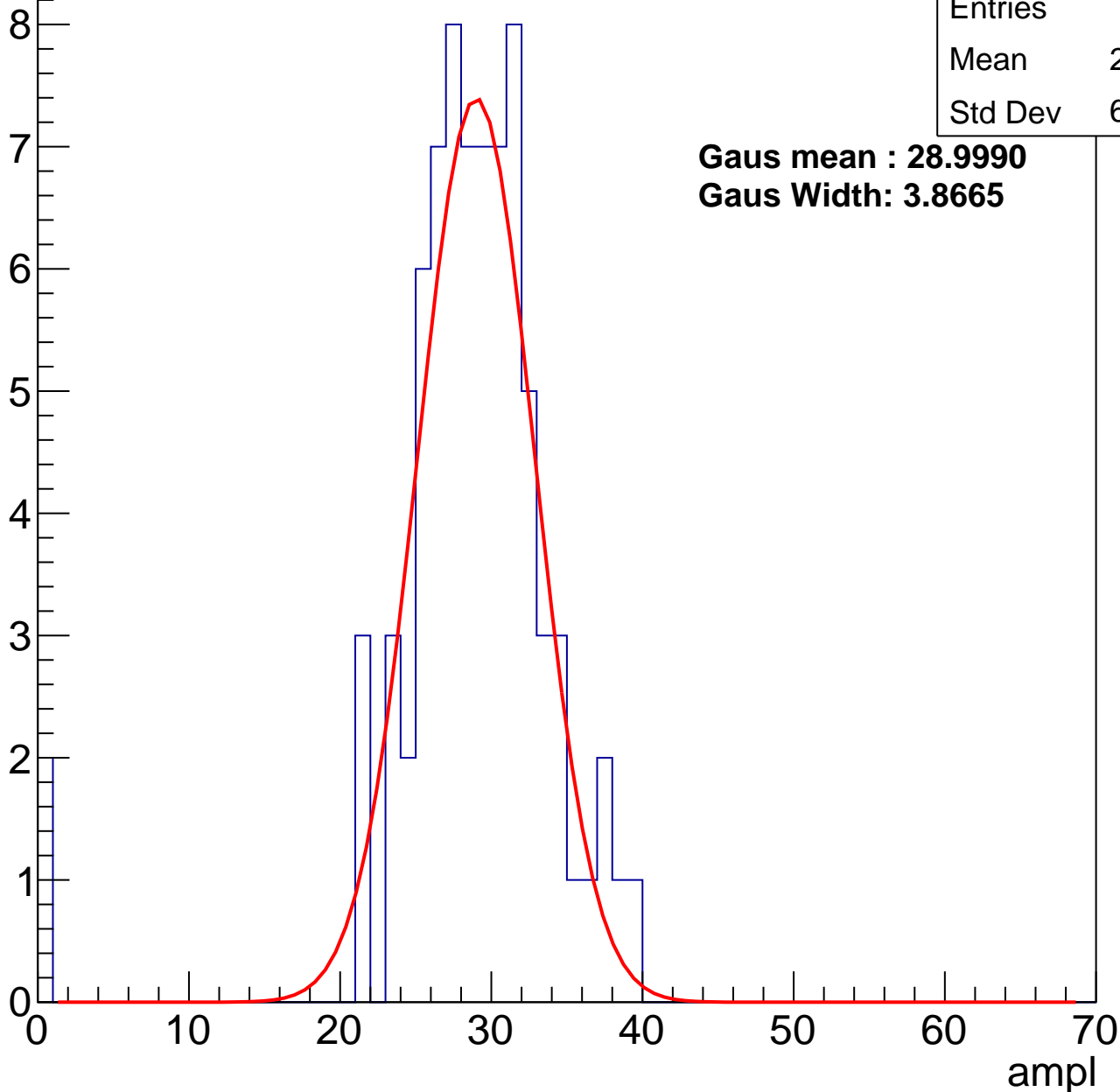
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28.16
Std Dev	6.004

Gaus mean : 28.9990

Gaus Width: 3.8665



B1L103S, U12-ch109, adc1

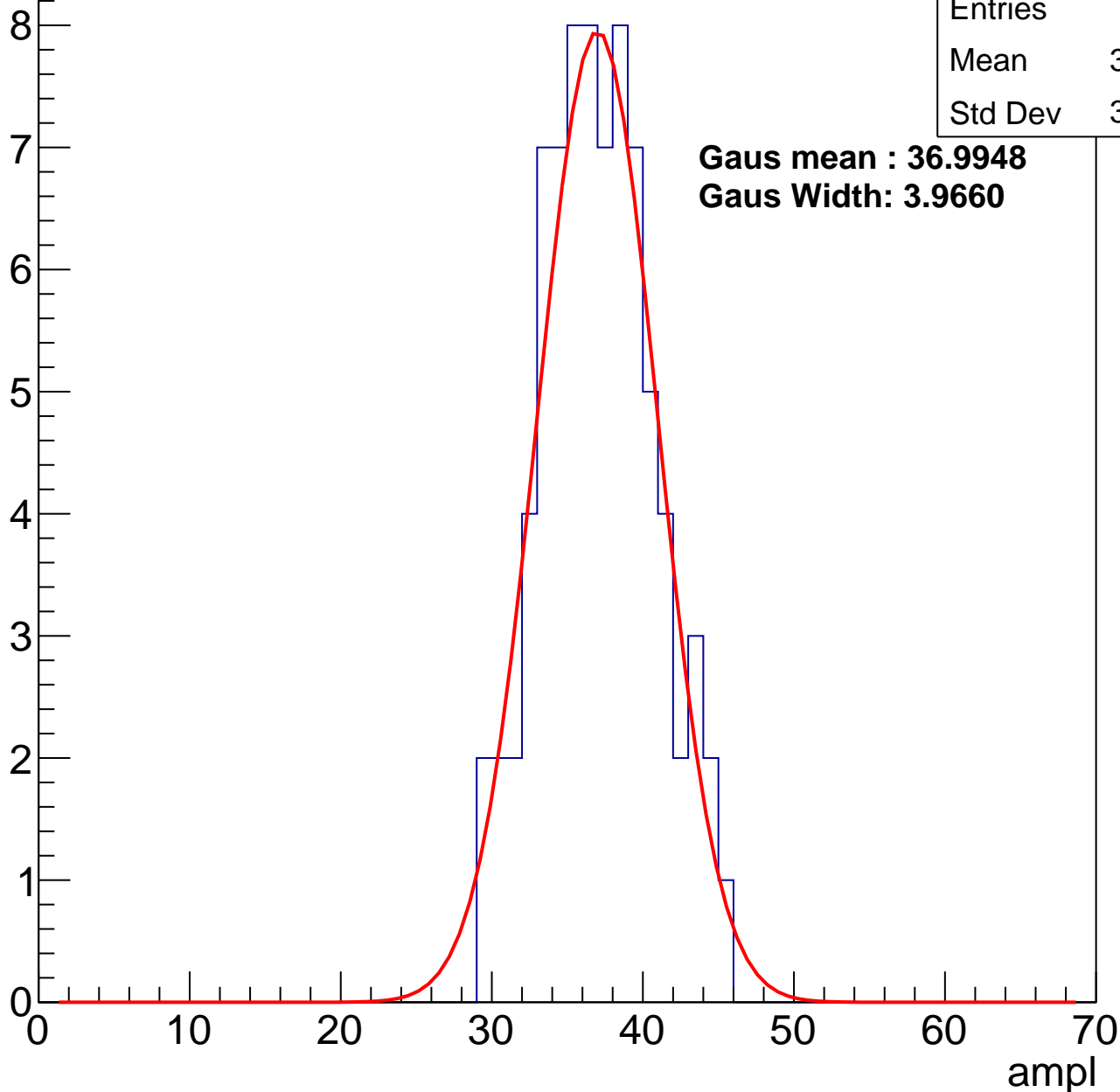
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	36.59
Std Dev	3.672

Gaus mean : 36.9948

Gaus Width: 3.9660



B1L103S, U12-ch109, adc2

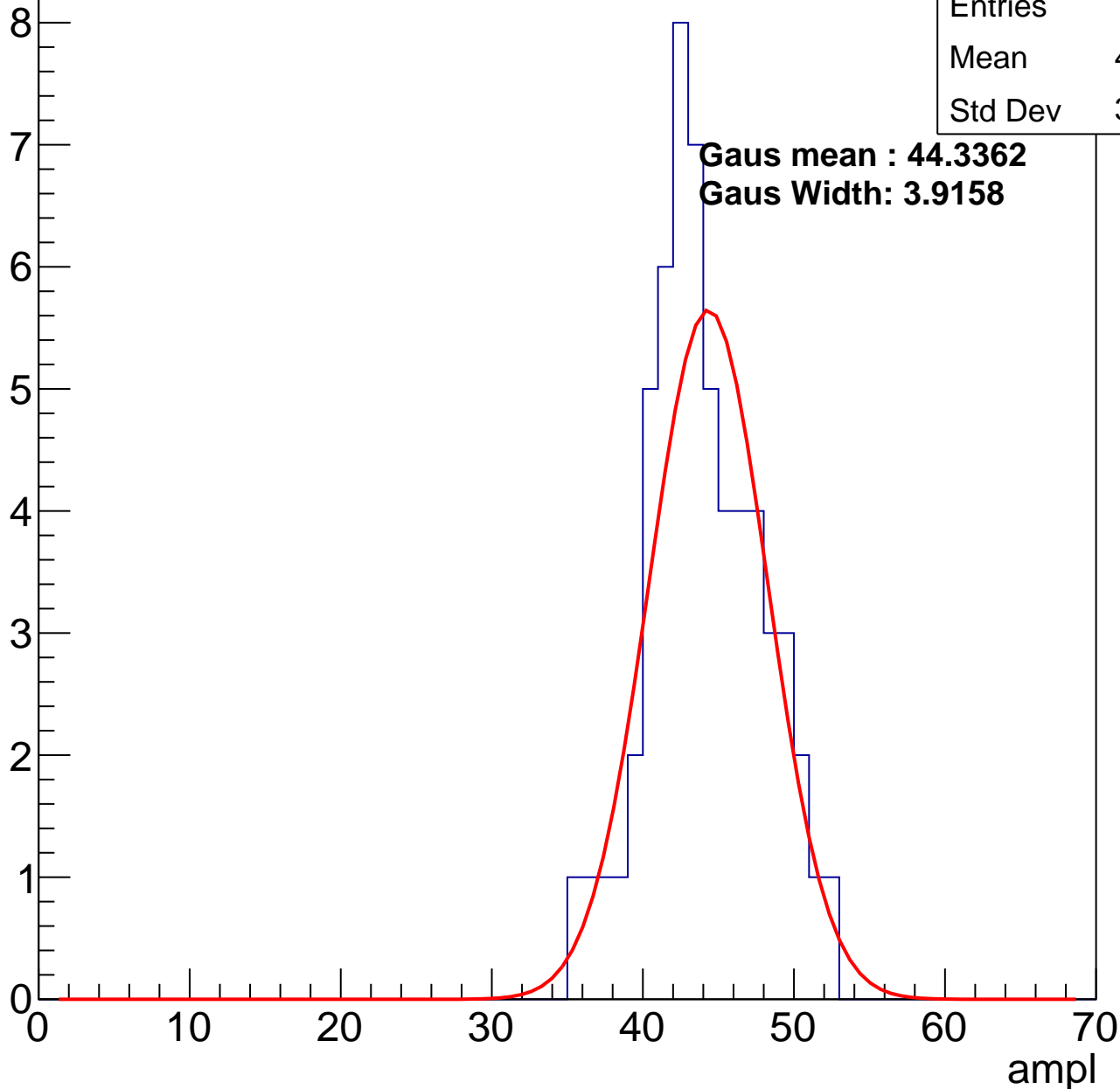
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	43.61
Std Dev	3.701

Gaus mean : 44.3362

Gaus Width: 3.9158

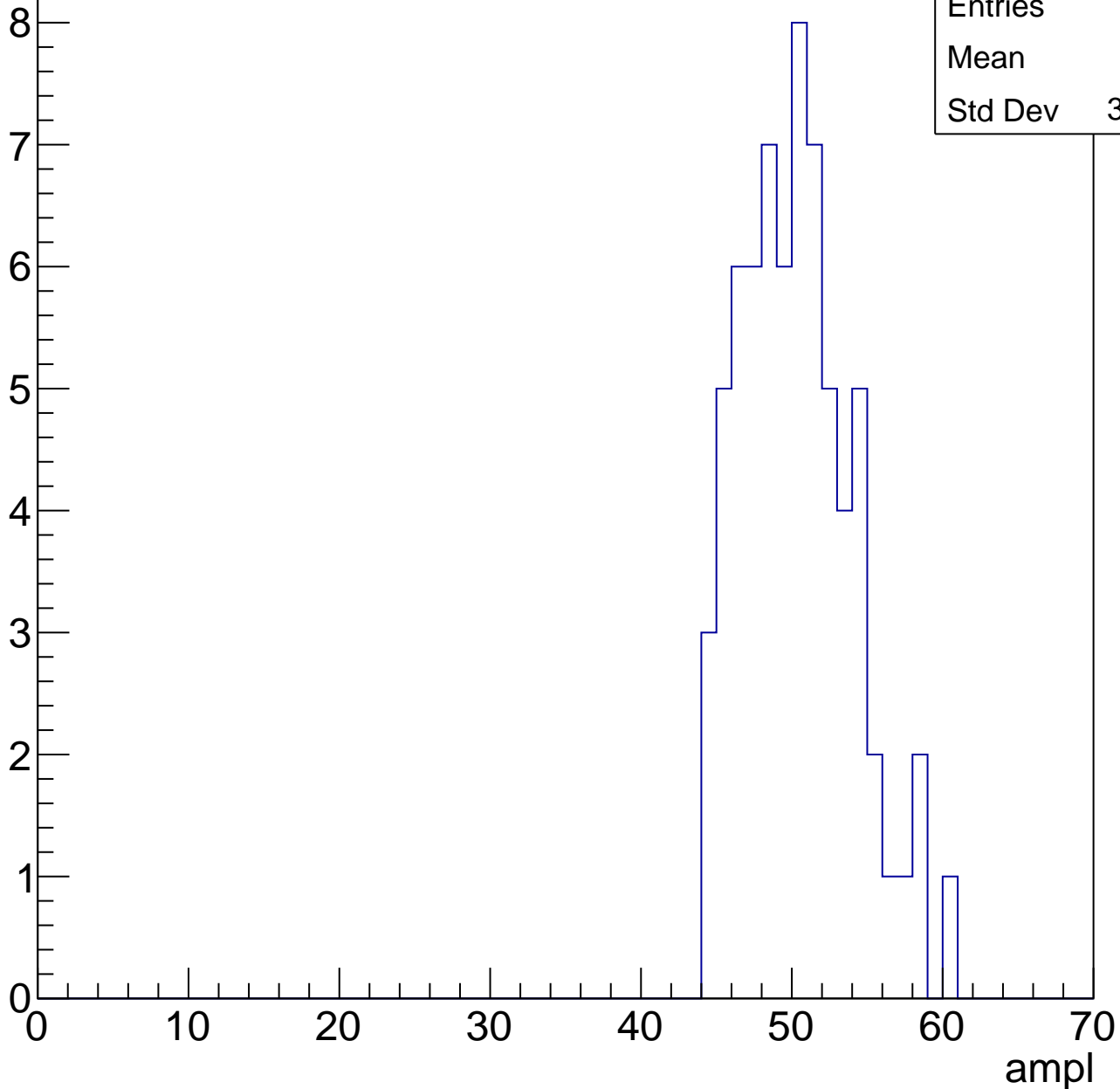


B1L103S, U12-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.9
Std Dev	3.656

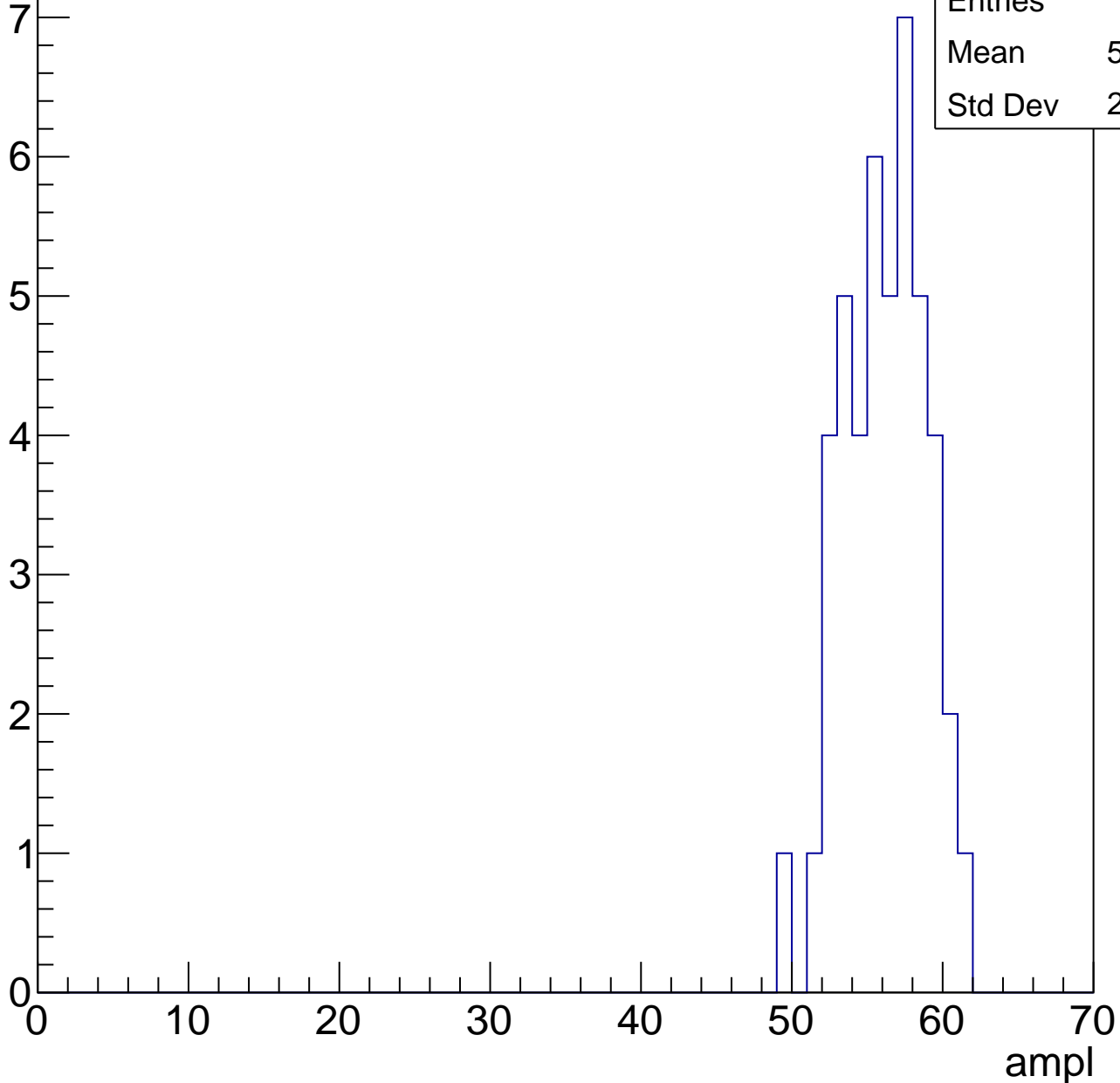


B1L103S, U12-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.67
Std Dev	2.667



B1L103S, U12-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	57
Mean	58.82
Std Dev	8.352

Entry

10

8

6

4

2

0

0

10

20

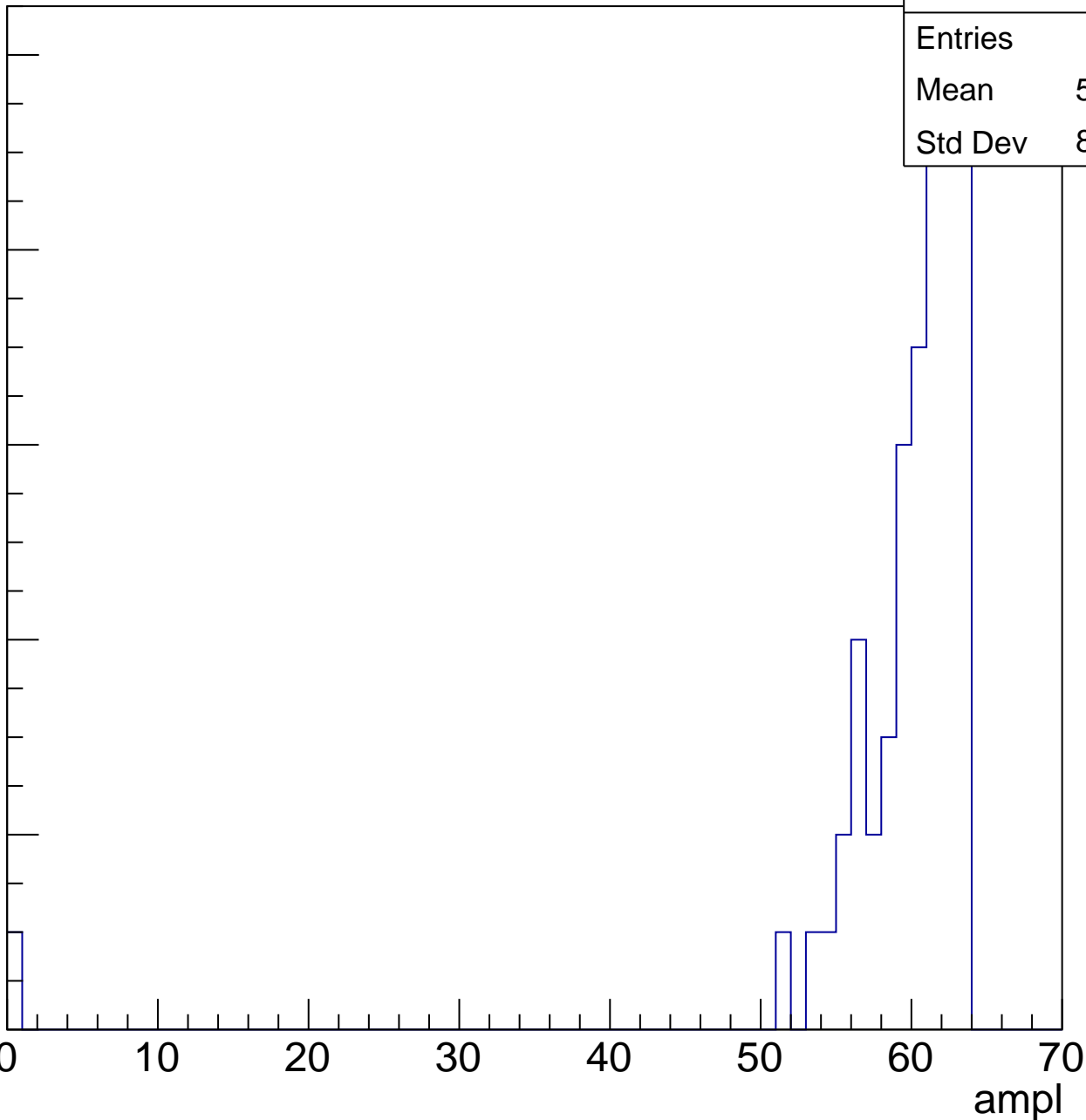
30

40

50

60

ampl



B1L103S, U12-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch110, adc0

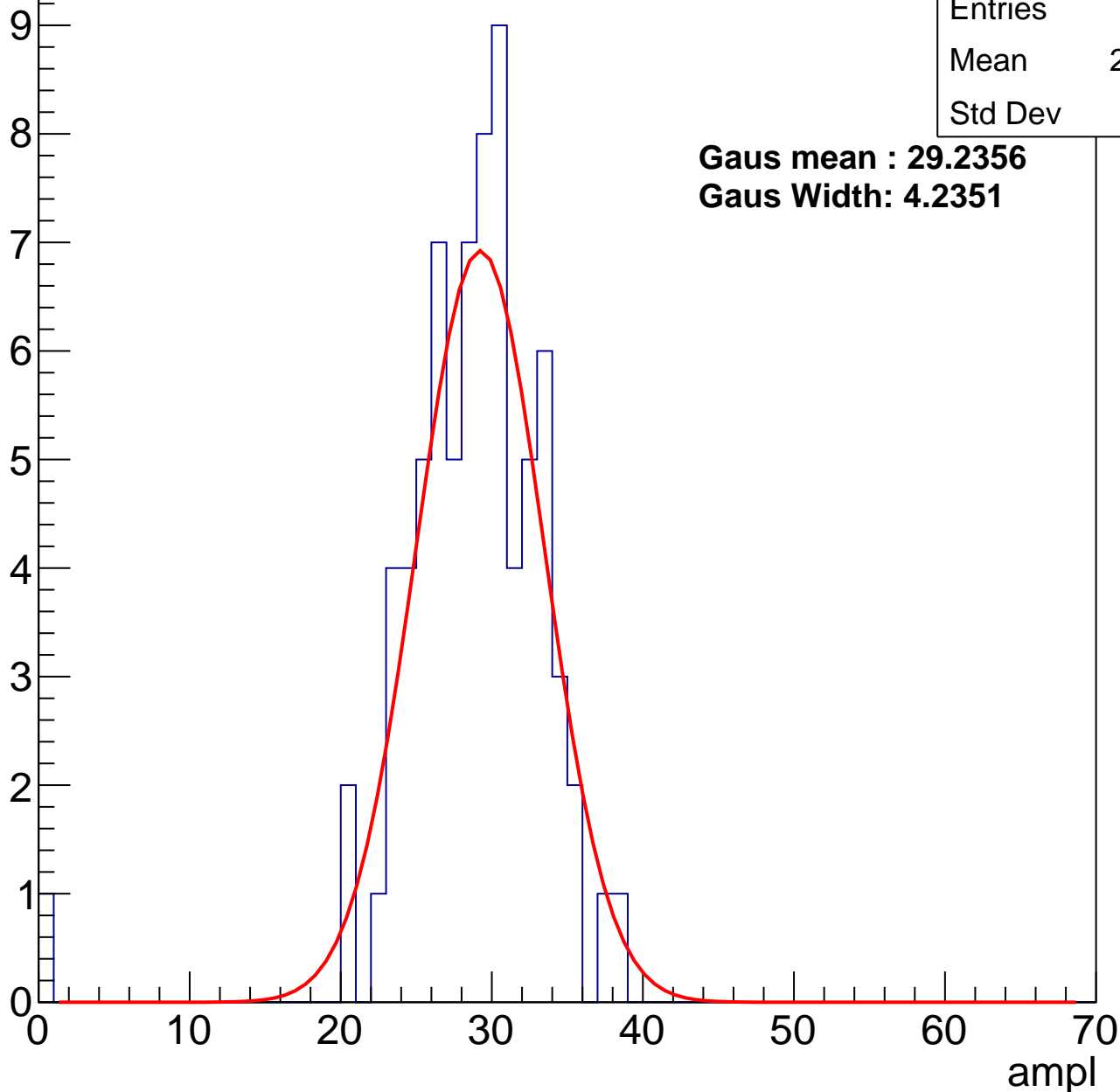
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	28.25
Std Dev	5.01

Gaus mean : 29.2356

Gaus Width: 4.2351



B1L103S, U12-ch110, adc1

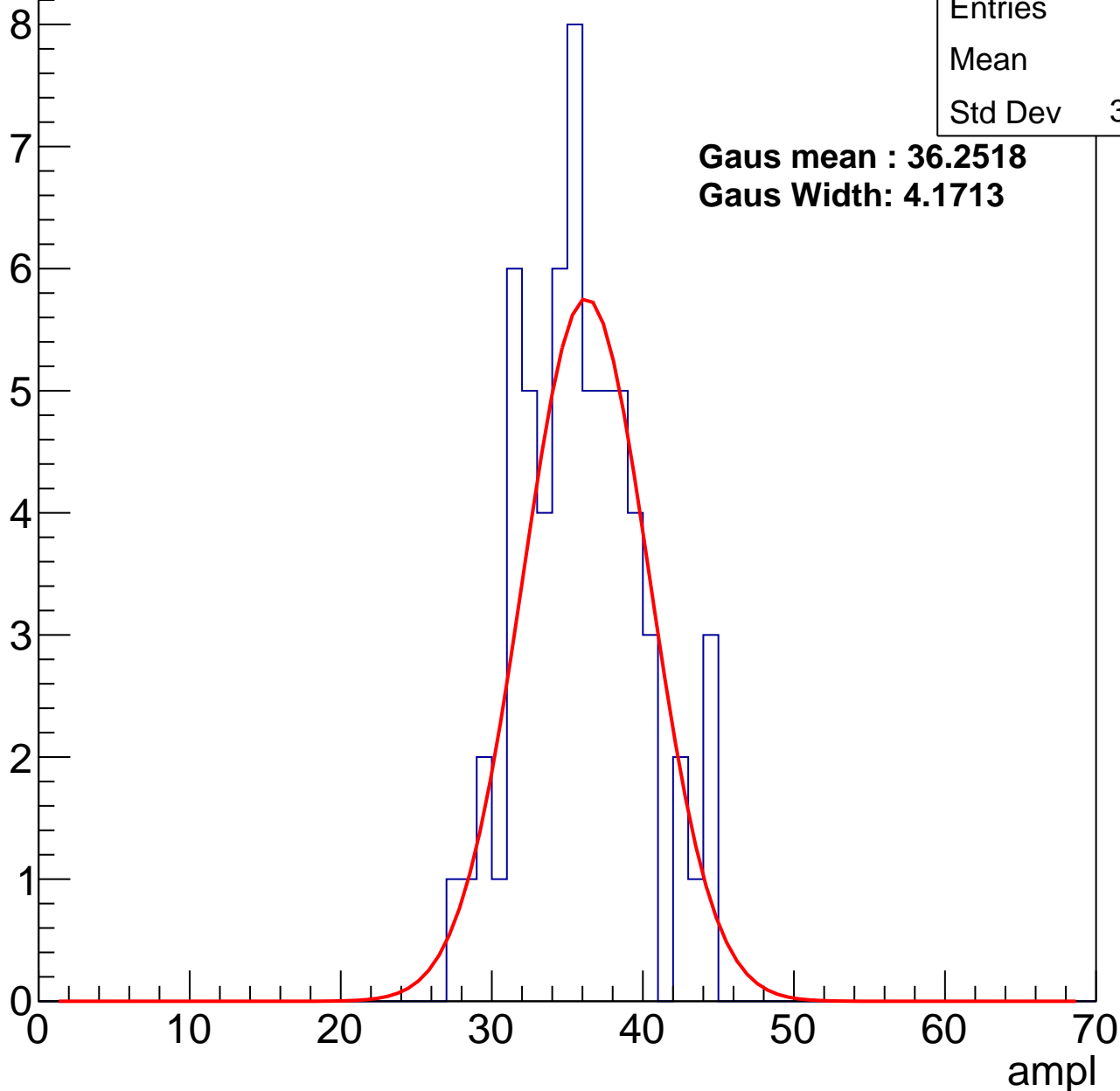
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.4
Std Dev	3.969

Gaus mean : 36.2518

Gaus Width: 4.1713



B1L103S, U12-ch110, adc2

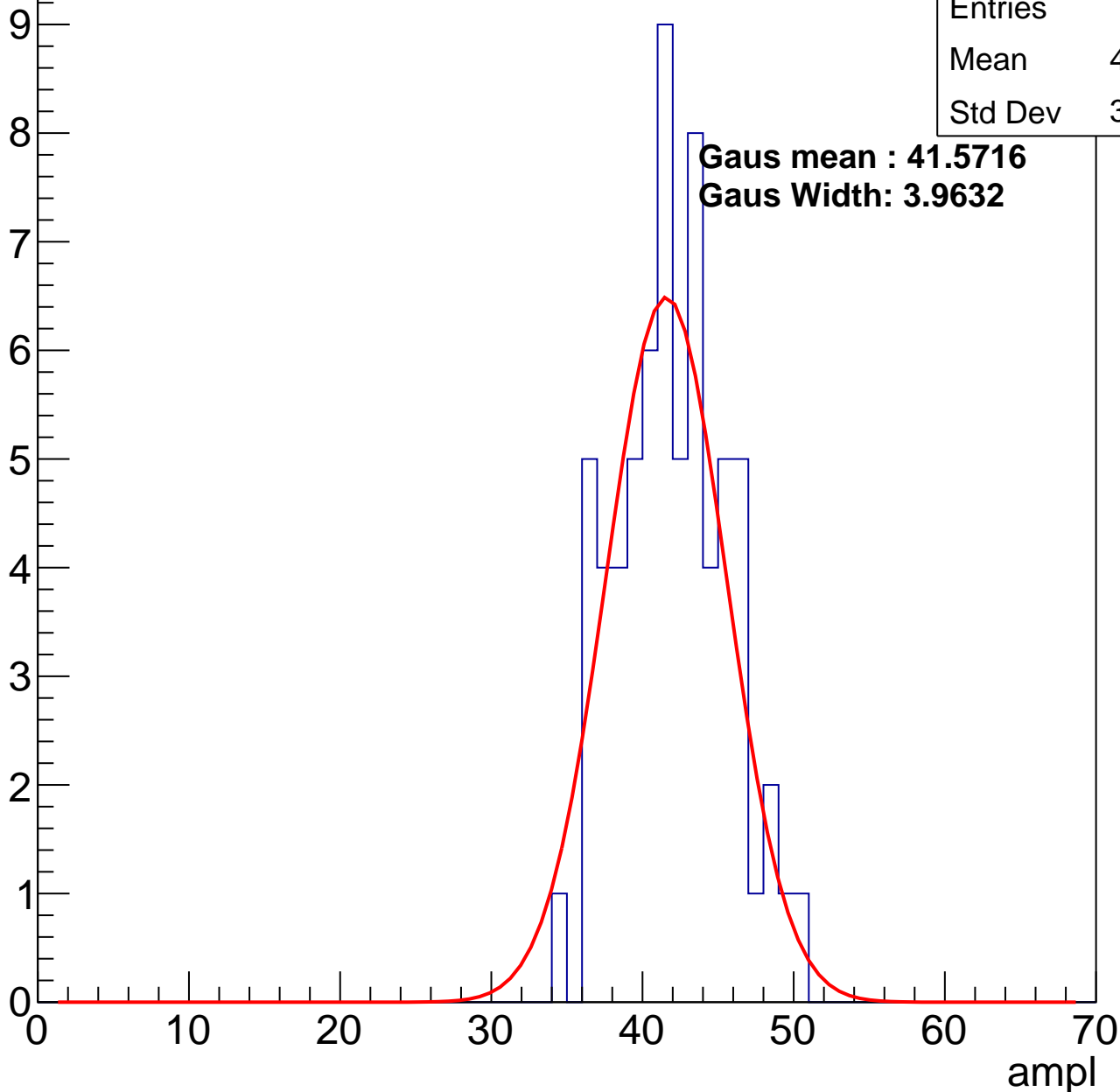
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.59
Std Dev	3.555

Gaus mean : 41.5716

Gaus Width: 3.9632

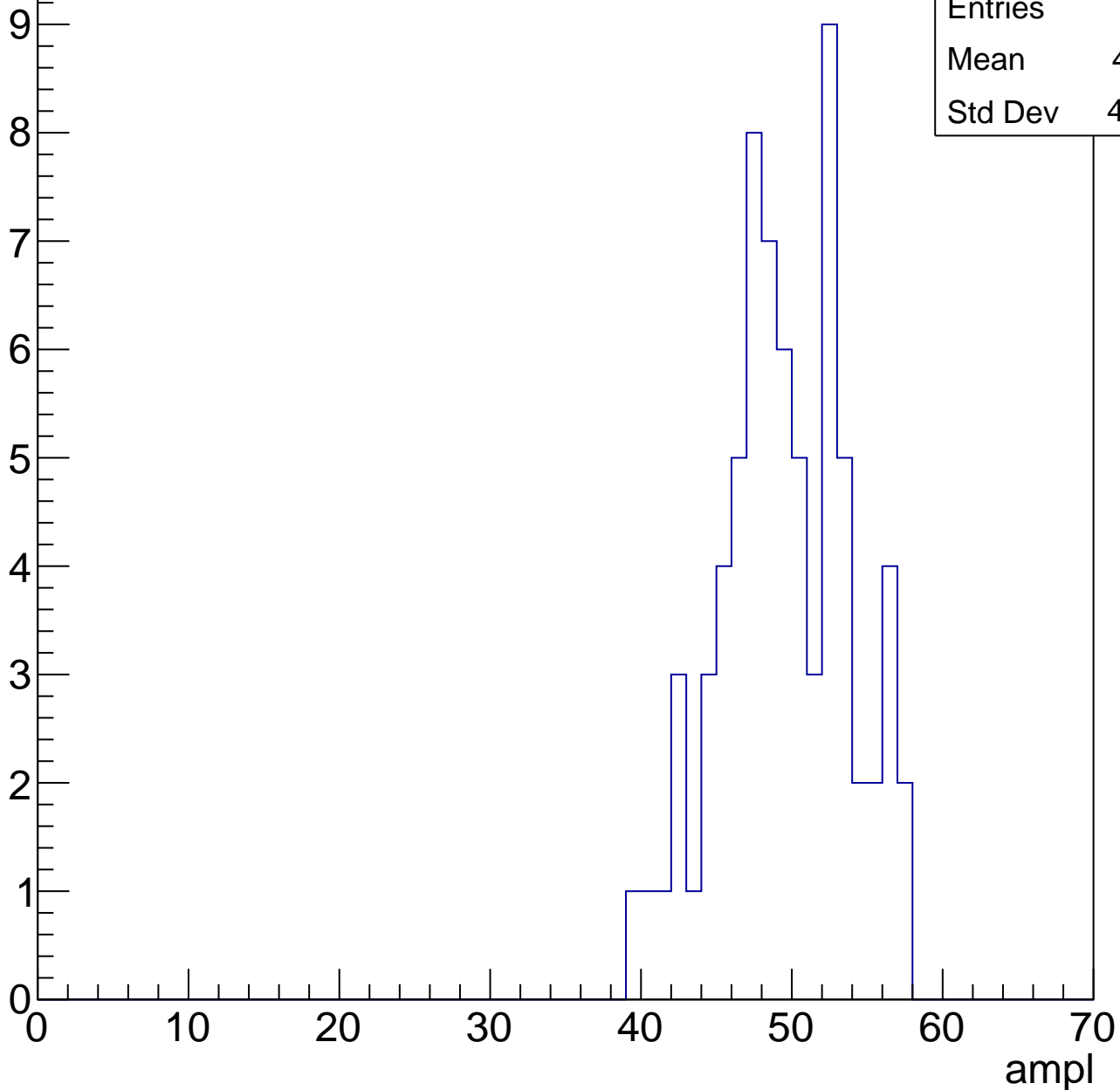


B1L103S, U12-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.01
Std Dev	4.208

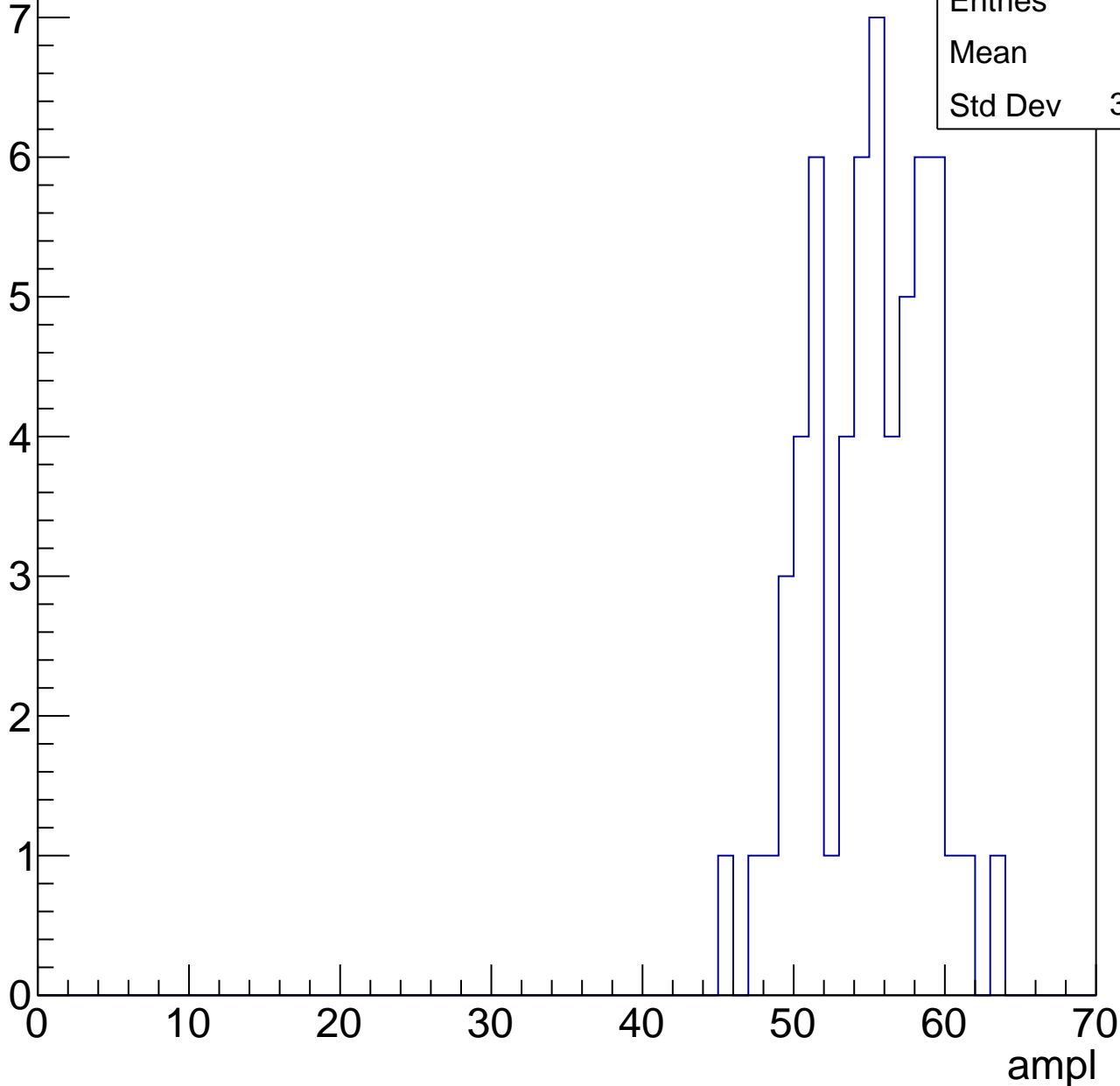


B1L103S, U12-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	54.5
Std Dev	3.789

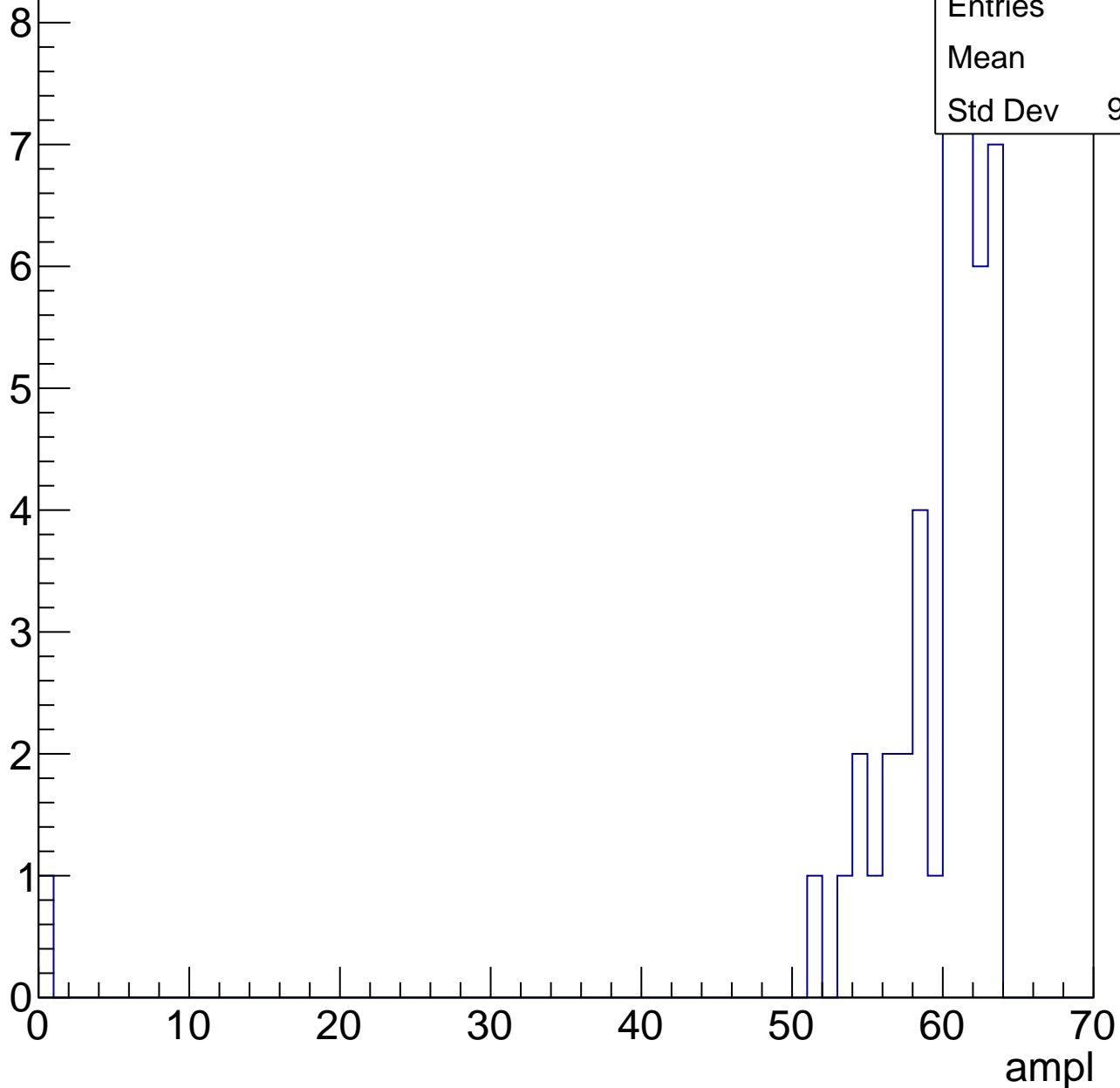


B1L103S, U12-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

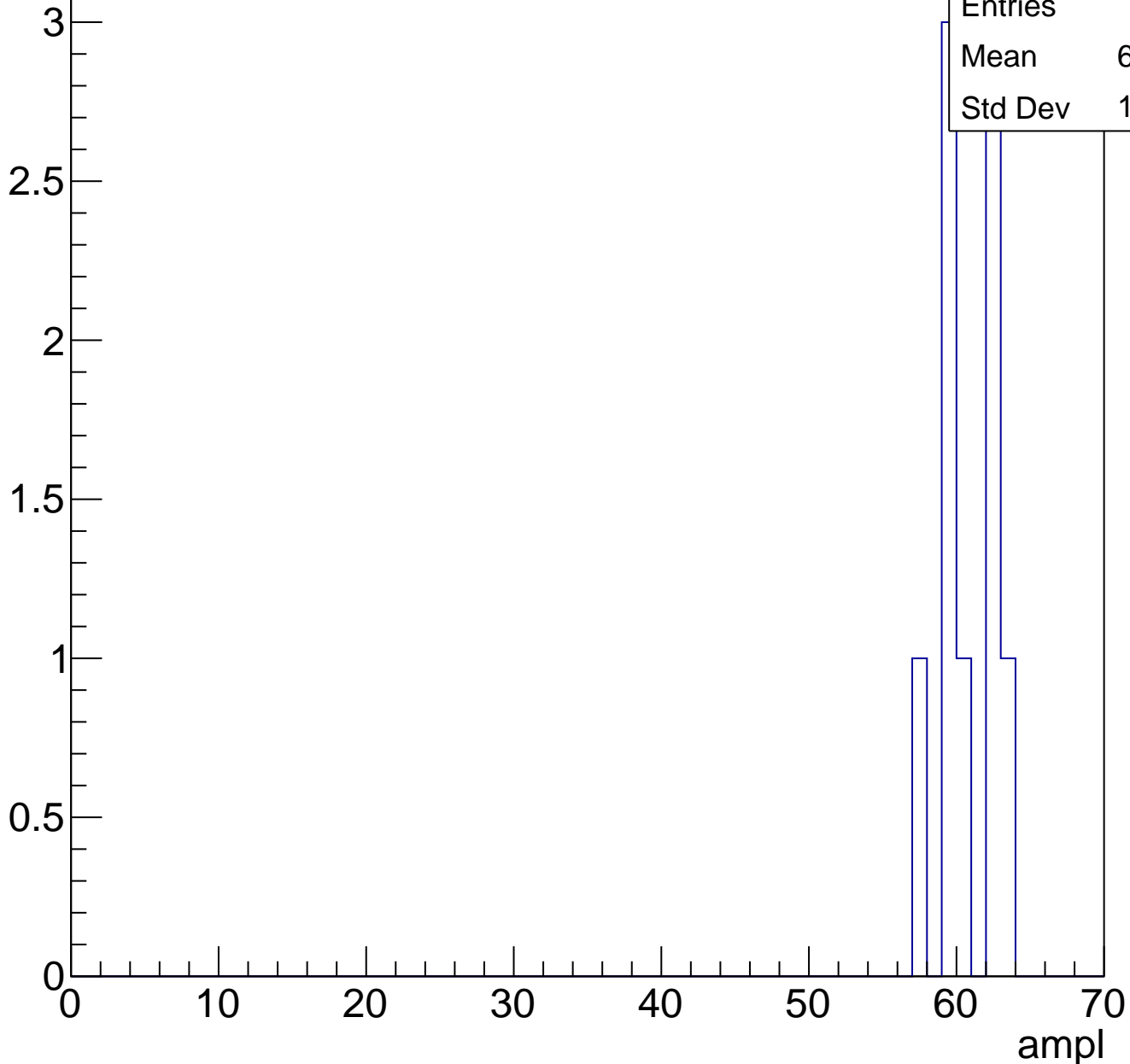
Entries	44
Mean	58.3
Std Dev	9.368



B1L103S, U12-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

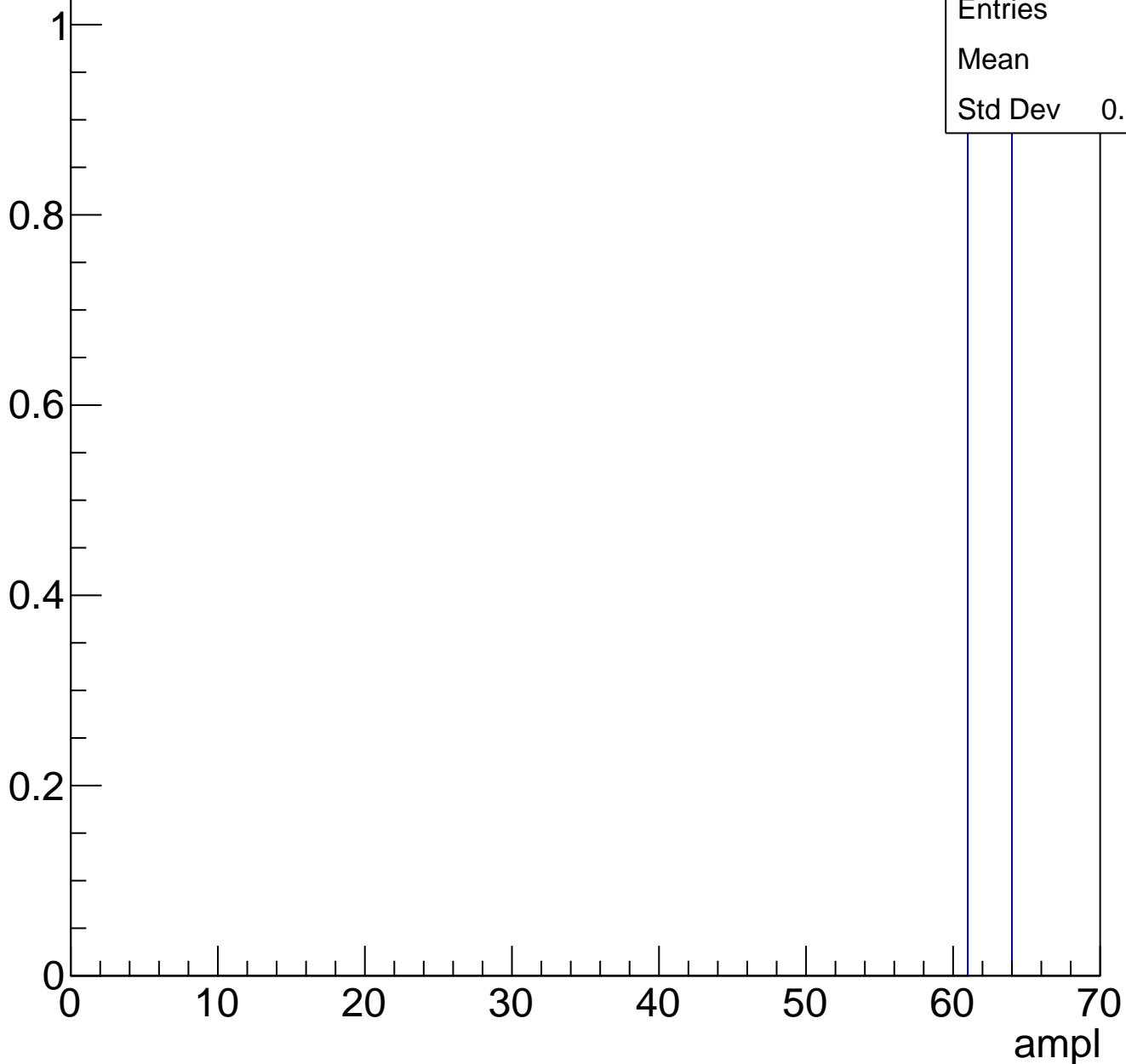
Entry



B1L103S, U12-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch111, adc0

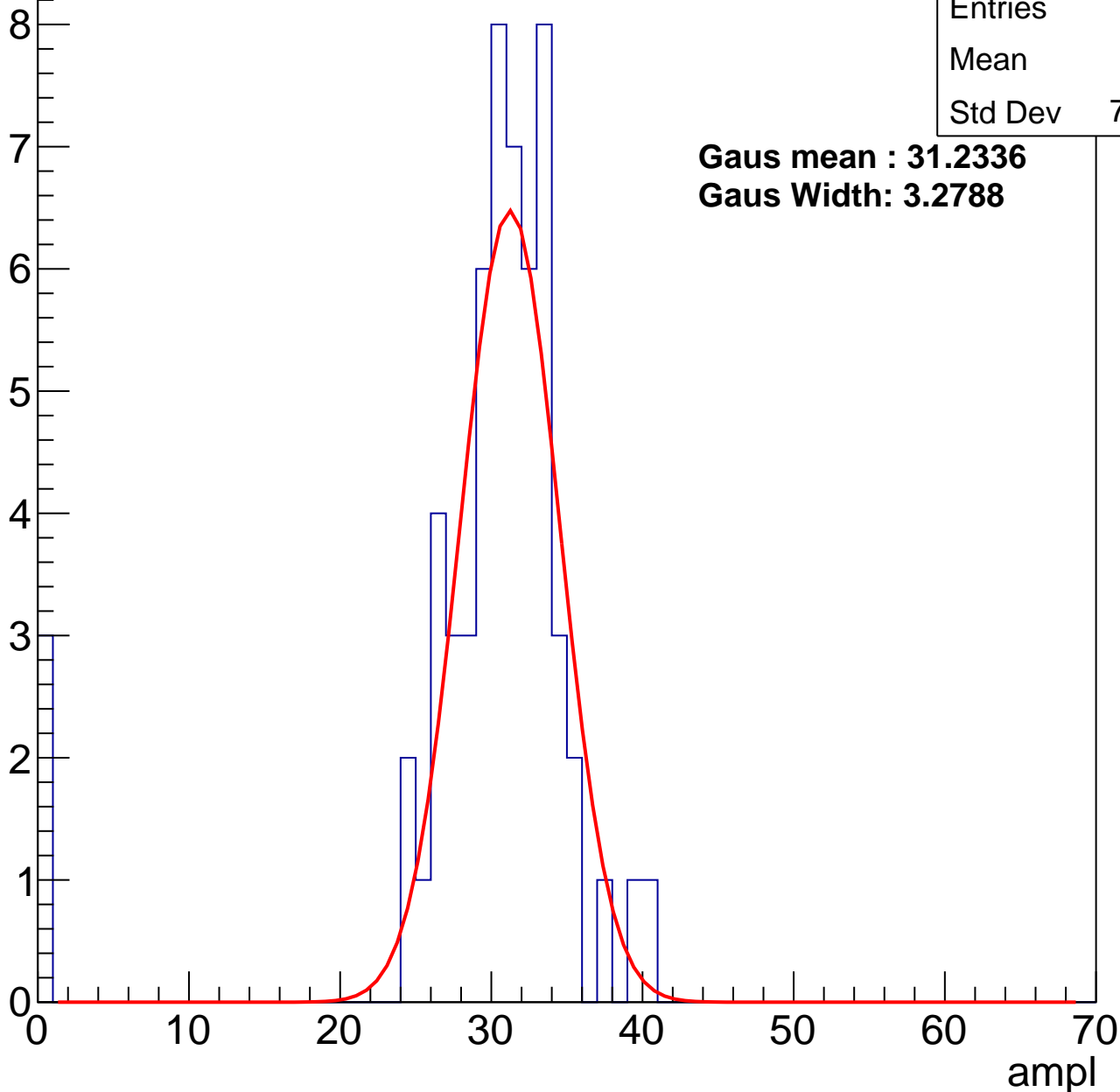
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	29.1
Std Dev	7.467

Gaus mean : 31.2336

Gaus Width: 3.2788



B1L103S, U12-ch111, adc1

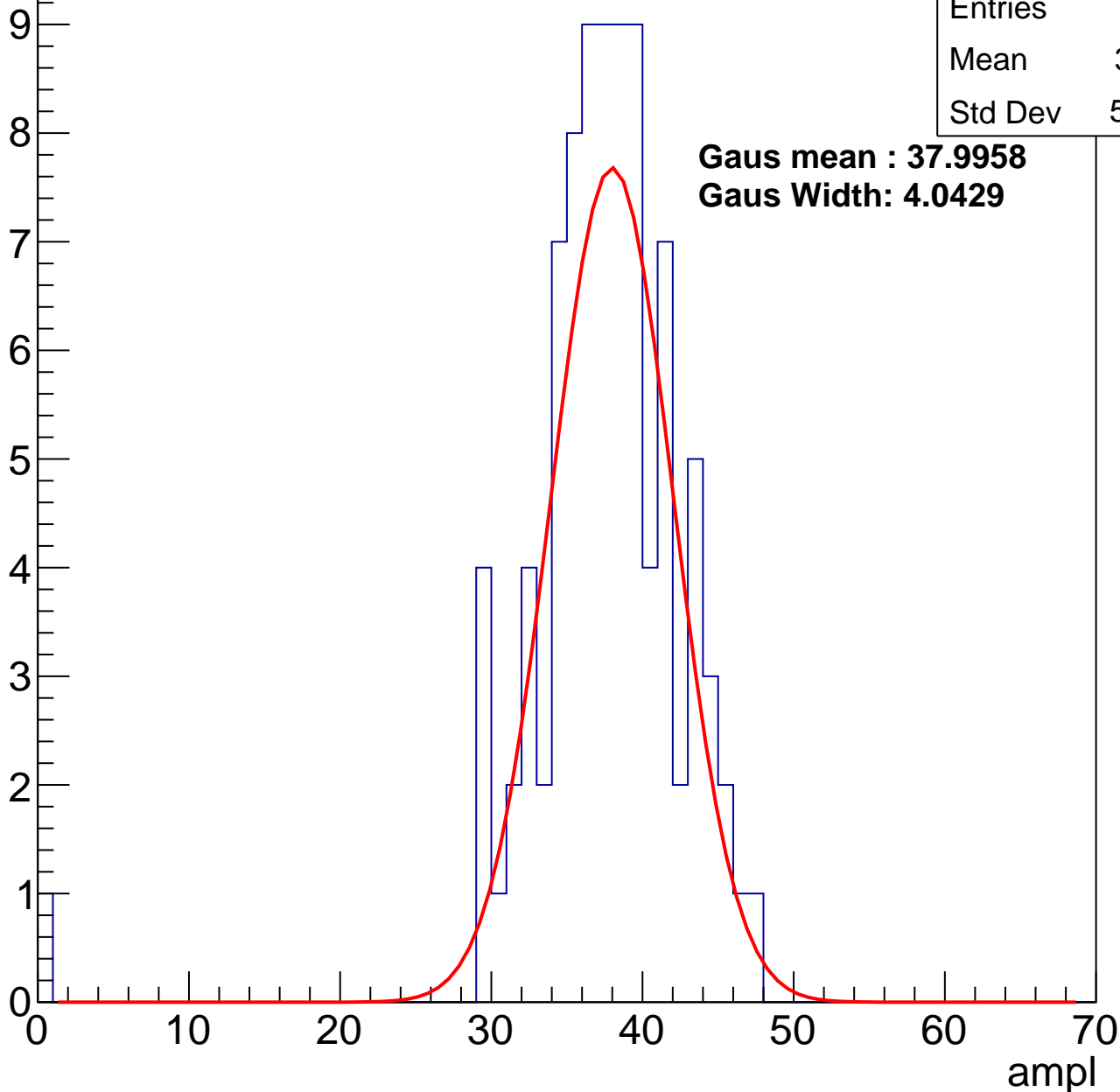
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	37.01
Std Dev	5.638

Gaus mean : 37.9958

Gaus Width: 4.0429



B1L103S, U12-ch111, adc2

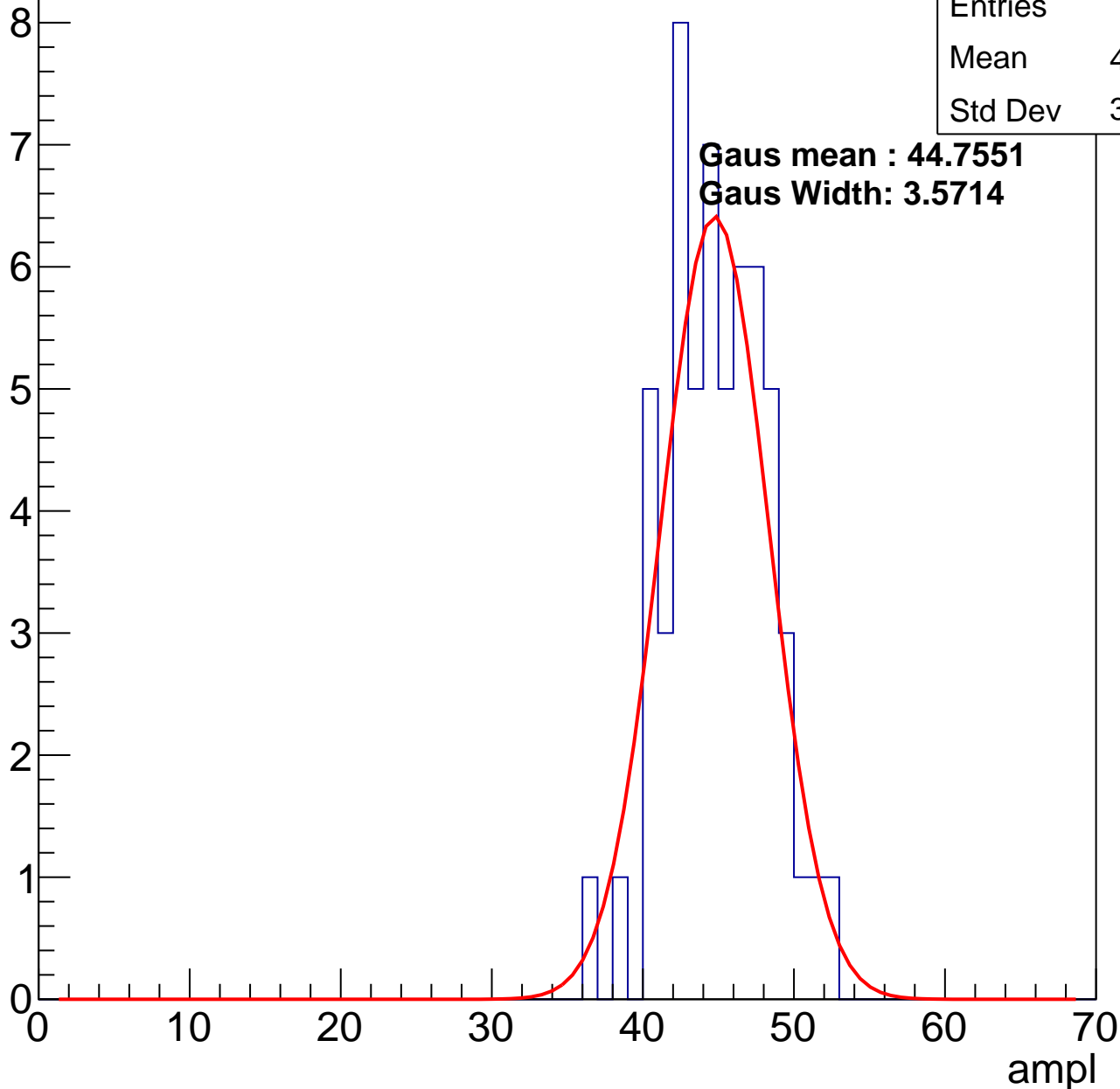
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.47
Std Dev	3.265

Gaus mean : 44.7551

Gaus Width: 3.5714

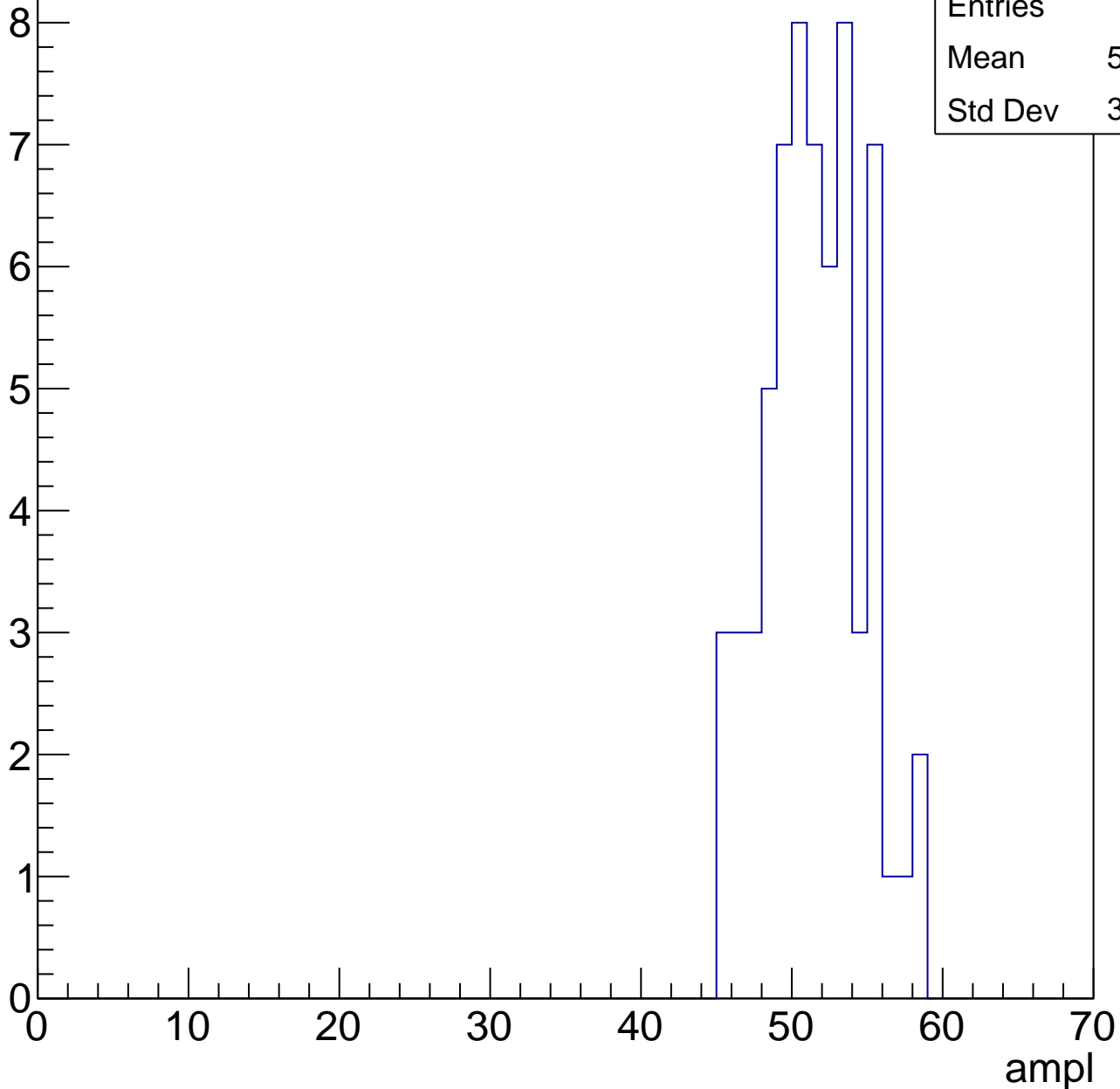


B1L103S, U12-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	51.03
Std Dev	3.182



B1L103S, U12-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

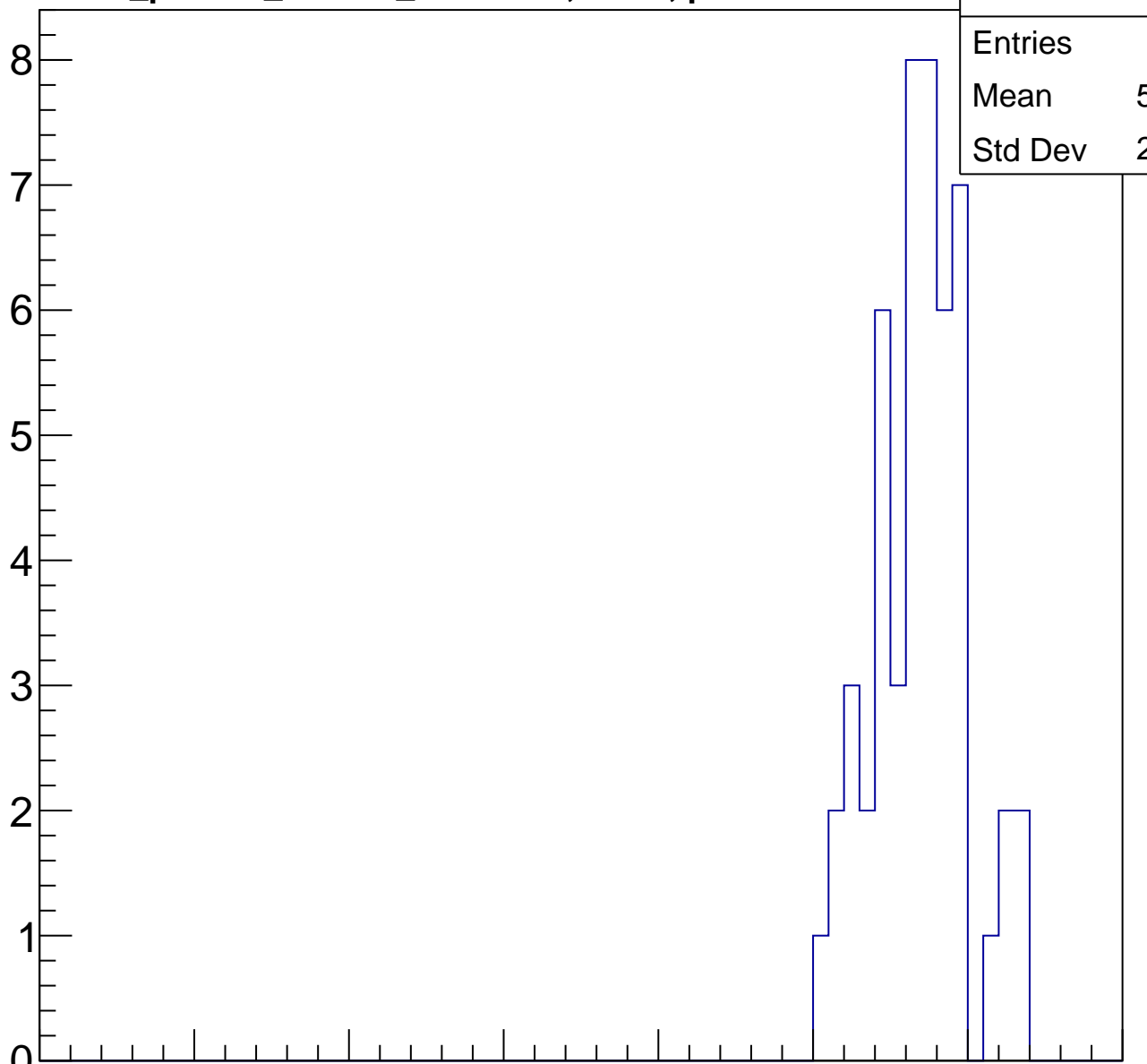
Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	56.45
Std Dev	2.999

ampl

0 10 20 30 40 50 60 70

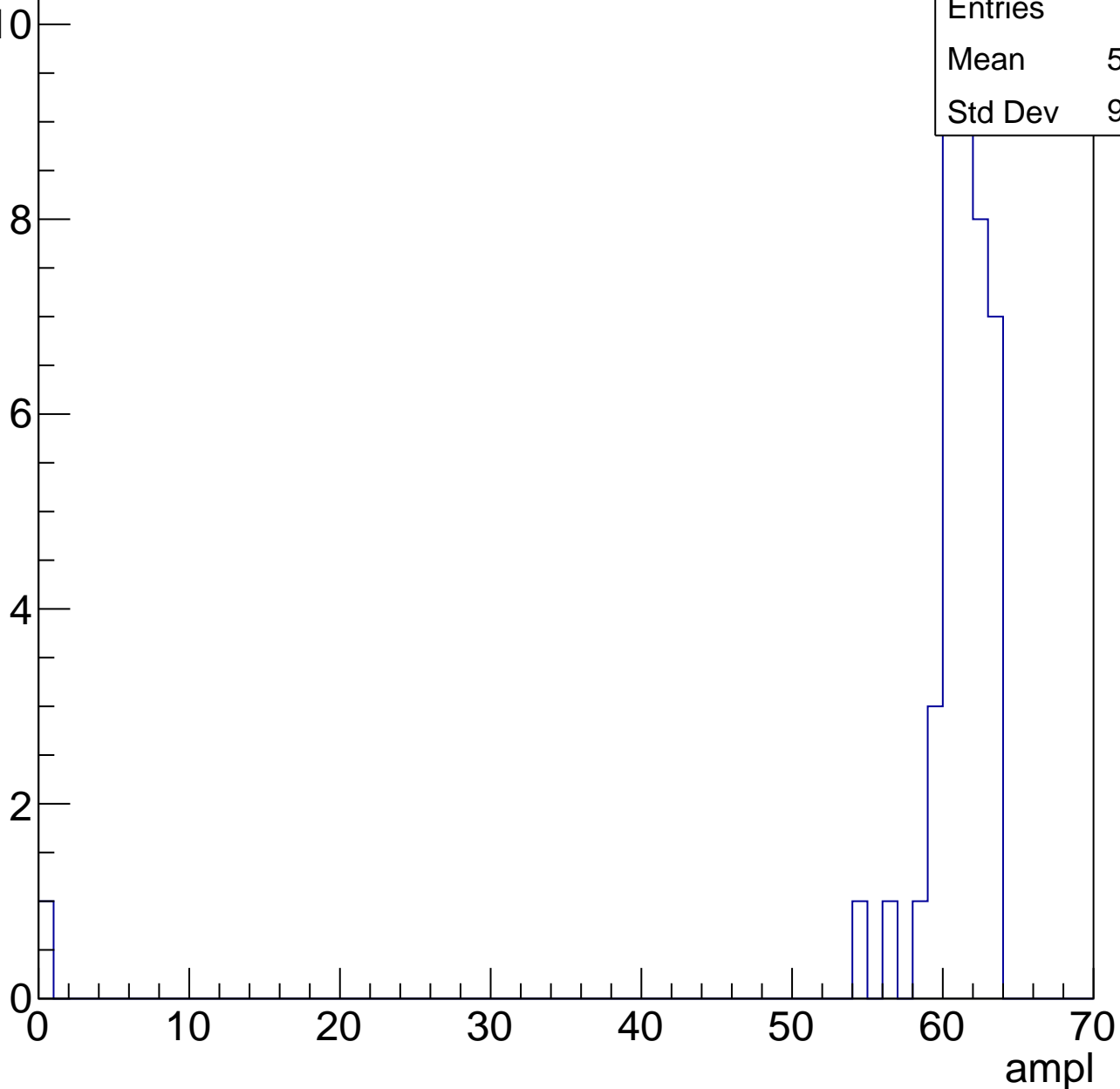


B1L103S, U12-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	59.29
Std Dev	9.554



B1L103S, U12-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

59.33

Std Dev

2.625

B1L103S, U12-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch112, adc0

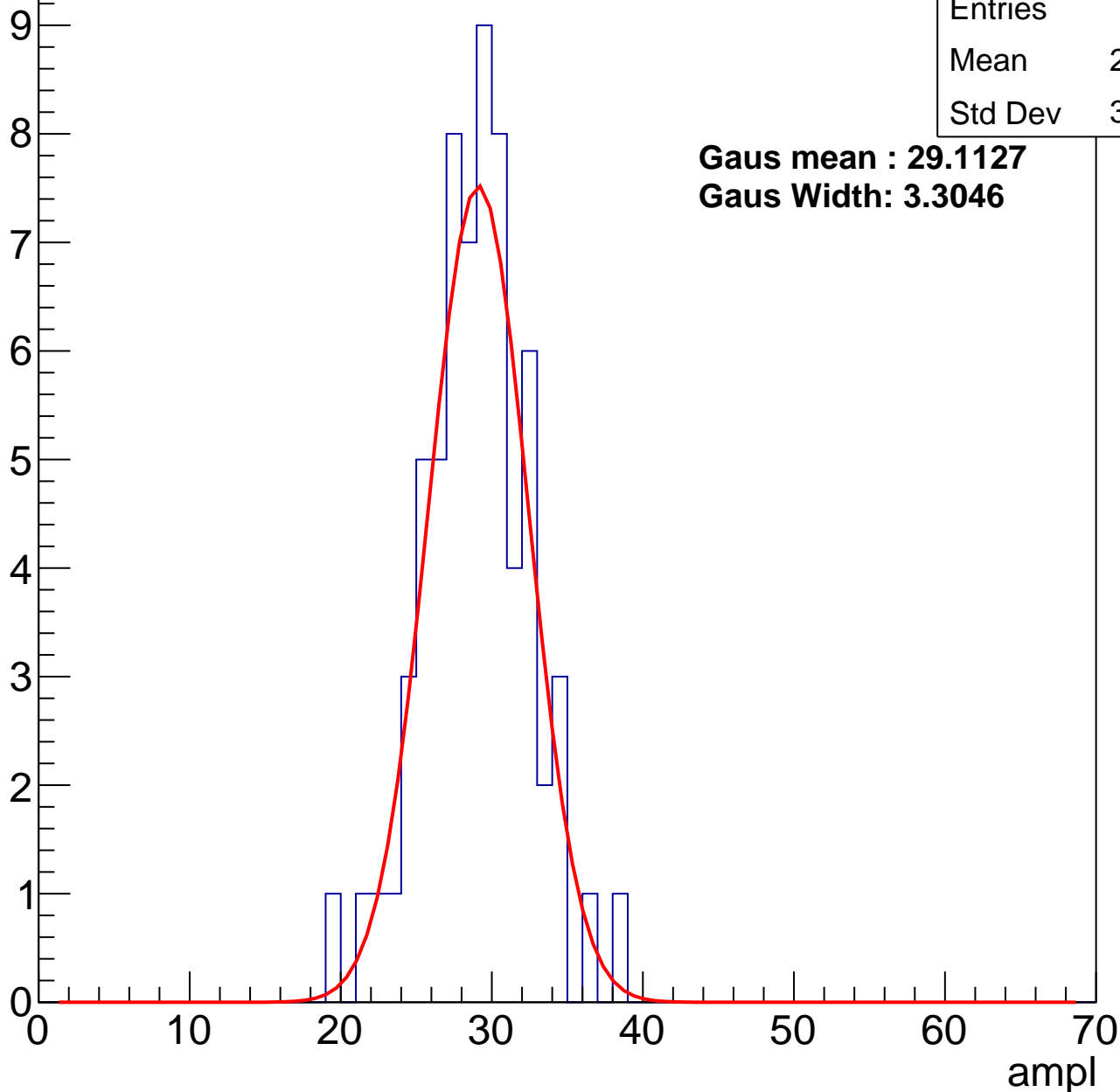
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	28.53
Std Dev	3.456

Gaus mean : 29.1127

Gaus Width: 3.3046



B1L103S, U12-ch112, adc1

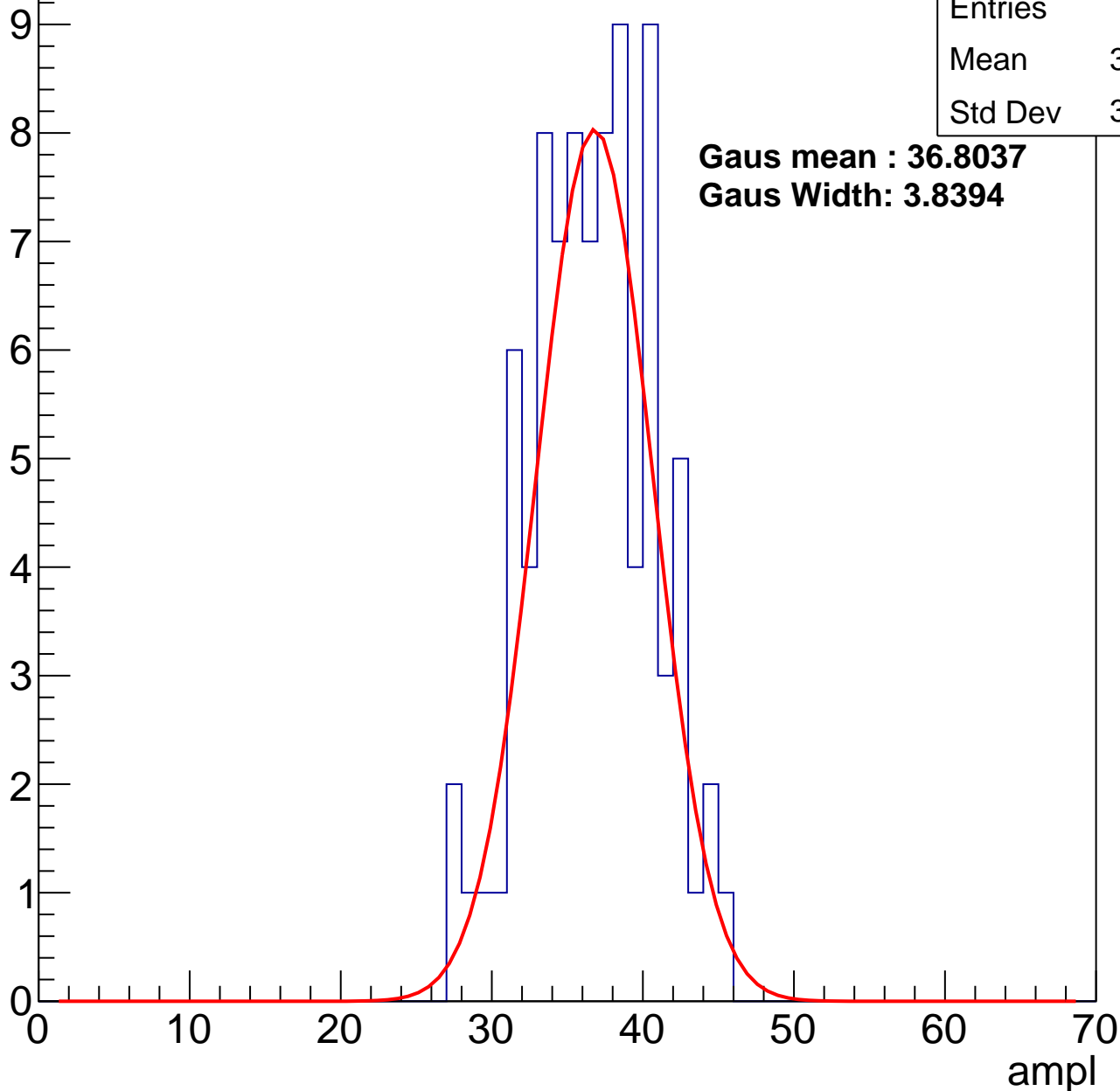
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	36.23
Std Dev	3.962

Gaus mean : 36.8037

Gaus Width: 3.8394



B1L103S, U12-ch112, adc2

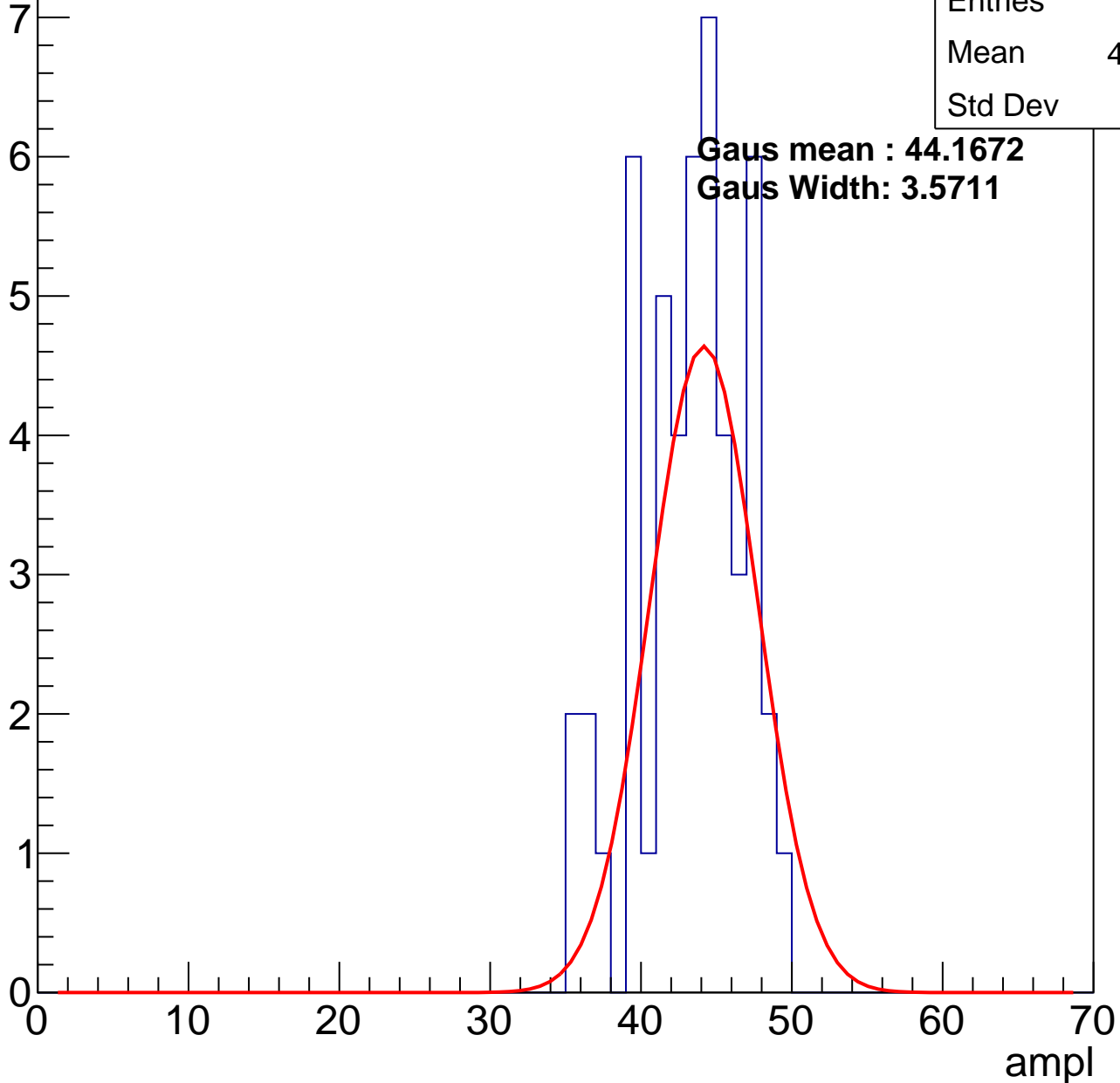
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.74
Std Dev	3.52

Gaus mean : 44.1672

Gaus Width: 3.5711

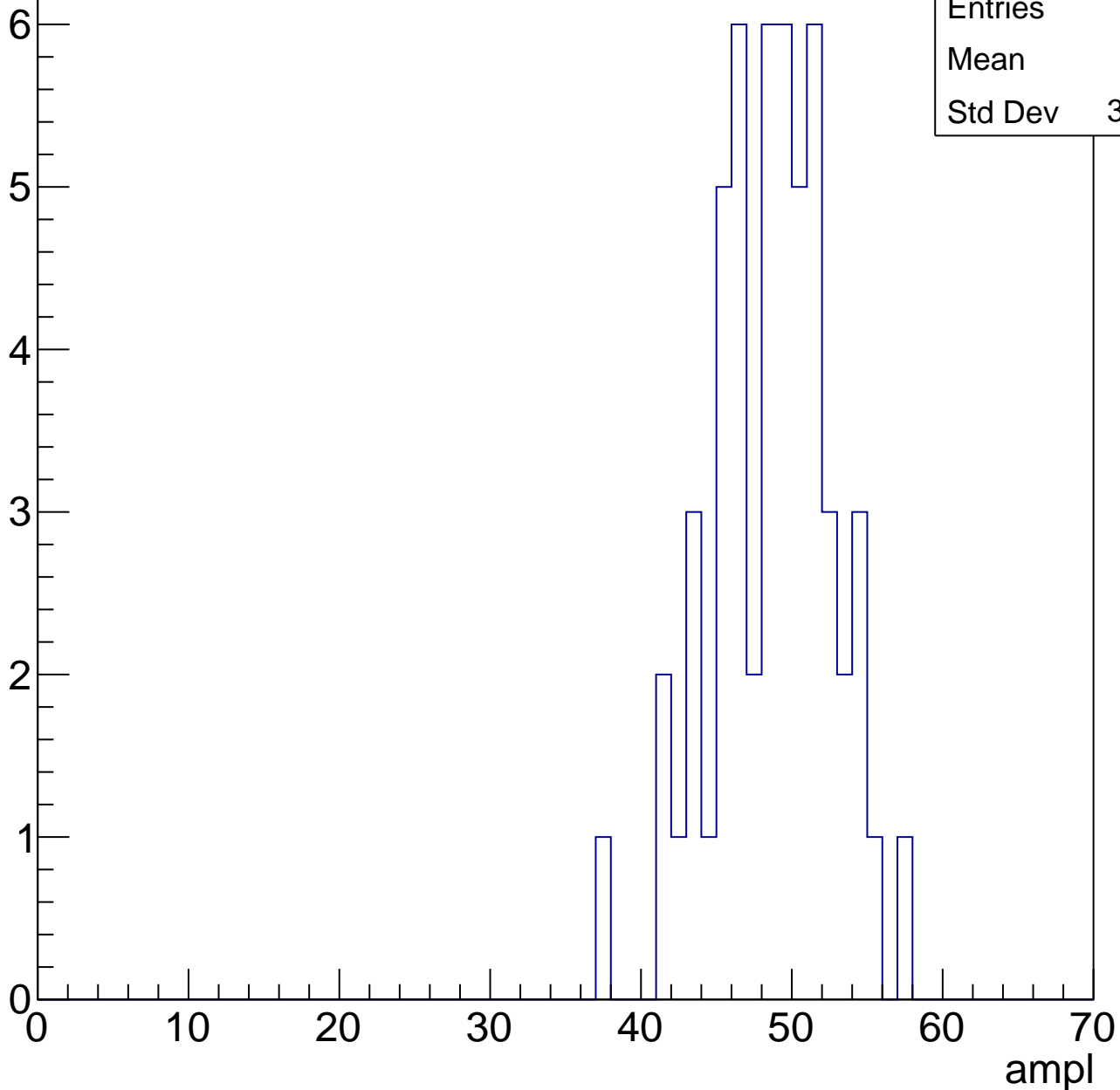


B1L103S, U12-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

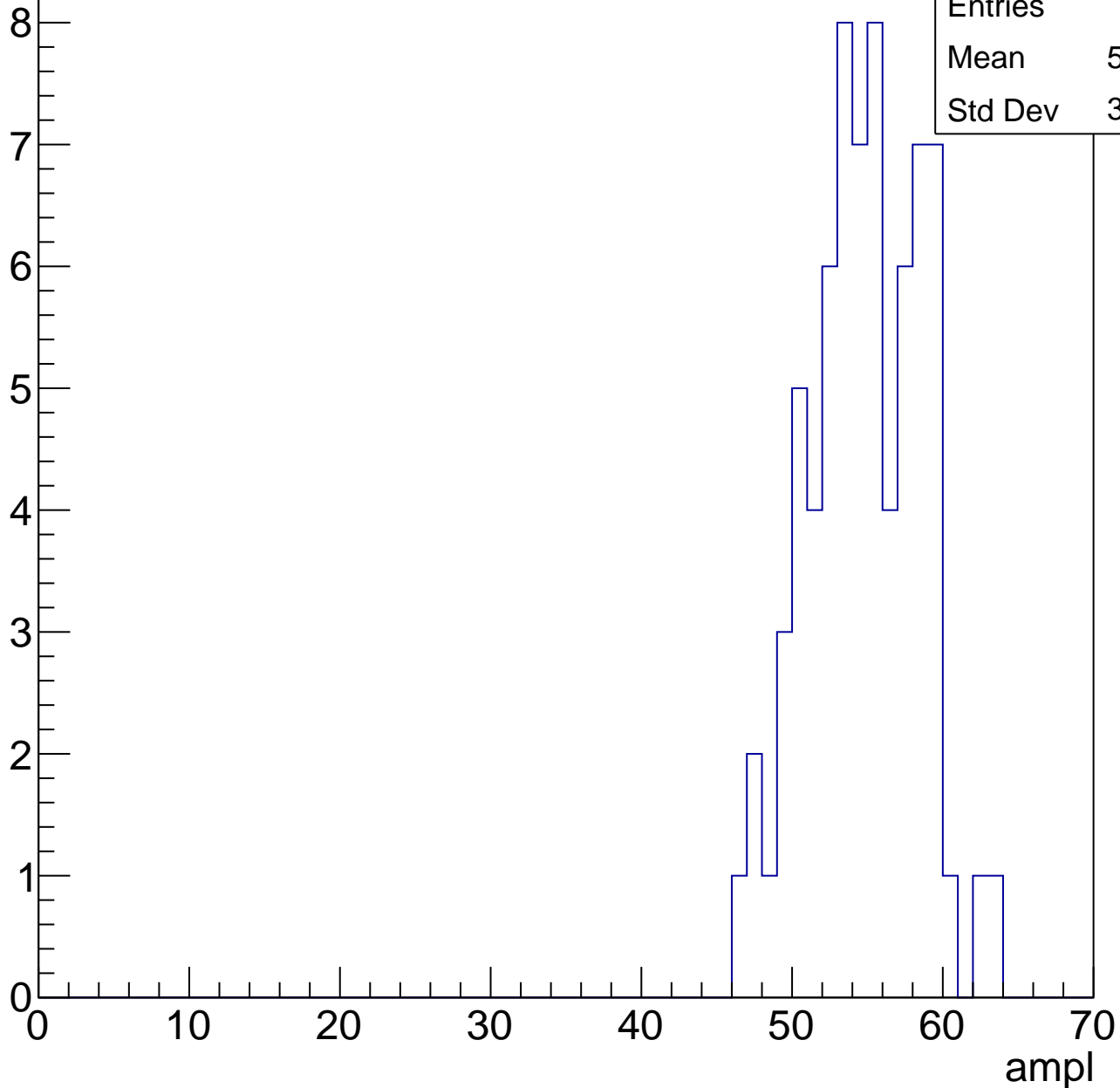
Entries	54
Mean	48.2
Std Dev	3.922



B1L103S, U12-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

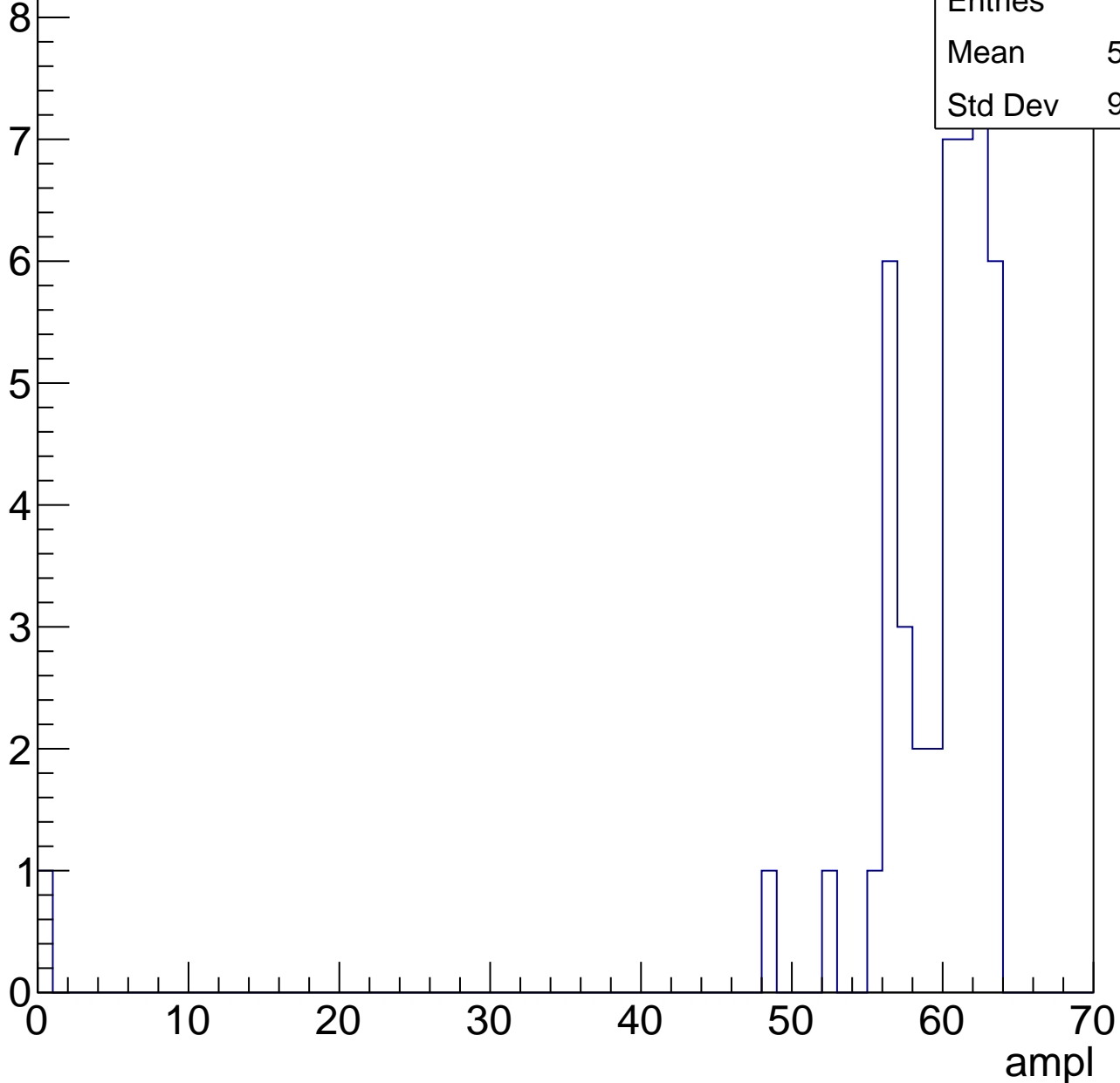


B1L103S, U12-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.16
Std Dev	9.317



B1L103S, U12-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

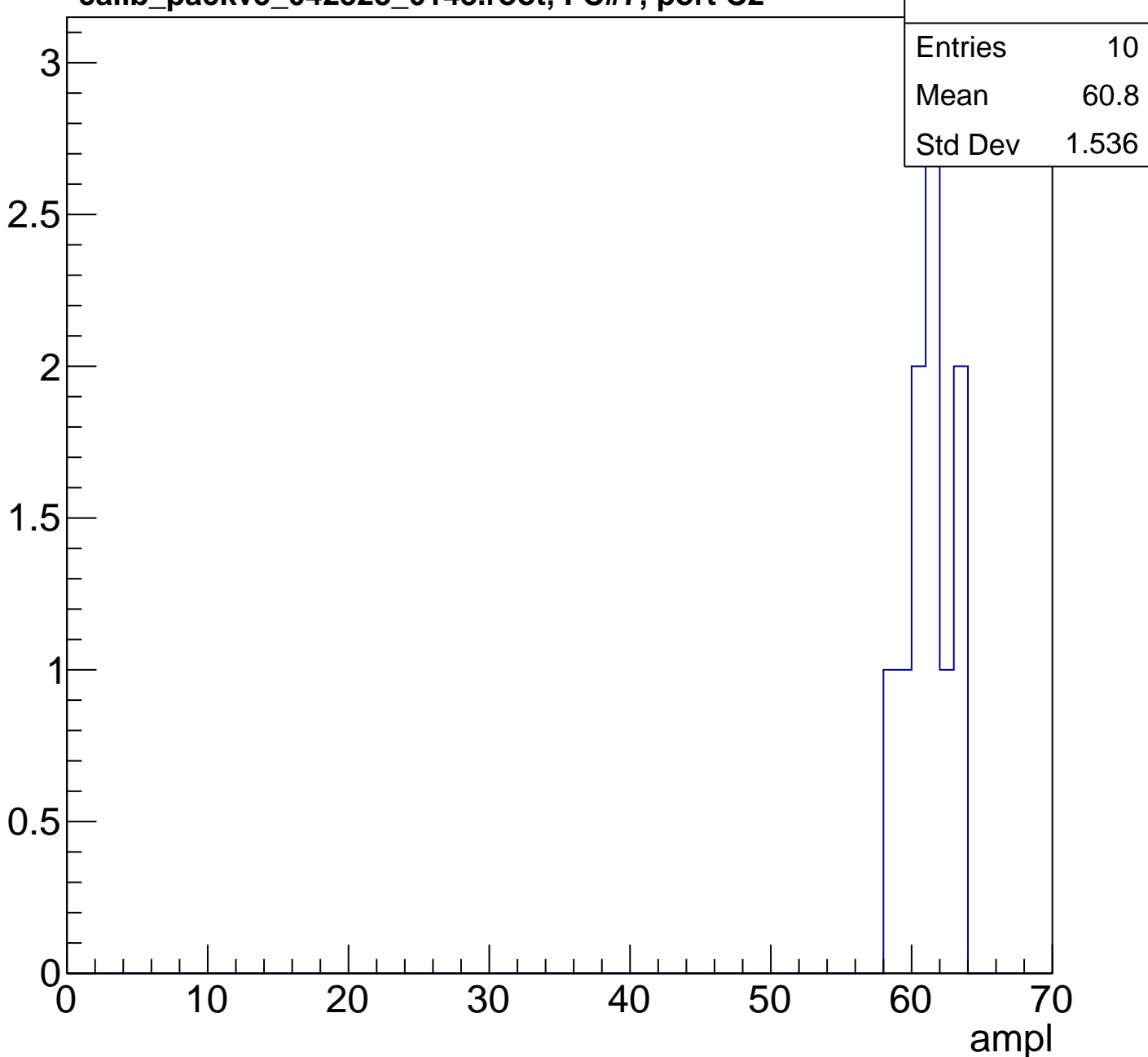
50

60

70

ampl

Entries	10
Mean	60.8
Std Dev	1.536



B1L103S, U12-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U12-ch113, adc0

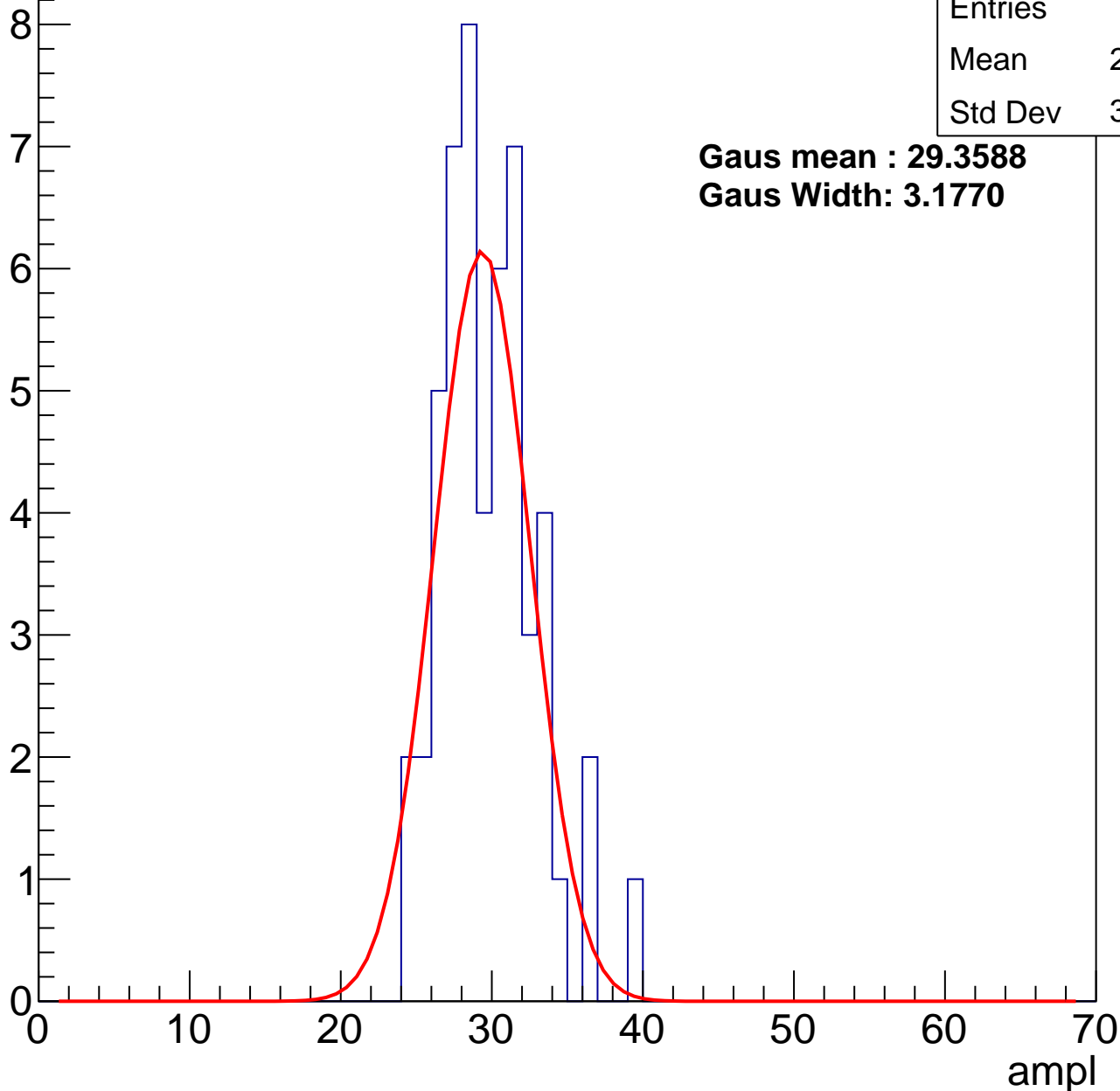
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	29.37
Std Dev	3.113

Gaus mean : 29.3588

Gaus Width: 3.1770



B1L103S, U12-ch113, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	36.37
Std Dev	3.679

Gaus mean : 36.1862

Gaus Width: 4.1624

10

8

6

4

2

0

0

10

20

30

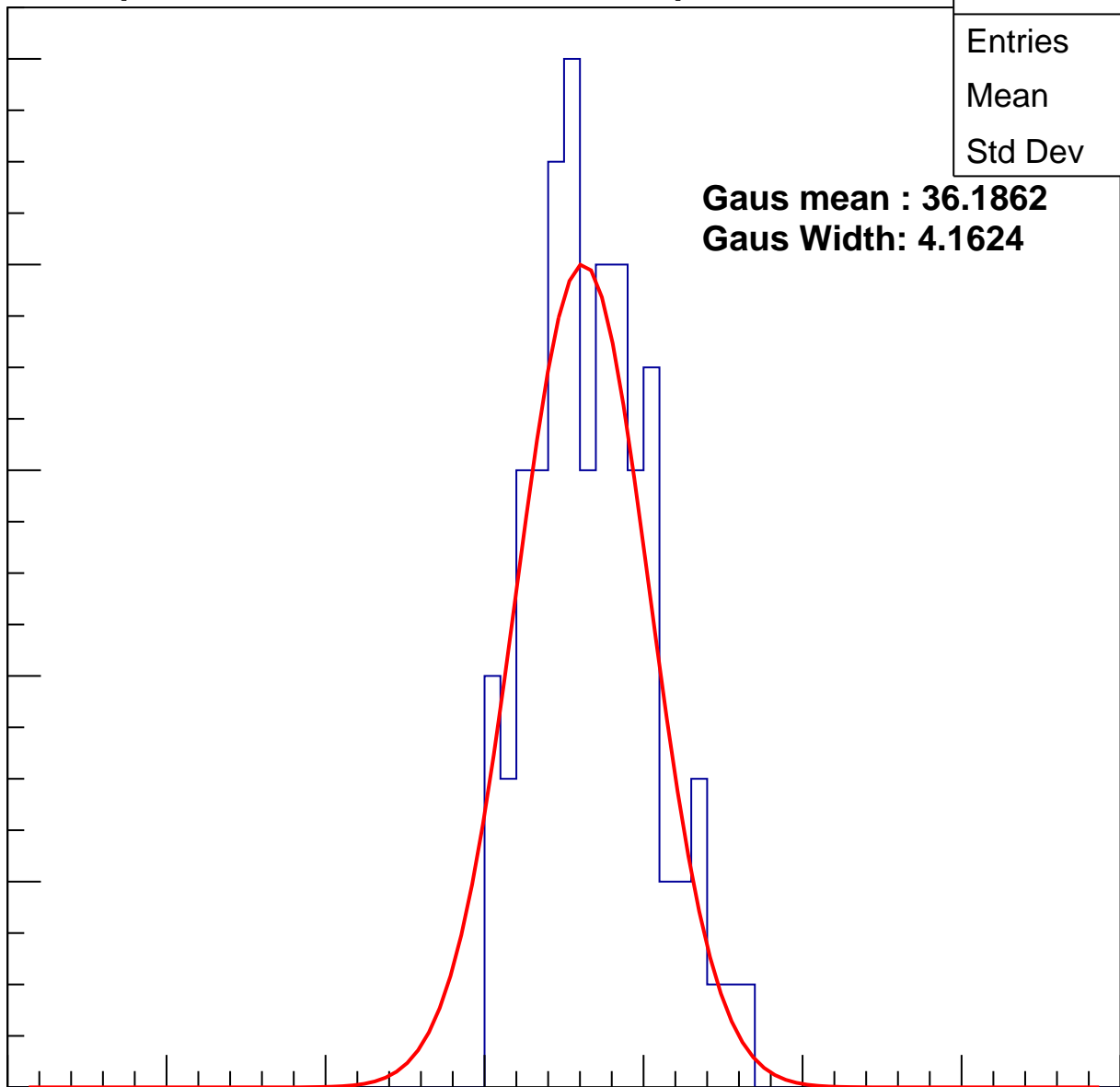
40

50

60

70

ampl



B1L103S, U12-ch113, adc2

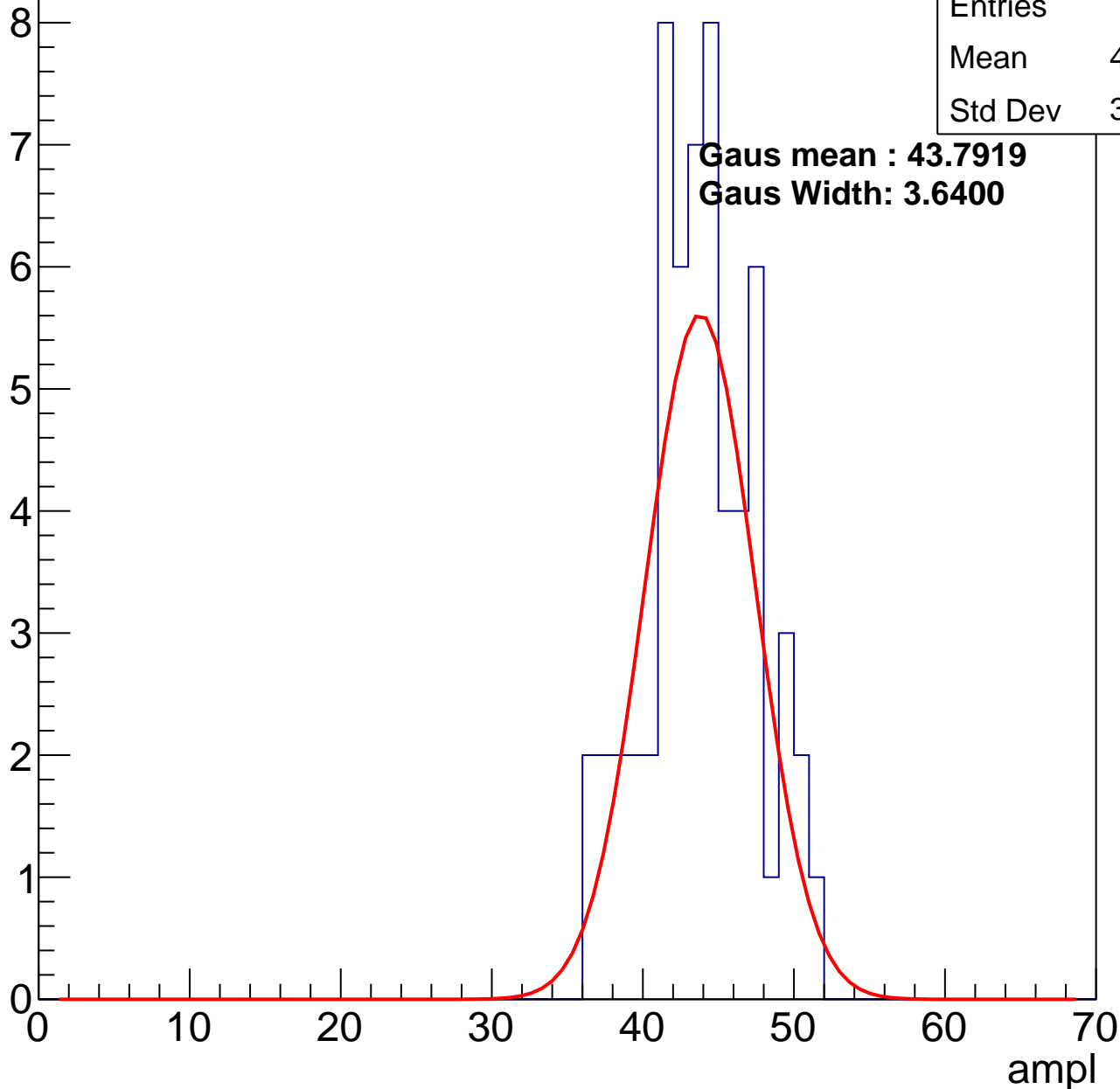
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.42
Std Dev	3.532

Gaus mean : 43.7919

Gaus Width: 3.6400

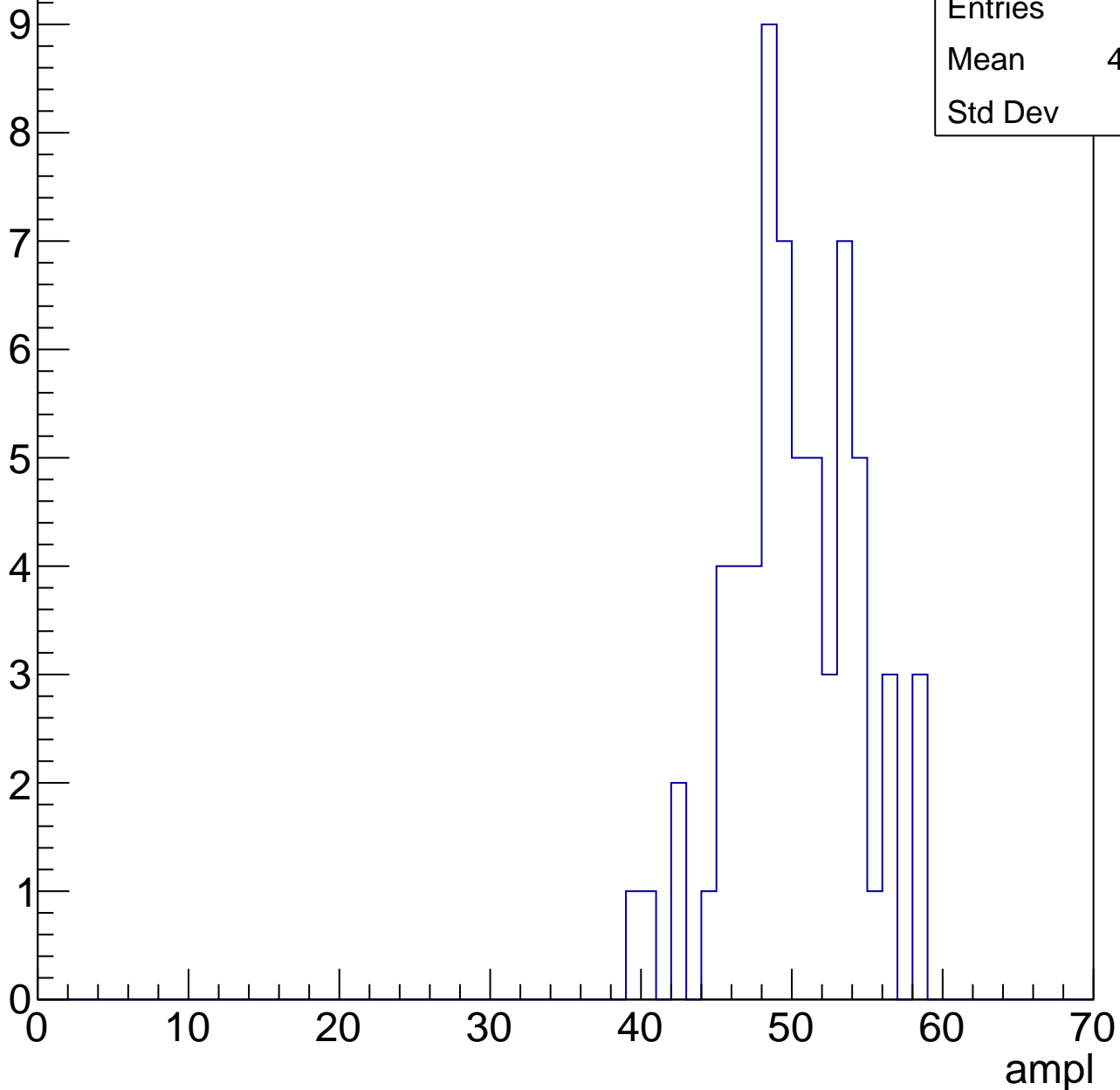


B1L103S, U12-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.74
Std Dev	4.13

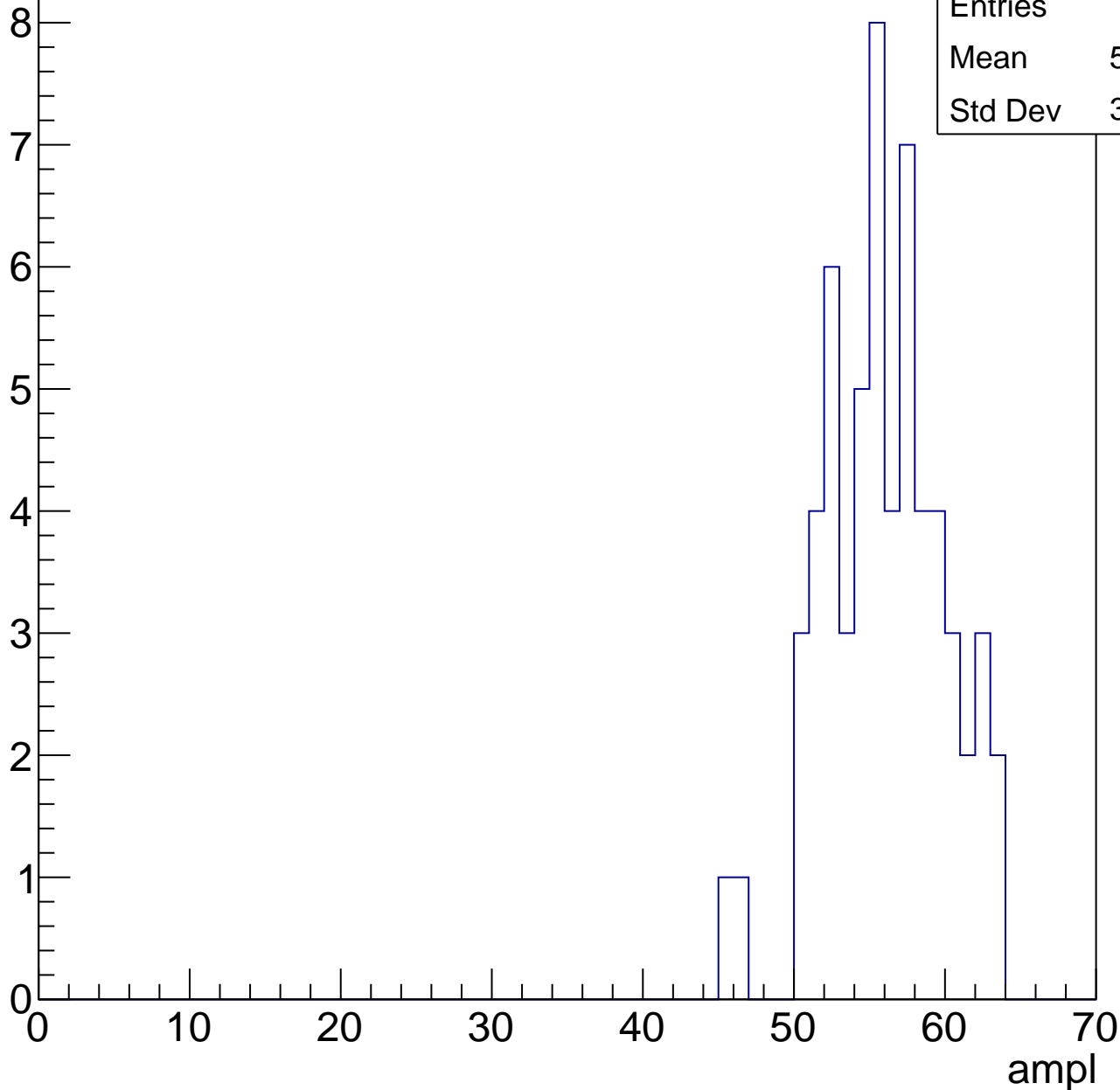


B1L103S, U12-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.52
Std Dev	3.935

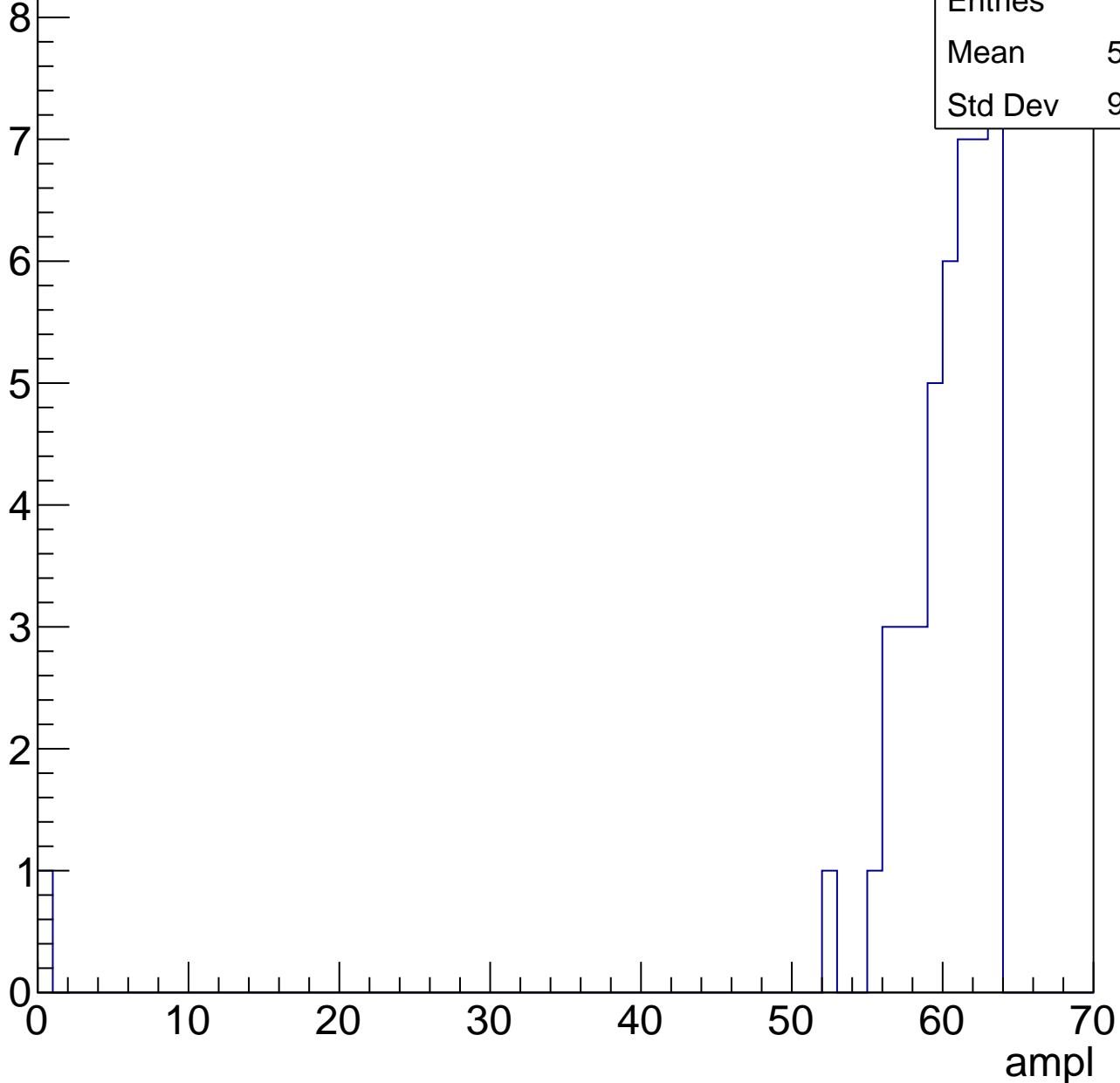


B1L103S, U12-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

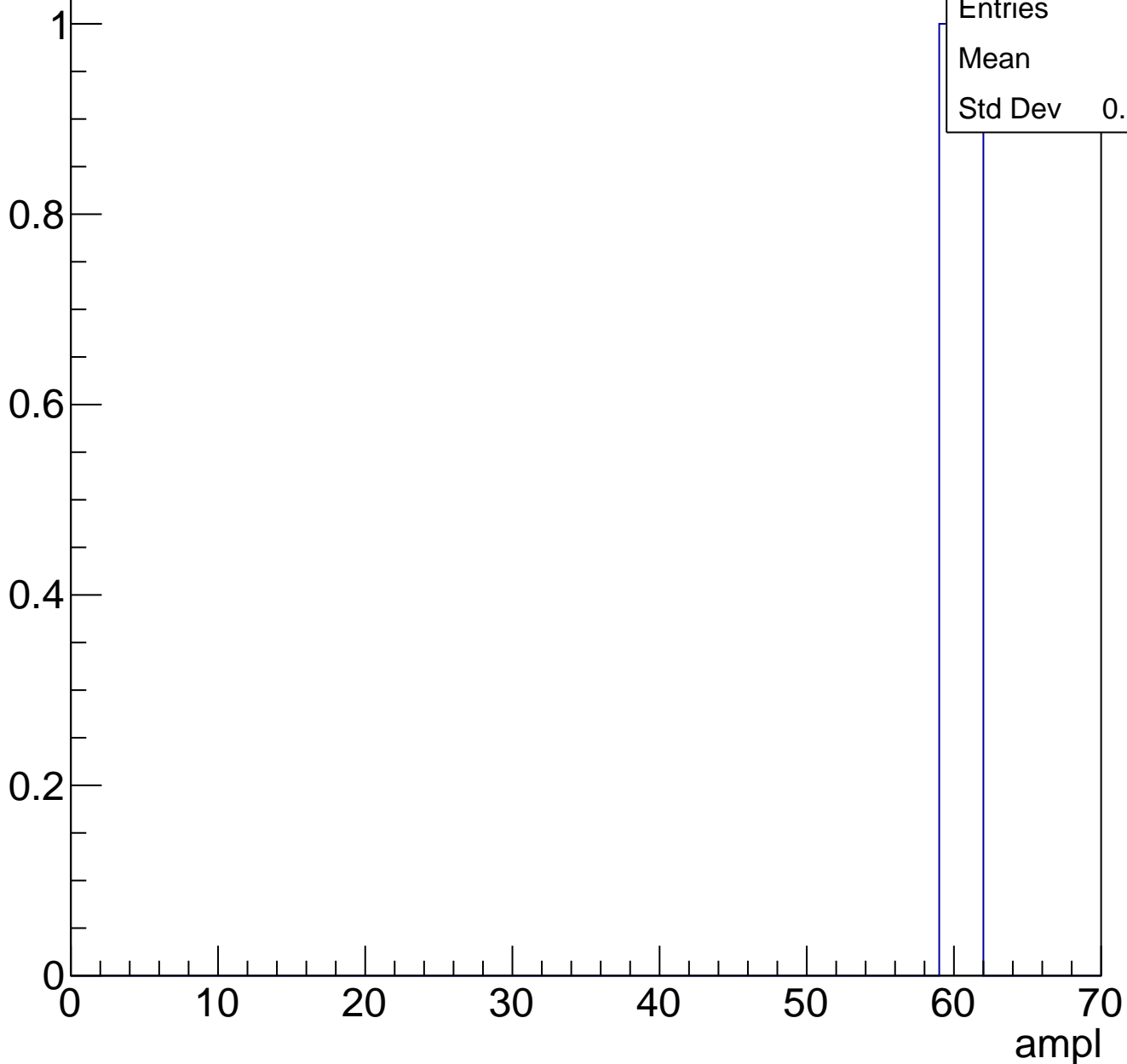
Entries	45
Mean	58.67
Std Dev	9.199



B1L103S, U12-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch114, adc0

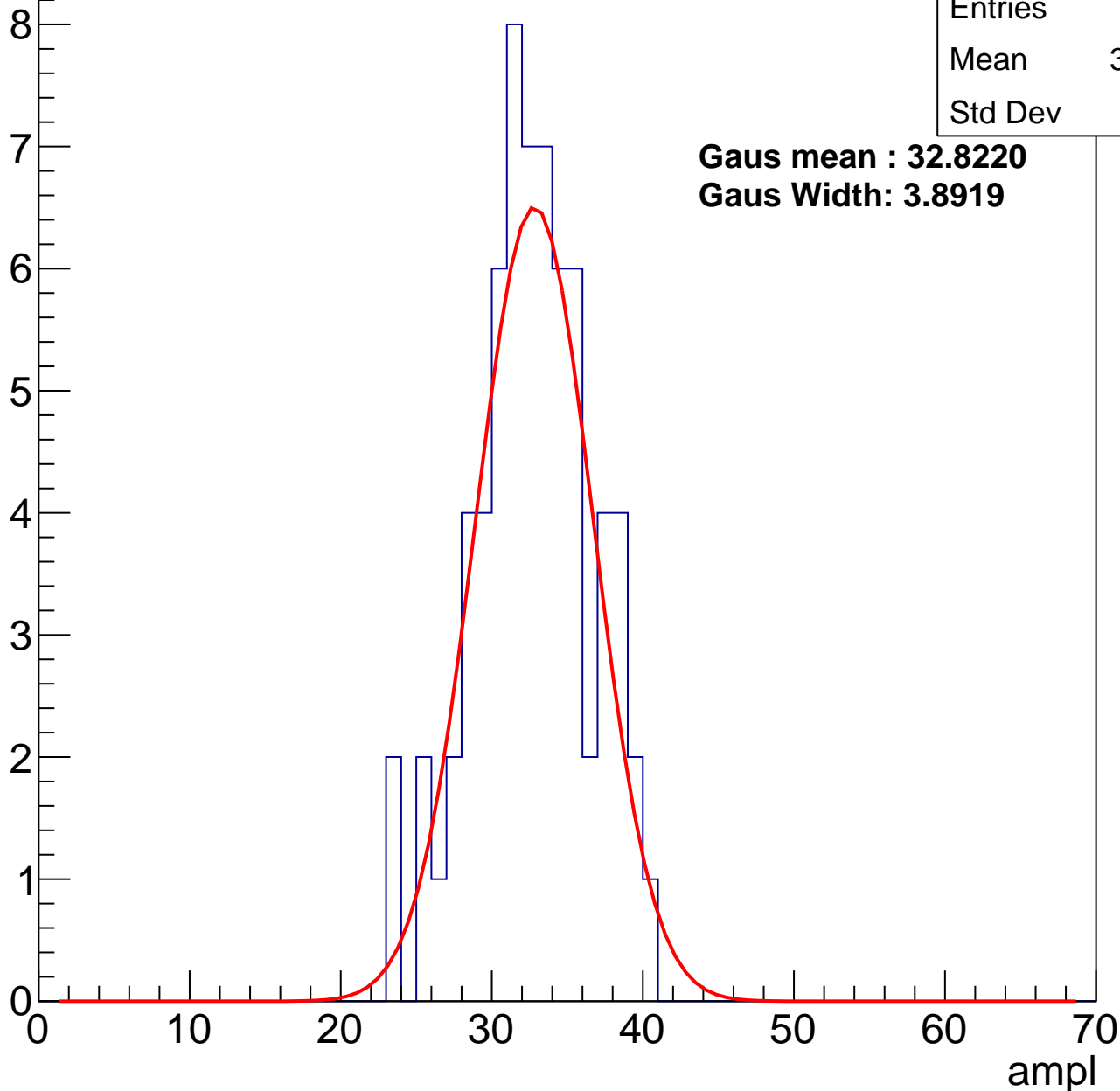
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	32.22
Std Dev	3.83

Gaus mean : 32.8220

Gaus Width: 3.8919



B1L103S, U12-ch114, adc1

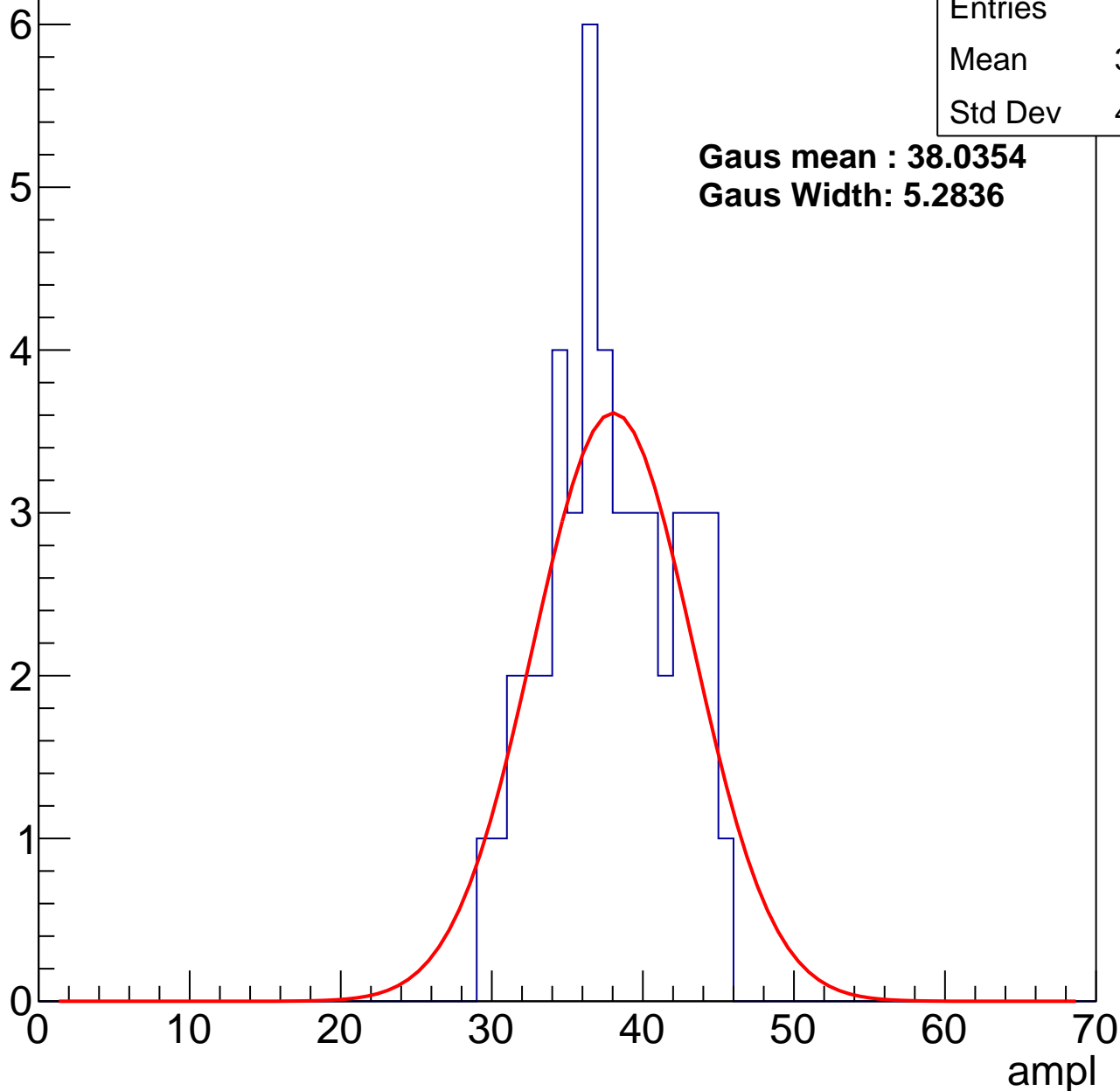
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	37.41
Std Dev	4.121

Gaus mean : 38.0354

Gaus Width: 5.2836



B1L103S, U12-ch114, adc2

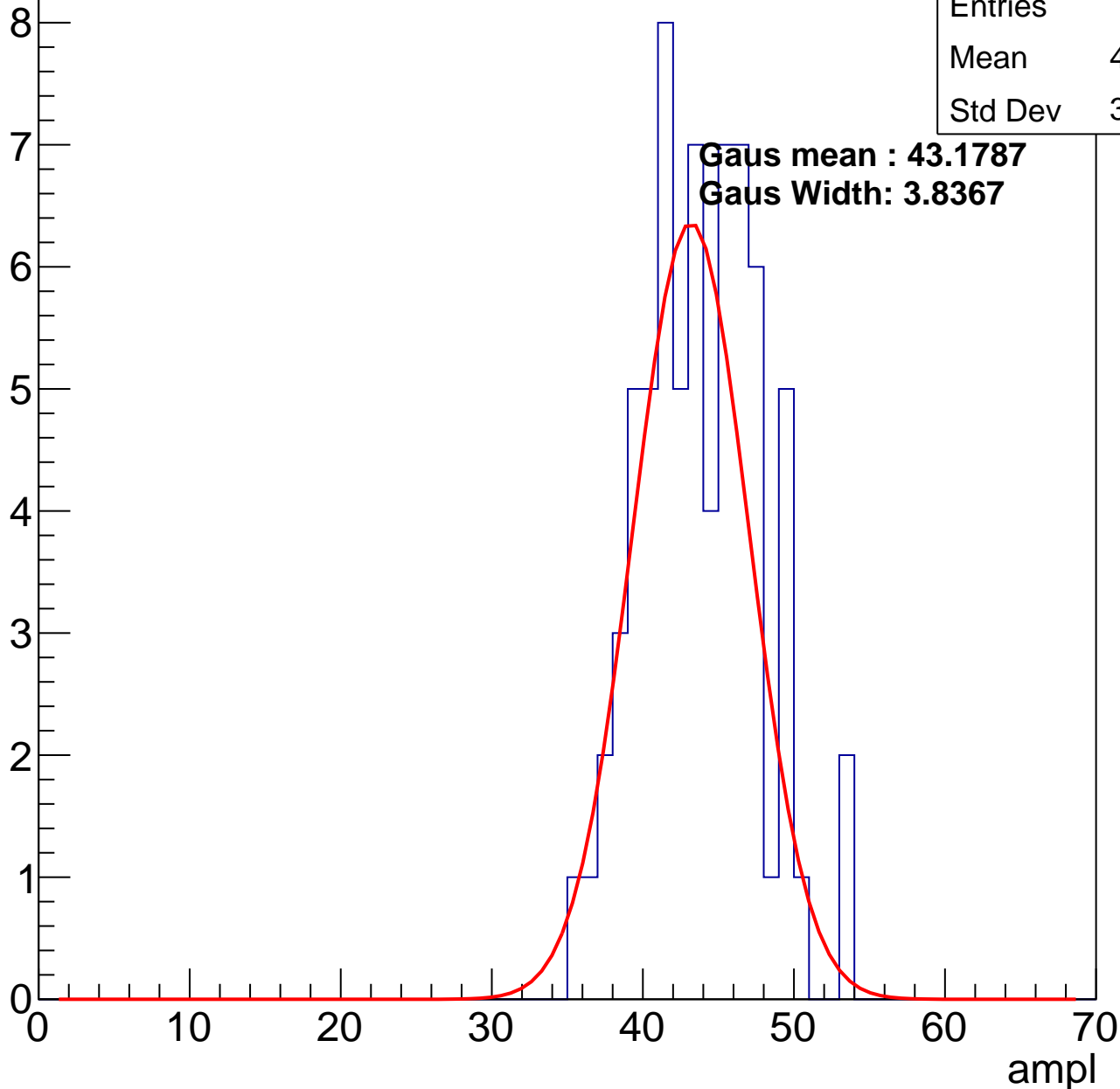
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.39
Std Dev	3.885

Gaus mean : 43.1787

Gaus Width: 3.8367

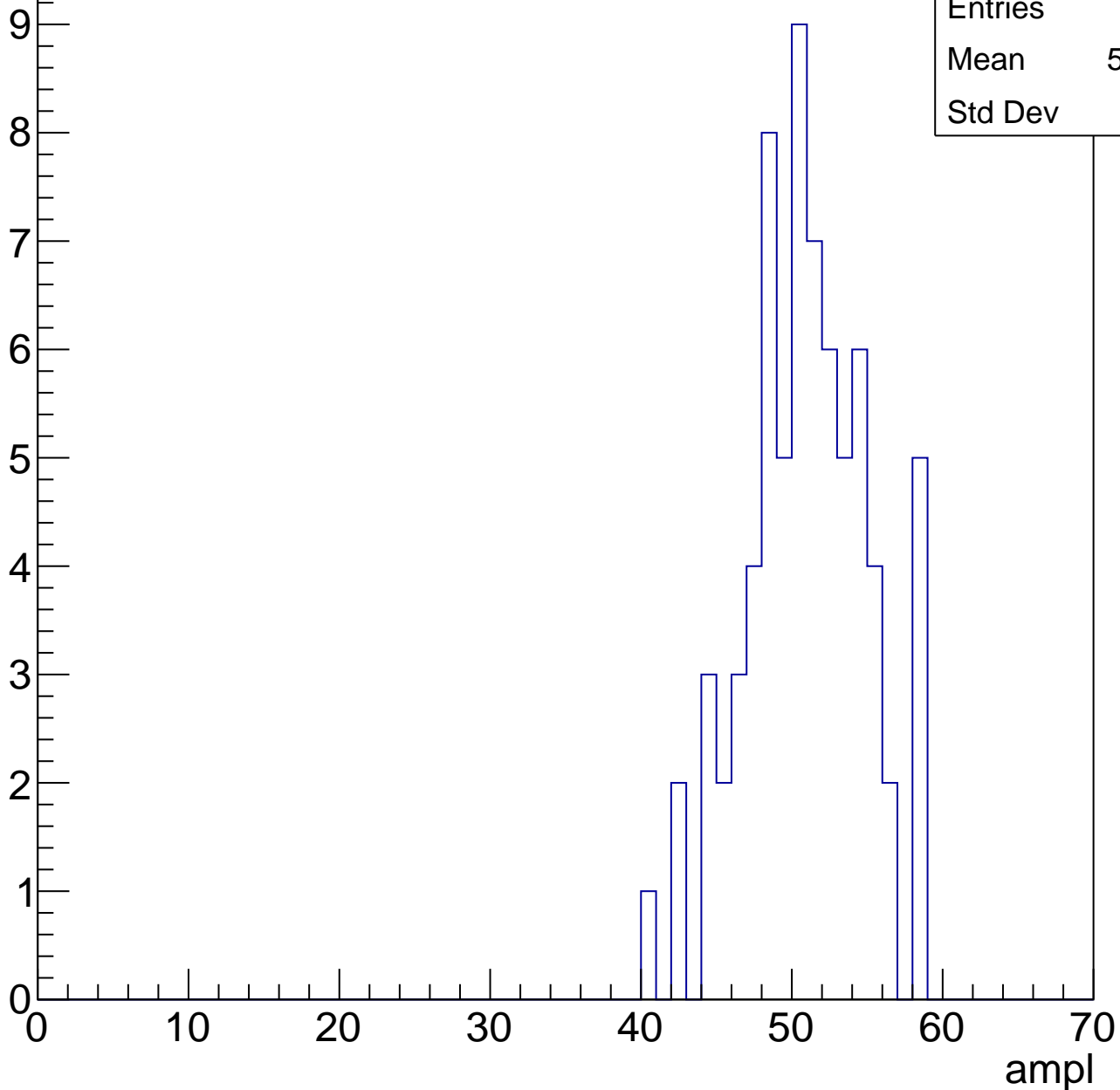


B1L103S, U12-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	50.43
Std Dev	4.01

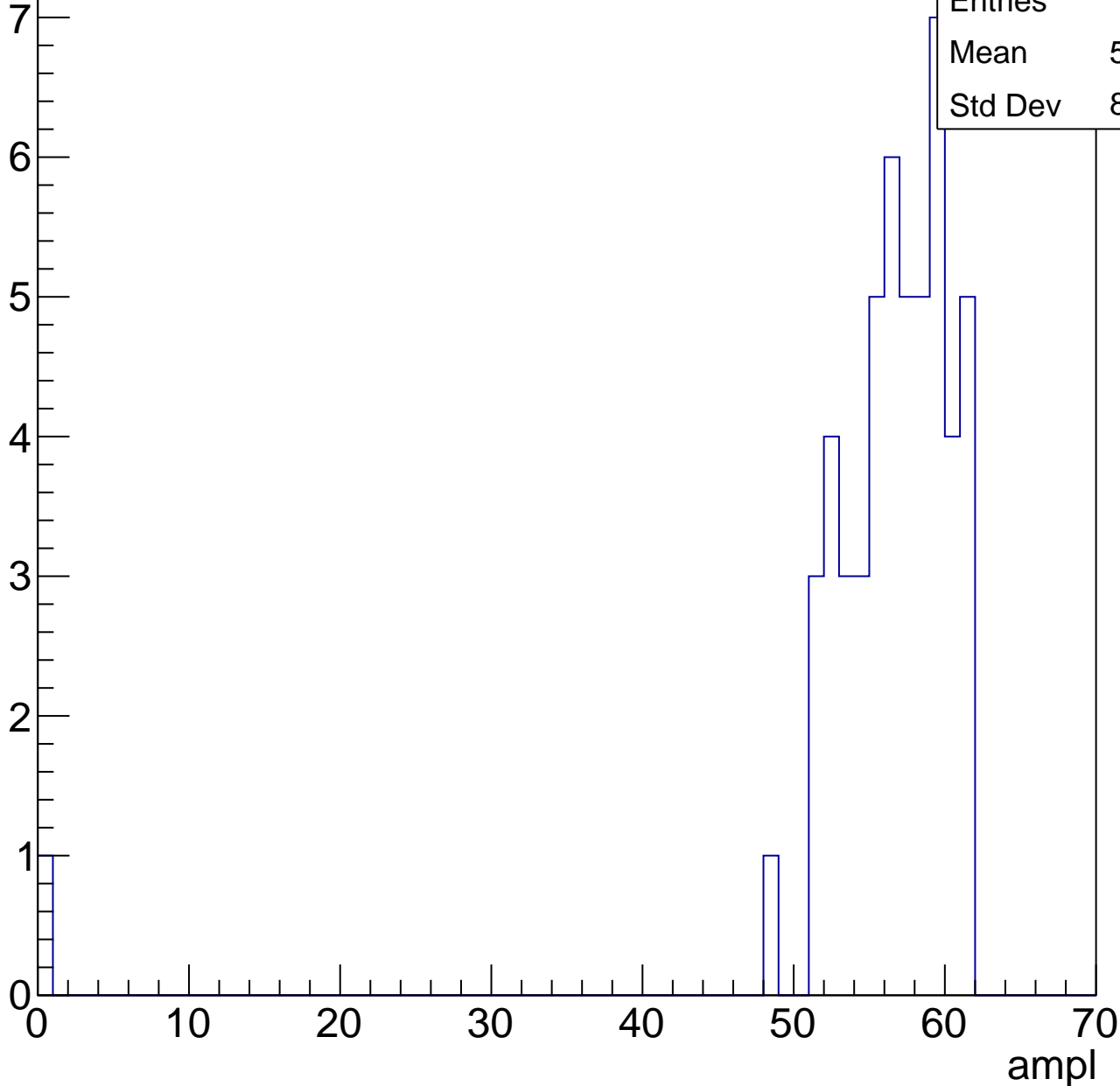


B1L103S, U12-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	55.27
Std Dev	8.358

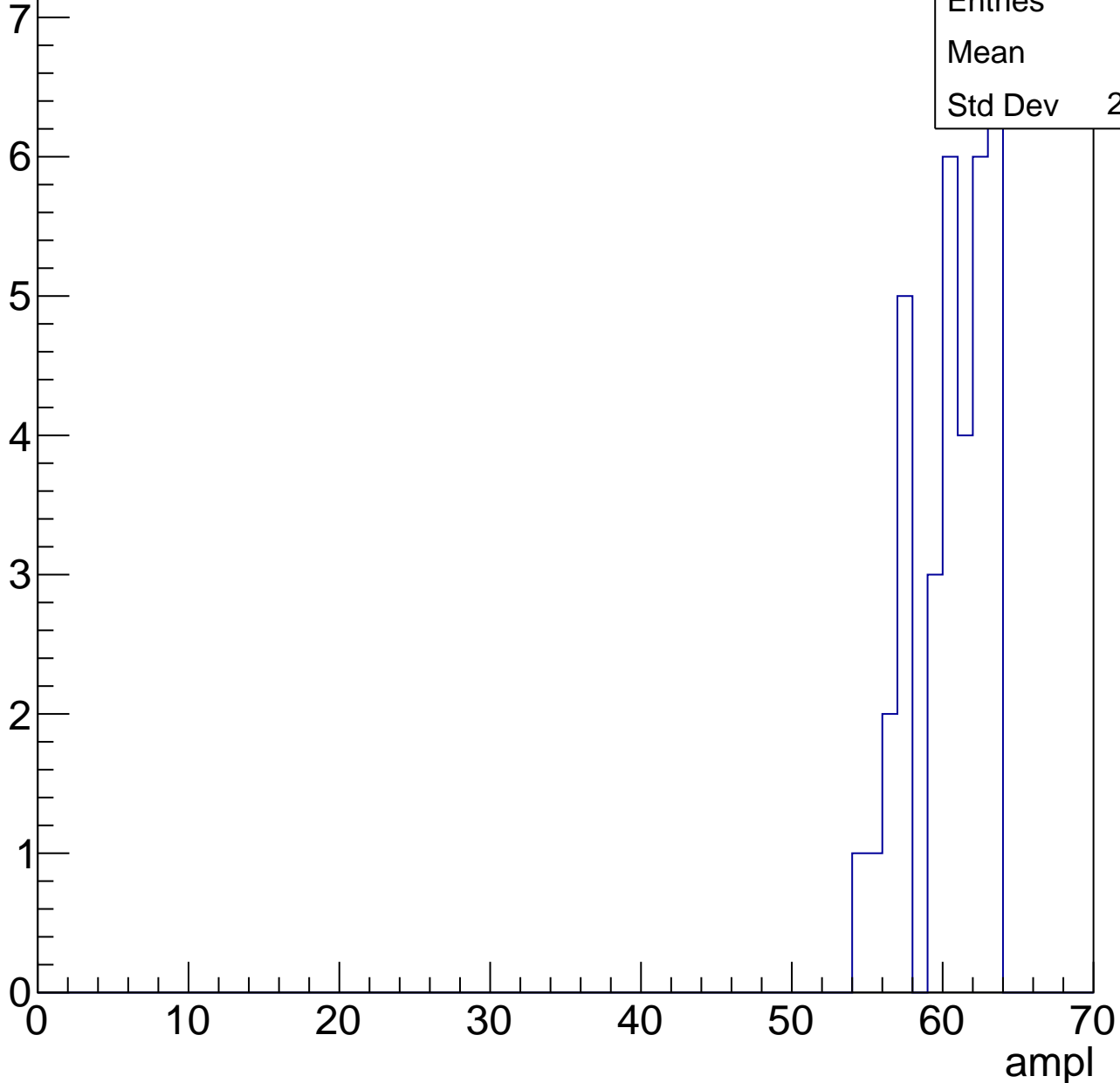


B1L103S, U12-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

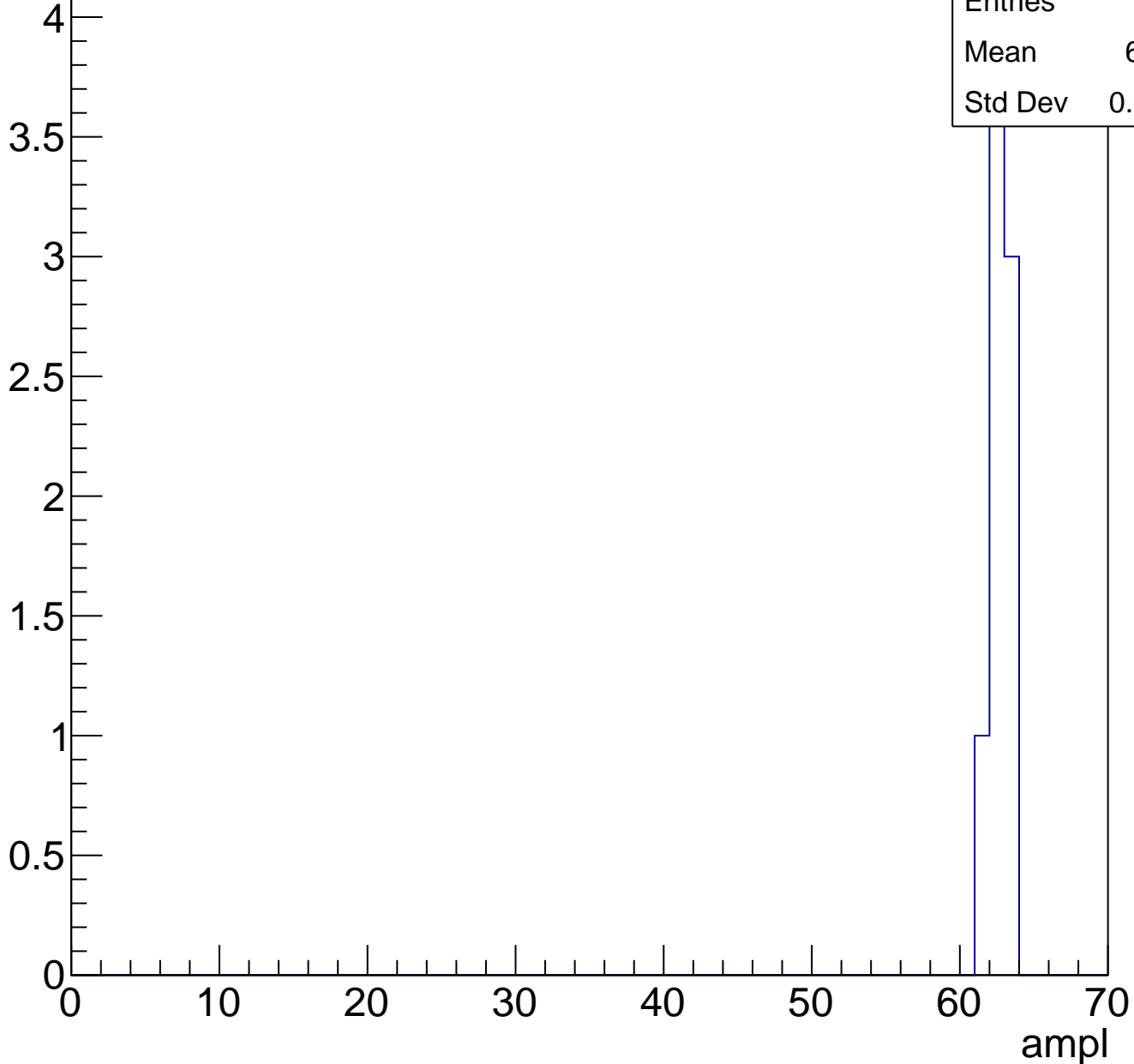
Entries	35
Mean	60
Std Dev	2.575



B1L103S, U12-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U12-ch115, adc0

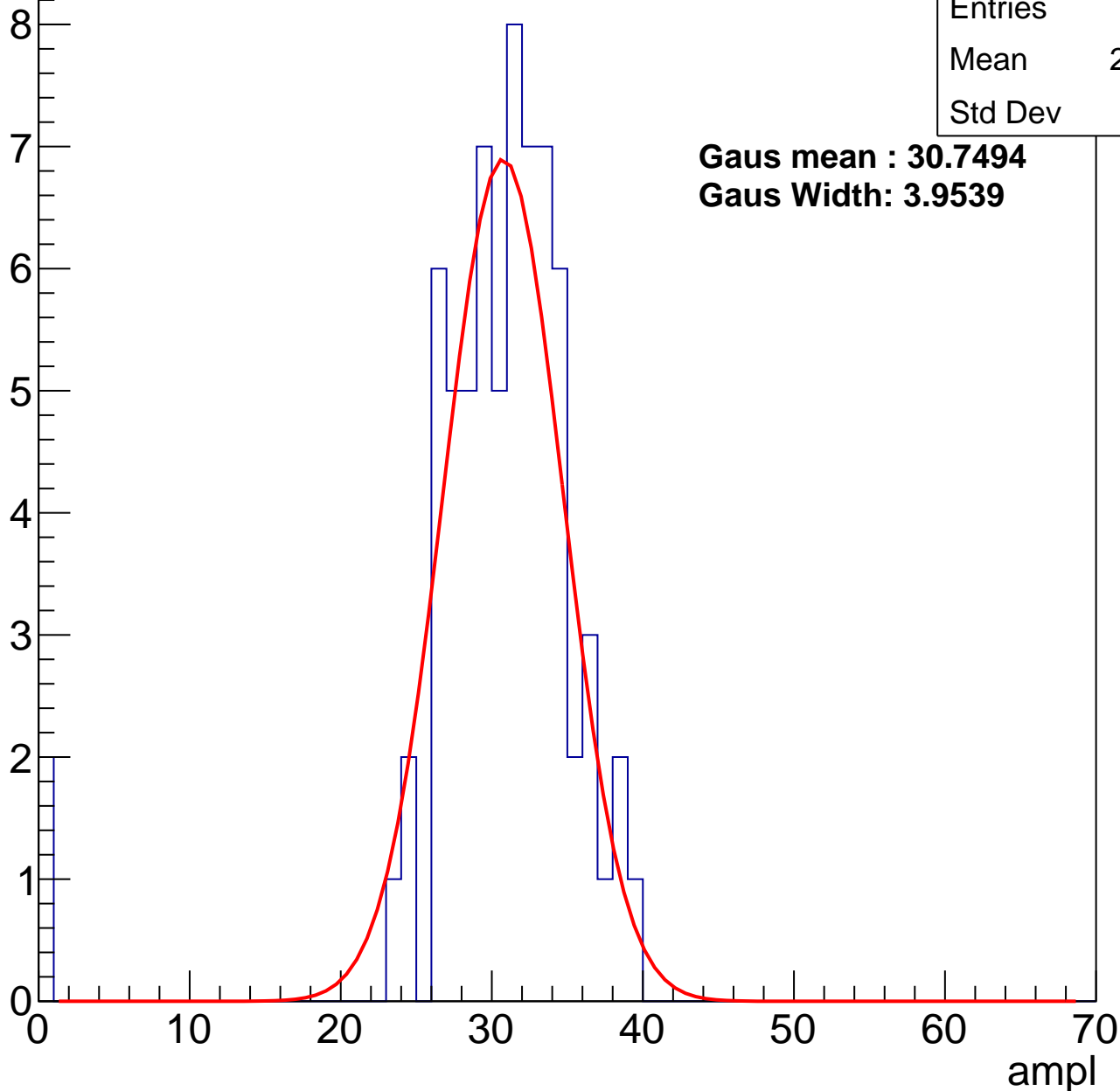
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.89
Std Dev	6.21

Gaus mean : 30.7494

Gaus Width: 3.9539



B1L103S, U12-ch115, adc1

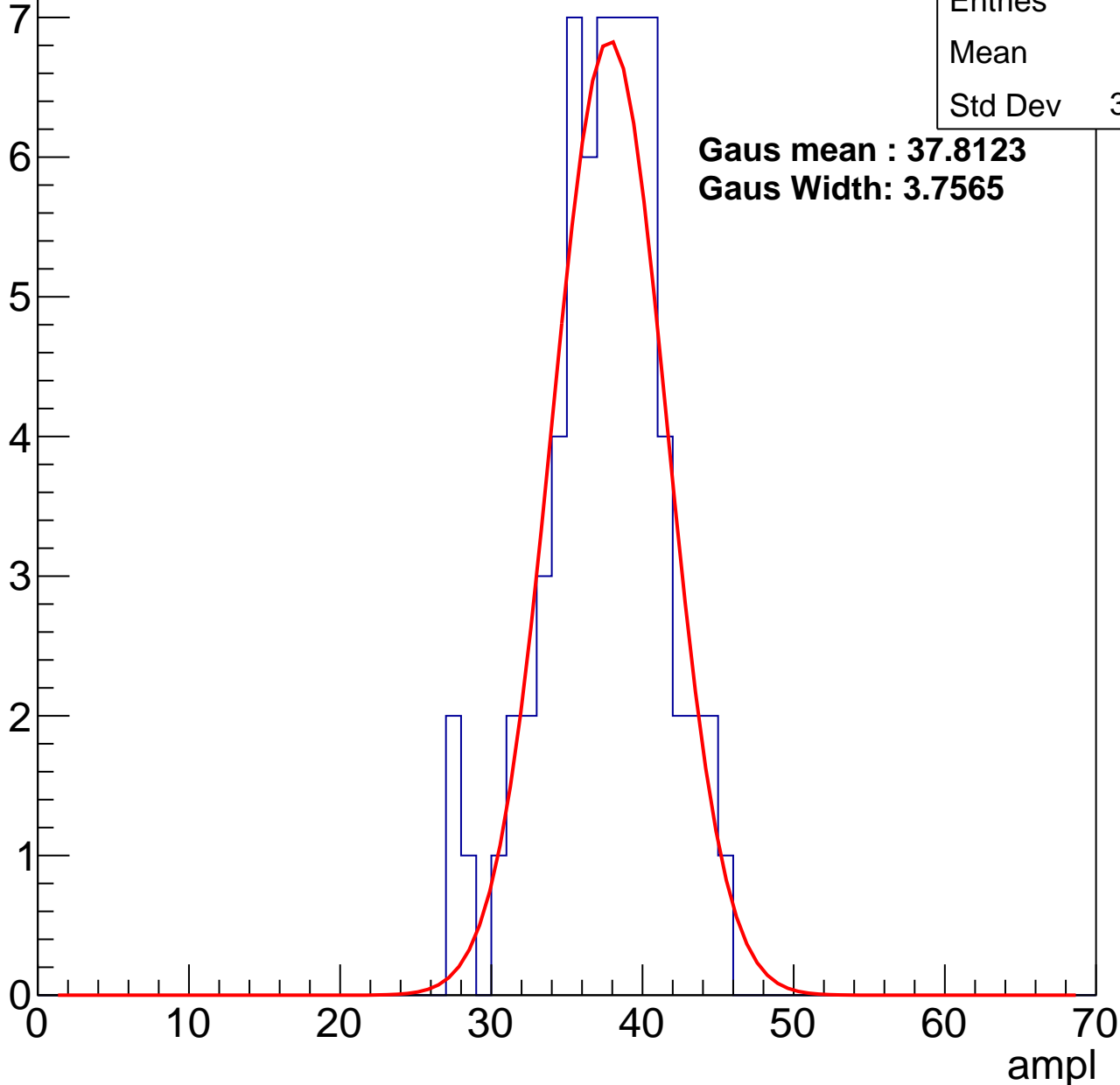
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37
Std Dev	3.894

Gaus mean : 37.8123

Gaus Width: 3.7565



B1L103S, U12-ch115, adc2

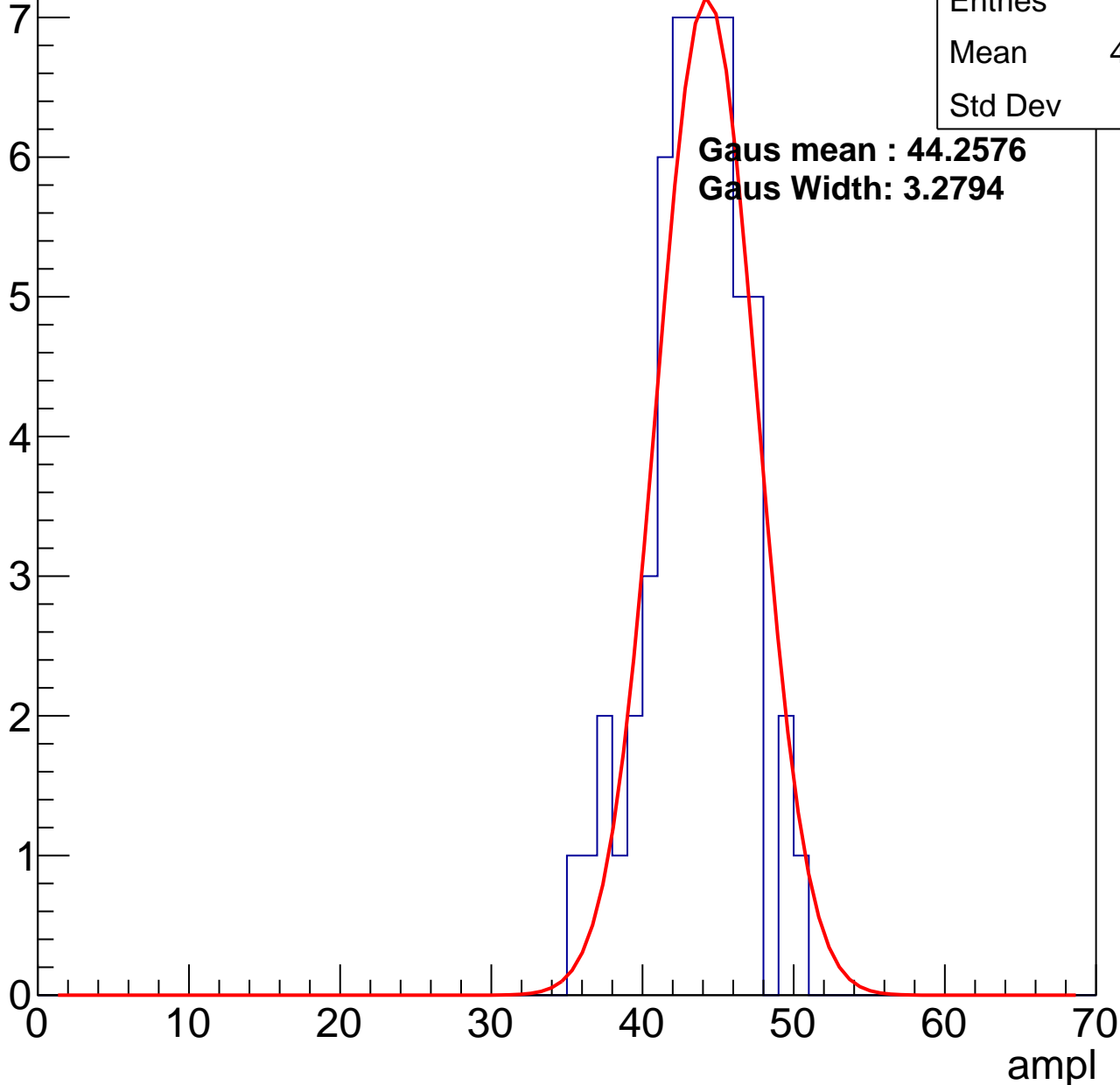
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.12
Std Dev	3.19

Gaus mean : 44.2576

Gaus Width: 3.2794

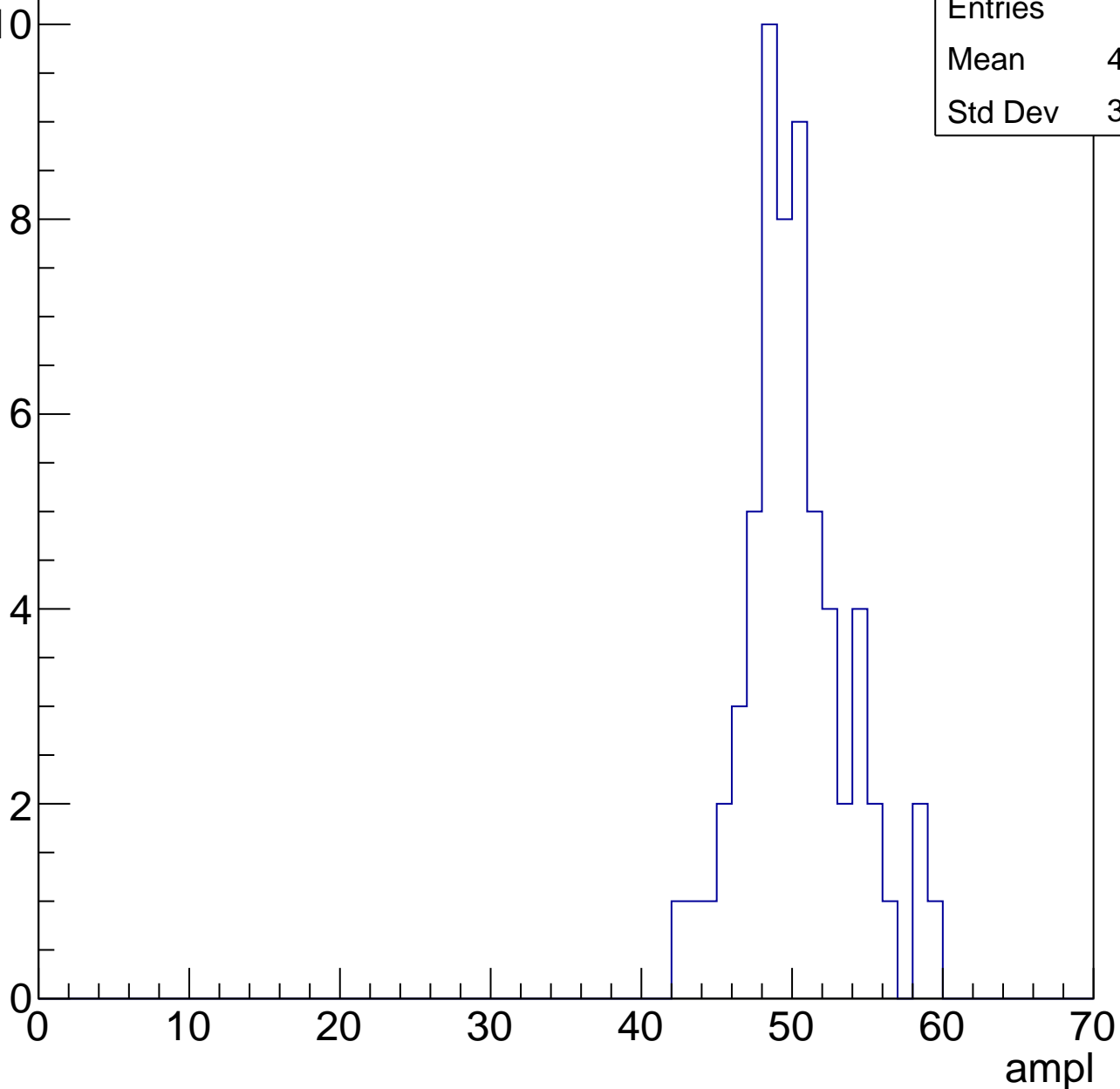


B1L103S, U12-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	49.84
Std Dev	3.474

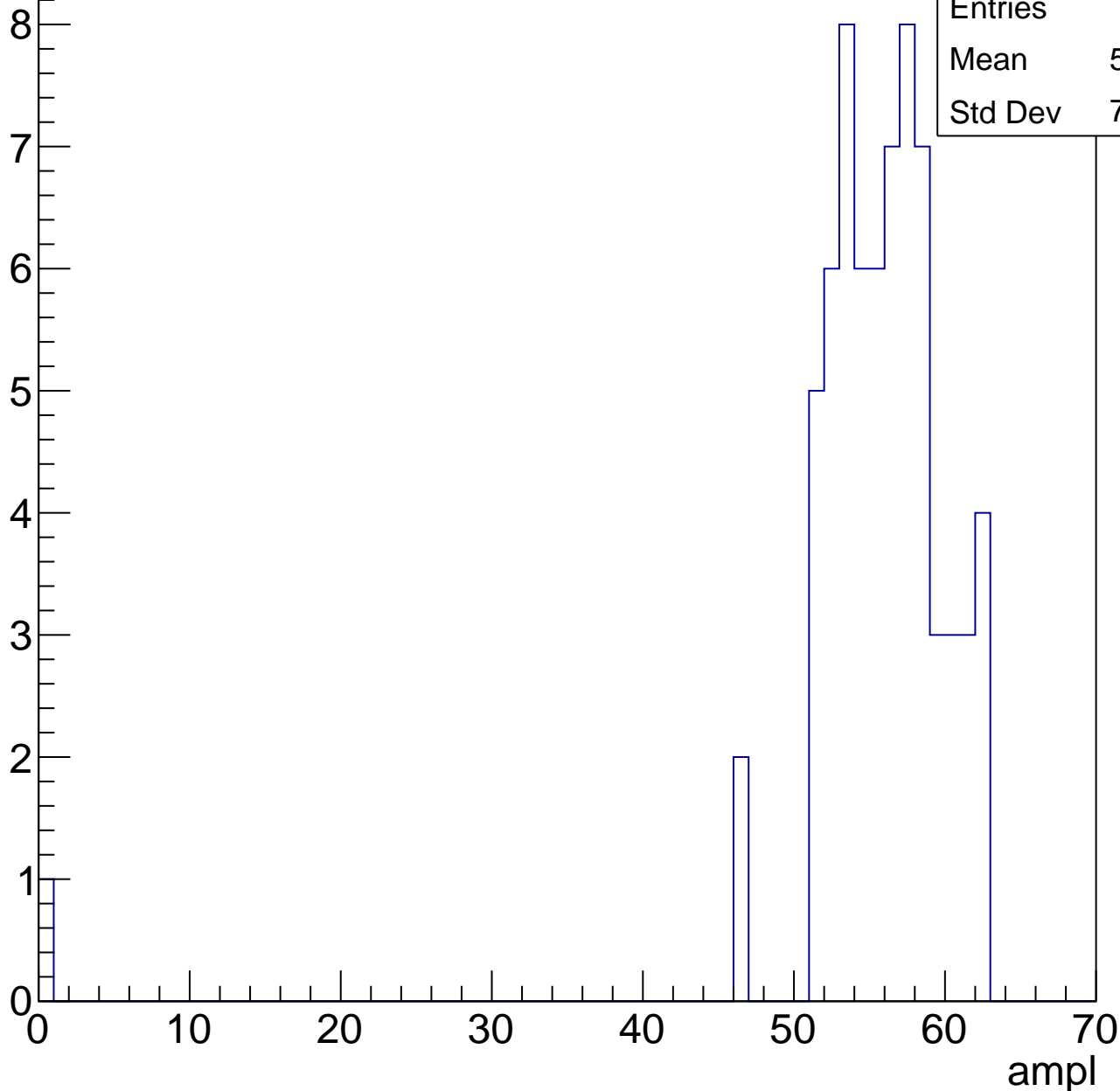


B1L103S, U12-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	54.77
Std Dev	7.503

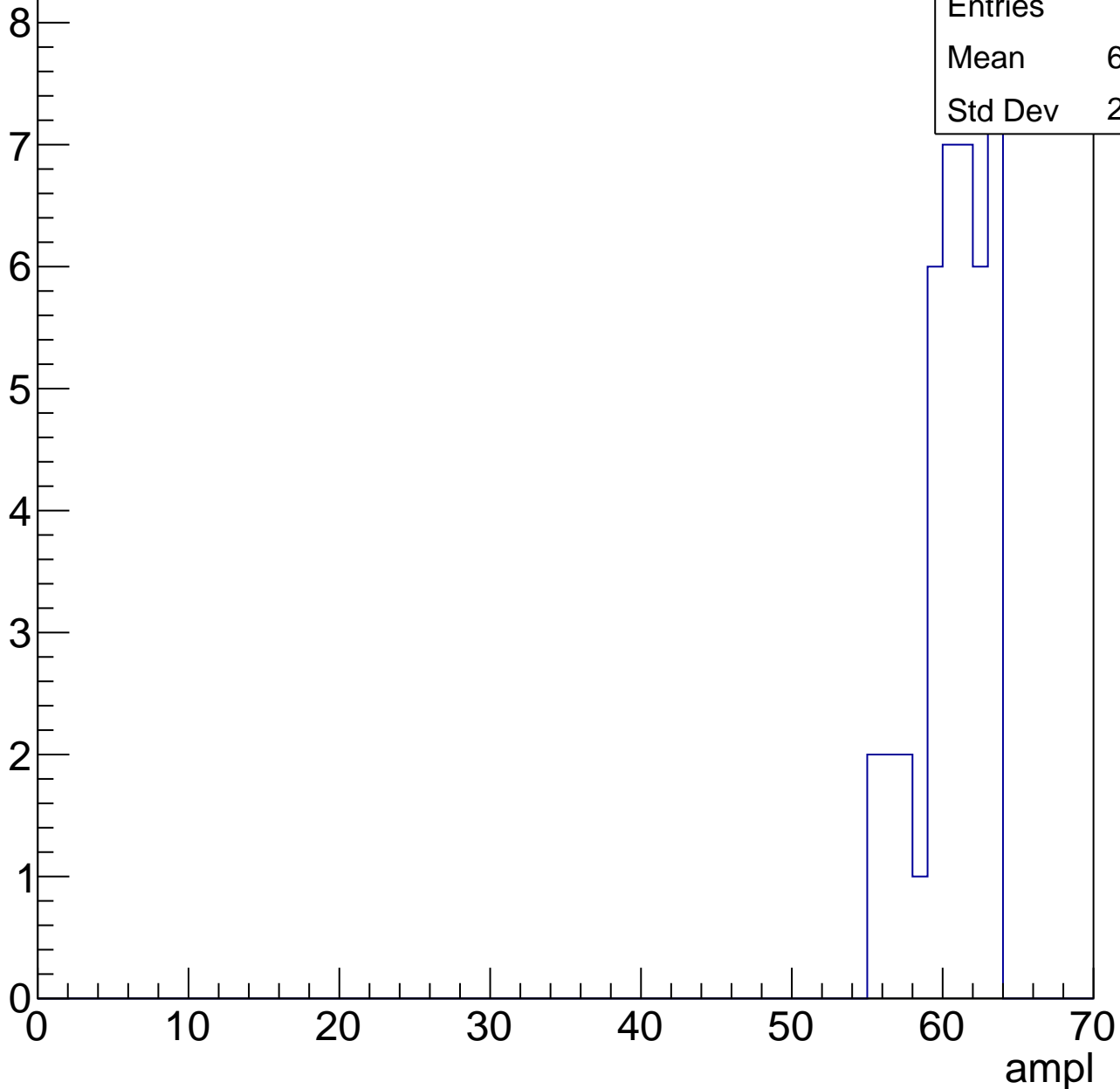


B1L103S, U12-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	60.27
Std Dev	2.263



B1L103S, U12-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	63
Std Dev	0

B1L103S, U12-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch116, adc0

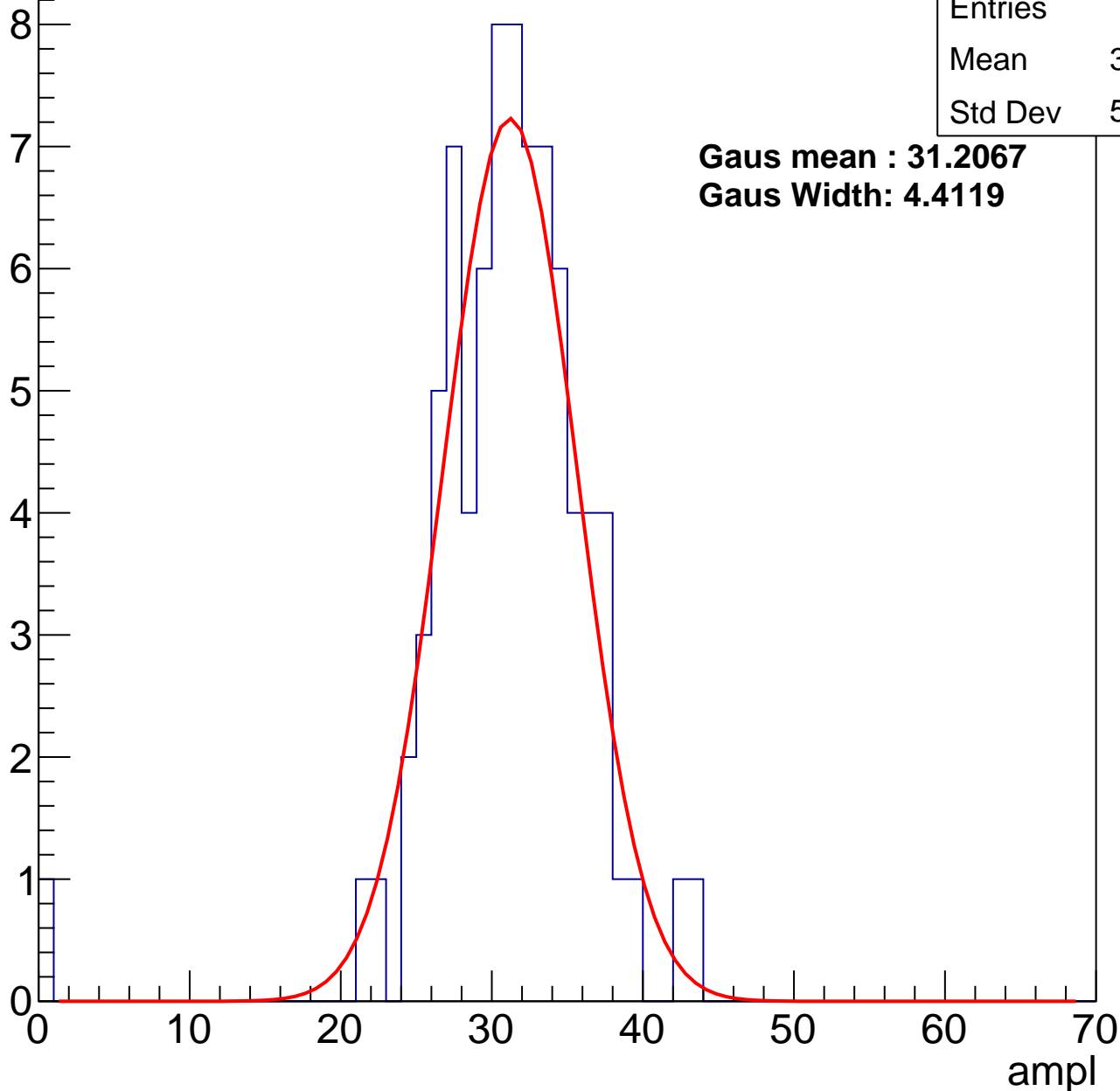
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	30.63
Std Dev	5.429

Gaus mean : 31.2067

Gaus Width: 4.4119



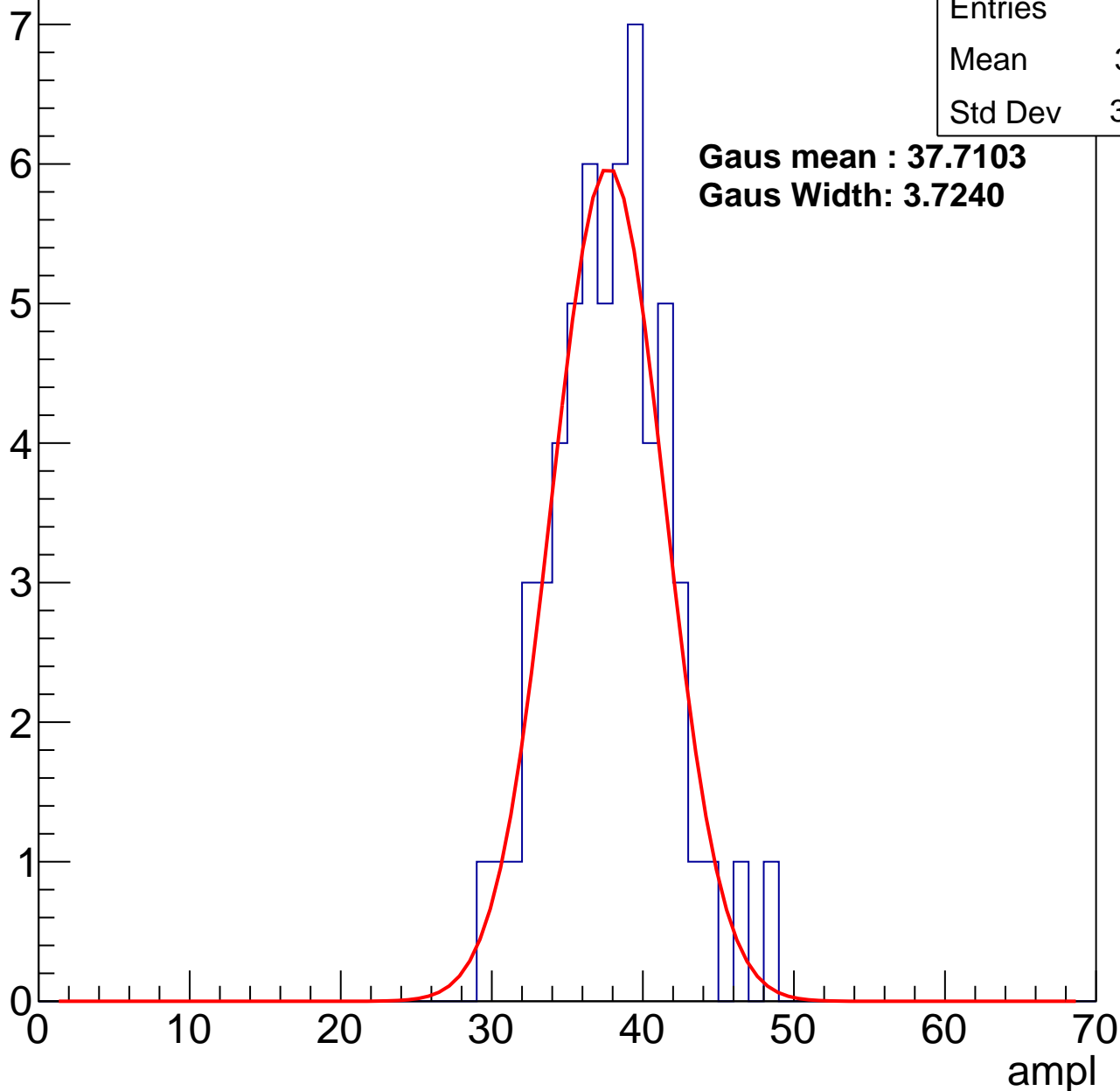
B1L103S, U12-ch116, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.41
Std Dev	3.797

Gaus mean : 37.7103
Gaus Width: 3.7240



B1L103S, U12-ch116, adc2

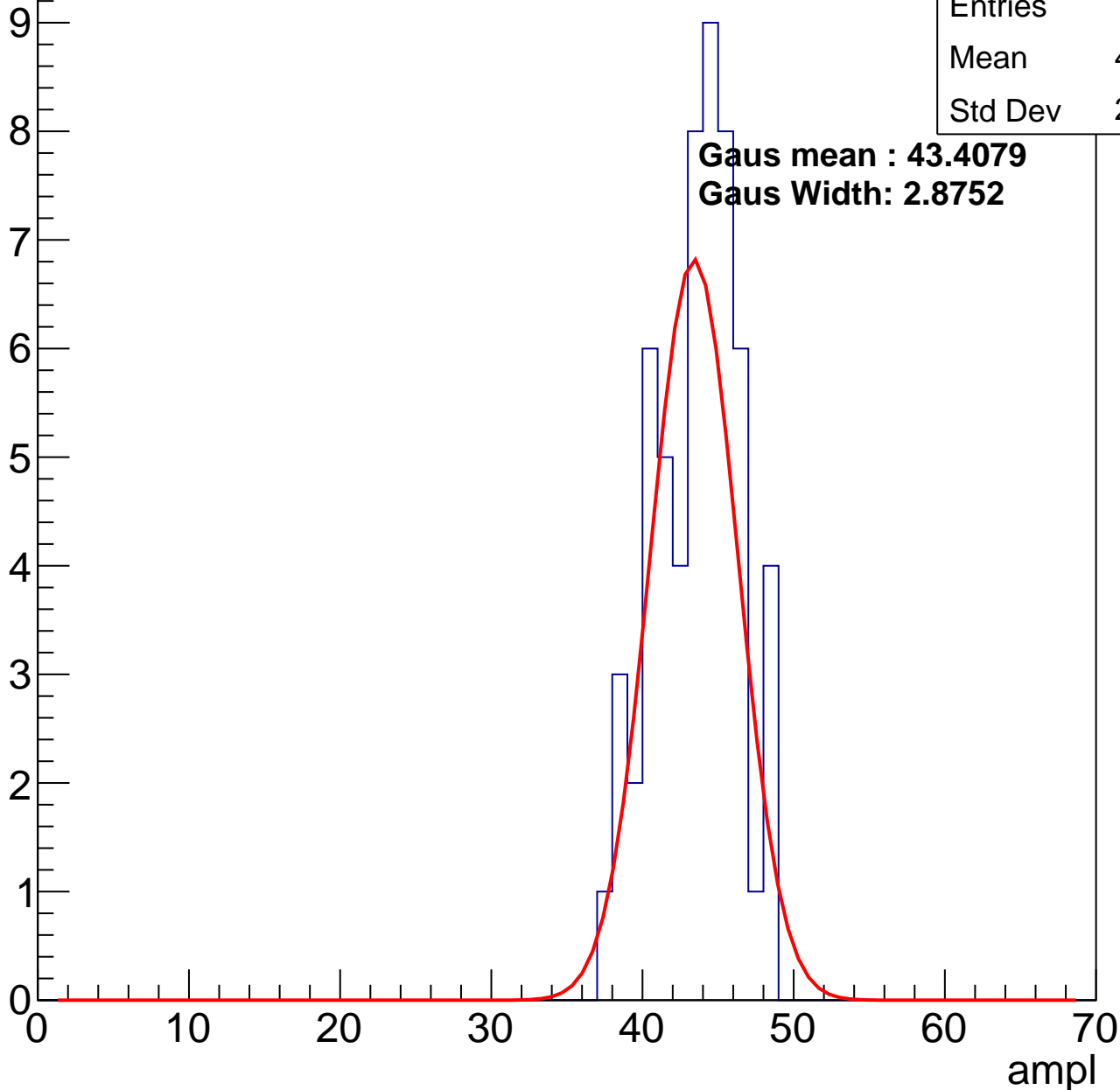
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.11
Std Dev	2.751

Gaus mean : 43.4079

Gaus Width: 2.8752

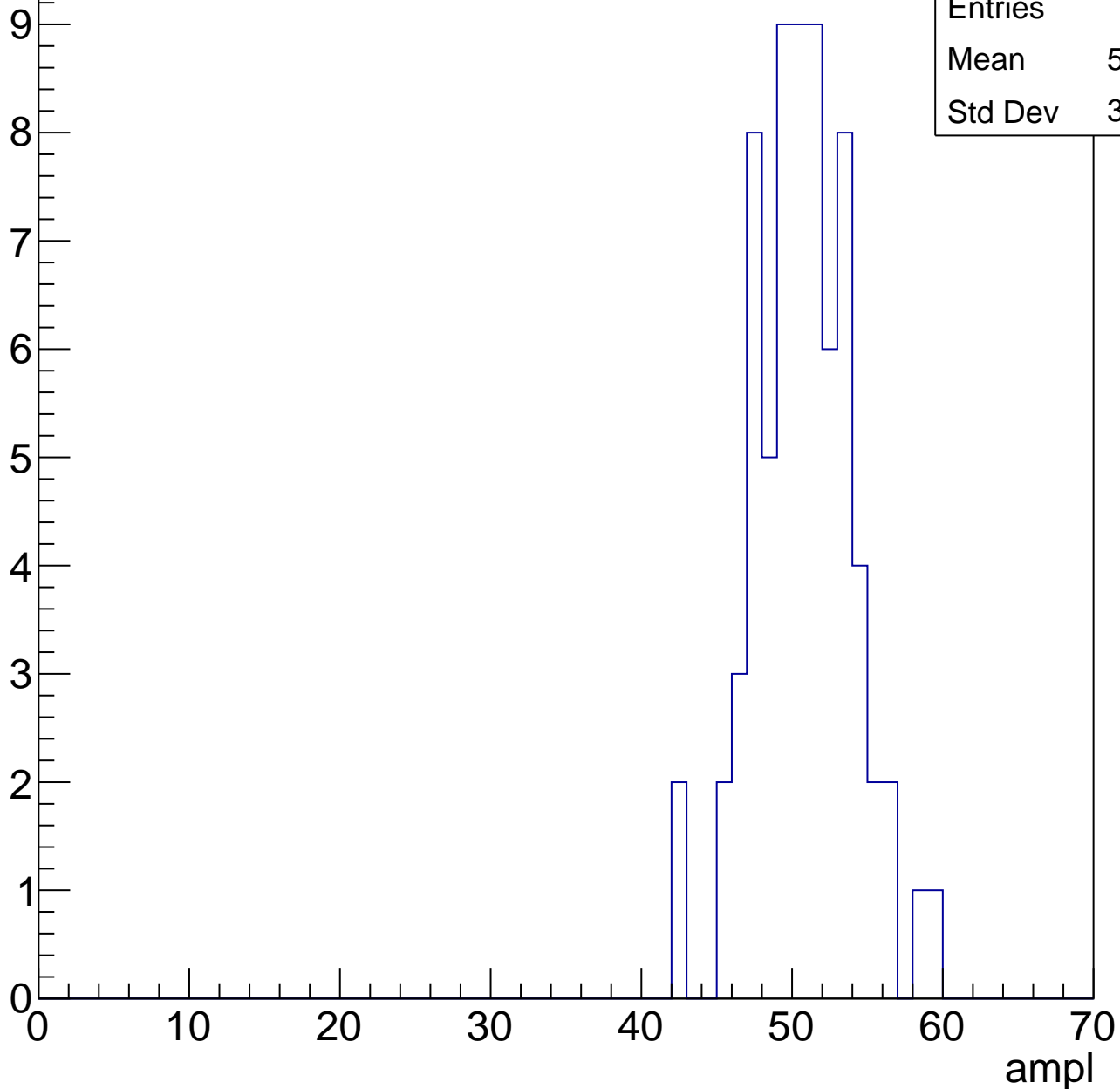


B1L103S, U12-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.27
Std Dev	3.267

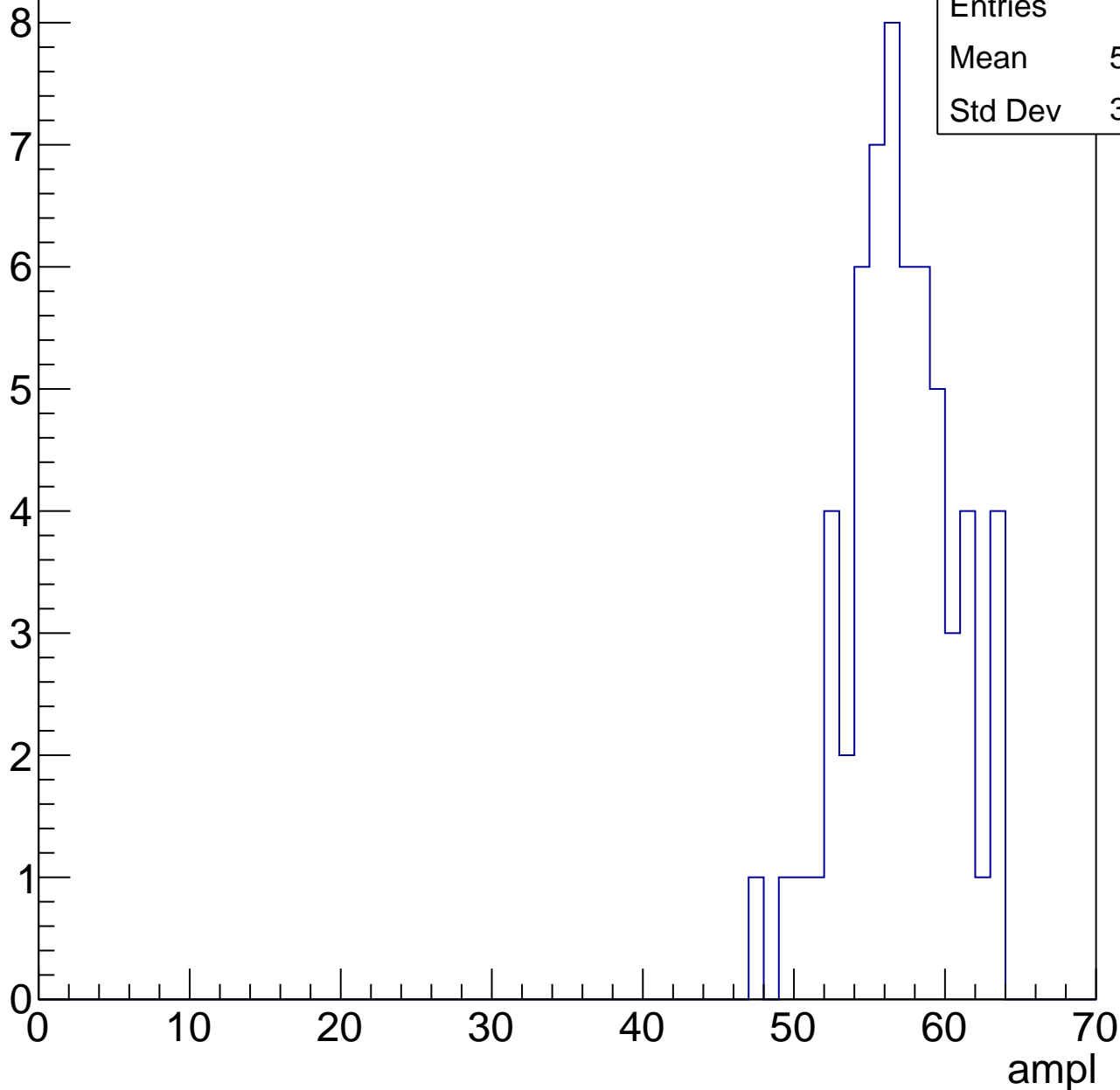


B1L103S, U12-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.52
Std Dev	3.519

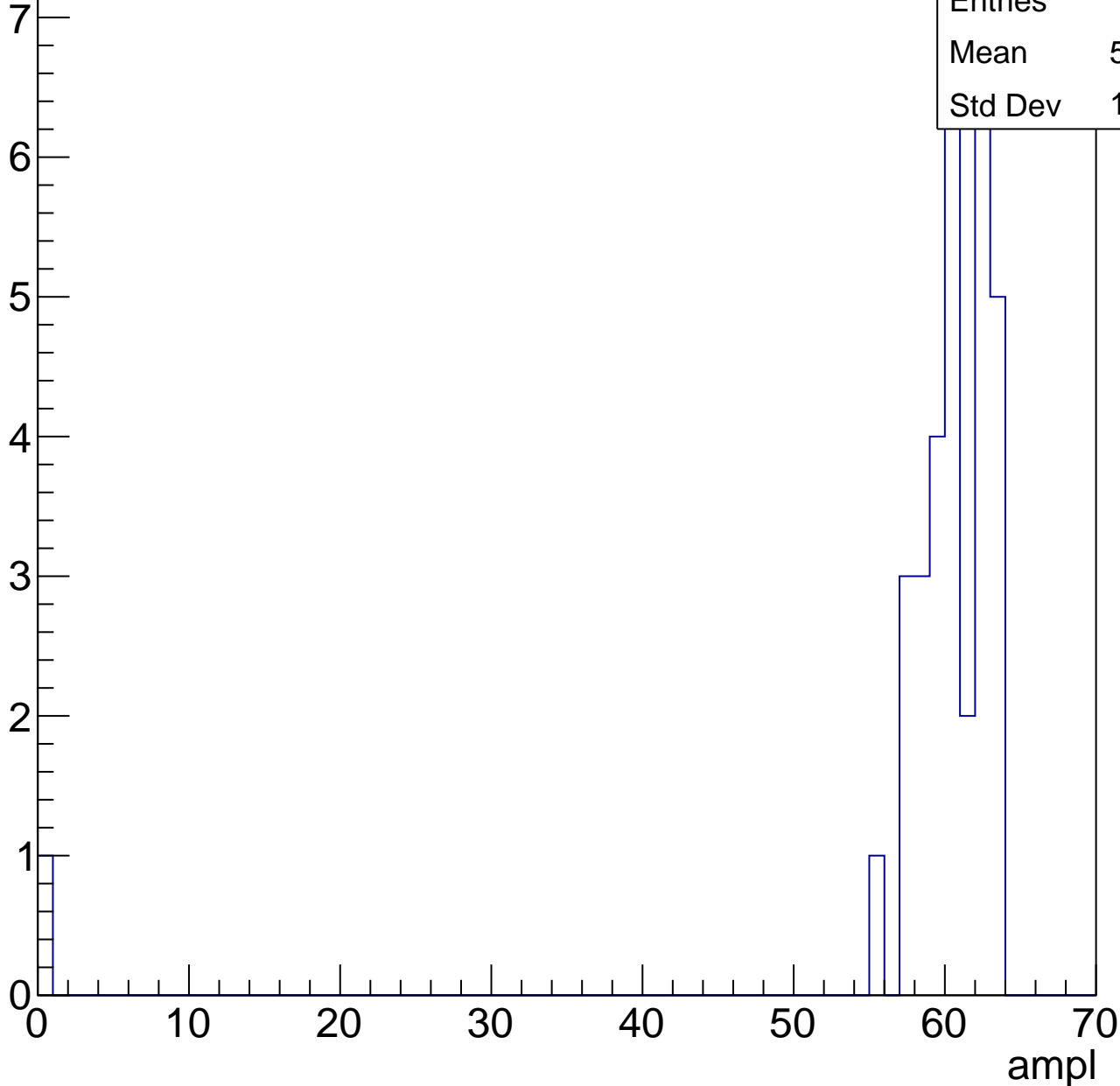


B1L103S, U12-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

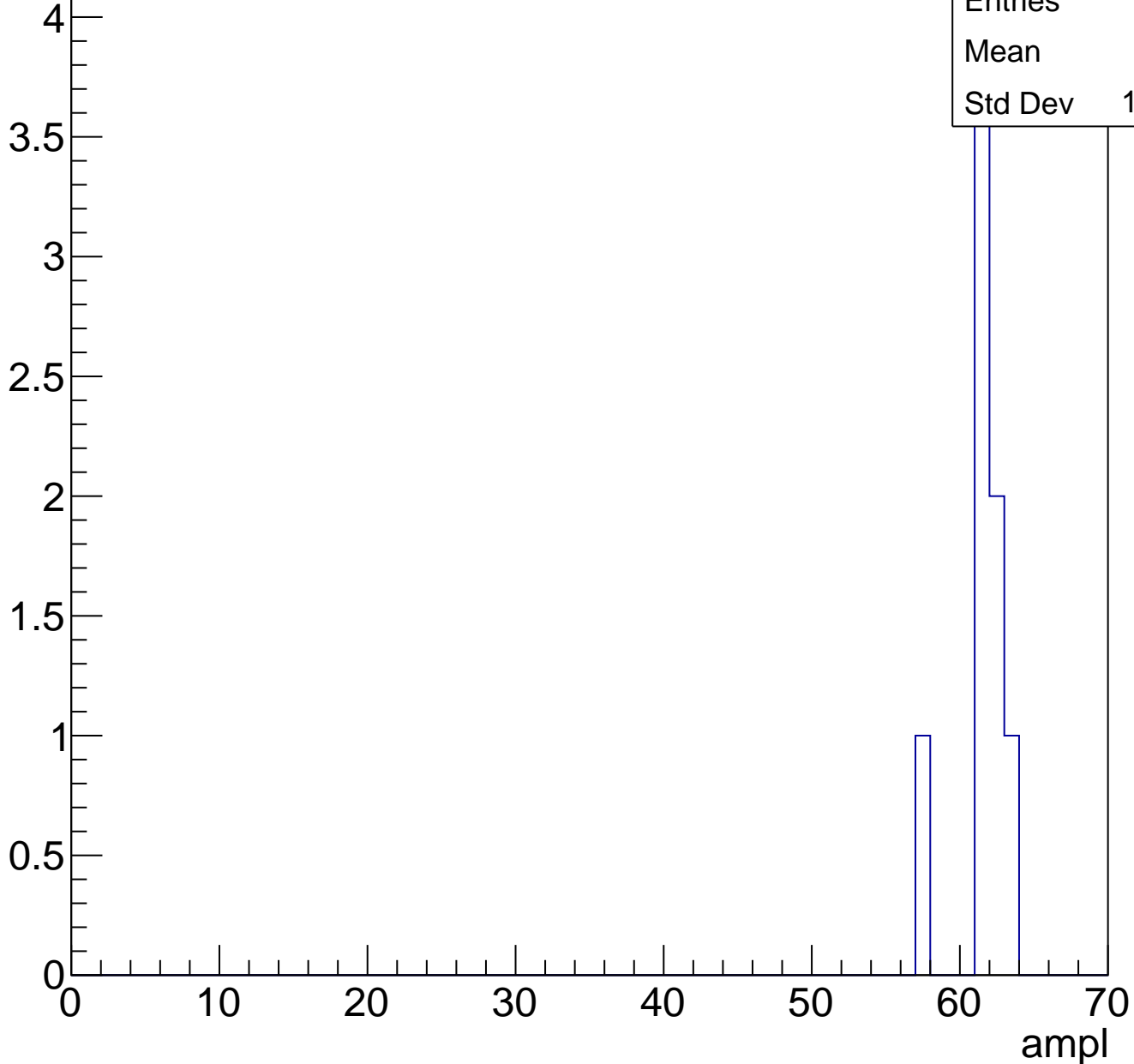
Entries	33
Mean	58.39
Std Dev	10.53



B1L103S, U12-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch116, adc7

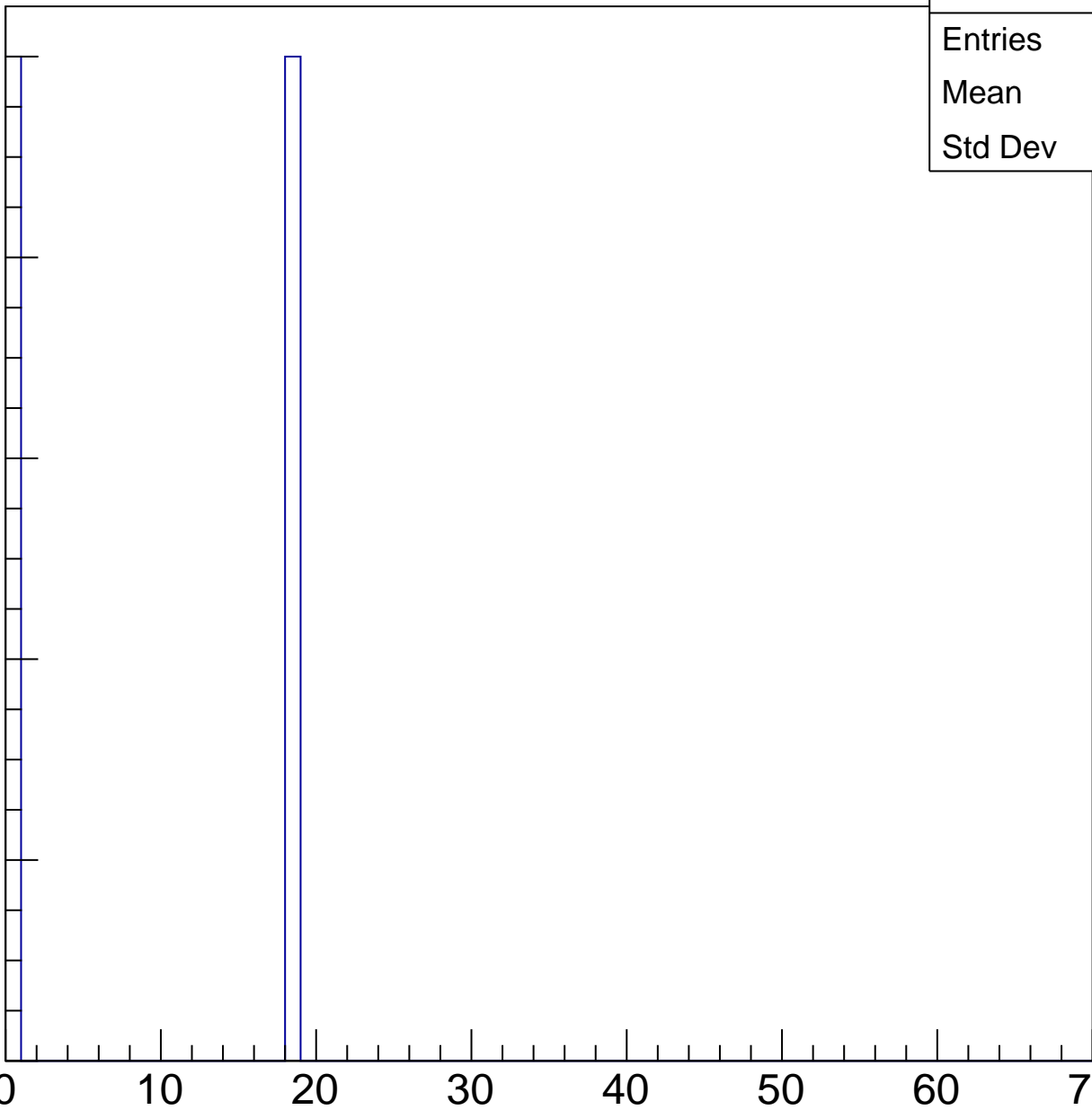
calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	9
Std Dev	9

ampl



B1L103S, U12-ch117, adc0

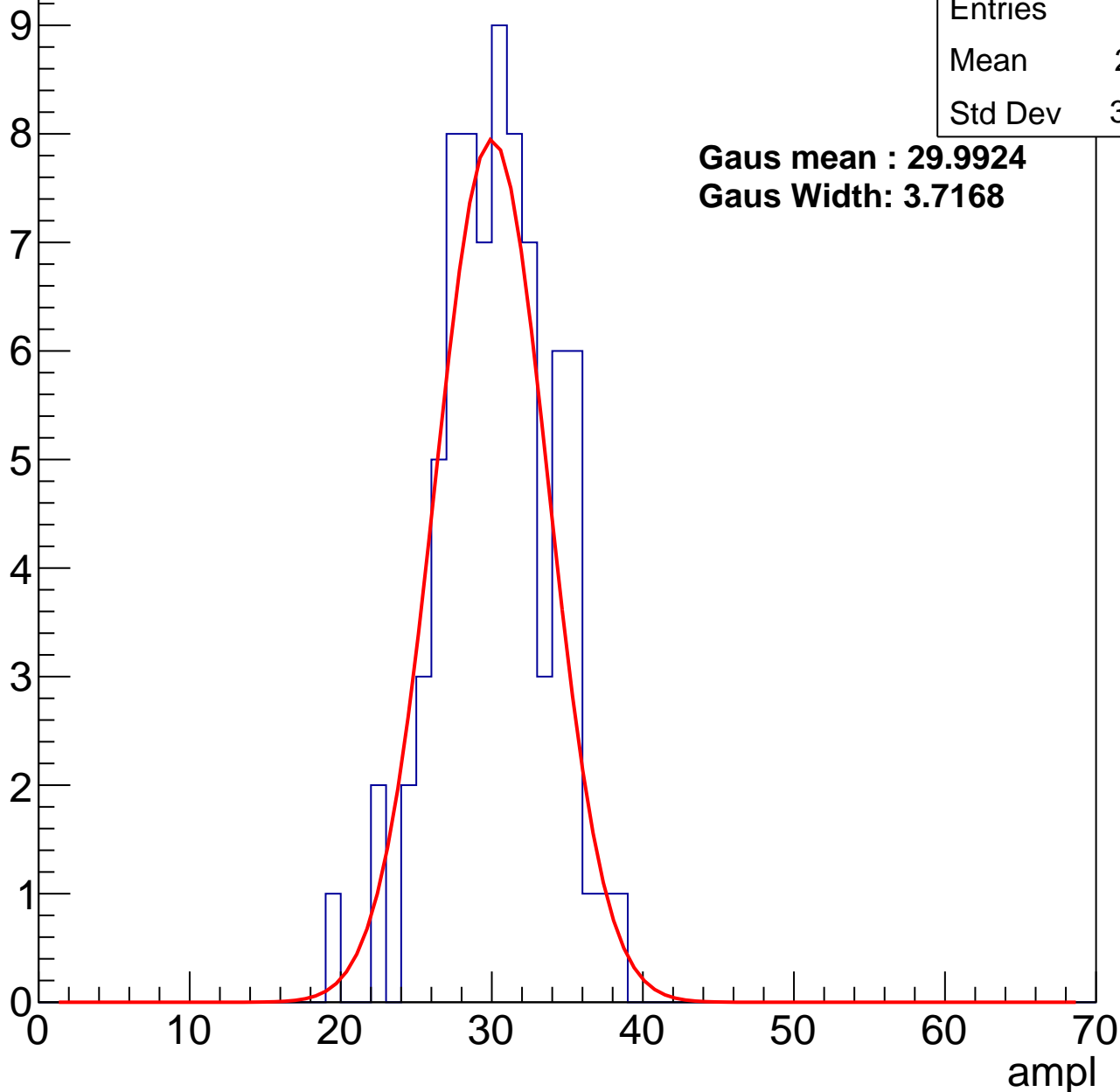
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.81
Std Dev	3.655

Gaus mean : 29.9924

Gaus Width: 3.7168



B1L103S, U12-ch117, adc1

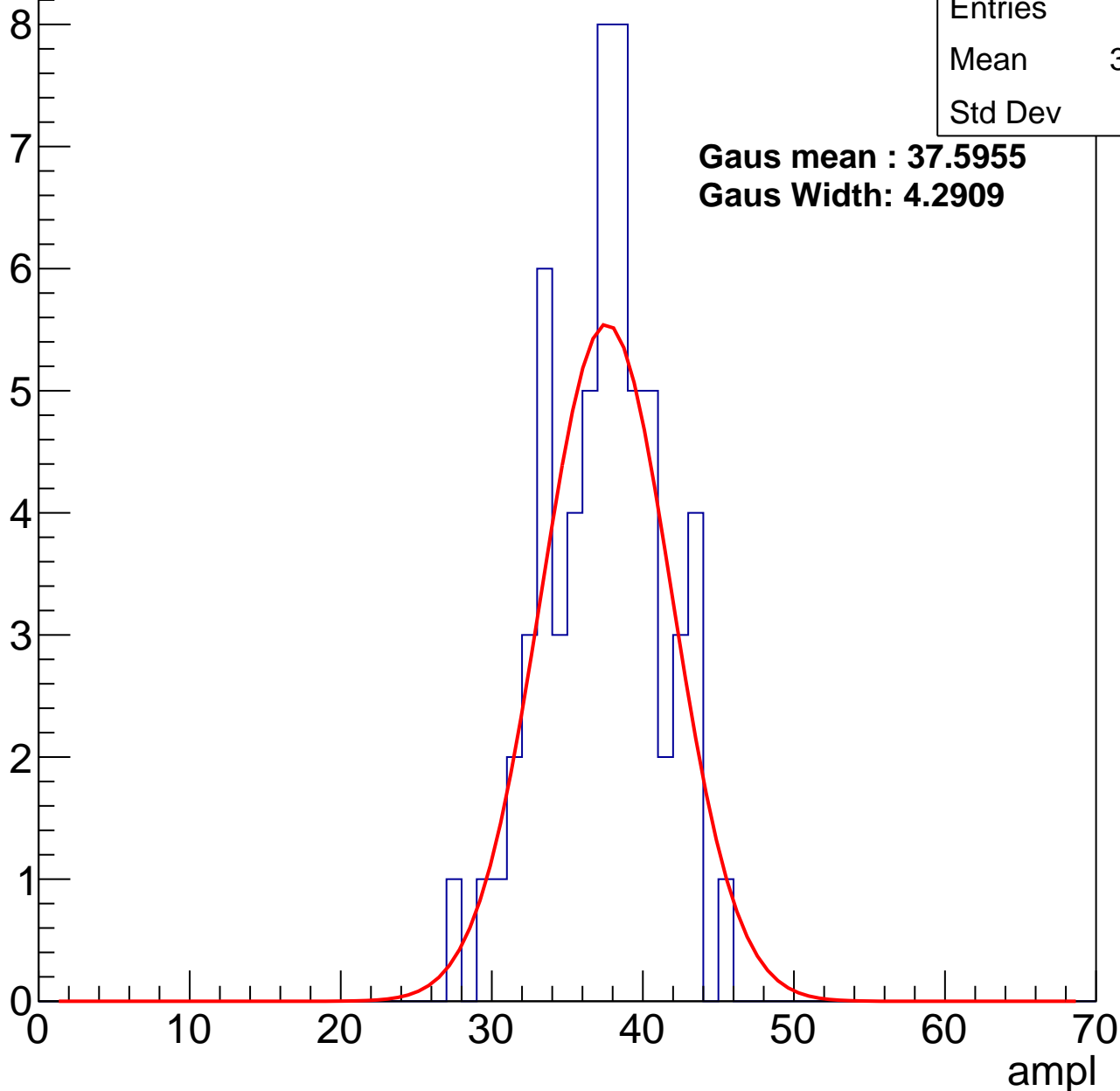
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	36.84
Std Dev	3.79

Gaus mean : 37.5955

Gaus Width: 4.2909



B1L103S, U12-ch117, adc2

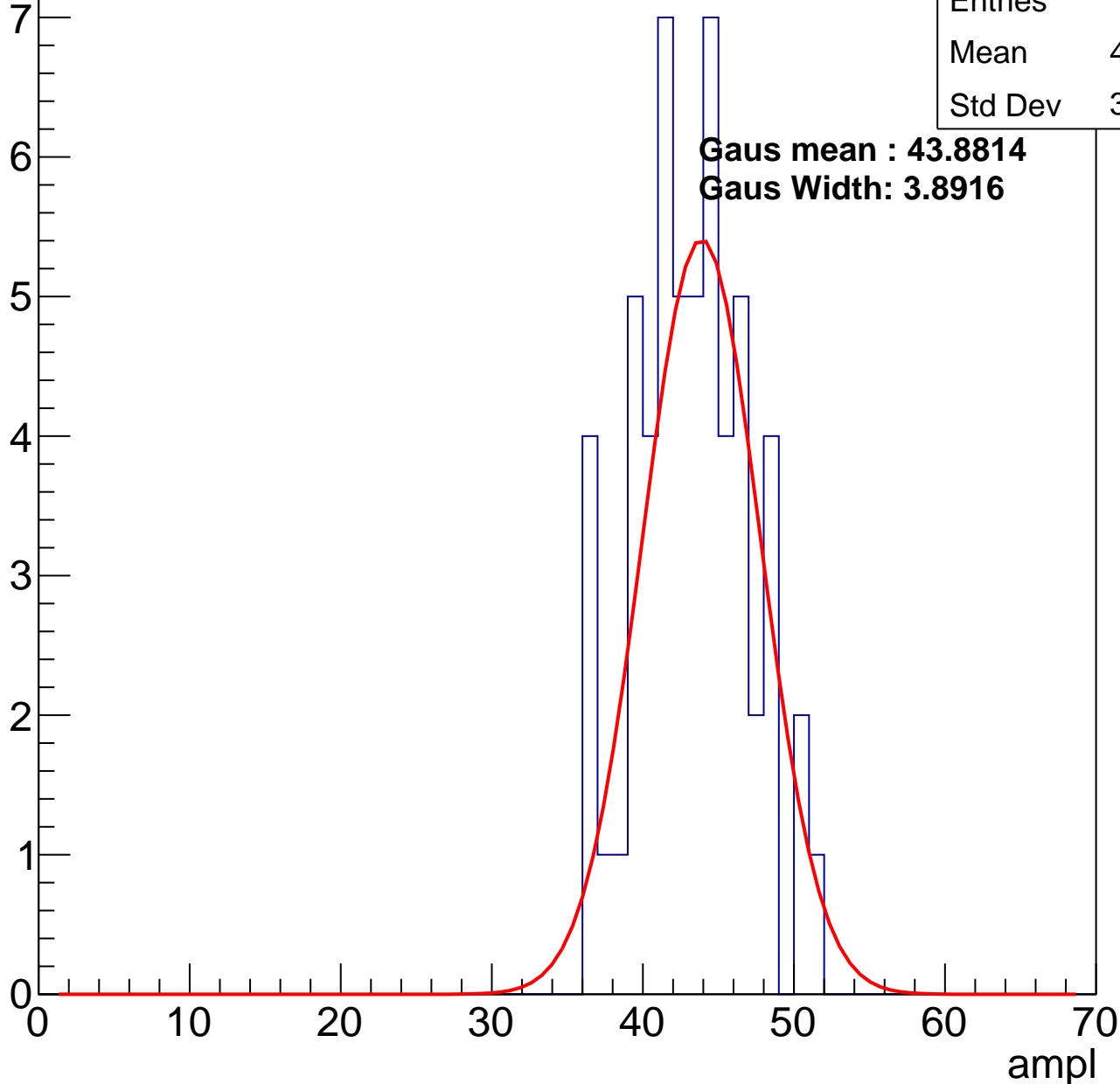
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	42.82
Std Dev	3.666

Gaus mean : 43.8814

Gaus Width: 3.8916

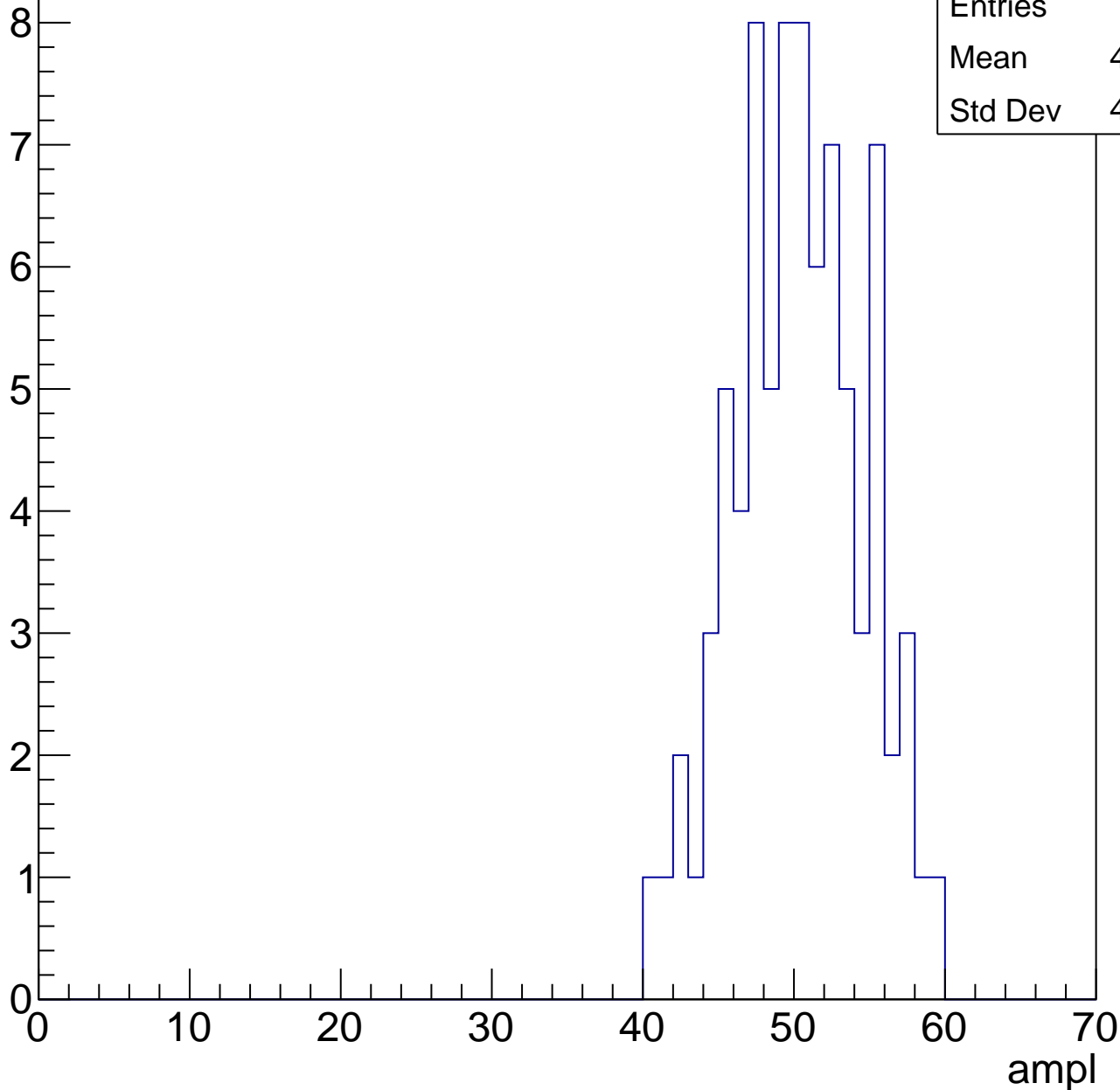


B1L103S, U12-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	49.86
Std Dev	4.186

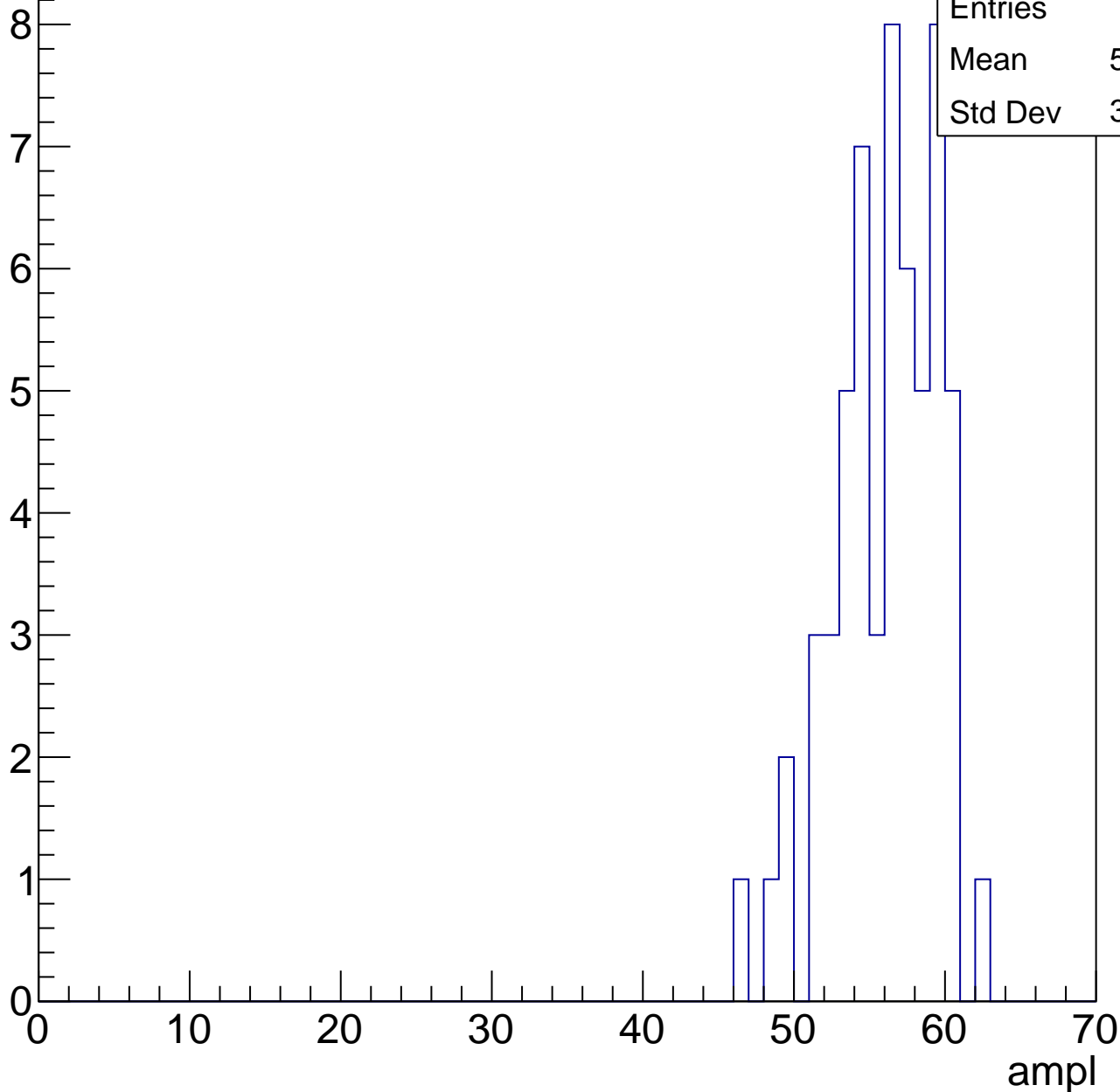


B1L103S, U12-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	55.57
Std Dev	3.384

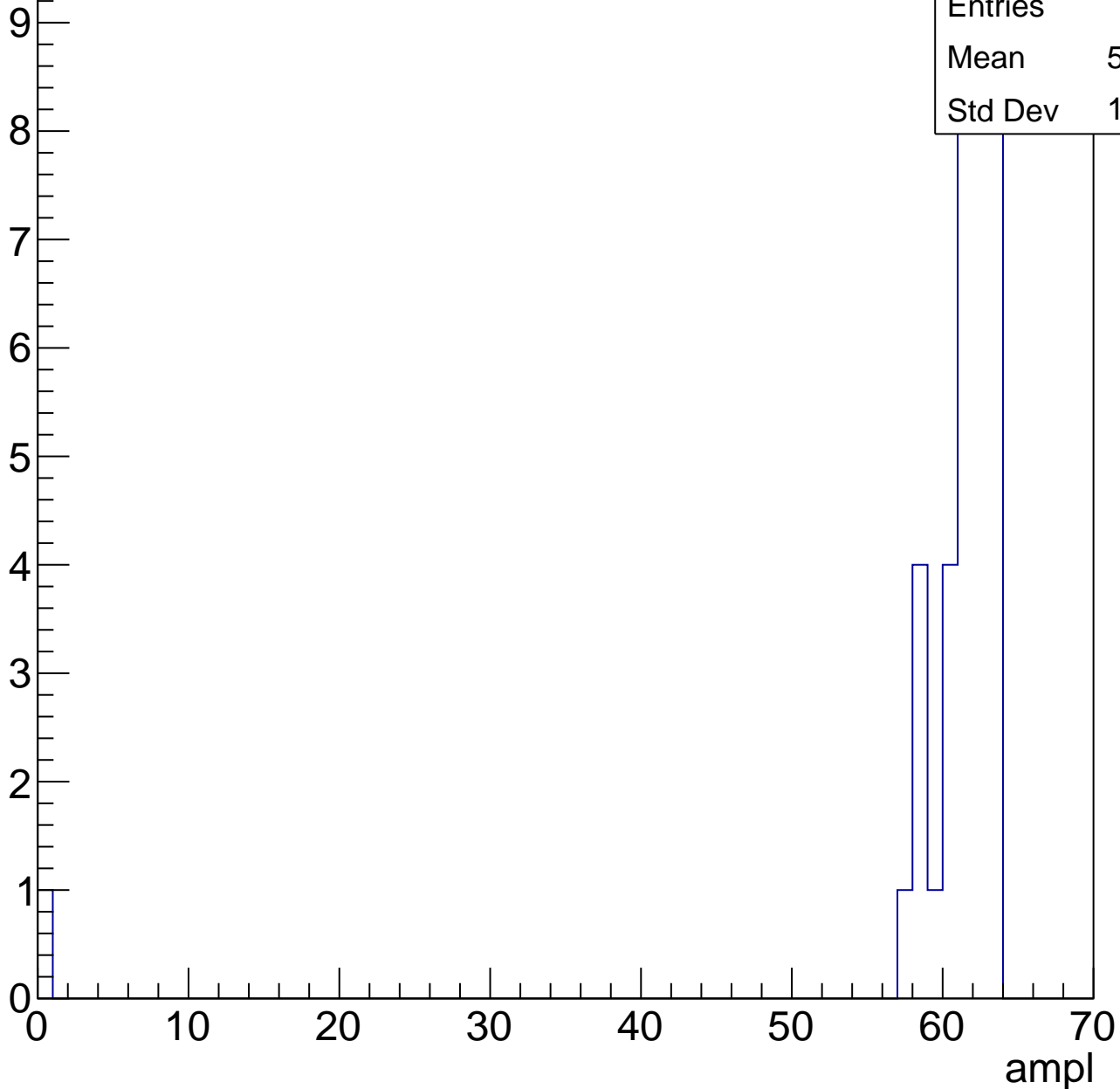


B1L103S, U12-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	59.46
Std Dev	10.05



B1L103S, U12-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

61.5

Std Dev

1.118

B1L103S, U12-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch118, adc0

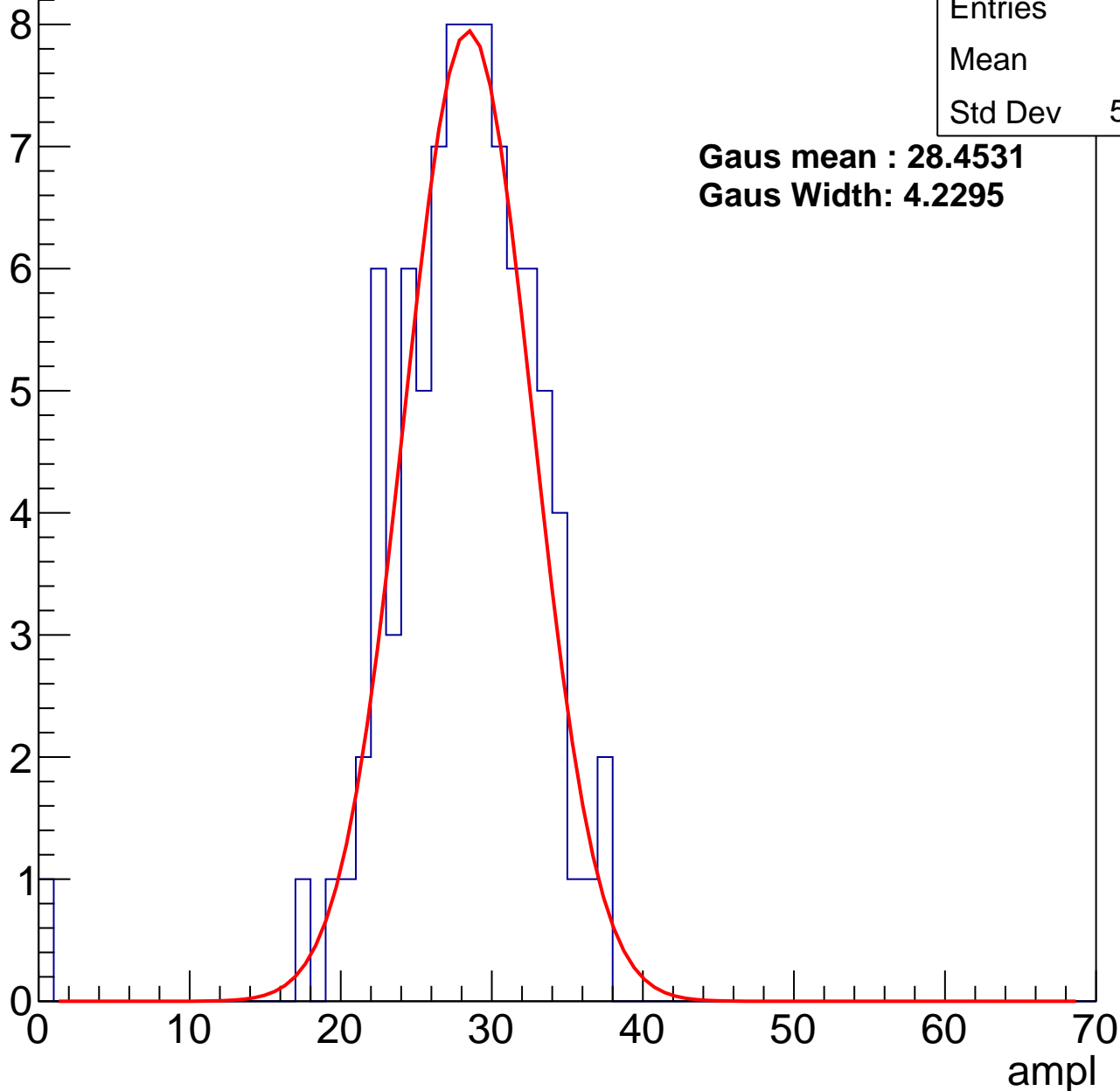
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	27.6
Std Dev	5.118

Gaus mean : 28.4531

Gaus Width: 4.2295



B1L103S, U12-ch118, adc1

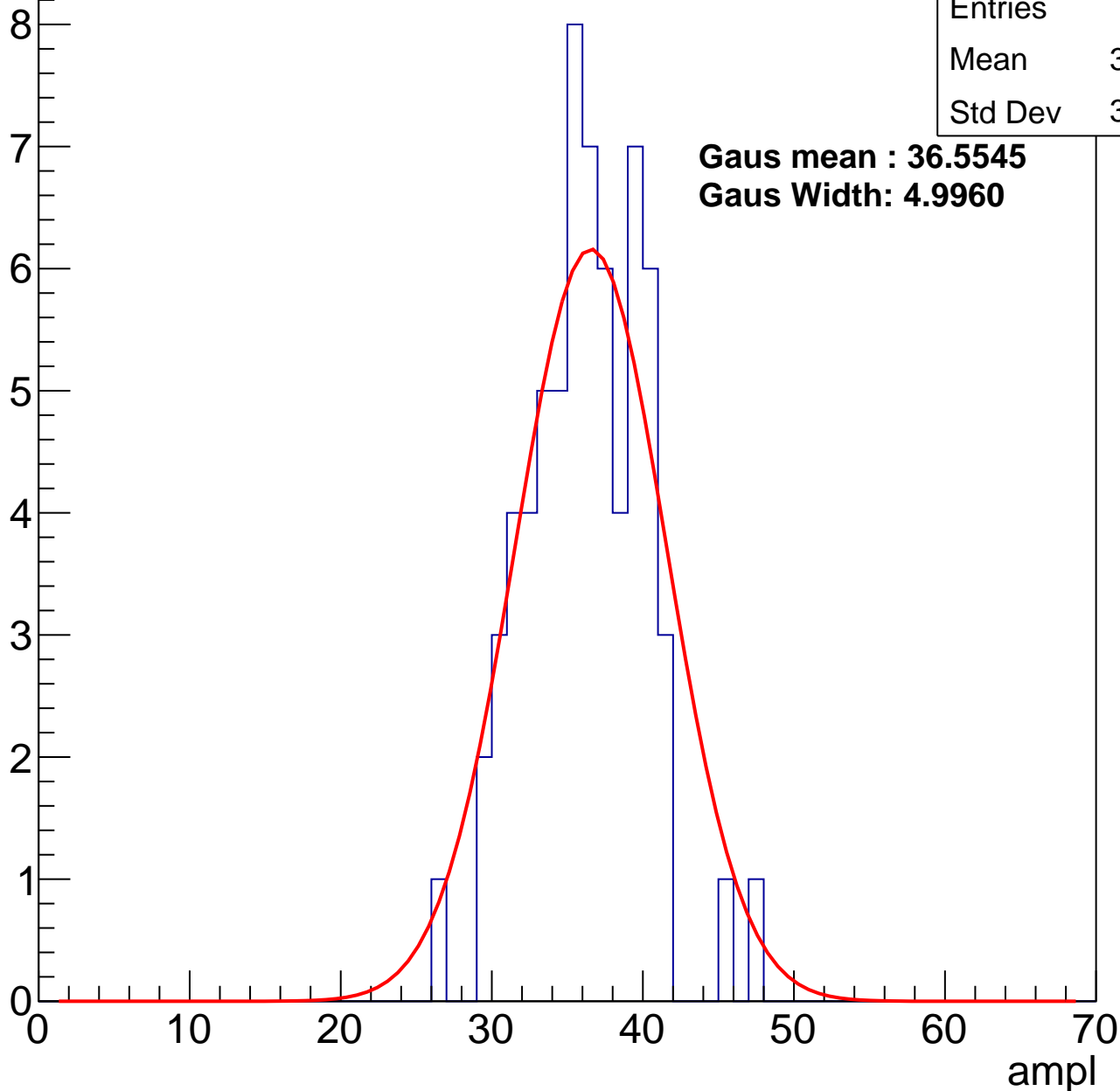
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.75
Std Dev	3.857

Gaus mean : 36.5545

Gaus Width: 4.9960



B1L103S, U12-ch118, adc2

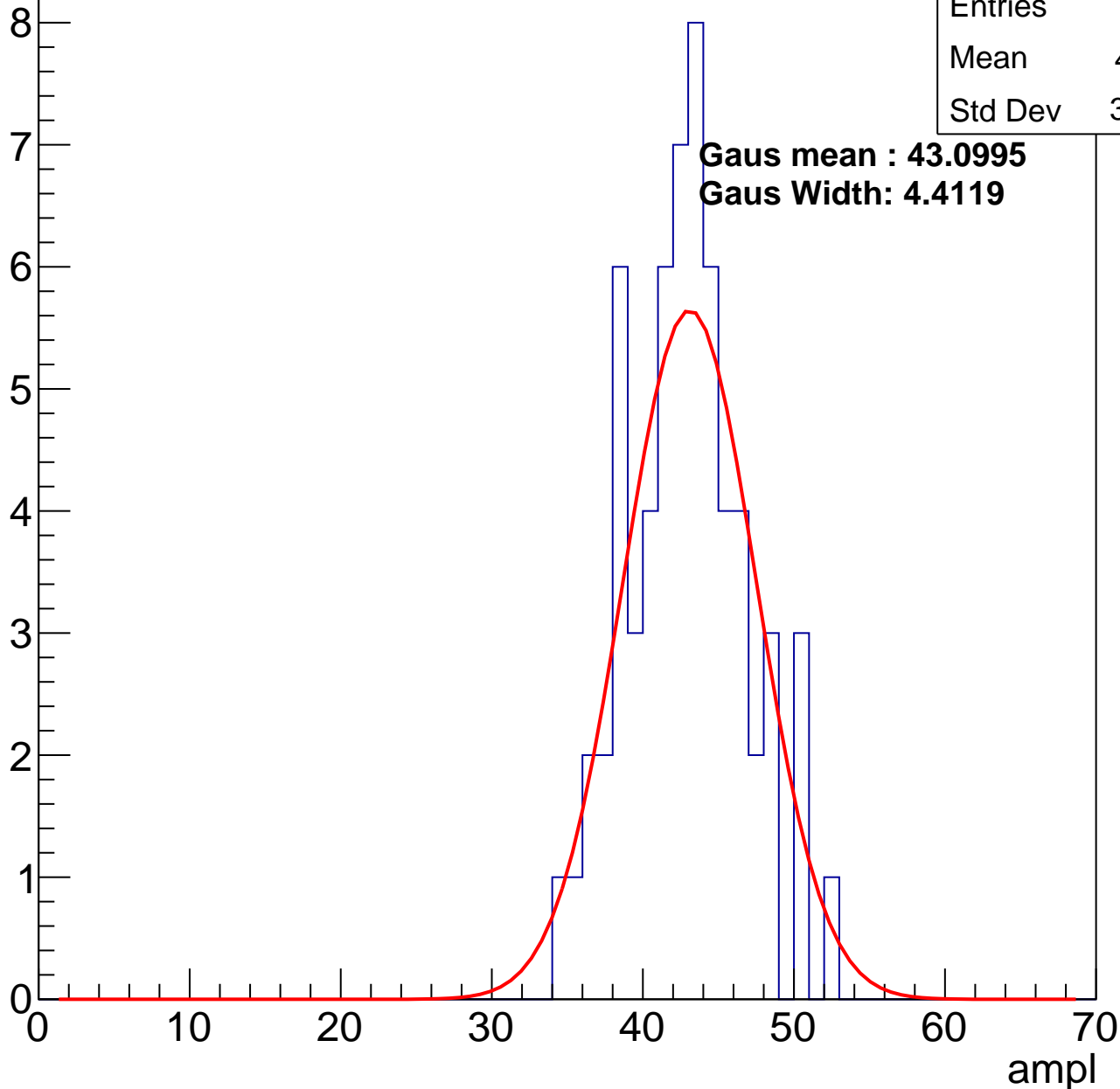
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.41
Std Dev	3.886

Gaus mean : 43.0995

Gaus Width: 4.4119

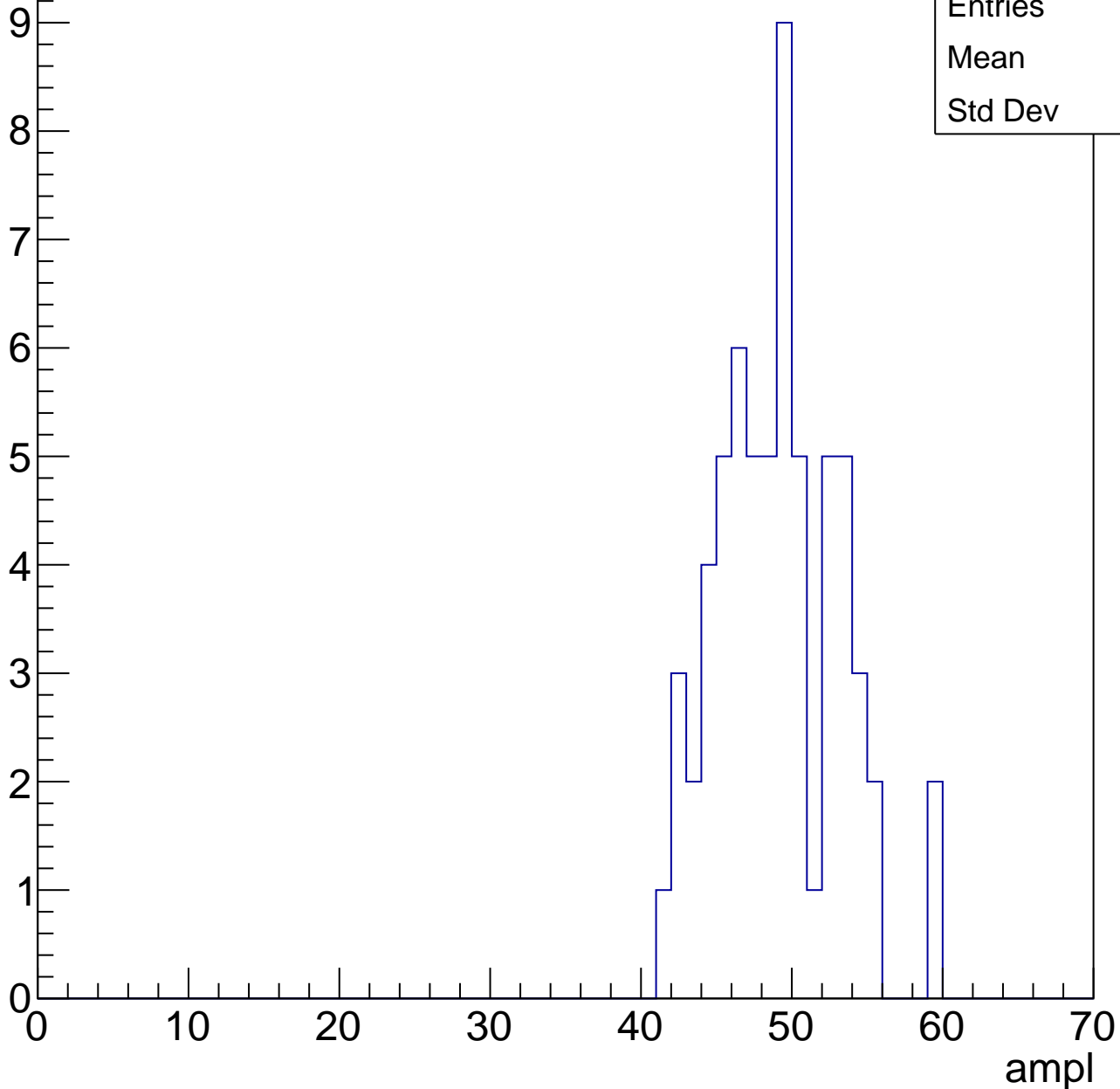


B1L103S, U12-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.6
Std Dev	4.01

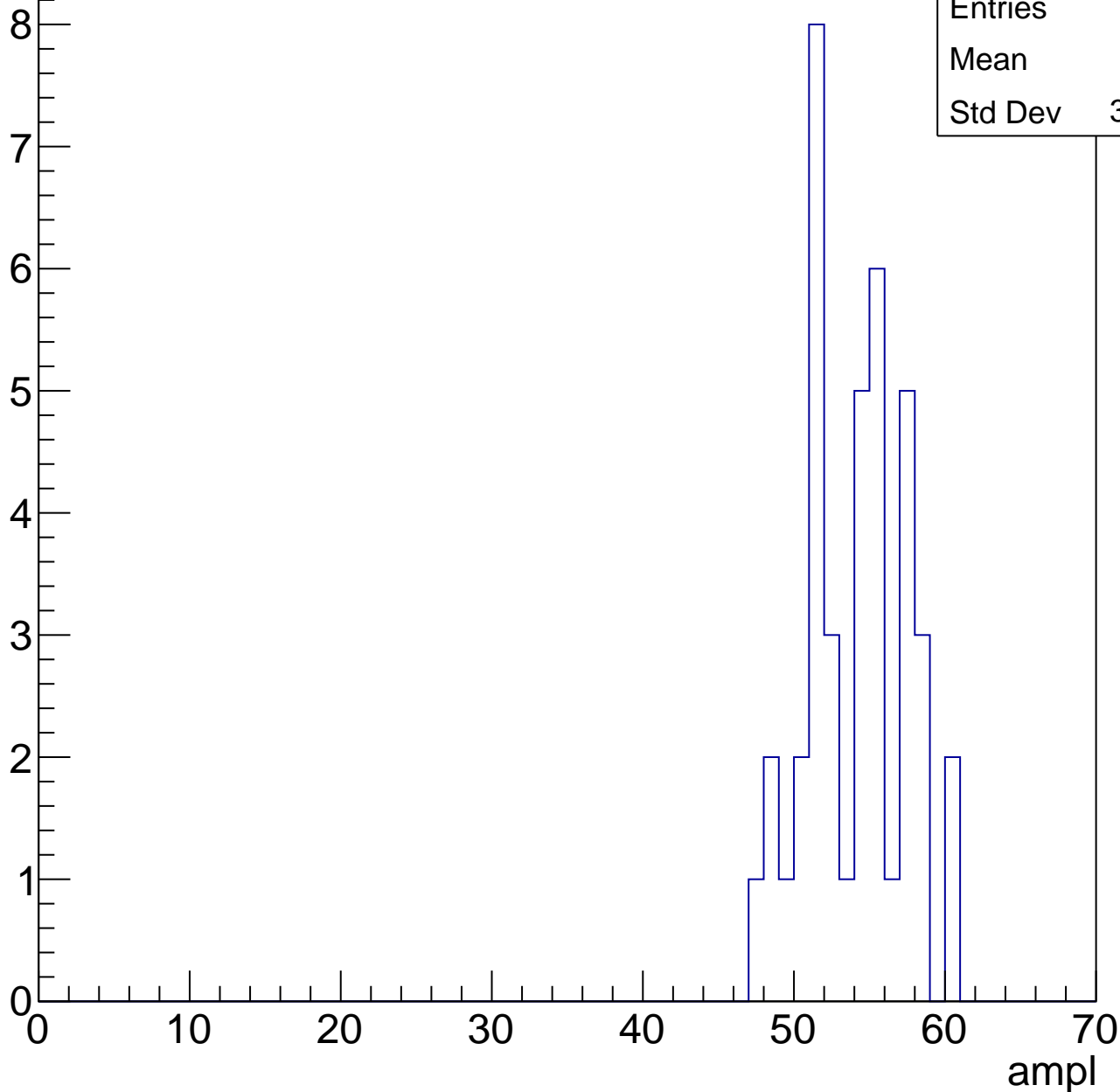


B1L103S, U12-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	53.6
Std Dev	3.285

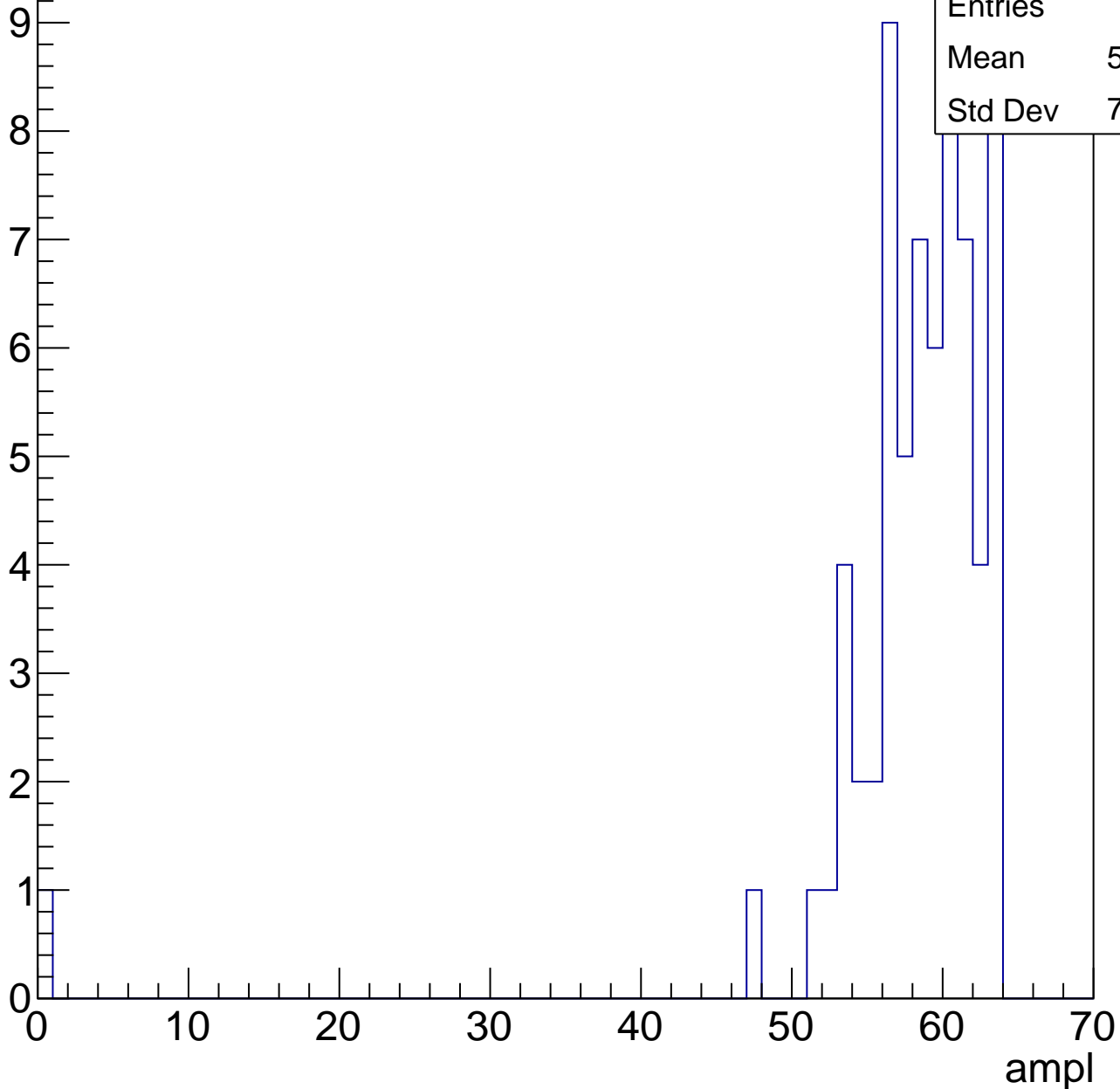


B1L103S, U12-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	57.39
Std Dev	7.883

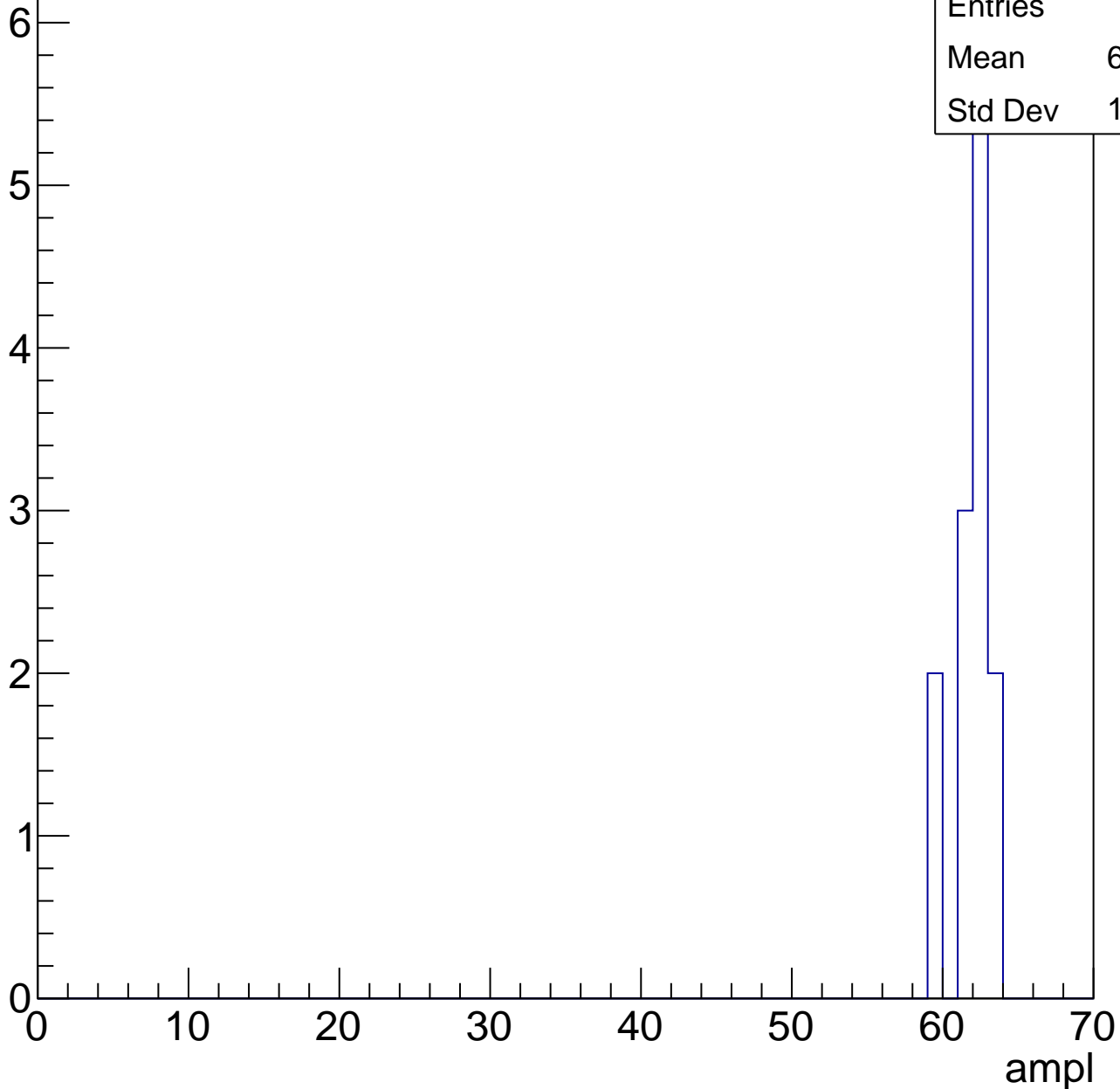


B1L103S, U12-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	61.46
Std Dev	1.216



B1L103S, U12-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch119, adc0

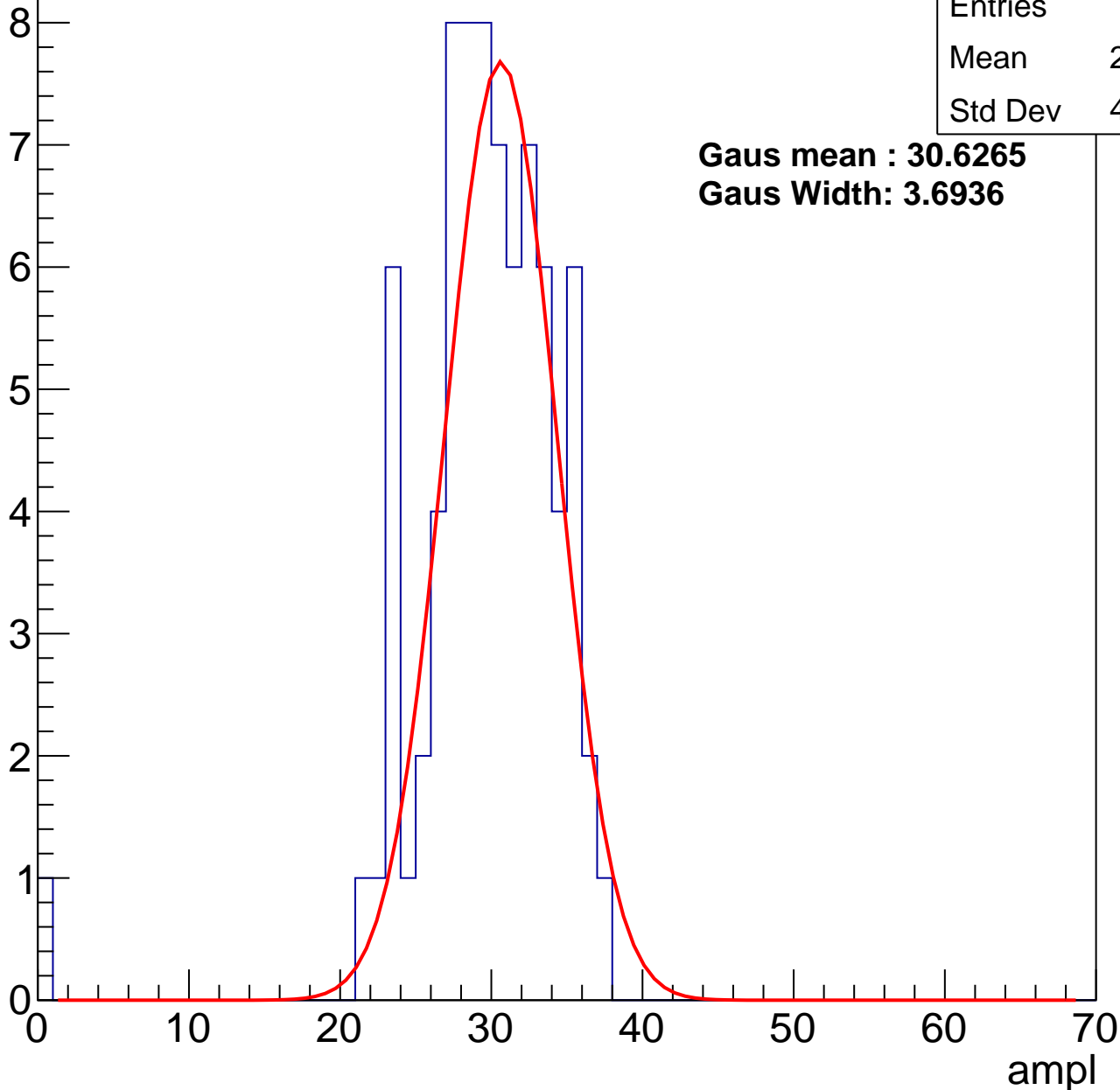
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	29.16
Std Dev	4.992

Gaus mean : 30.6265

Gaus Width: 3.6936



B1L103S, U12-ch119, adc1

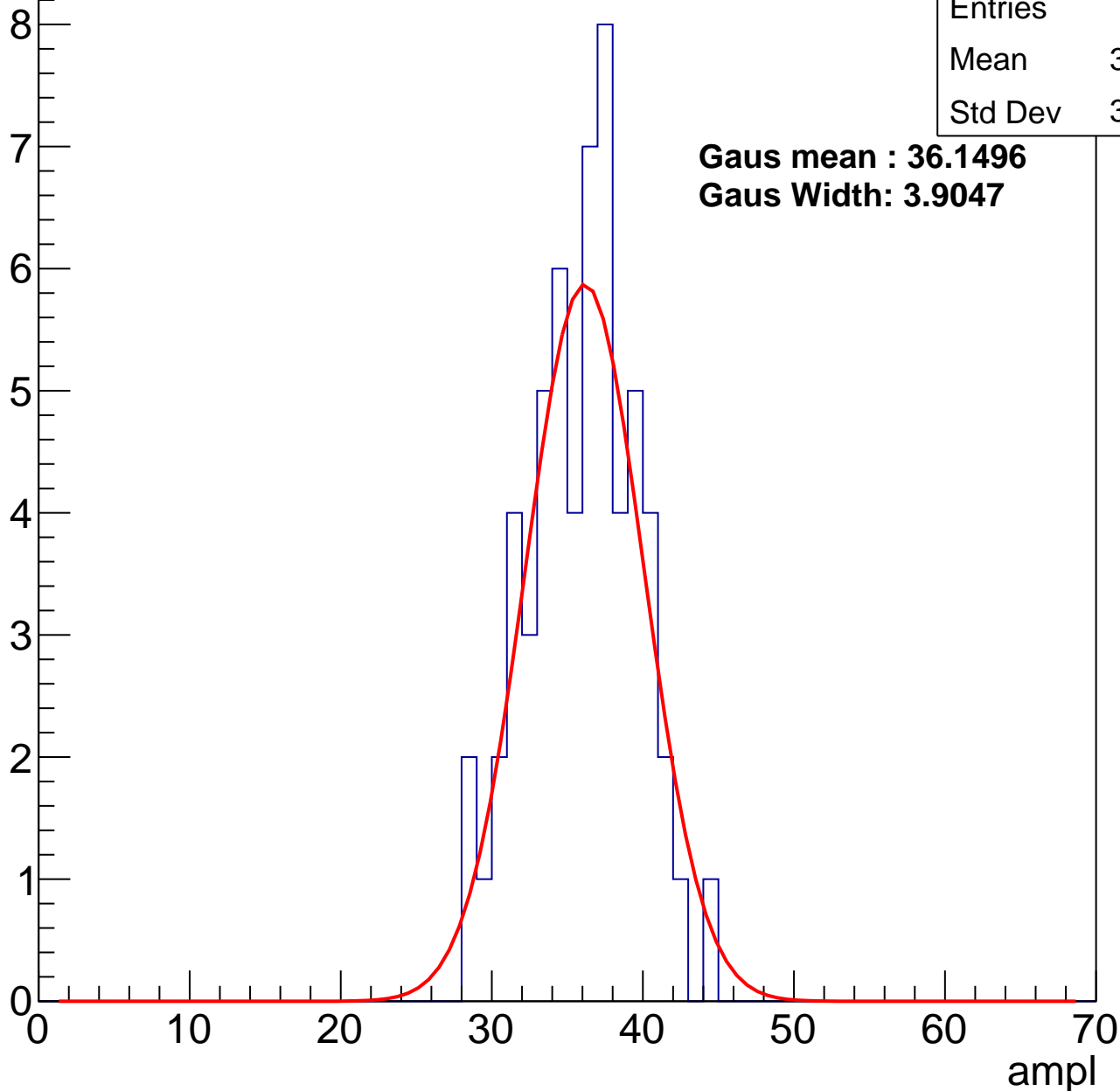
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.54
Std Dev	3.543

Gaus mean : 36.1496

Gaus Width: 3.9047



B1L103S, U12-ch119, adc2

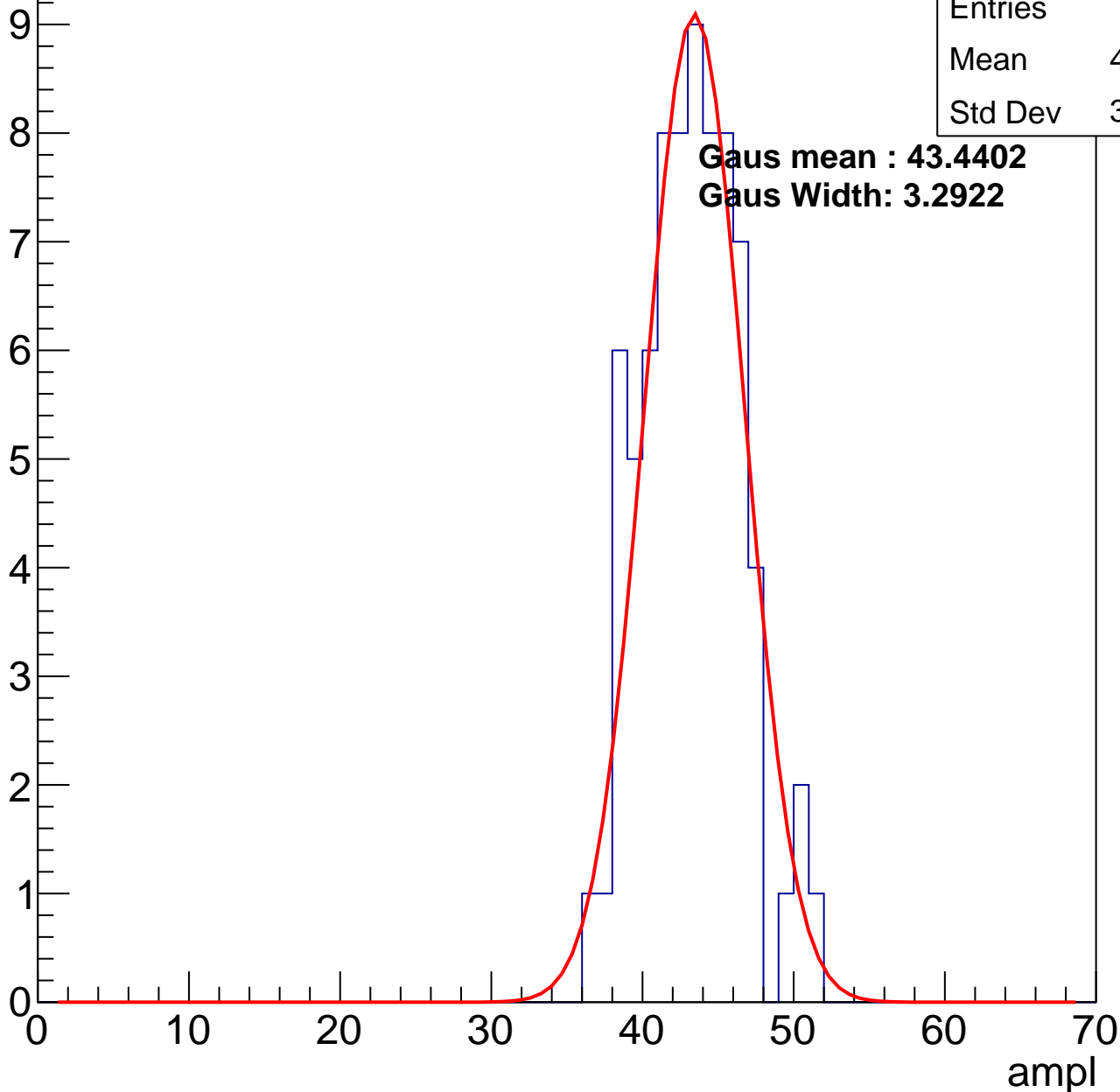
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	42.79
Std Dev	3.205

Gaus mean : 43.4402

Gaus Width: 3.2922

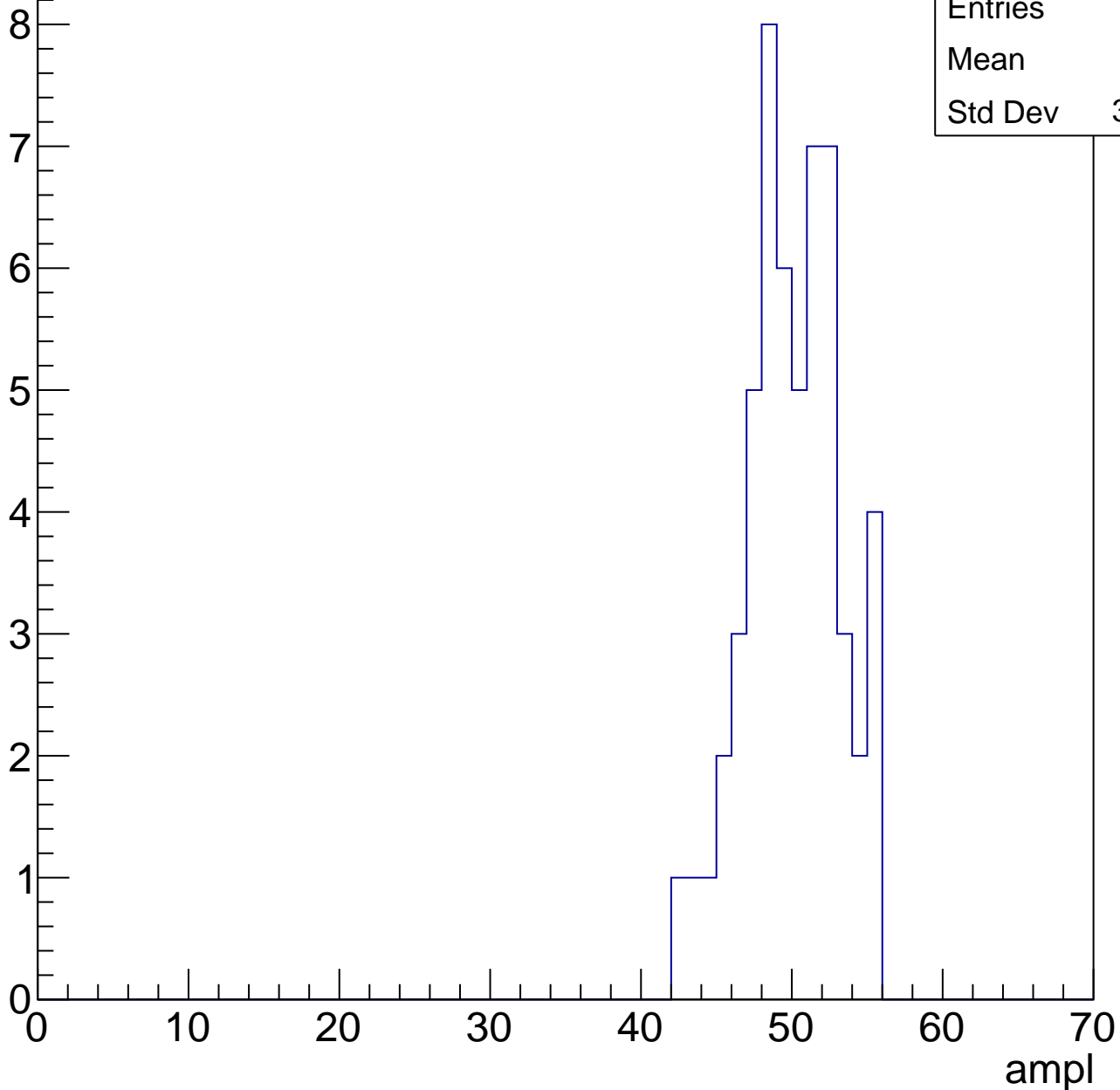


B1L103S, U12-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	49.6
Std Dev	3.061

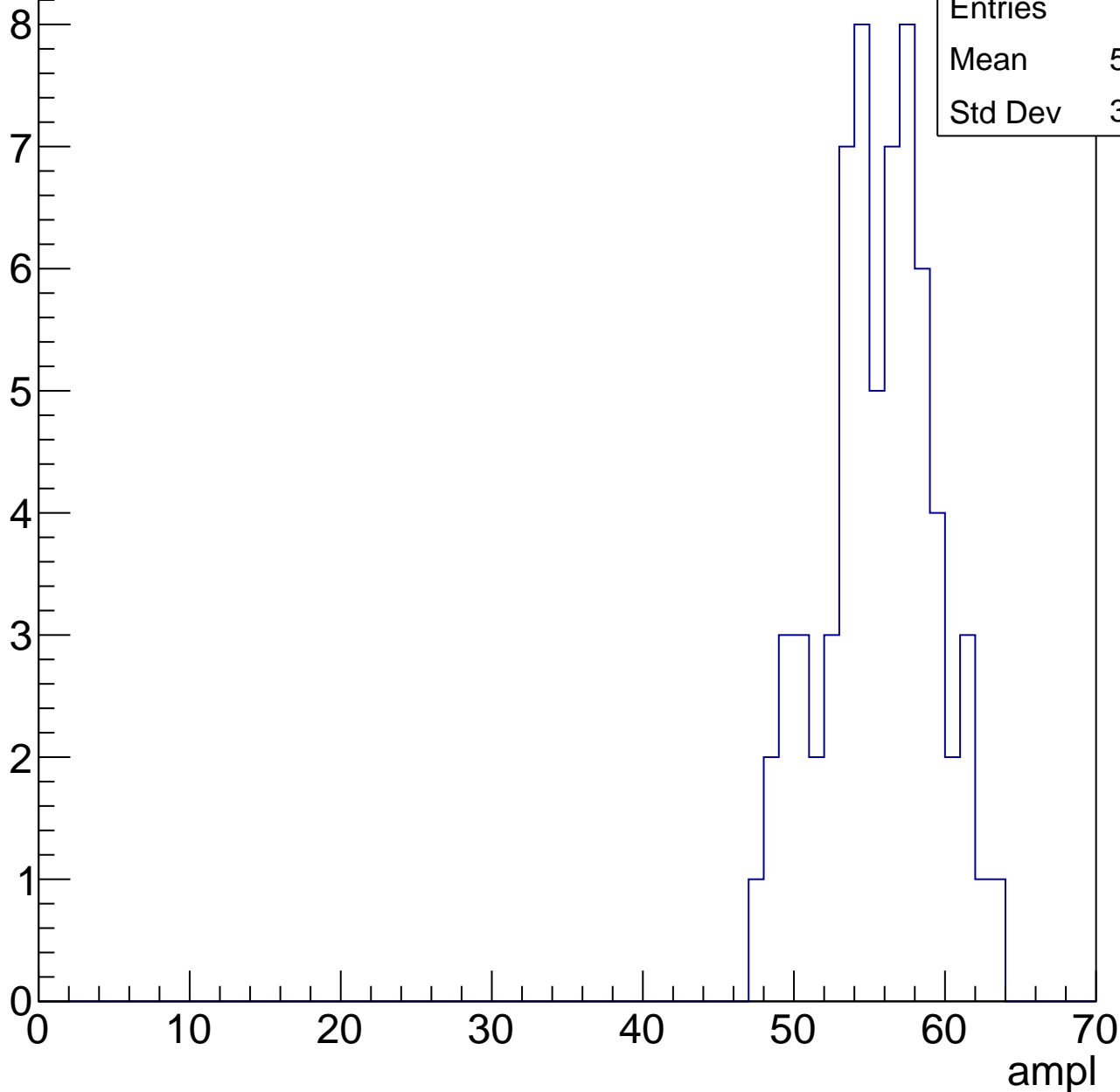


B1L103S, U12-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.09
Std Dev	3.625



B1L103S, U12-ch119, adc5

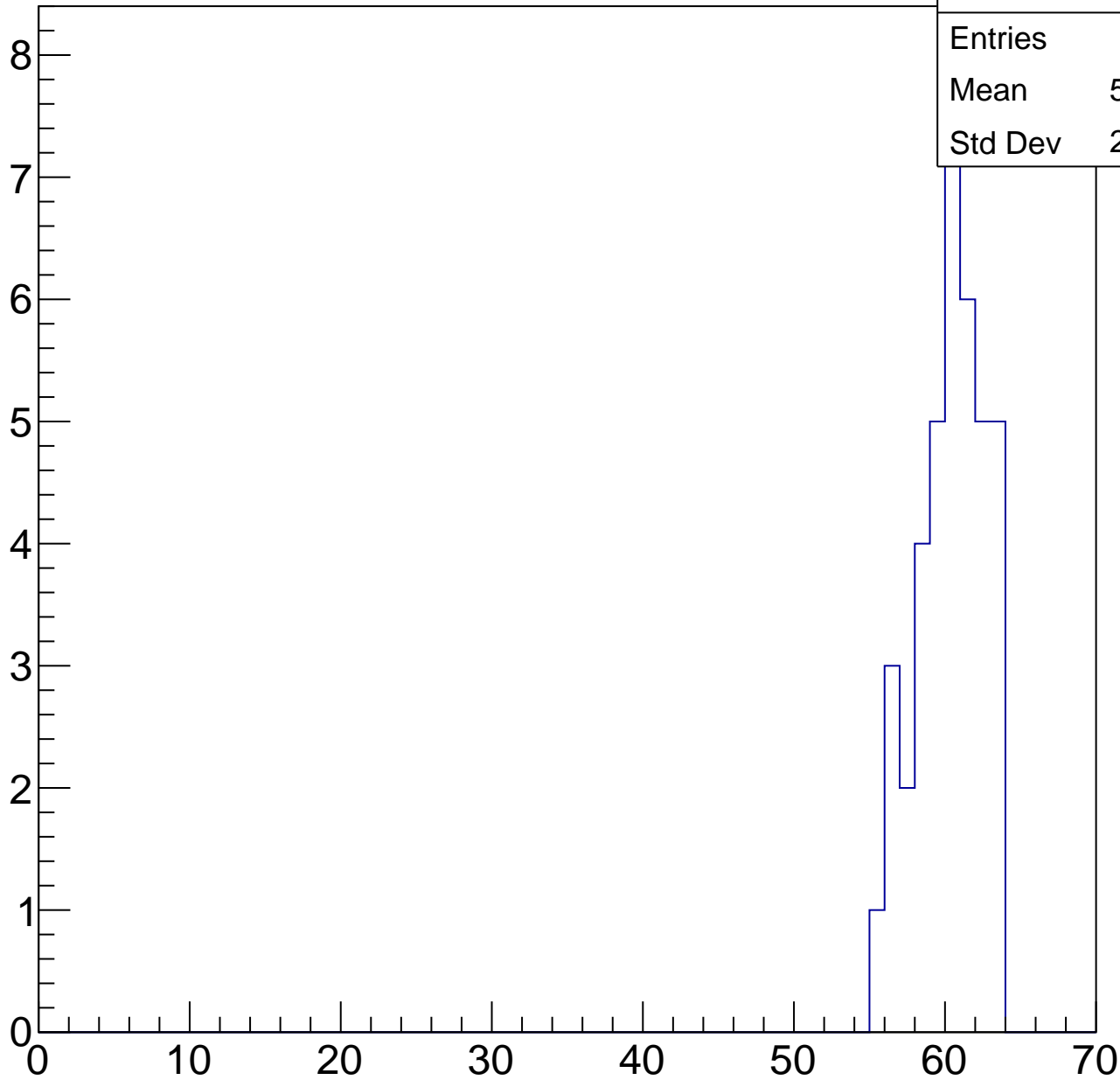
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.87
Std Dev	2.162

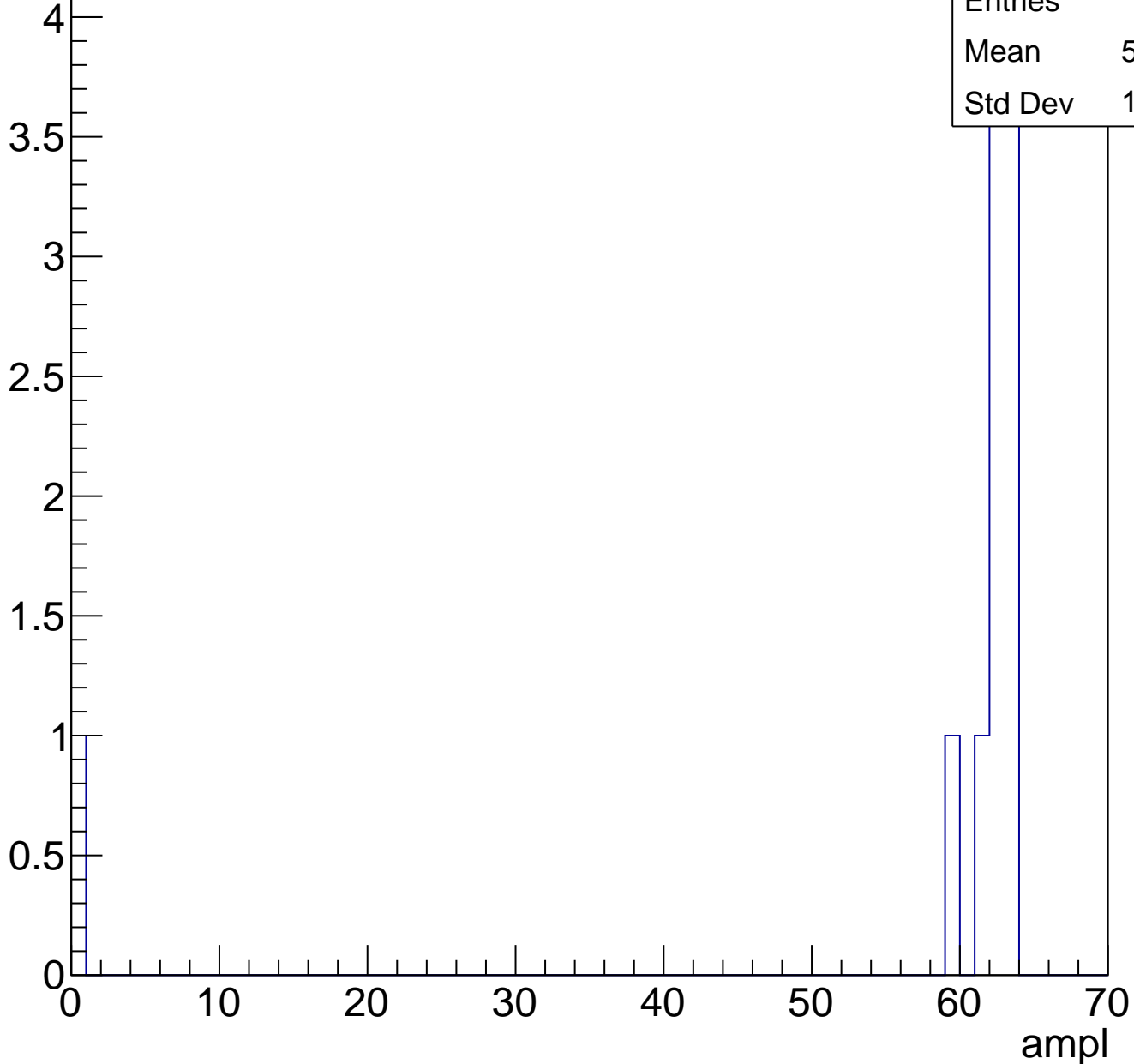
ampl



B1L103S, U12-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

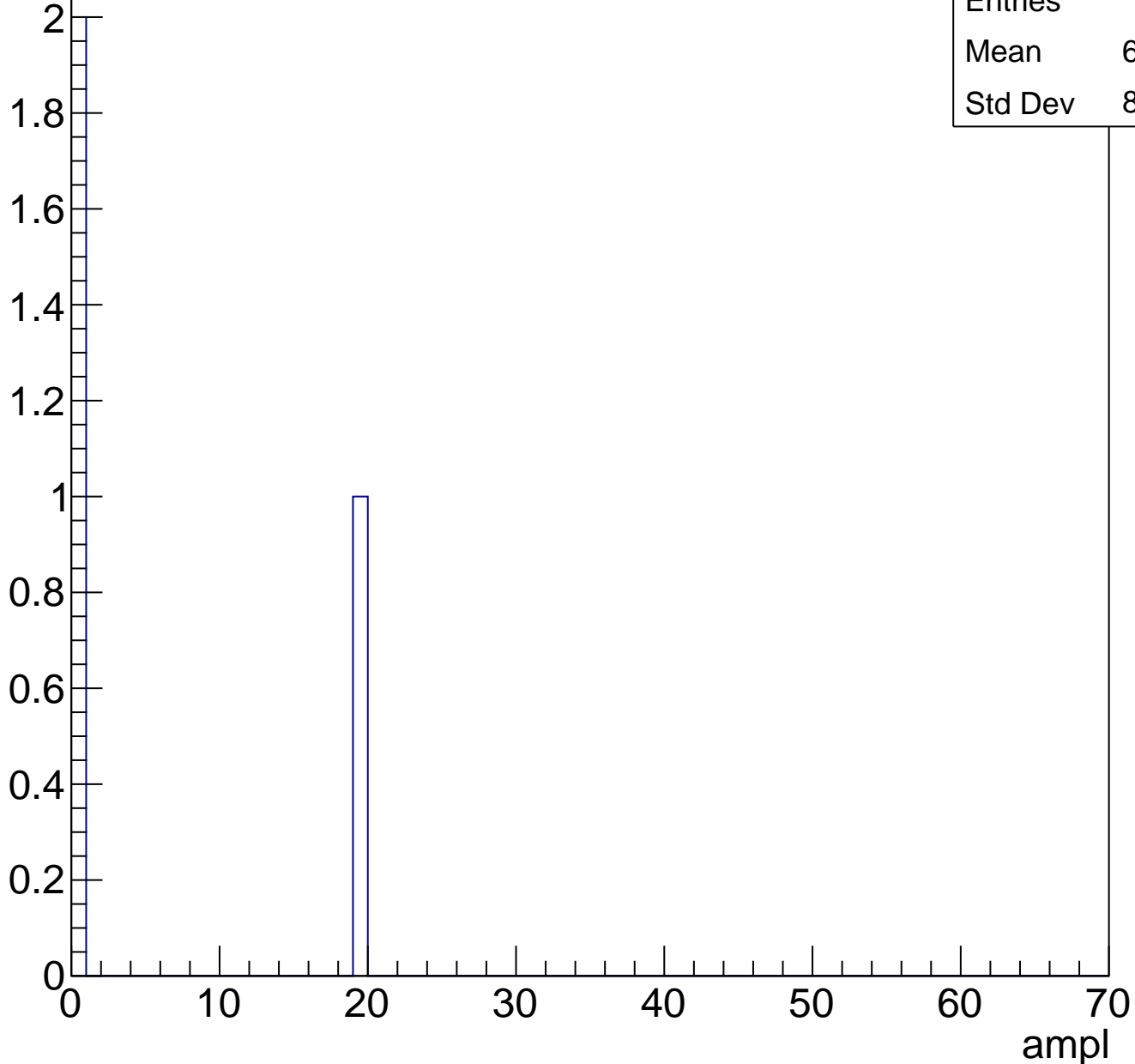
Entry



B1L103S, U12-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L103S, U12-ch120, adc0

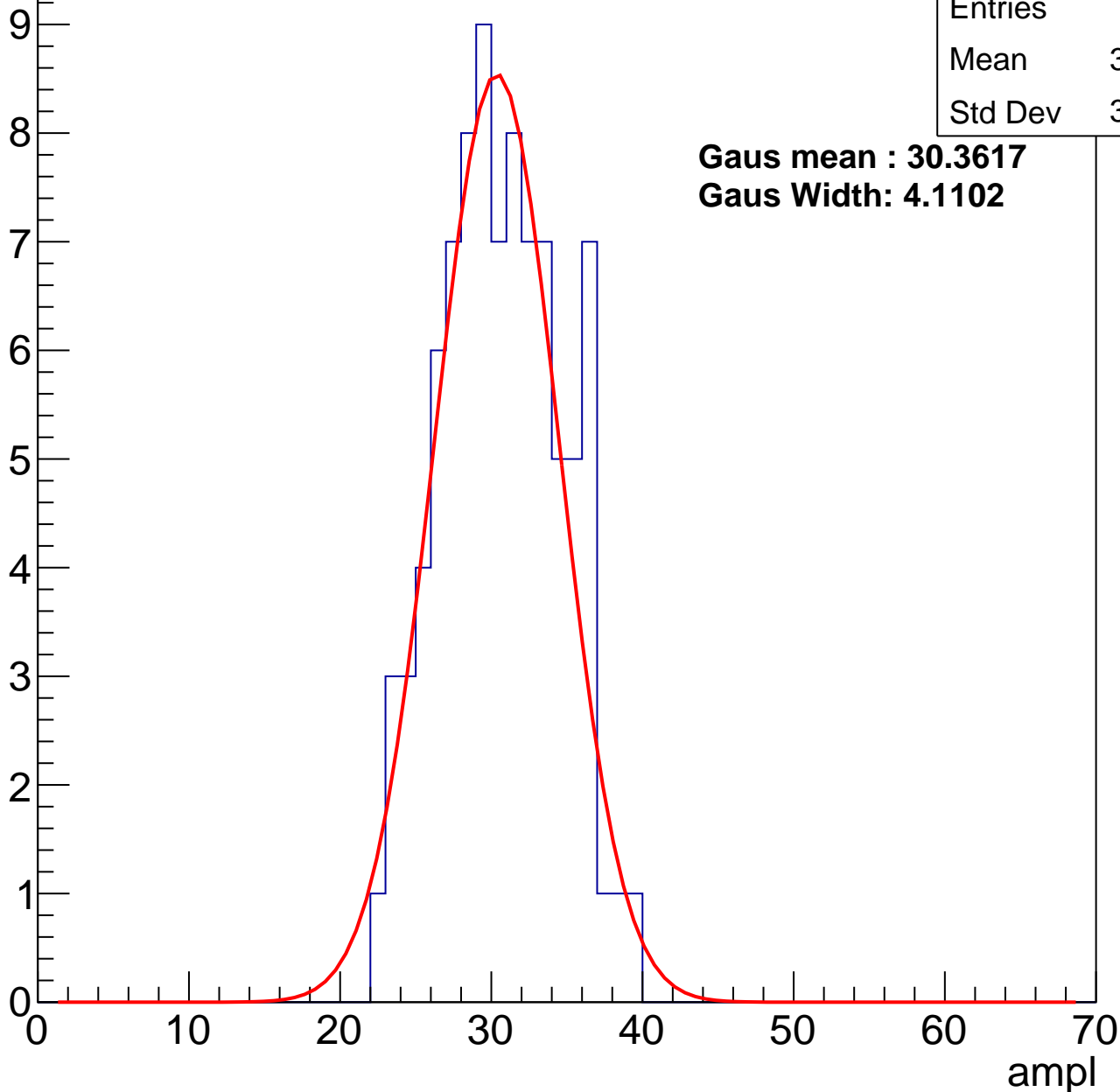
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	90
Mean	30.19
Std Dev	3.896

Gaus mean : 30.3617

Gaus Width: 4.1102



B1L103S, U12-ch120, adc1

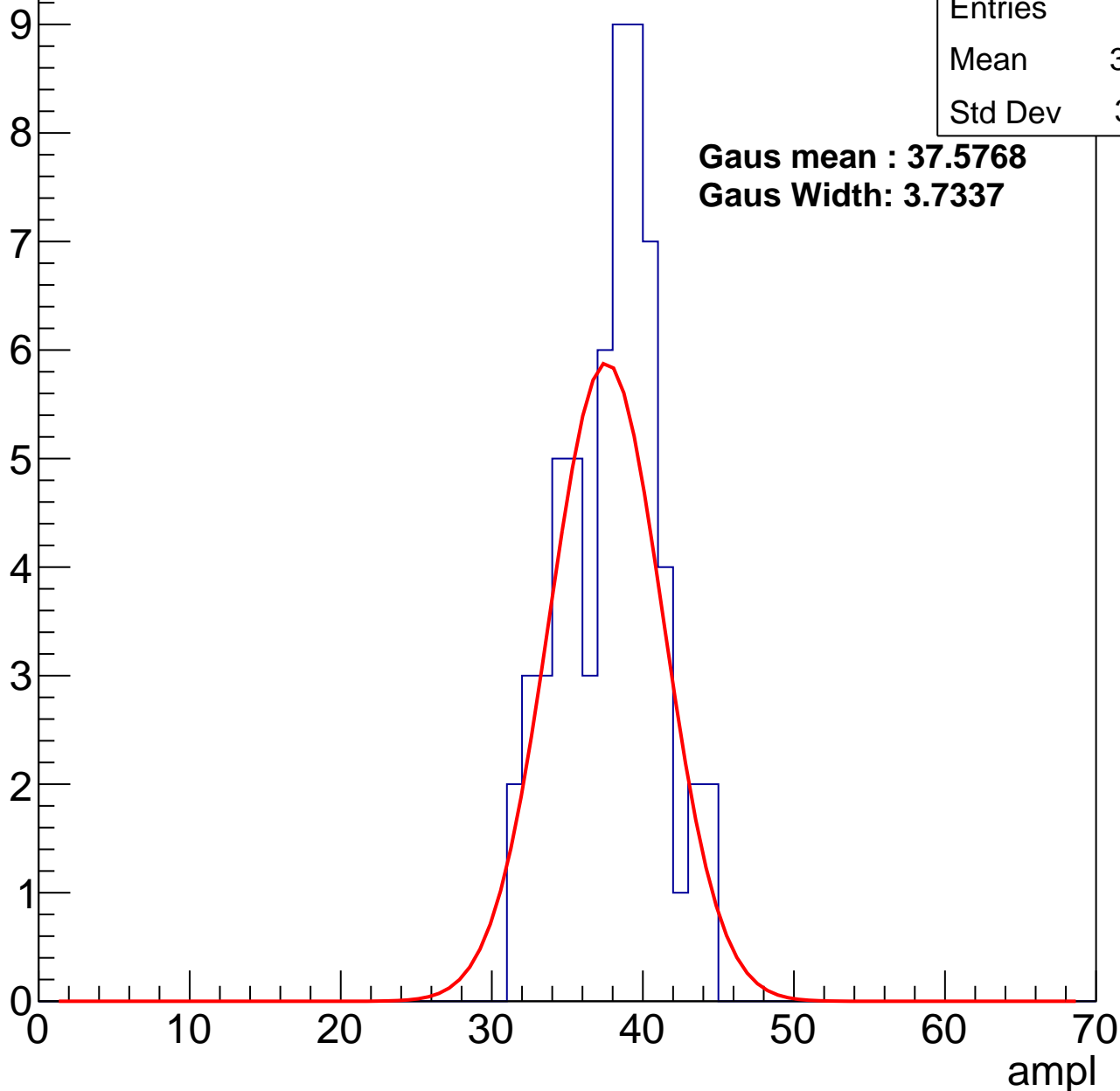
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.46
Std Dev	3.181

Gaus mean : 37.5768

Gaus Width: 3.7337



B1L103S, U12-ch120, adc2

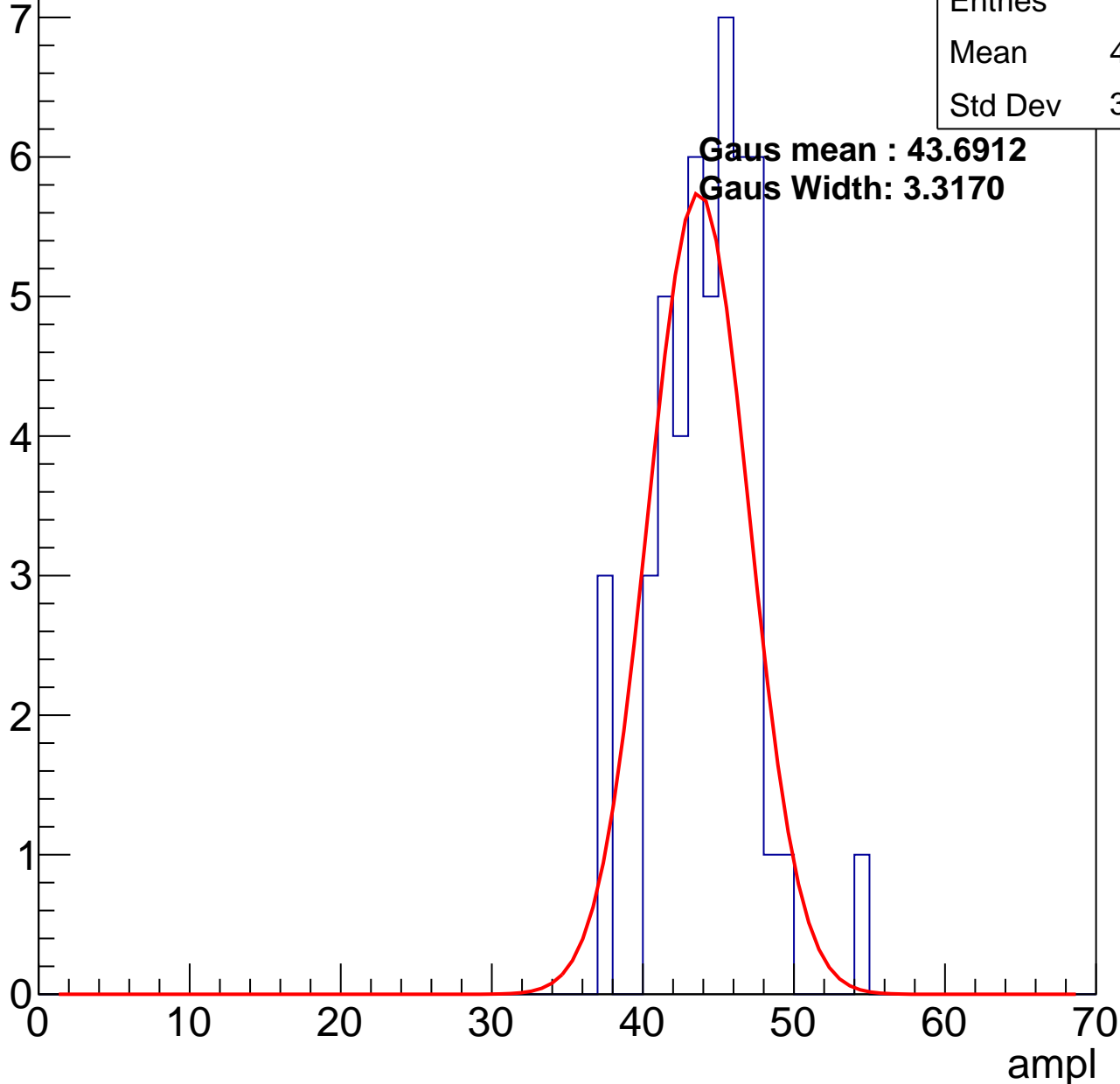
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	43.88
Std Dev	3.186

Gaus mean : 43.6912

Gaus Width: 3.3170

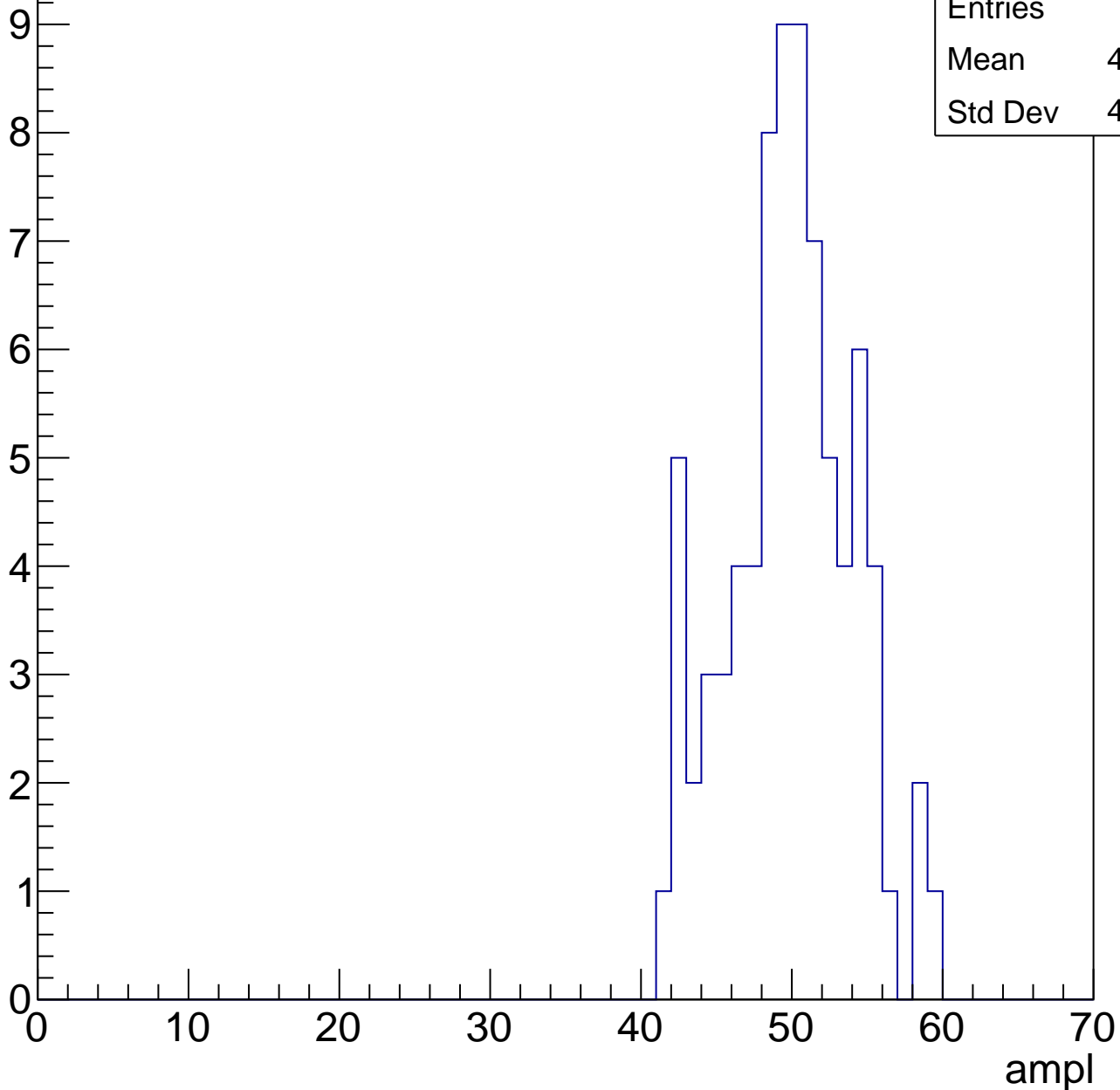


B1L103S, U12-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	49.42
Std Dev	4.084

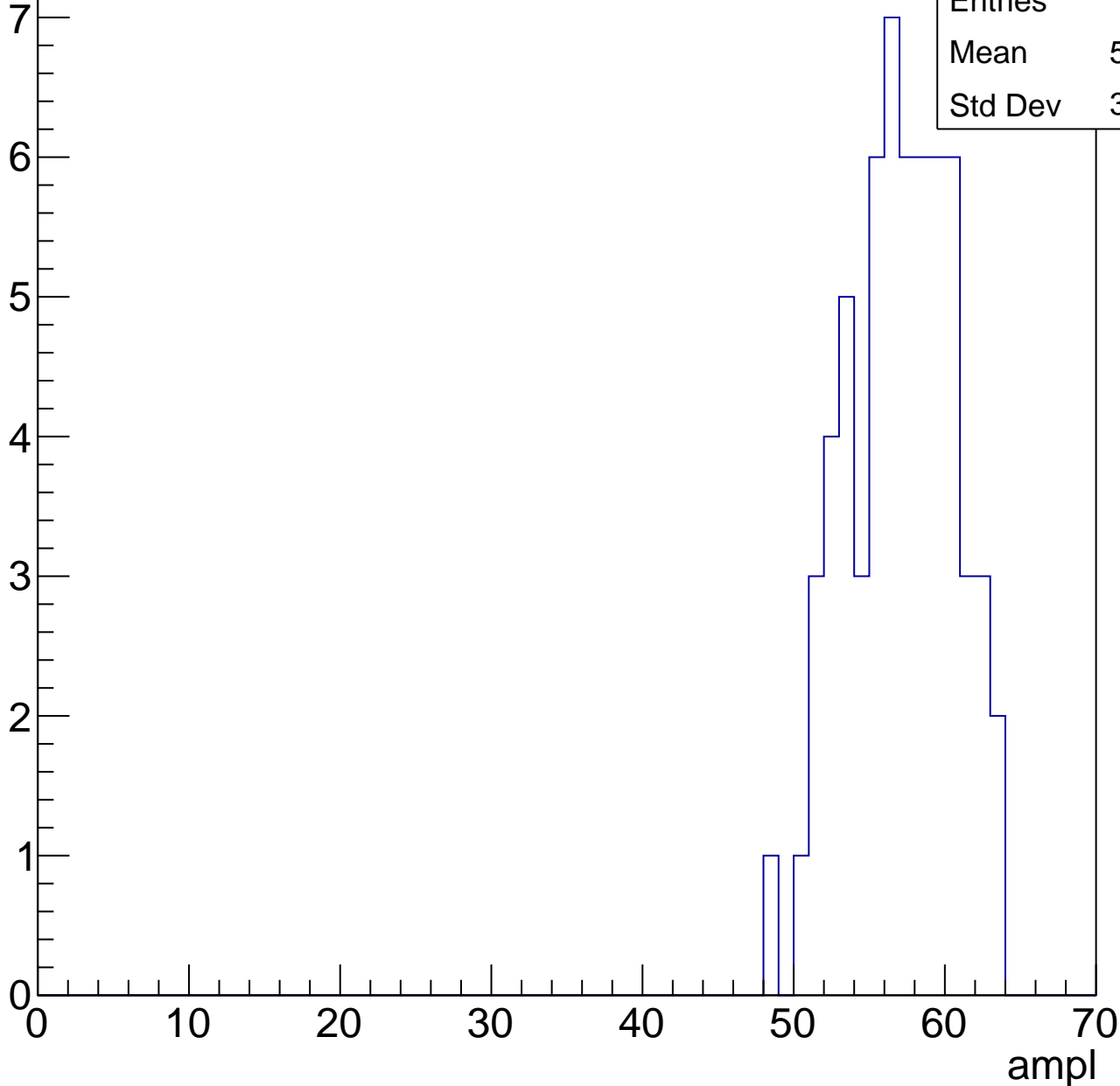


B1L103S, U12-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.56
Std Dev	3.462

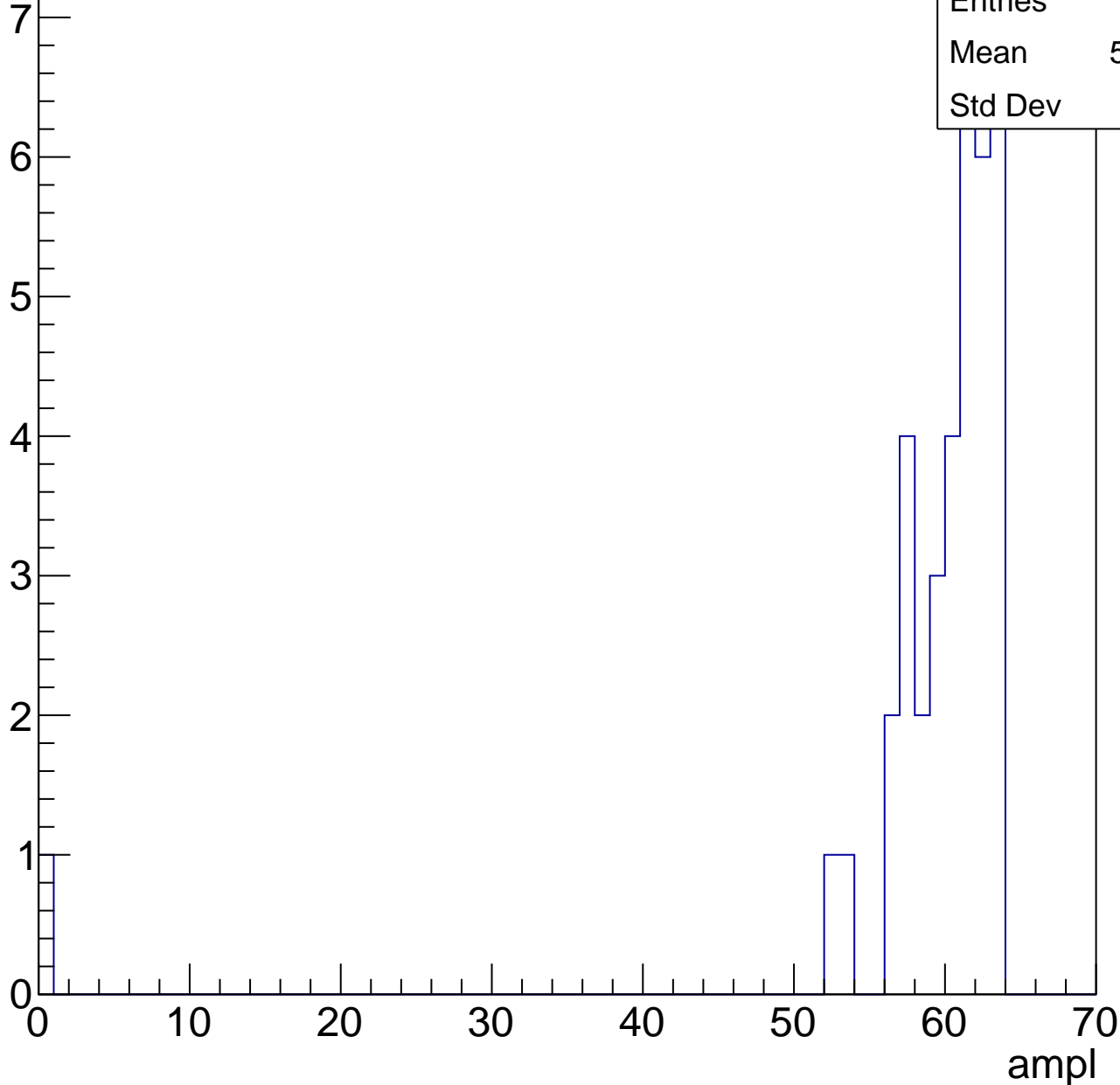


B1L103S, U12-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	58.37
Std Dev	9.98



B1L103S, U12-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U12-ch121, adc0

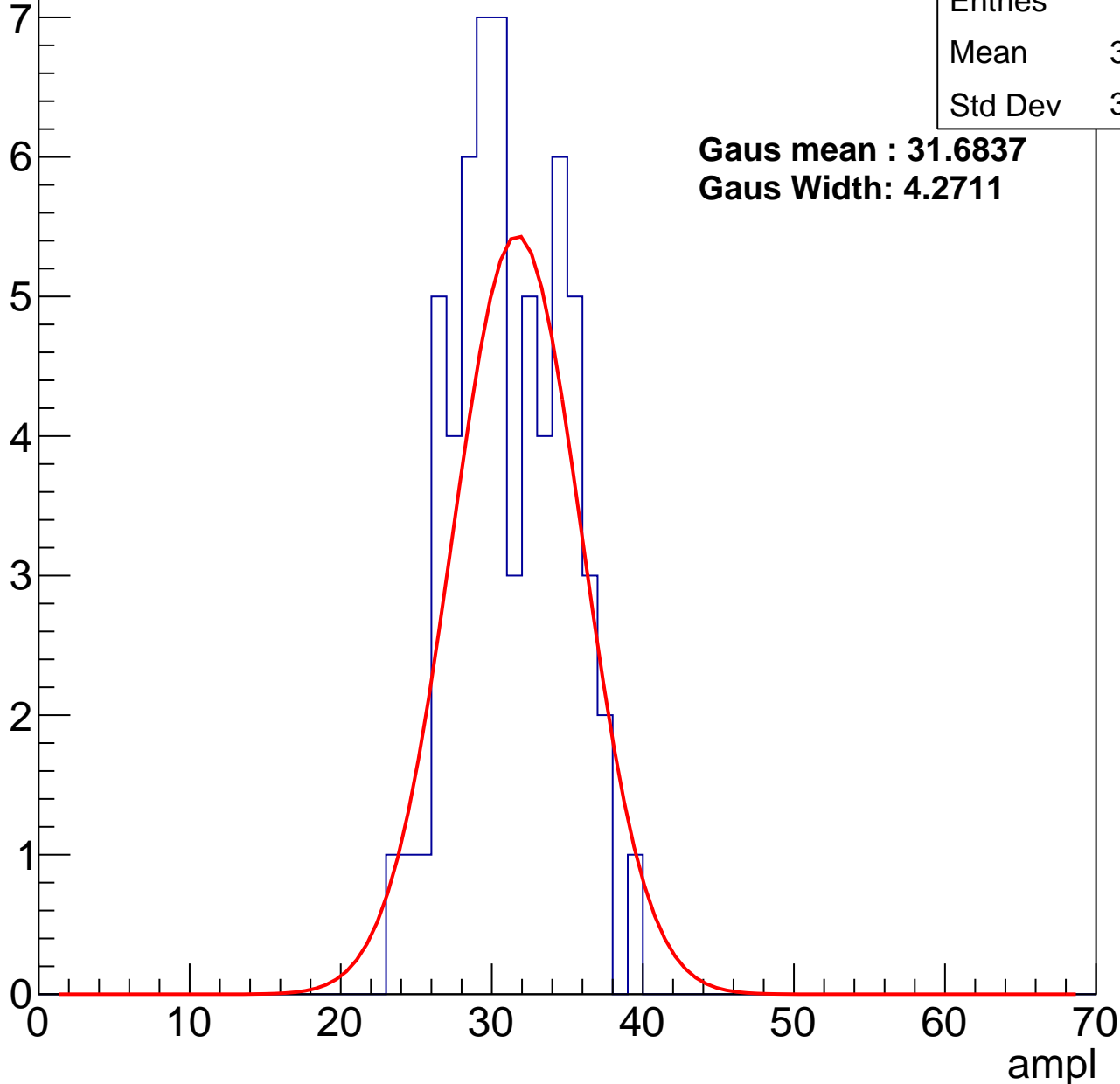
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	30.75
Std Dev	3.597

Gaus mean : 31.6837

Gaus Width: 4.2711



B1L103S, U12-ch121, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	92
Mean	37.59
Std Dev	4.263

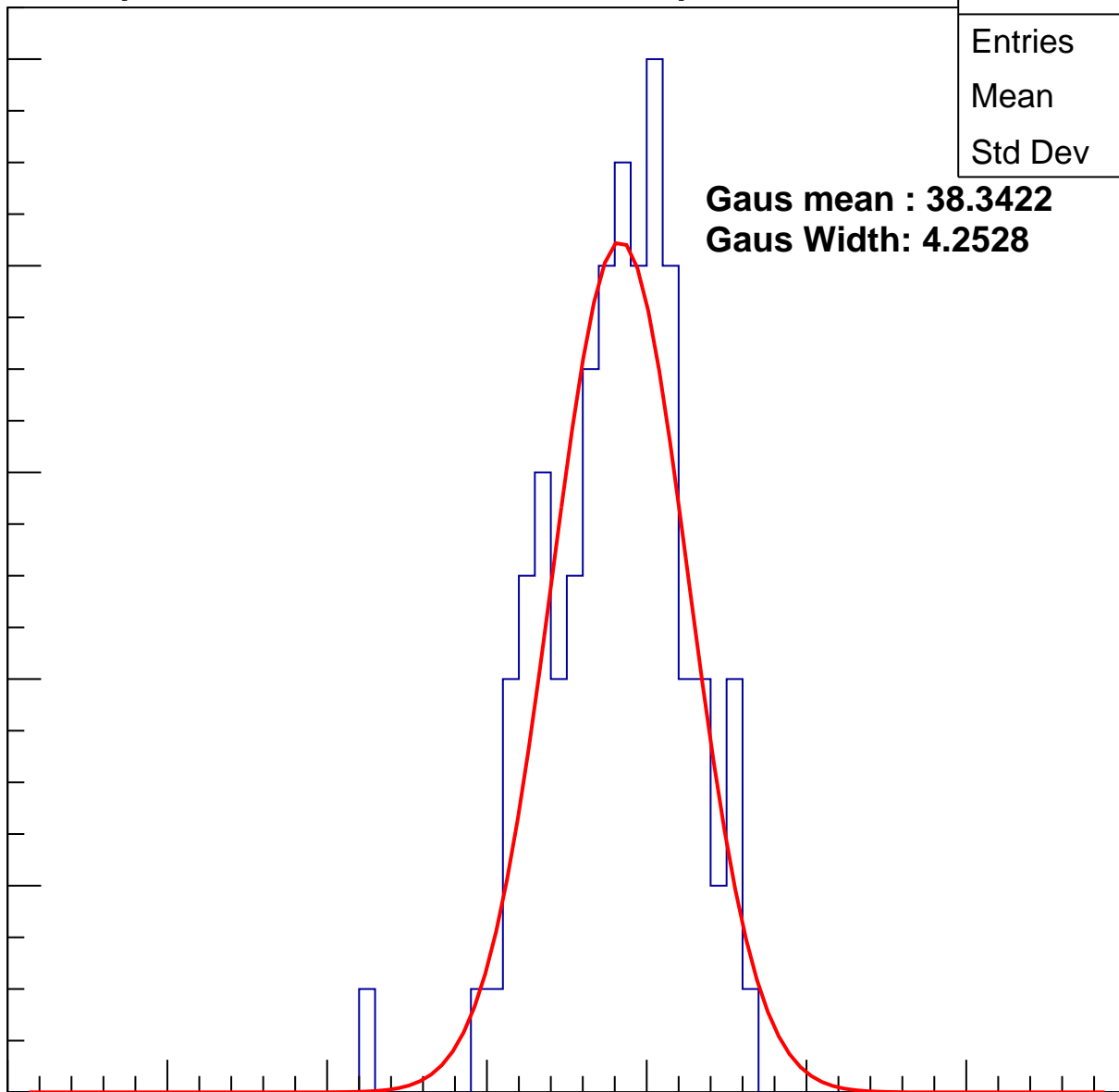
Gaus mean : 38.3422

Gaus Width: 4.2528

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch121, adc2

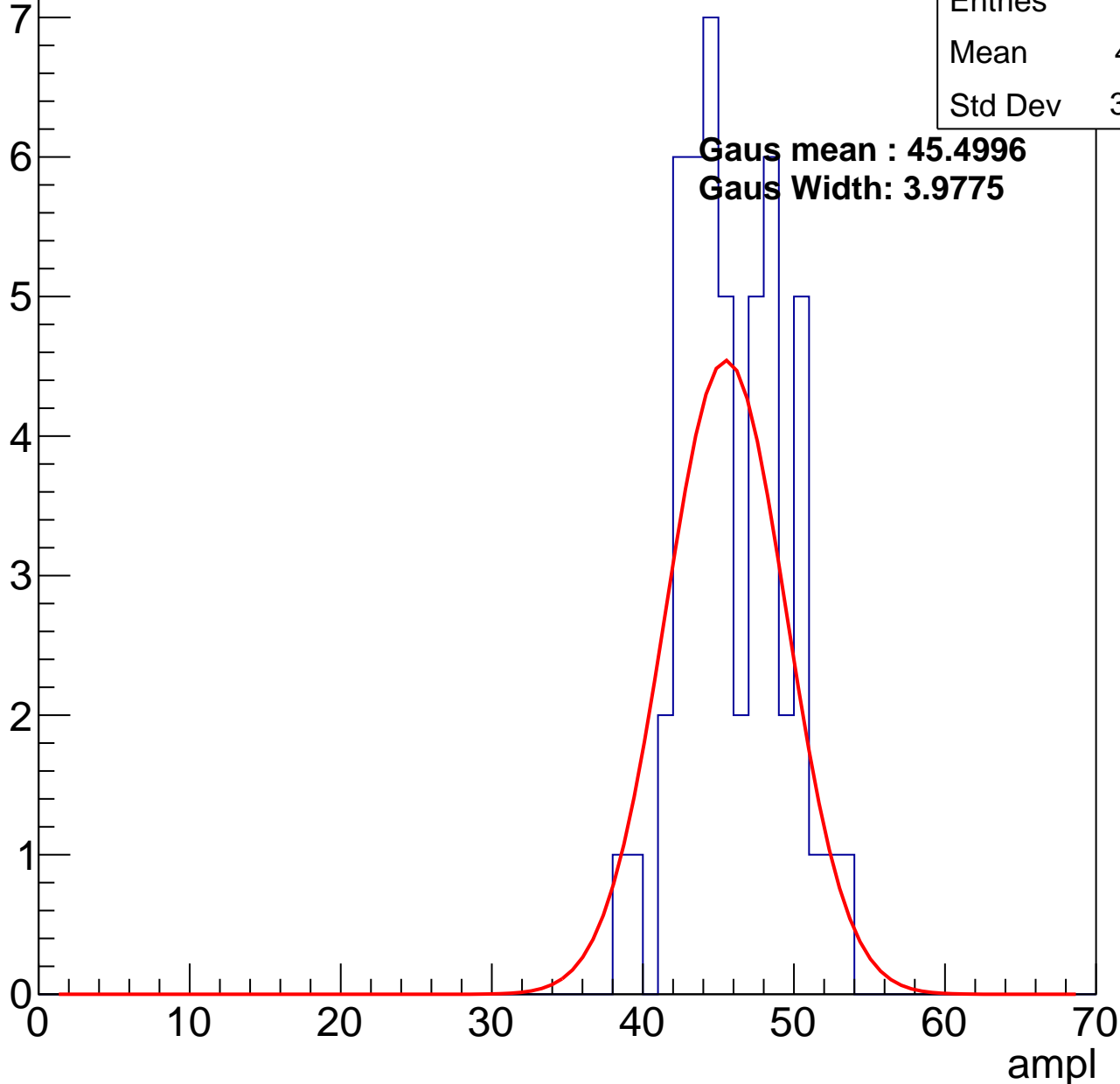
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	45.51
Std Dev	3.345

Gaus mean : 45.4996

Gaus Width: 3.9775

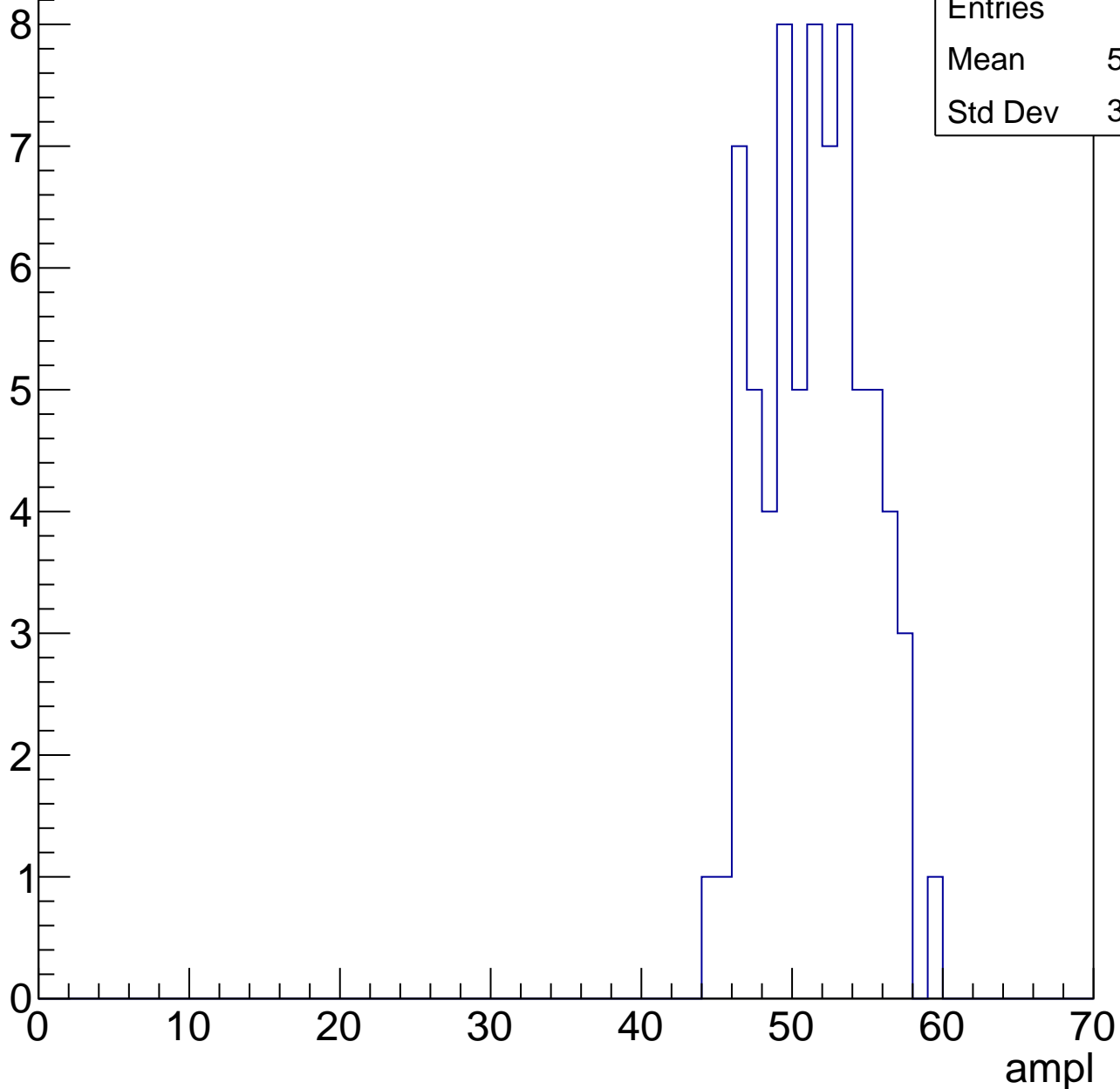


B1L103S, U12-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	51.04
Std Dev	3.434

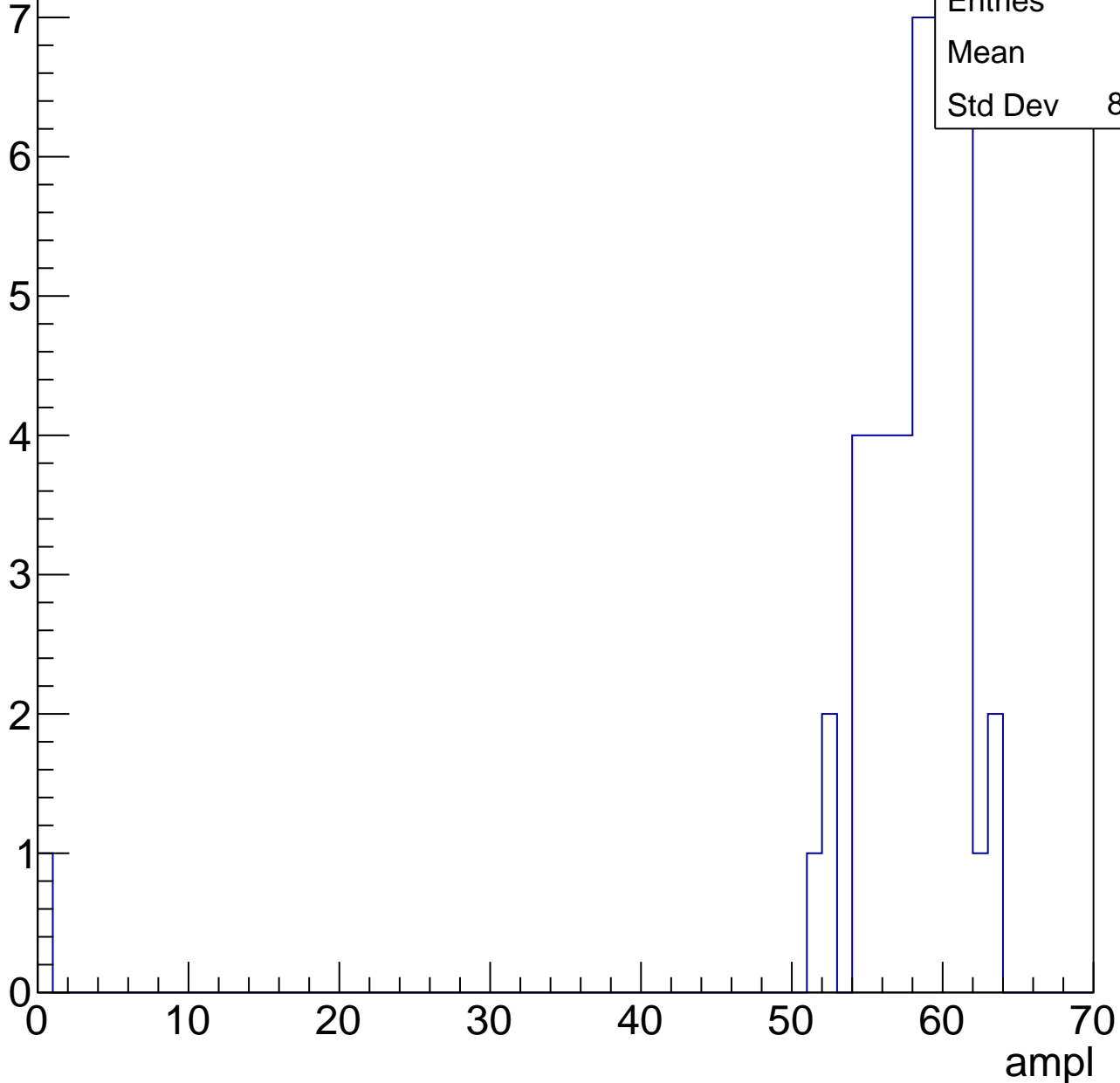


B1L103S, U12-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	56.8
Std Dev	8.513

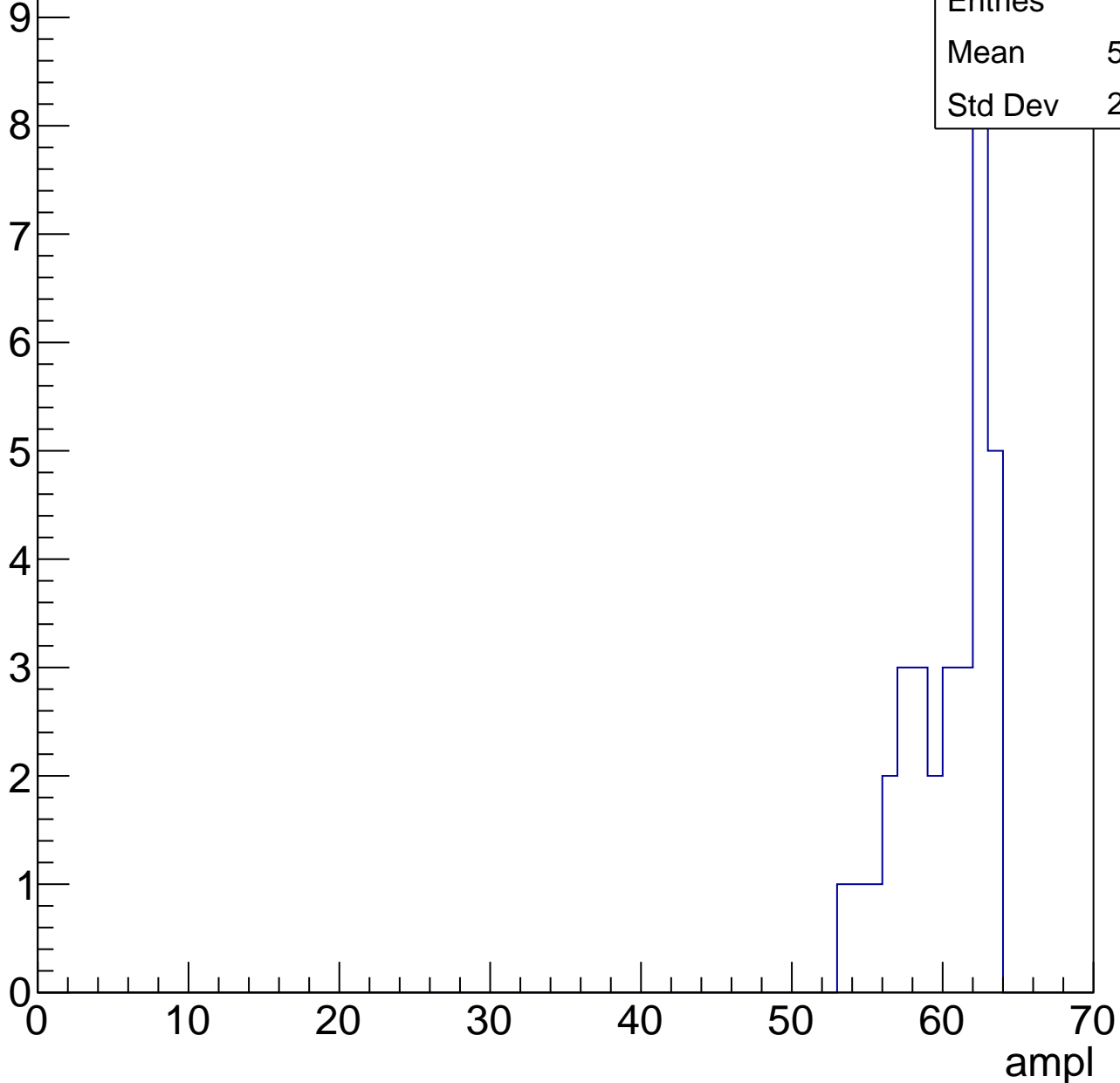


B1L103S, U12-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	59.79
Std Dev	2.837



B1L103S, U12-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch122, adc0

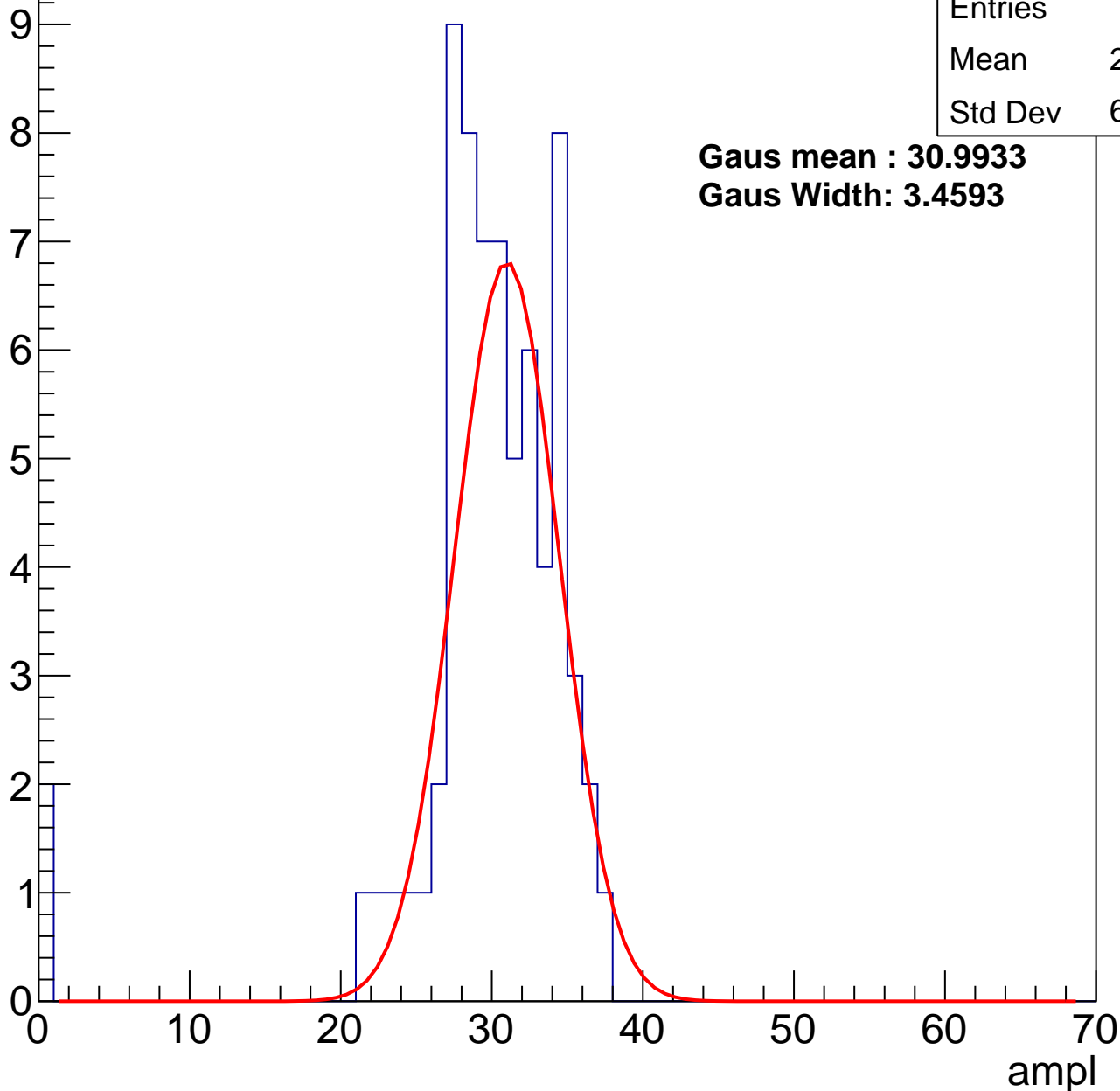
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	29.16
Std Dev	6.076

Gaus mean : 30.9933

Gaus Width: 3.4593



B1L103S, U12-ch122, adc1

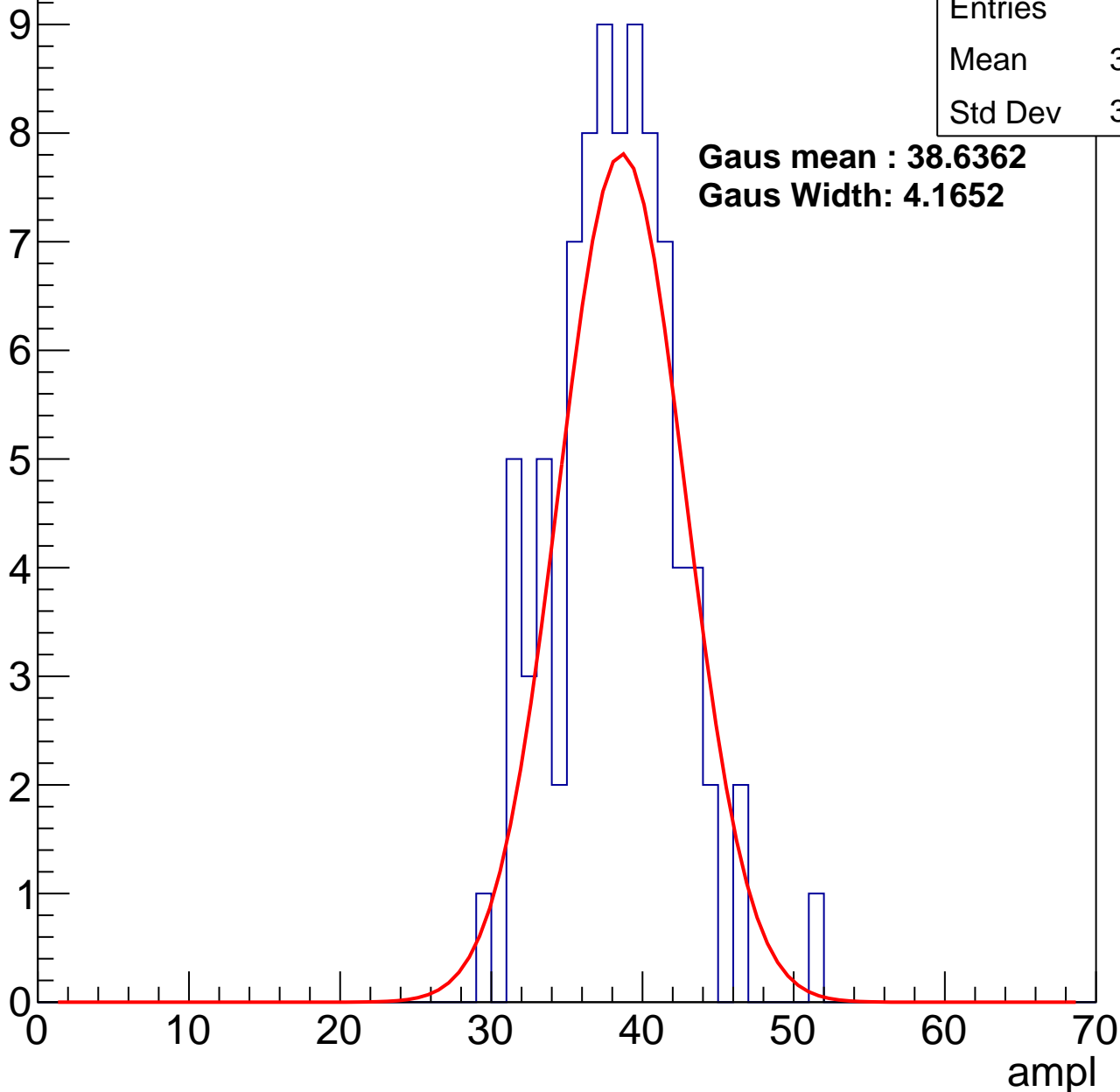
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	37.79
Std Dev	3.953

Gaus mean : 38.6362

Gaus Width: 4.1652



B1L103S, U12-ch122, adc2

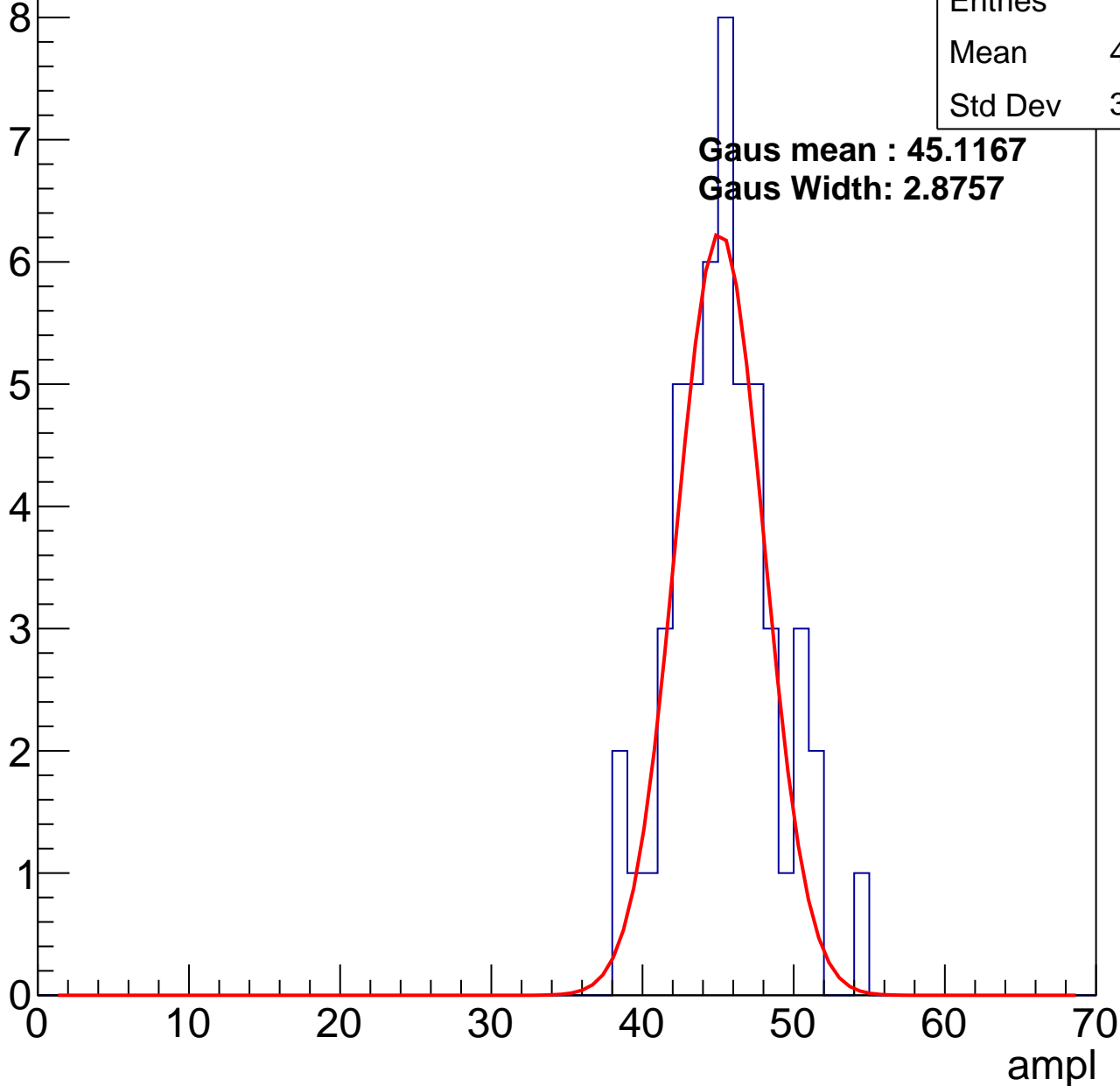
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	44.92
Std Dev	3.354

Gaus mean : 45.1167

Gaus Width: 2.8757

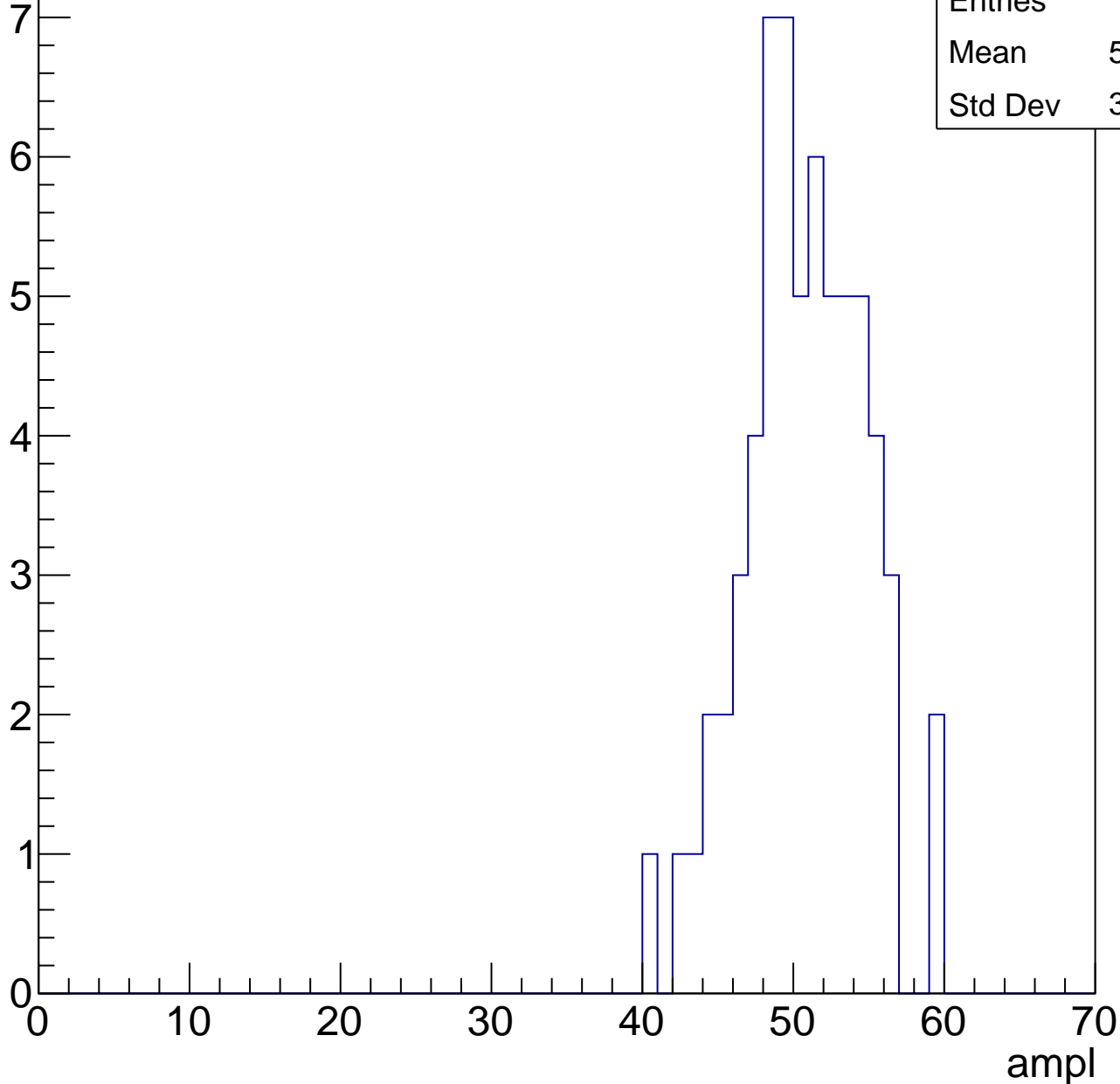


B1L103S, U12-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	50.24
Std Dev	3.927



B1L103S, U12-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	62
Mean	56.15
Std Dev	3.663

Entry

10

8

6

4

2

0

0

10

20

30

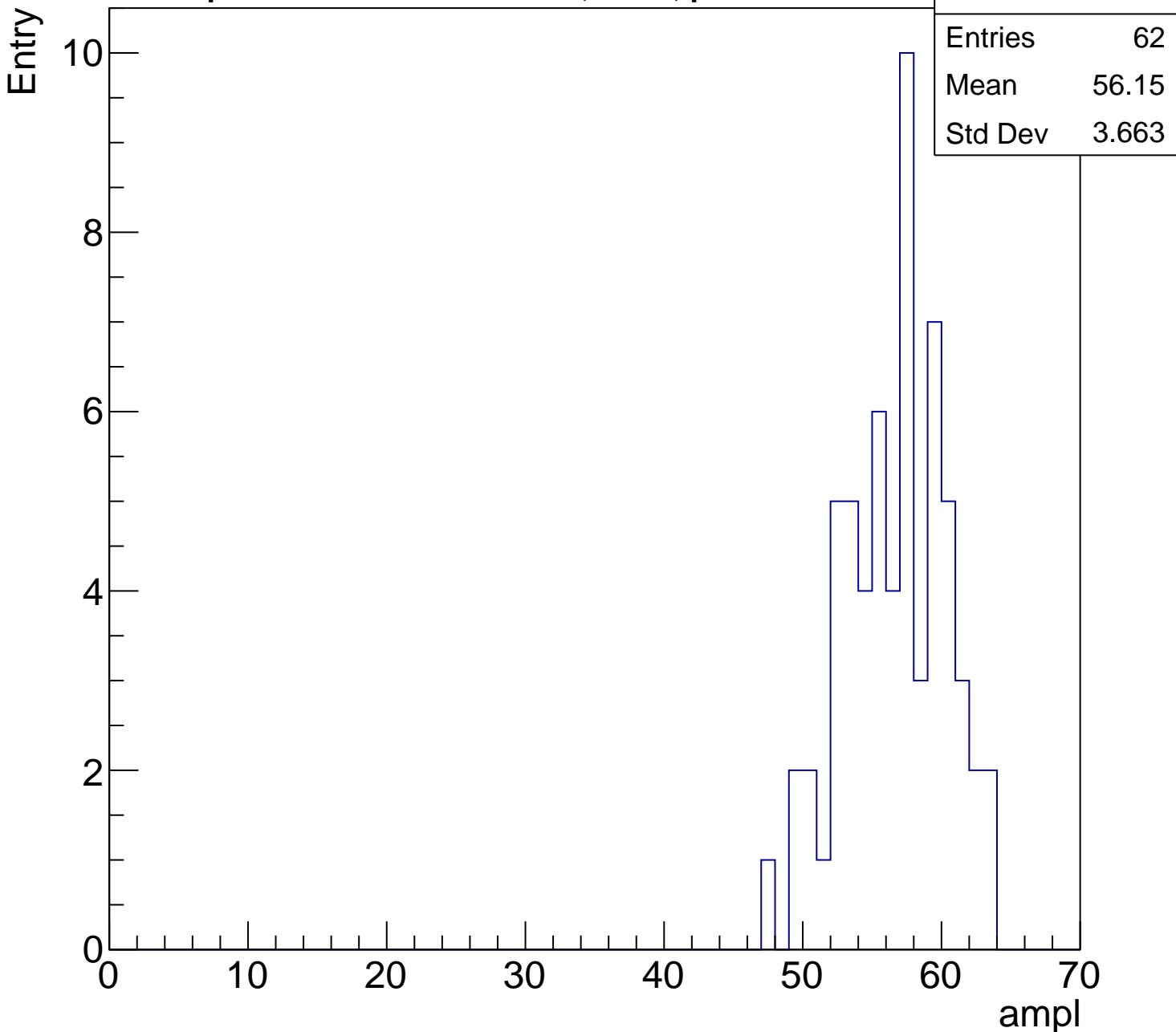
40

50

60

ampl

70

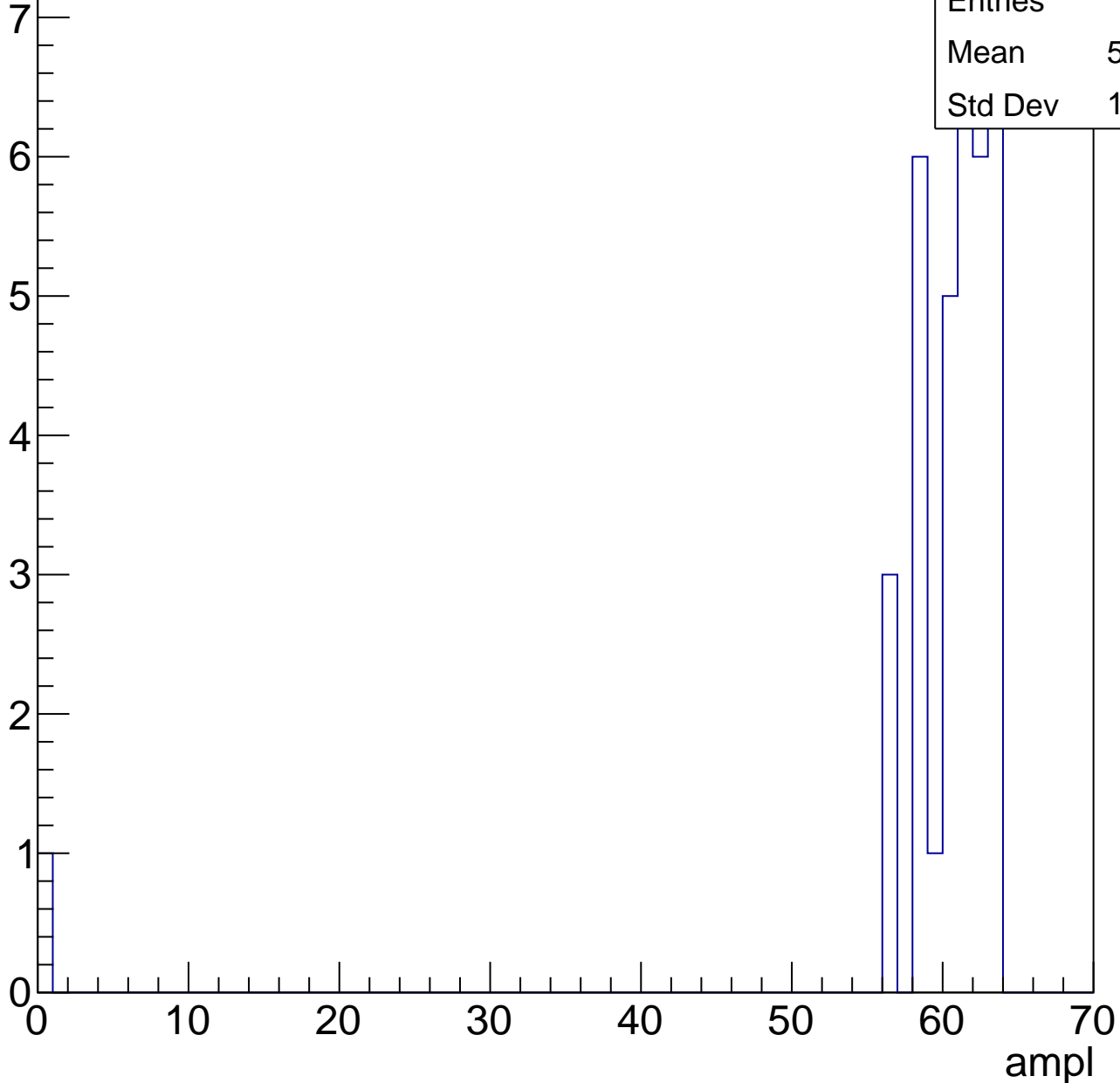


B1L103S, U12-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.75
Std Dev	10.15



B1L103S, U12-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

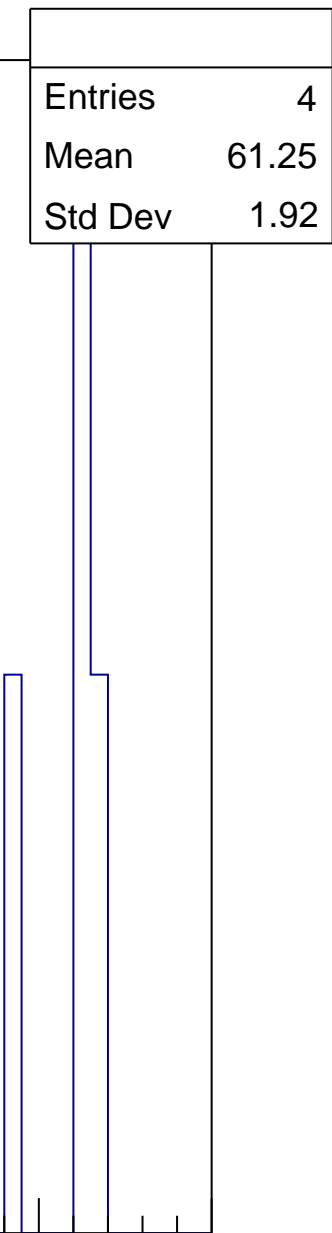
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.25
Std Dev	1.92

0 10 20 30 40 50 60 70

ampl



B1L103S, U12-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch123, adc0

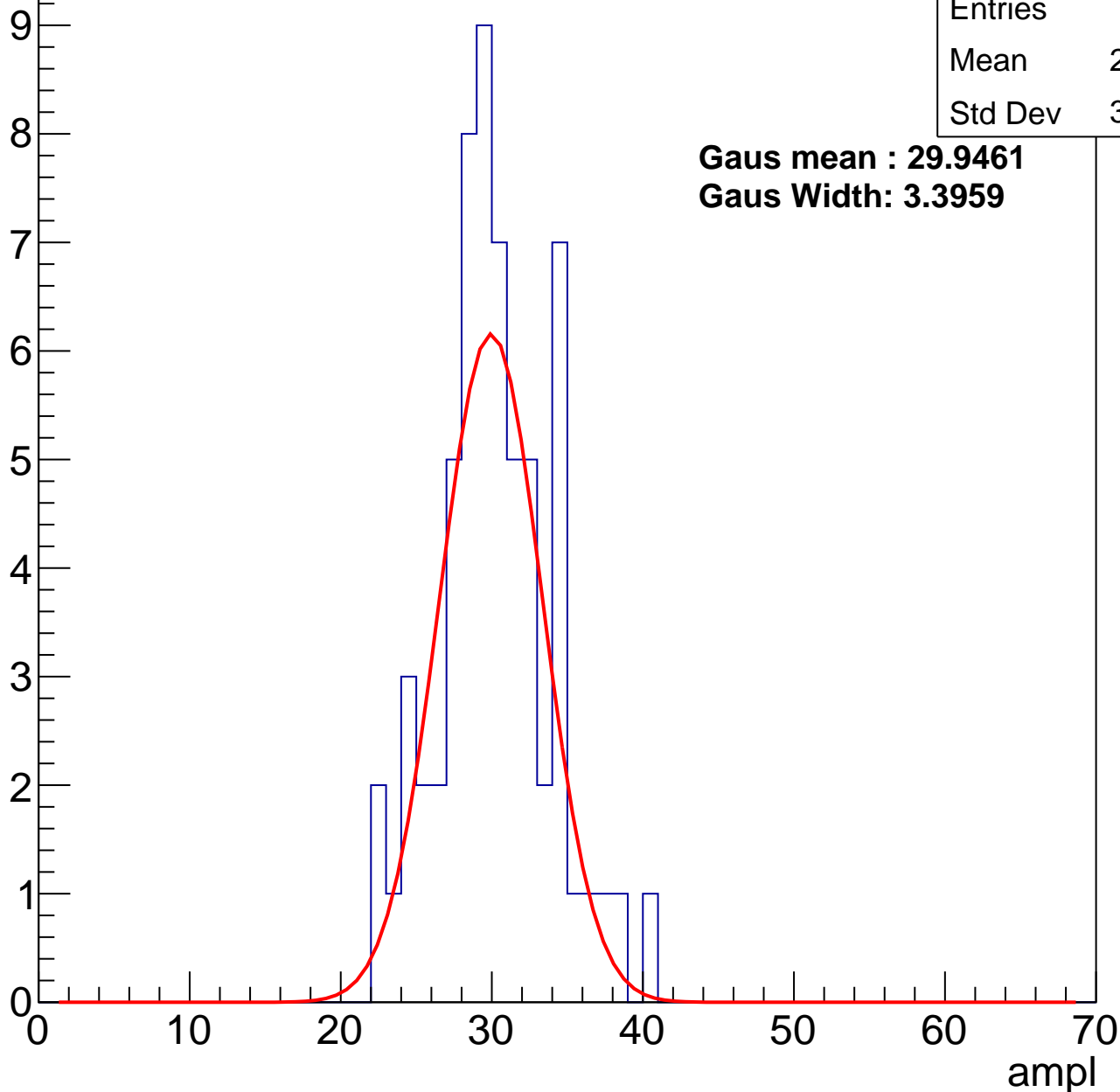
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.78
Std Dev	3.735

Gaus mean : 29.9461

Gaus Width: 3.3959



B1L103S, U12-ch123, adc1

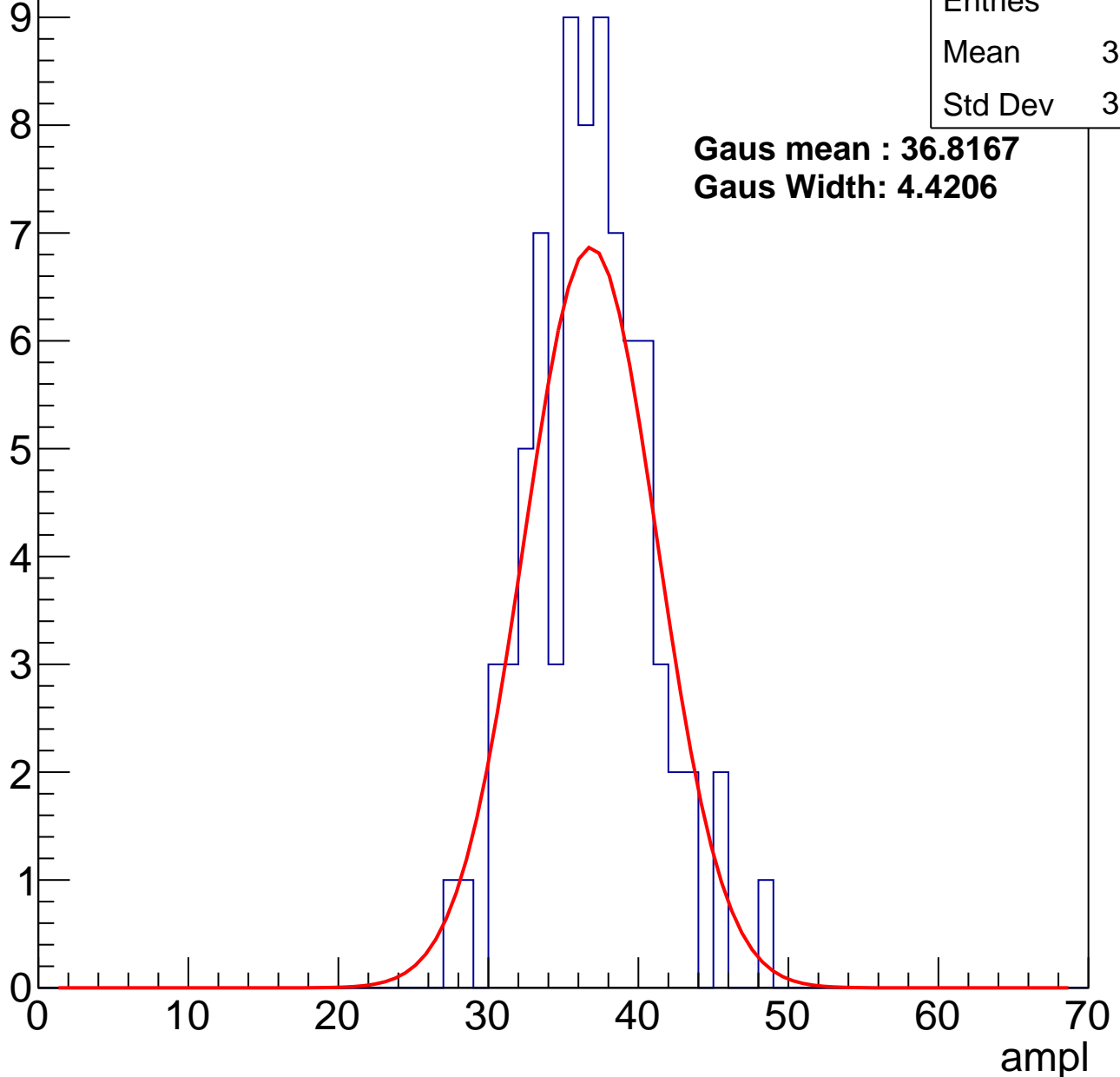
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	36.38
Std Dev	3.952

Gaus mean : 36.8167

Gaus Width: 4.4206



B1L103S, U12-ch123, adc2

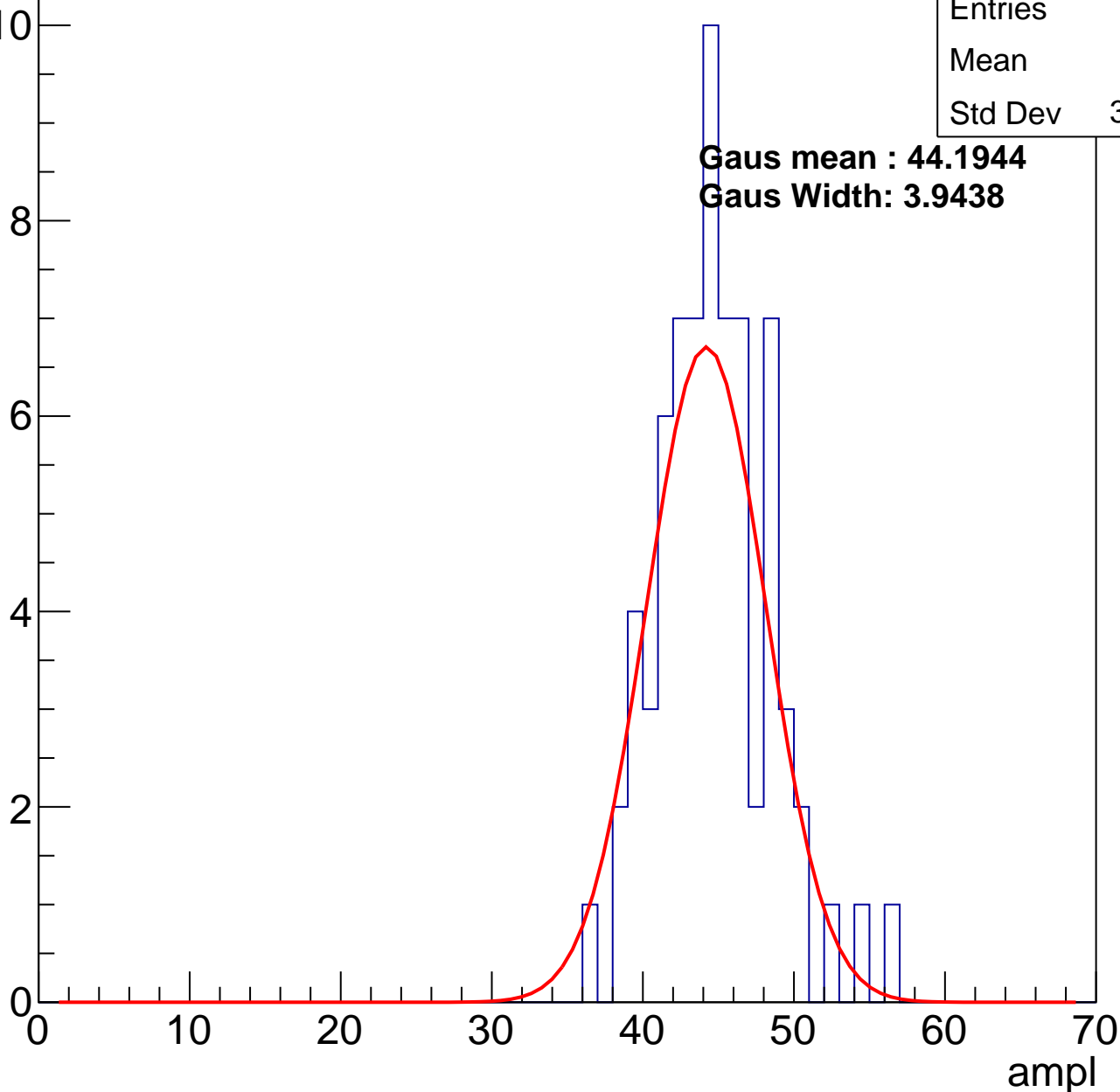
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	44.3
Std Dev	3.728

Gaus mean : 44.1944

Gaus Width: 3.9438

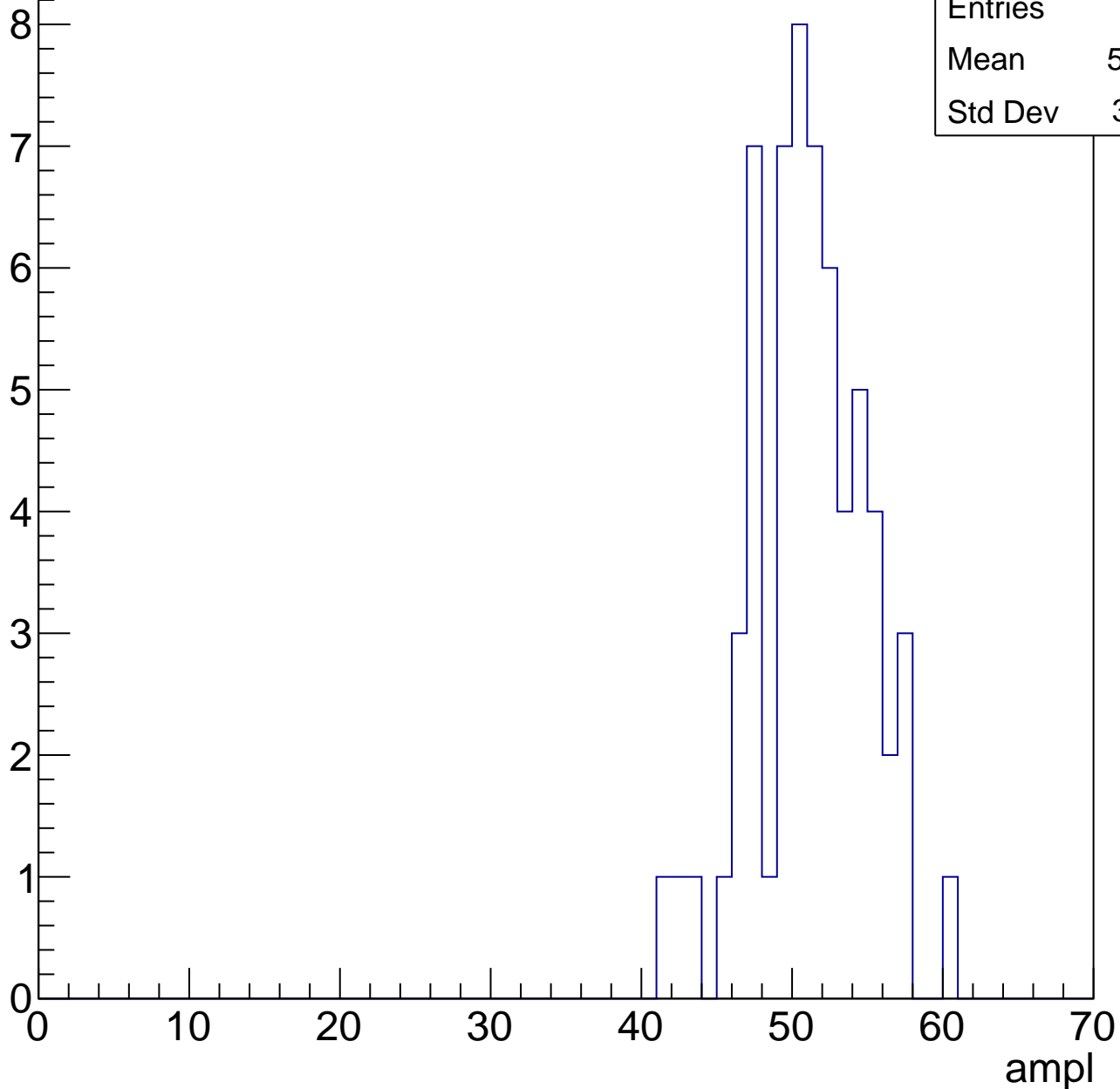


B1L103S, U12-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	50.69
Std Dev	3.761

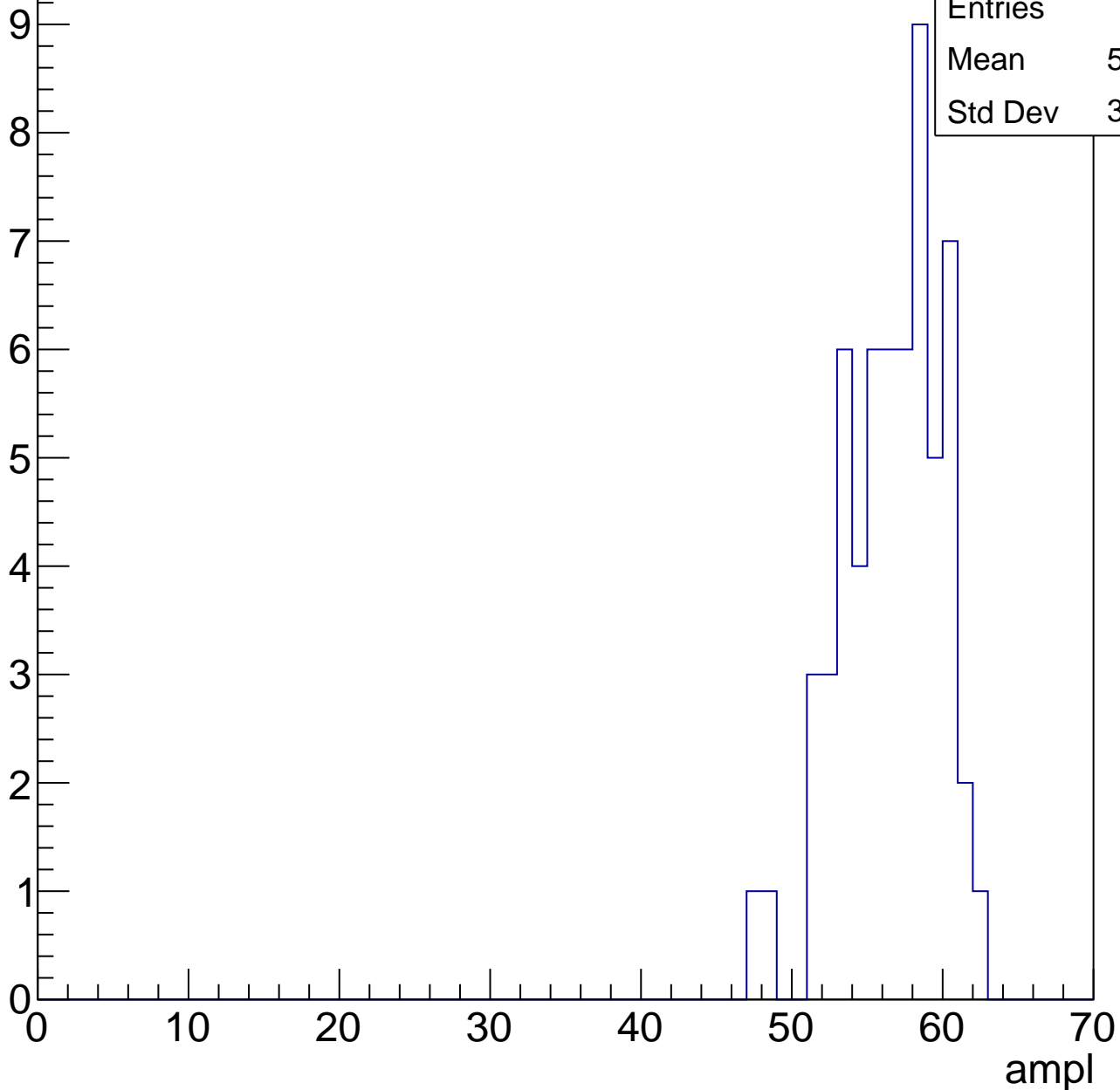


B1L103S, U12-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.12
Std Dev	3.236

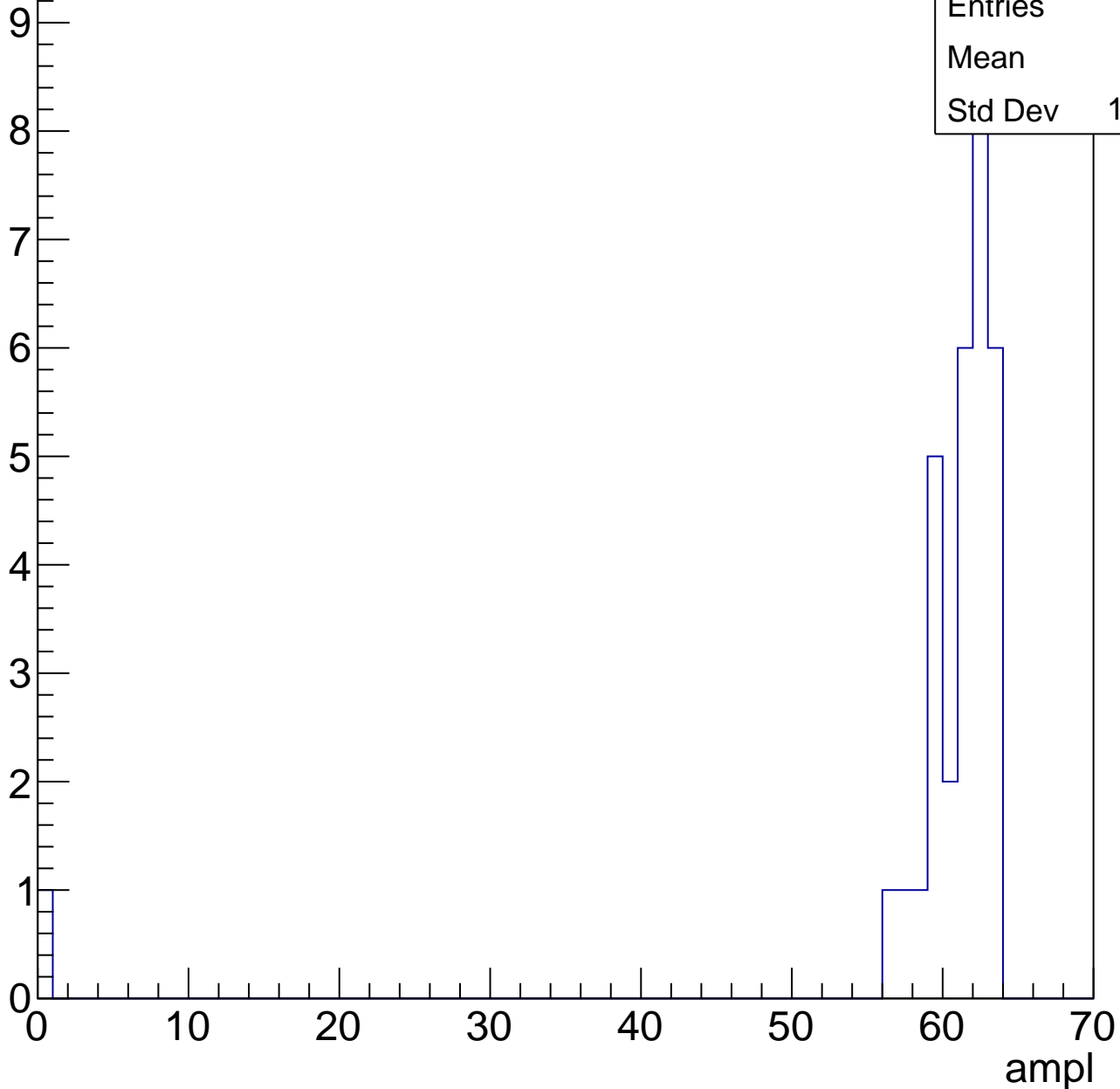


B1L103S, U12-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

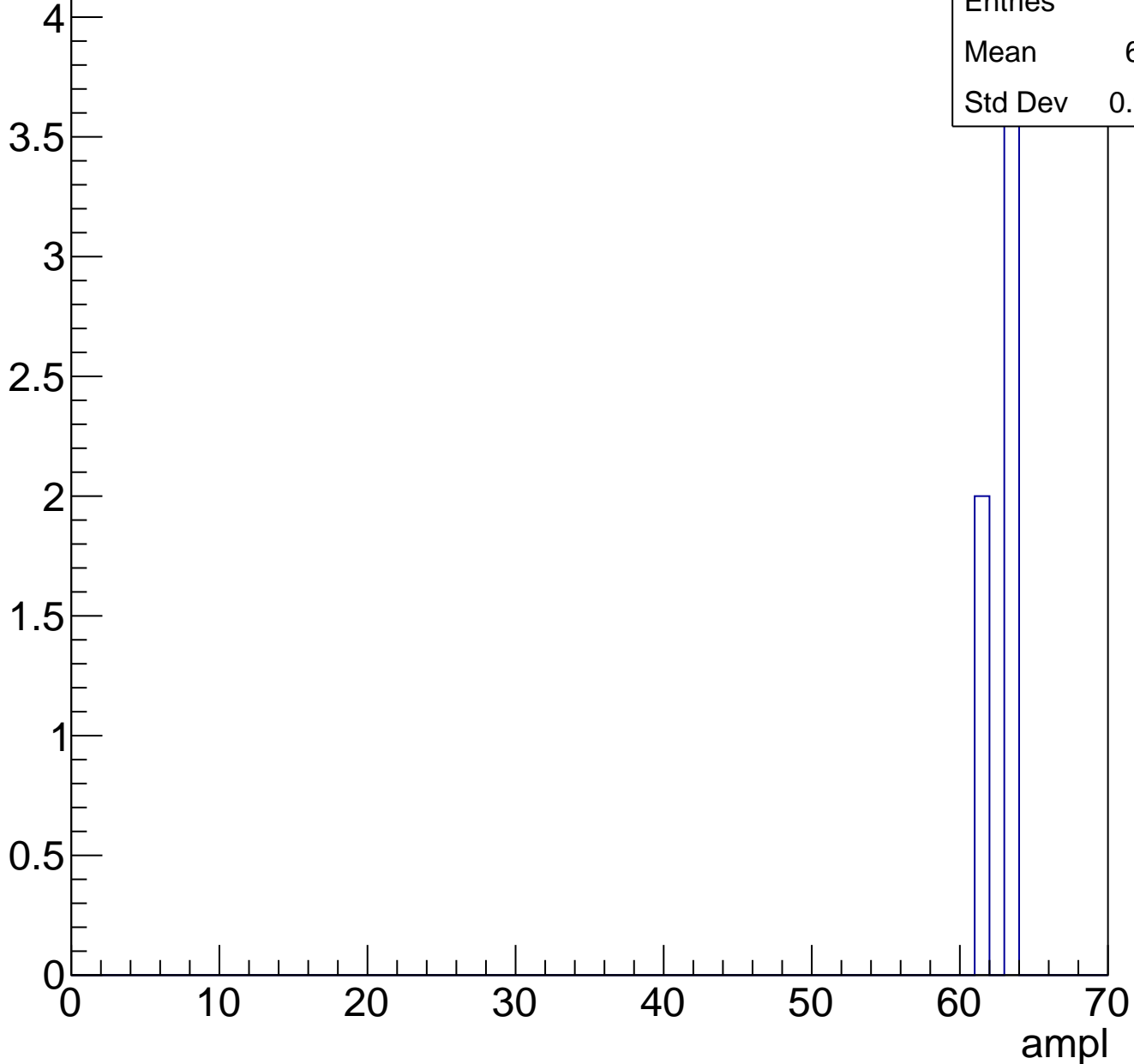
Entries	32
Mean	59
Std Dev	10.75



B1L103S, U12-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch124, adc0

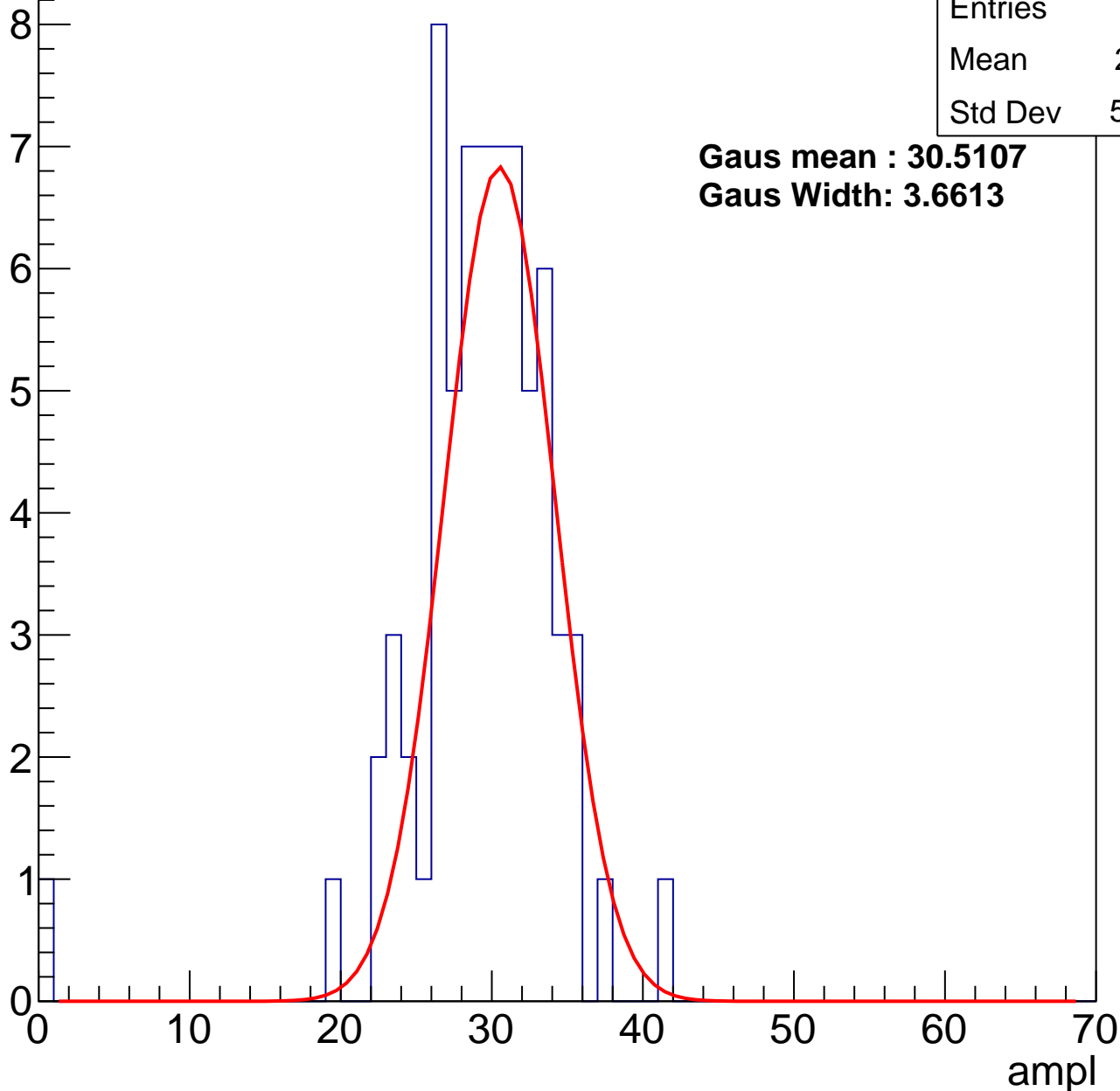
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.81
Std Dev	5.178

Gaus mean : 30.5107

Gaus Width: 3.6613



B1L103S, U12-ch124, adc1

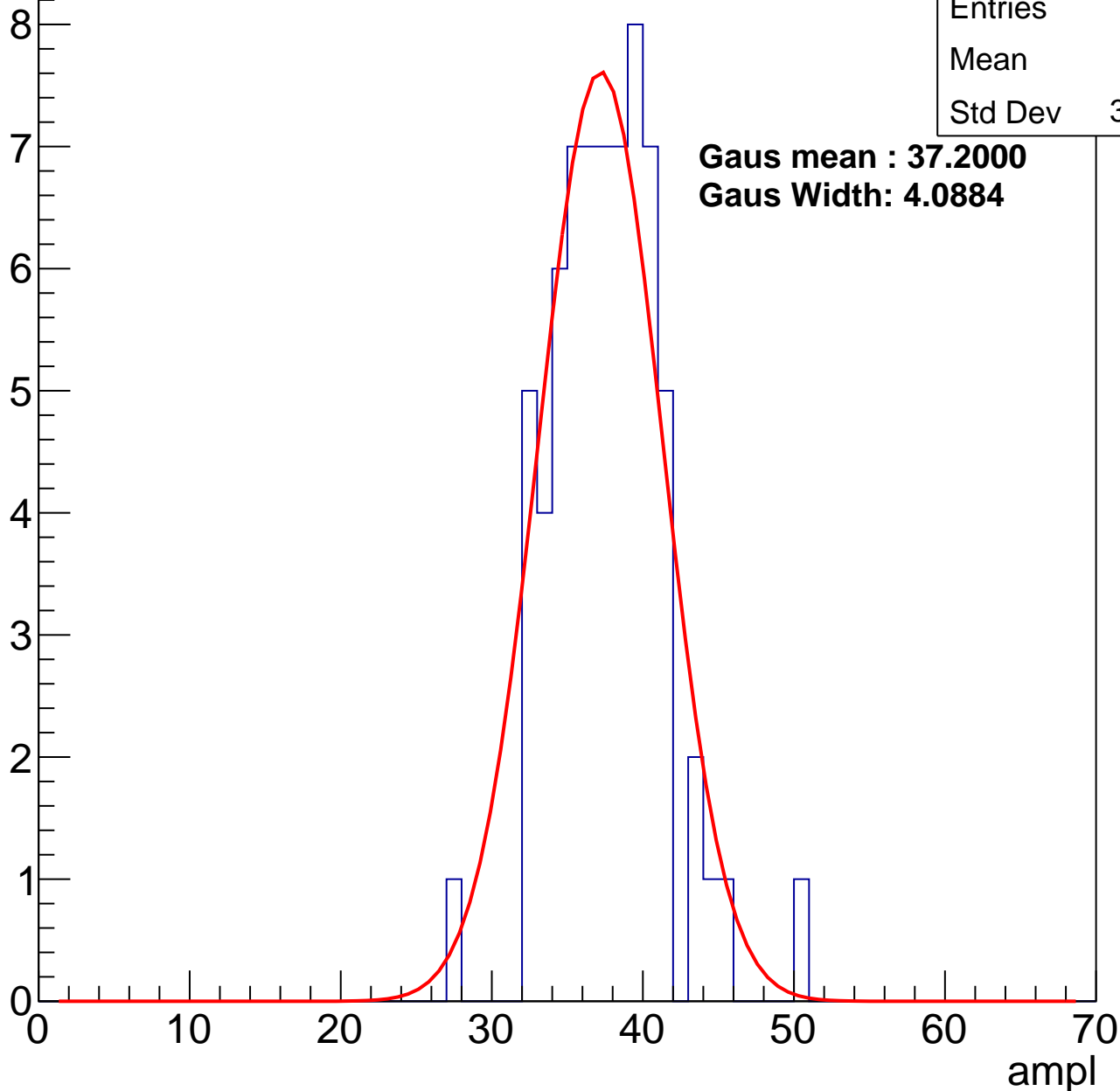
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	37.2
Std Dev	3.634

Gaus mean : 37.2000

Gaus Width: 4.0884



B1L103S, U12-ch124, adc2

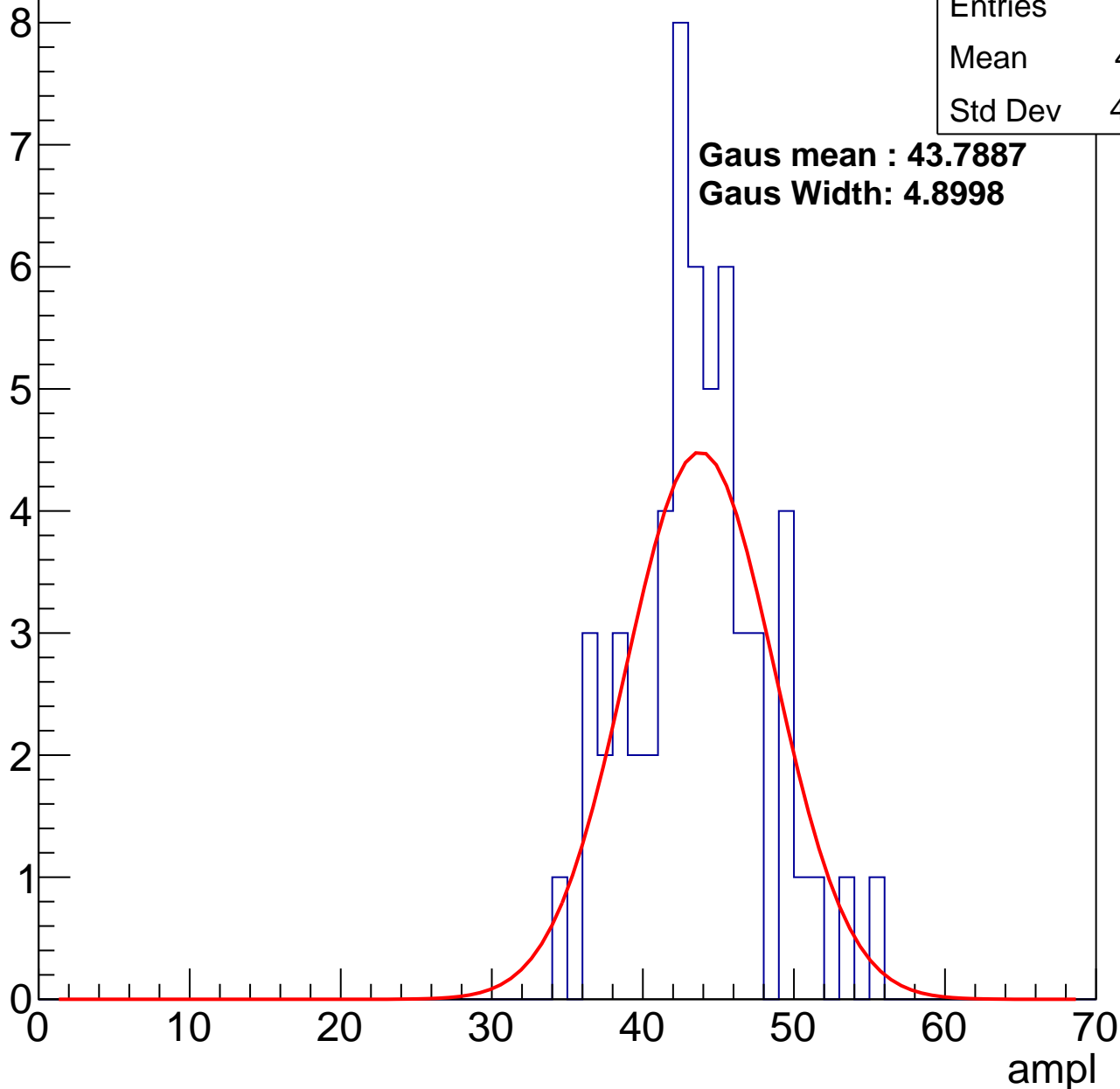
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.21
Std Dev	4.333

Gaus mean : 43.7887

Gaus Width: 4.8998

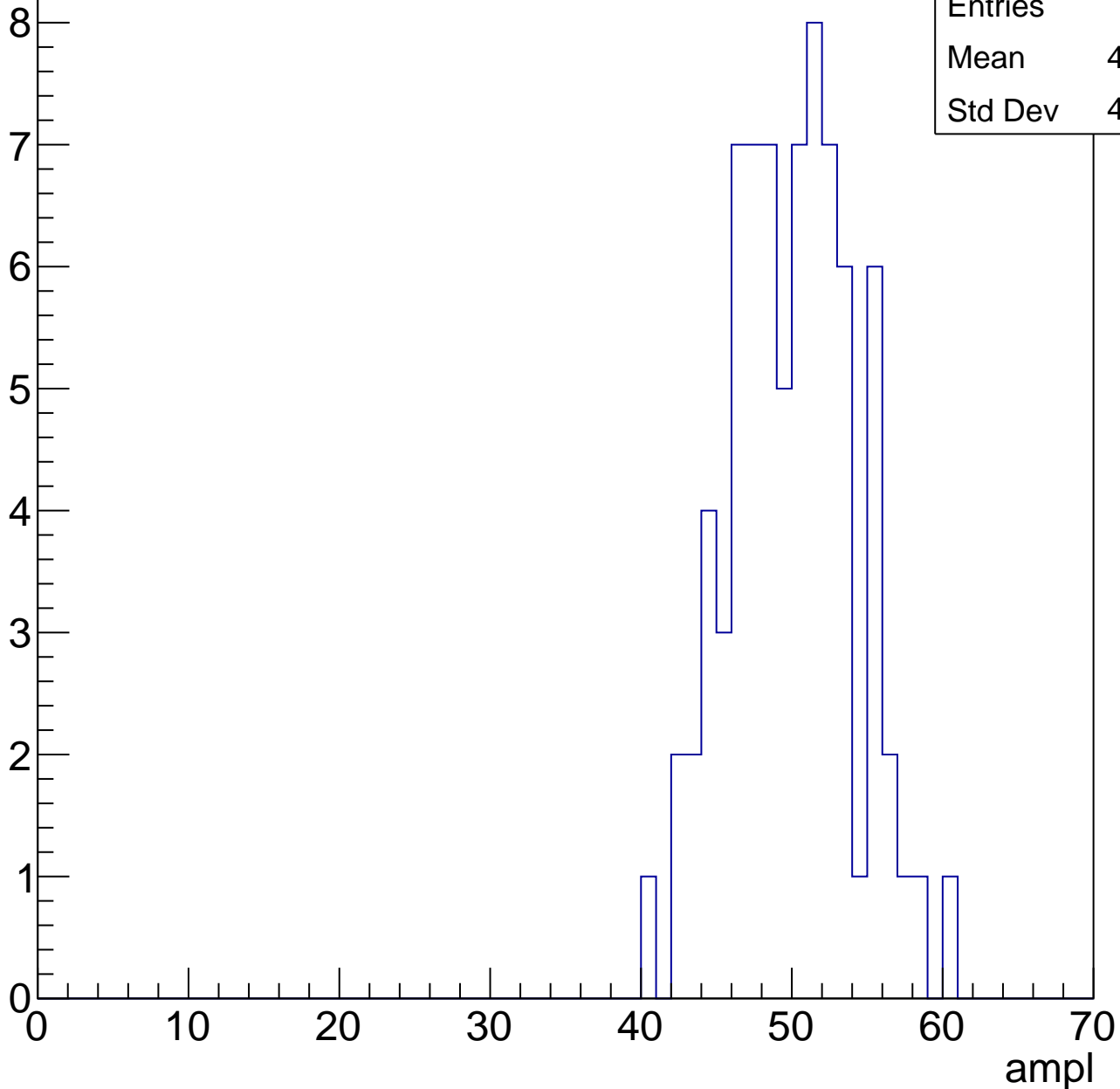


B1L103S, U12-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	49.54
Std Dev	4.047

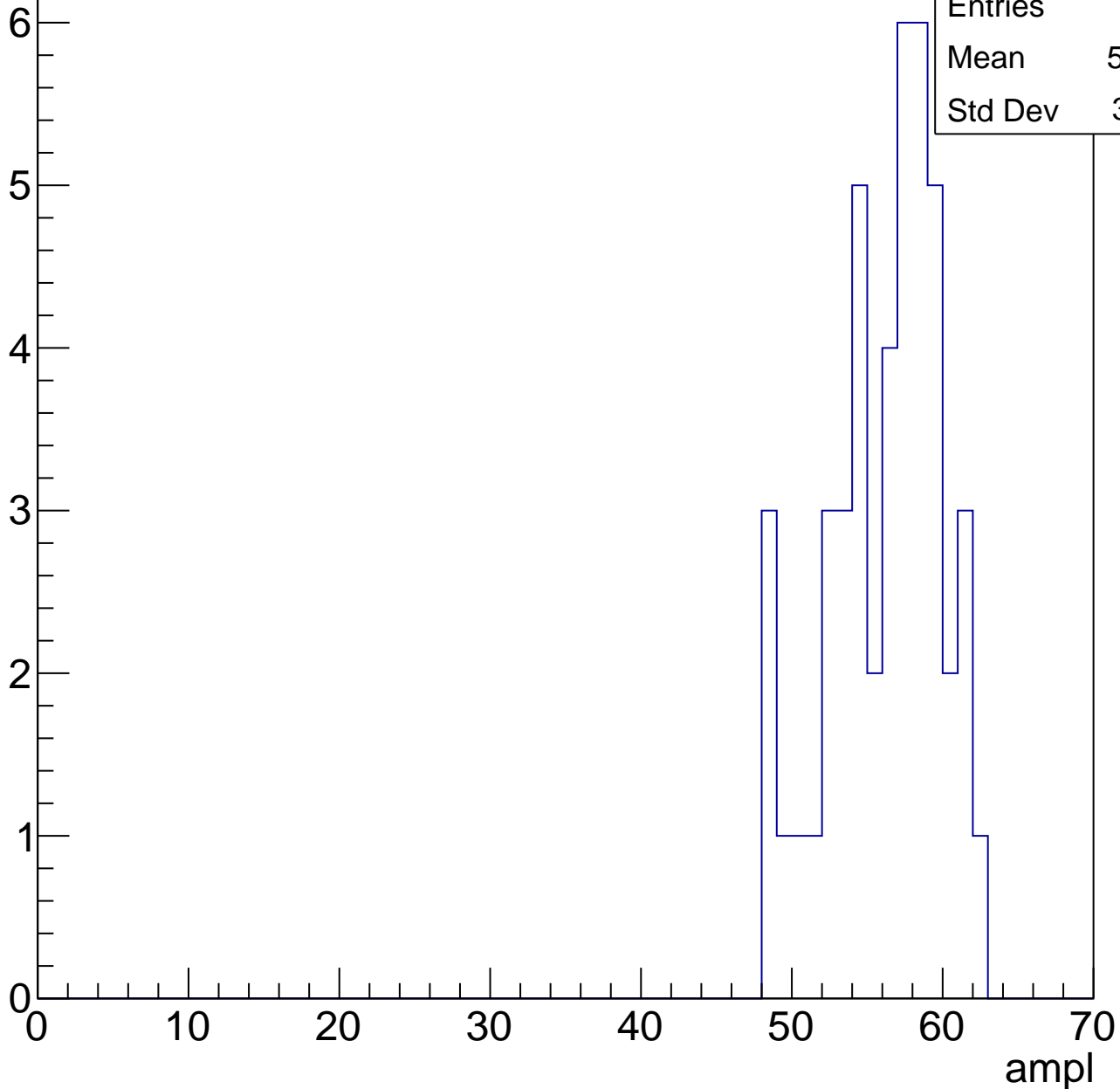


B1L103S, U12-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	55.72
Std Dev	3.651

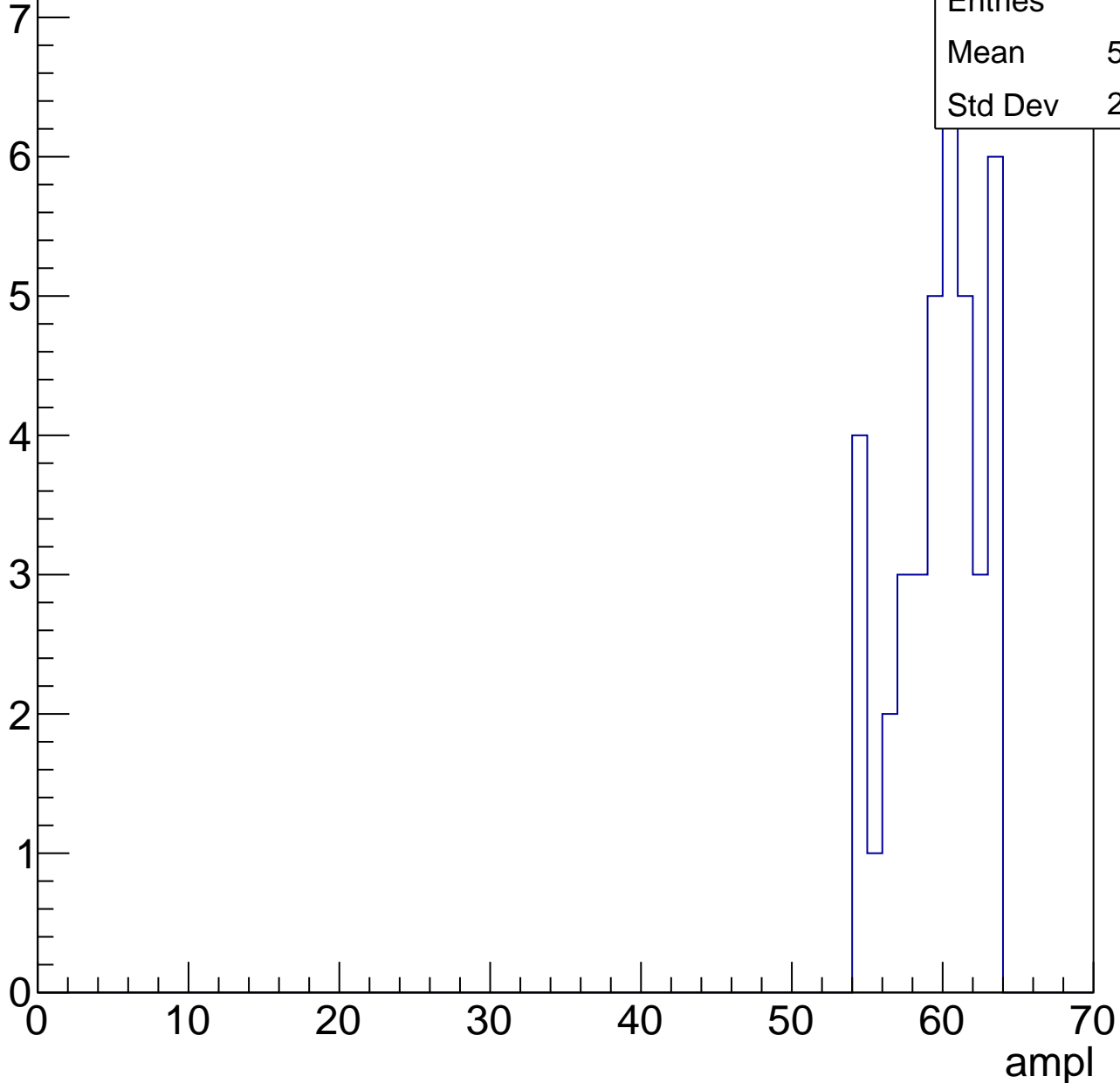


B1L103S, U12-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	59.28
Std Dev	2.754

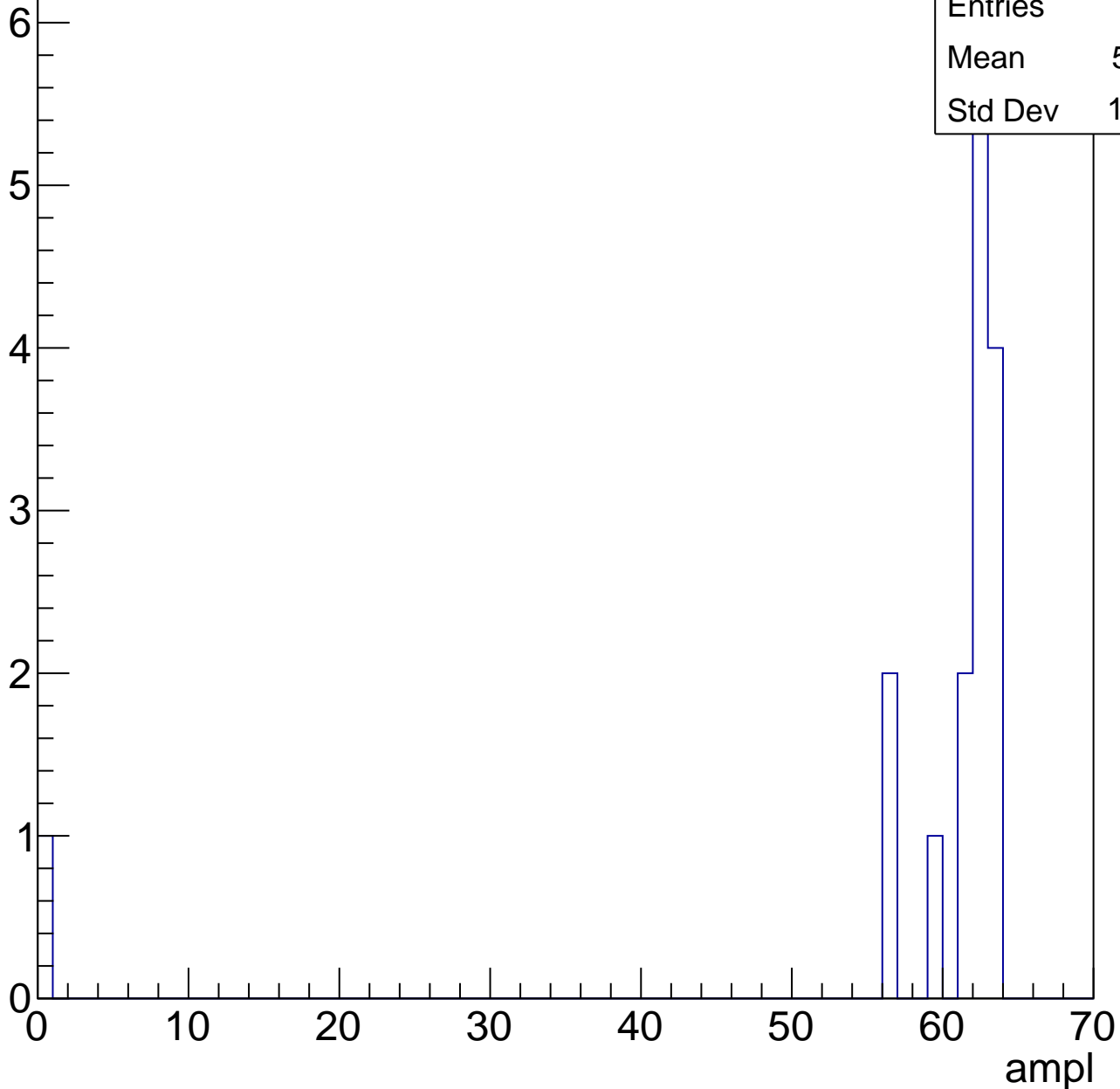


B1L103S, U12-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	57.31
Std Dev	14.96



B1L103S, U12-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	17
Std Dev	0

ampl

B1L103S, U12-ch125, adc0

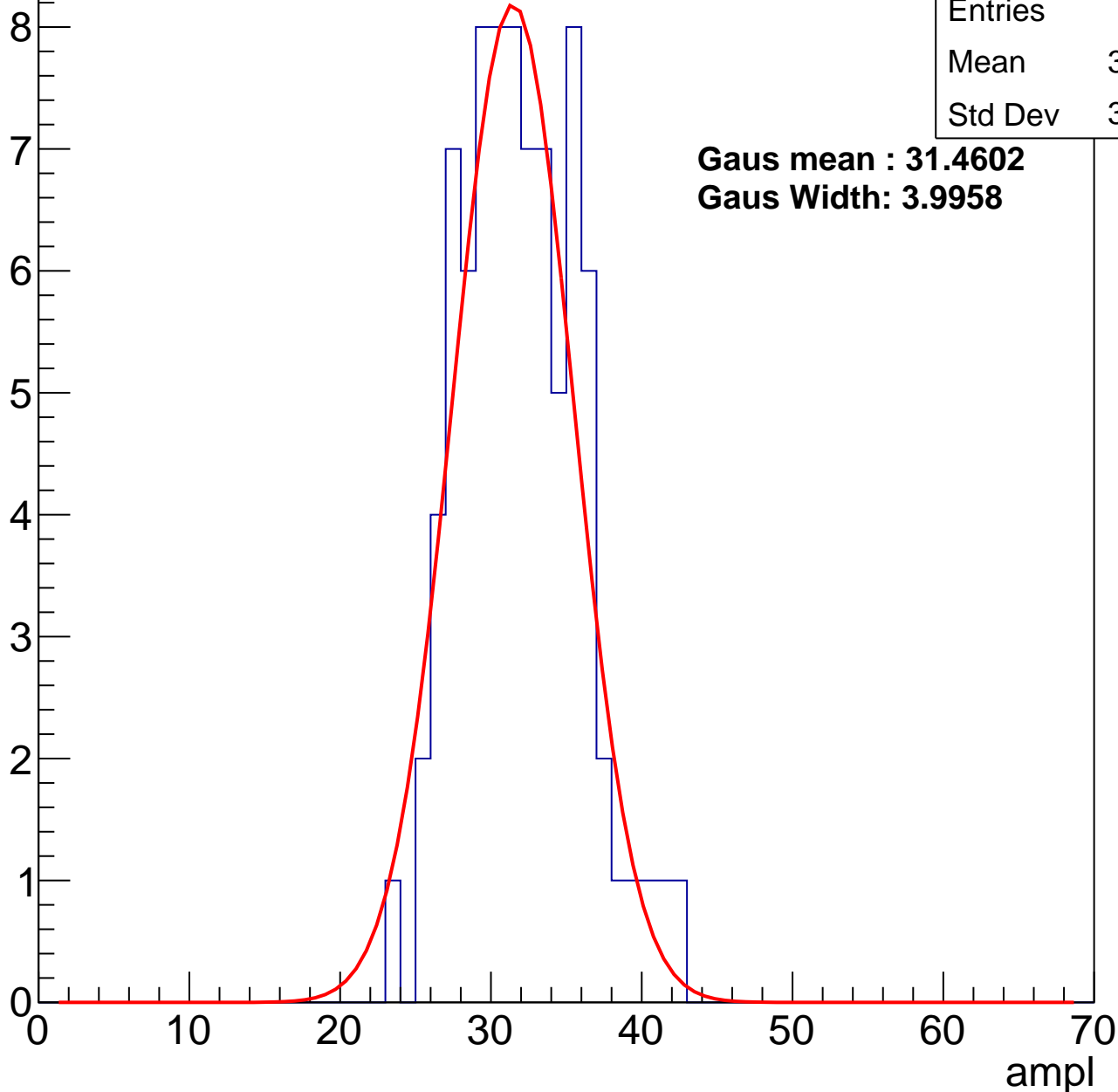
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	31.54
Std Dev	3.877

Gaus mean : 31.4602

Gaus Width: 3.9958



B1L103S, U12-ch125, adc1

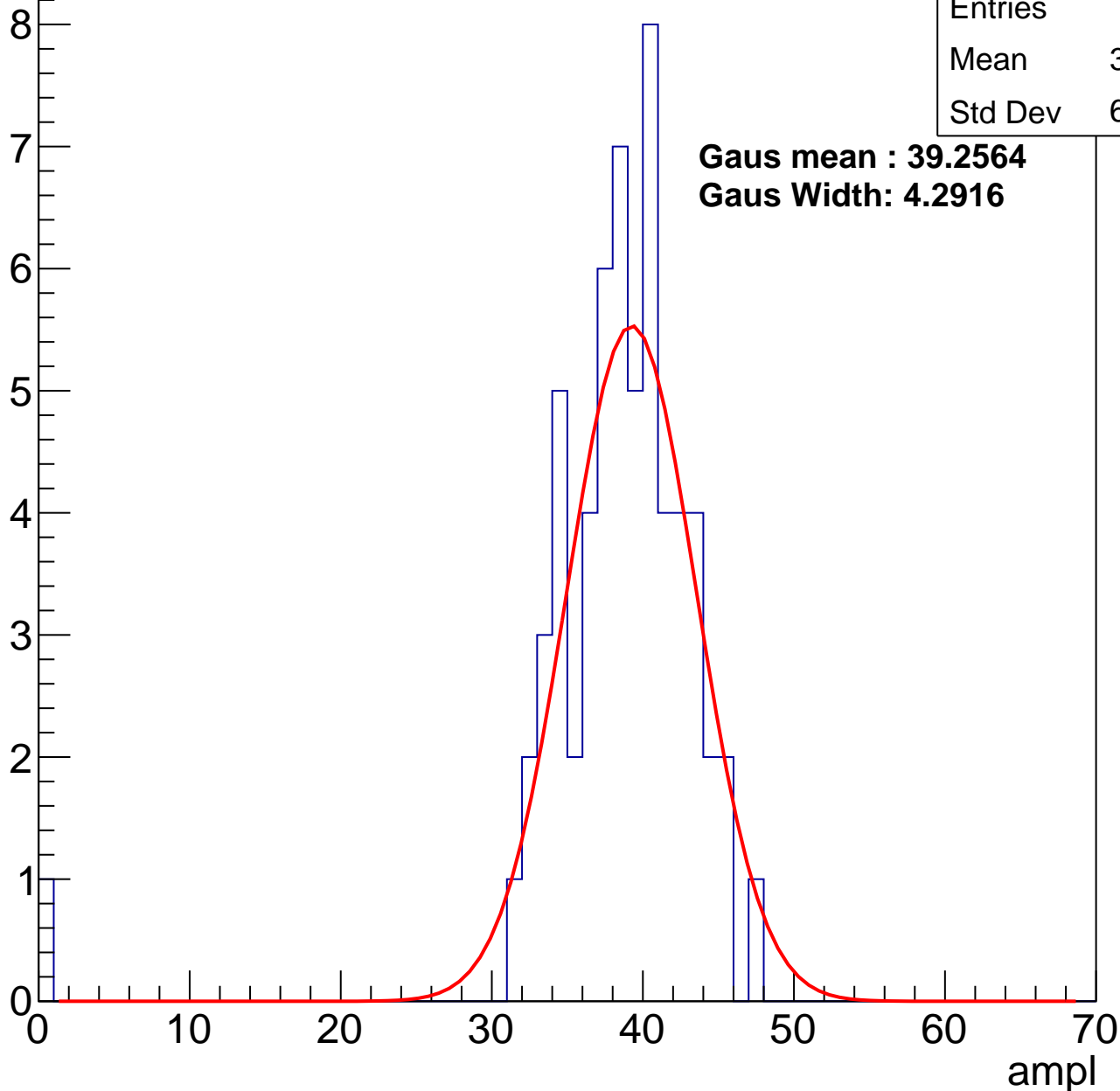
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.87
Std Dev	6.069

Gaus mean : 39.2564

Gaus Width: 4.2916



B1L103S, U12-ch125, adc2

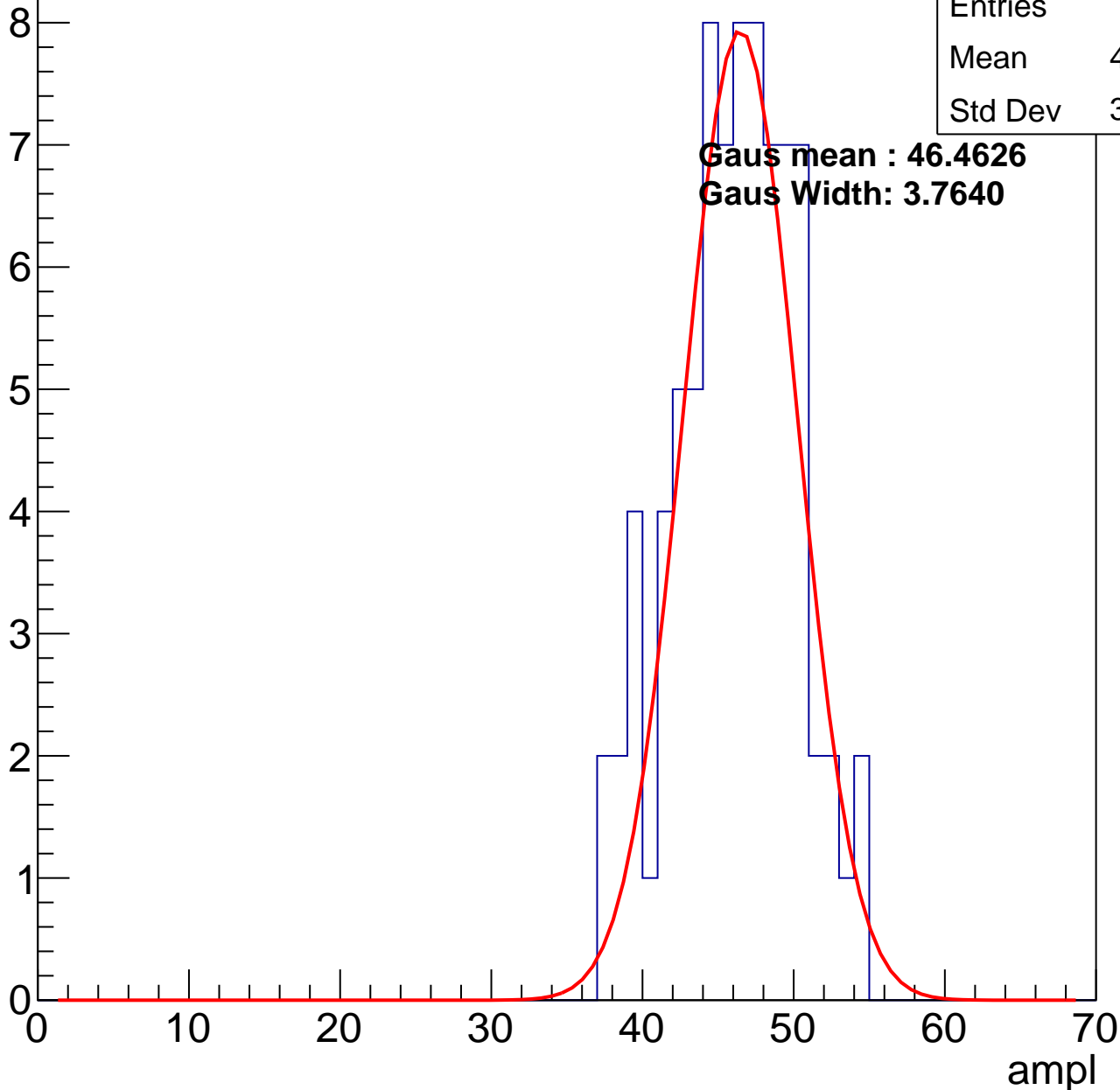
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	45.63
Std Dev	3.959

Gaus mean : 46.4626

Gaus Width: 3.7640

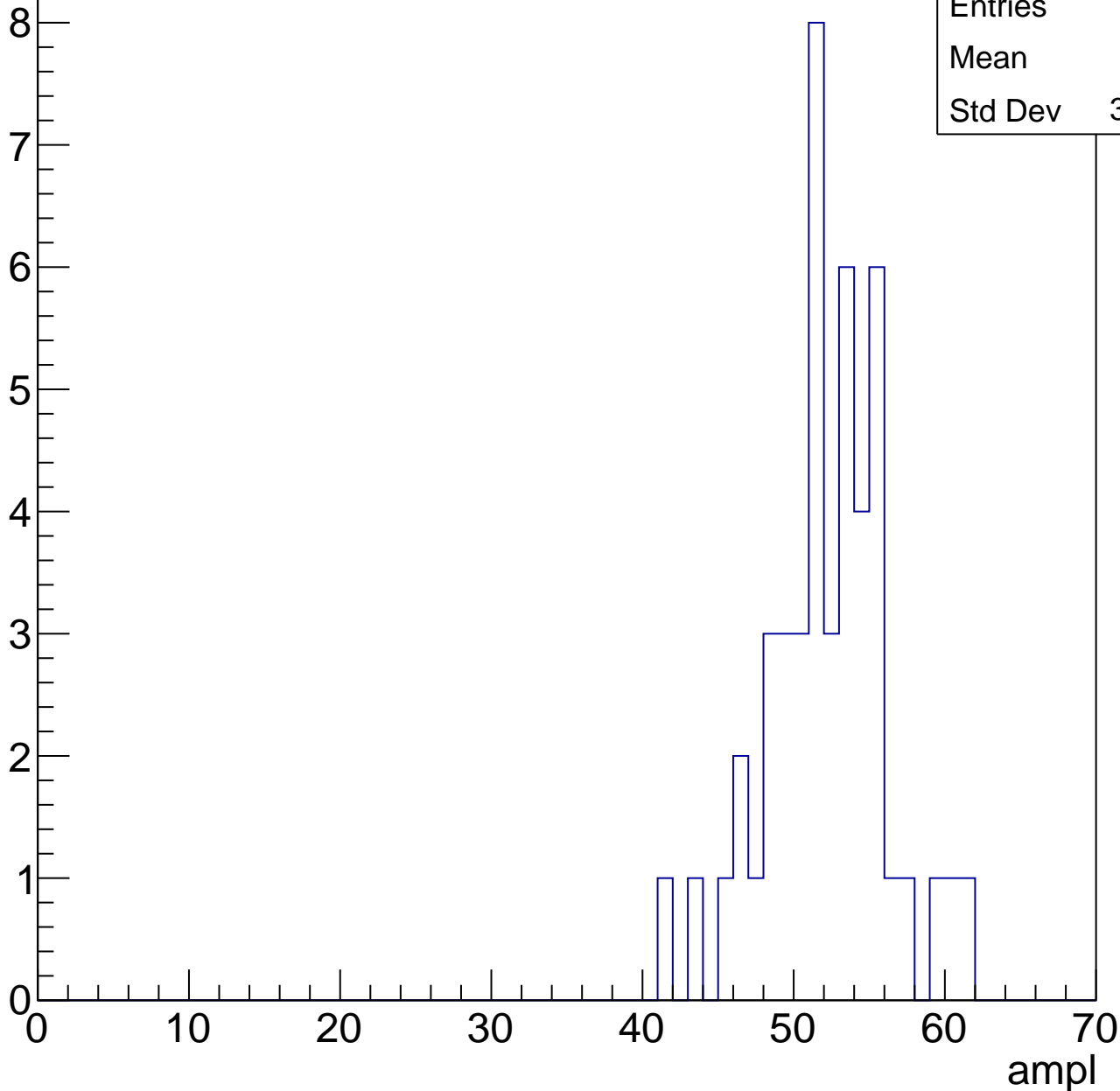


B1L103S, U12-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	51.7
Std Dev	3.994

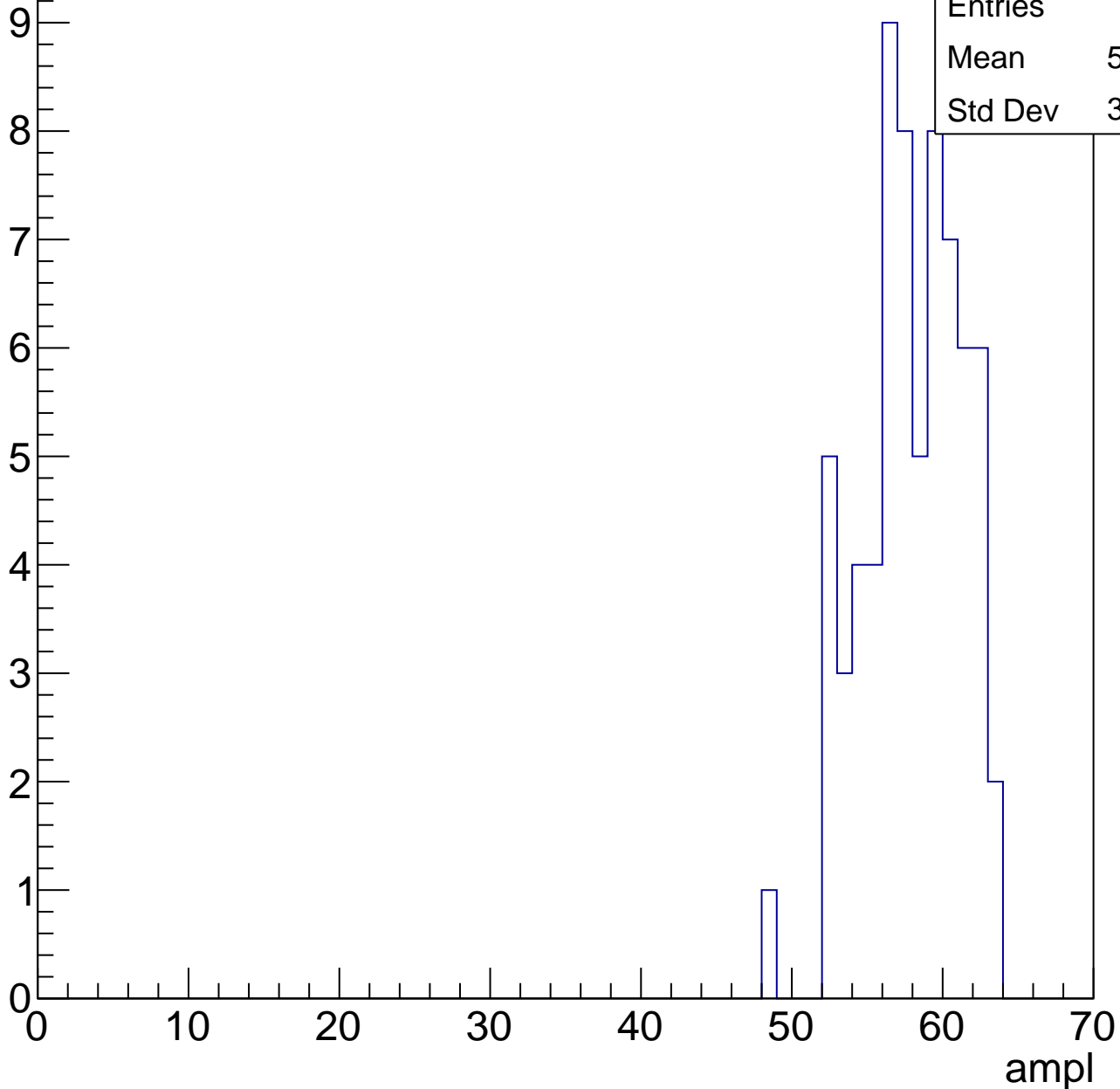


B1L103S, U12-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	57.49
Std Dev	3.247

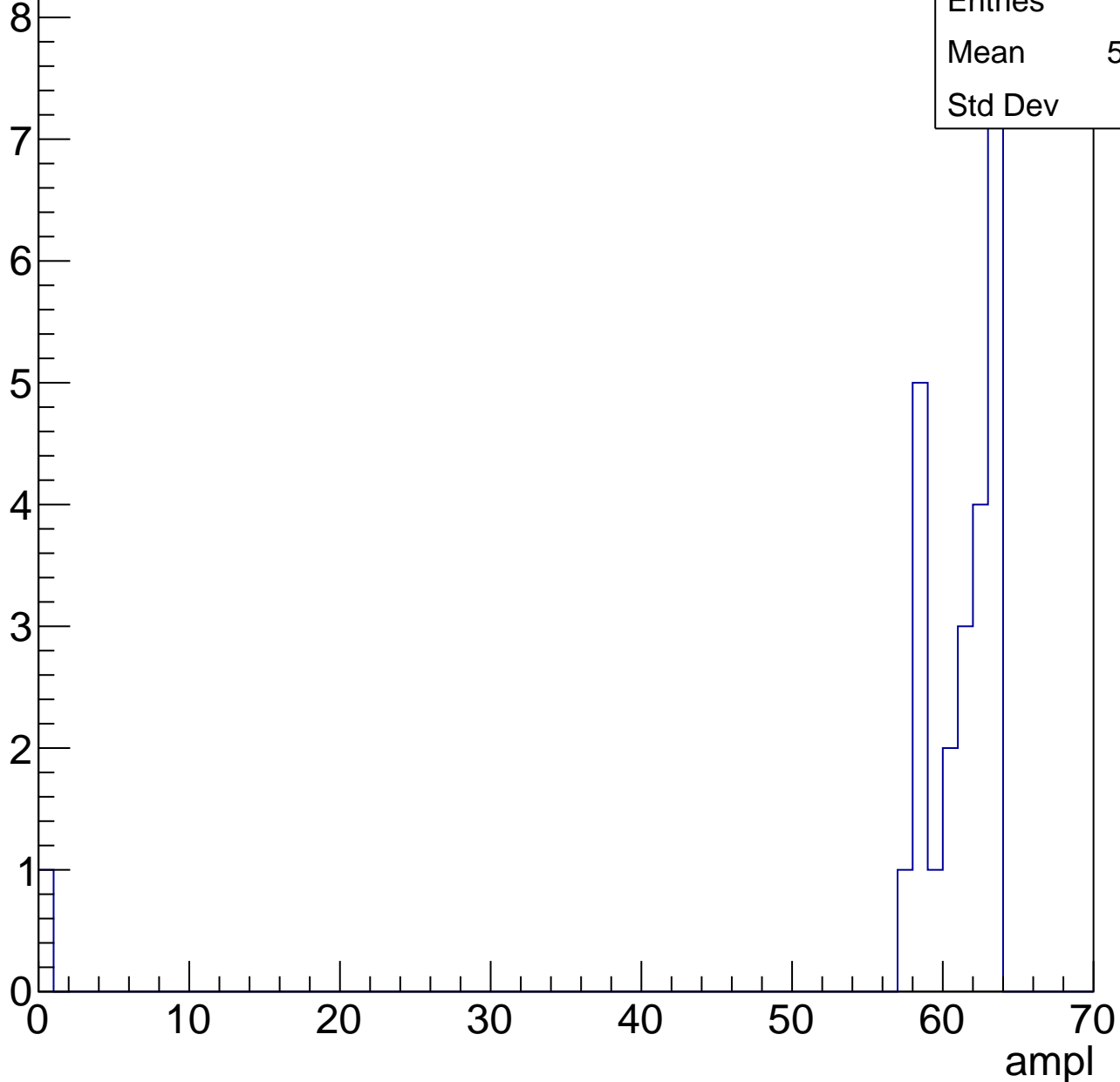


B1L103S, U12-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	25
Mean	58.44
Std Dev	12.1



B1L103S, U12-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U12-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch126, adc0

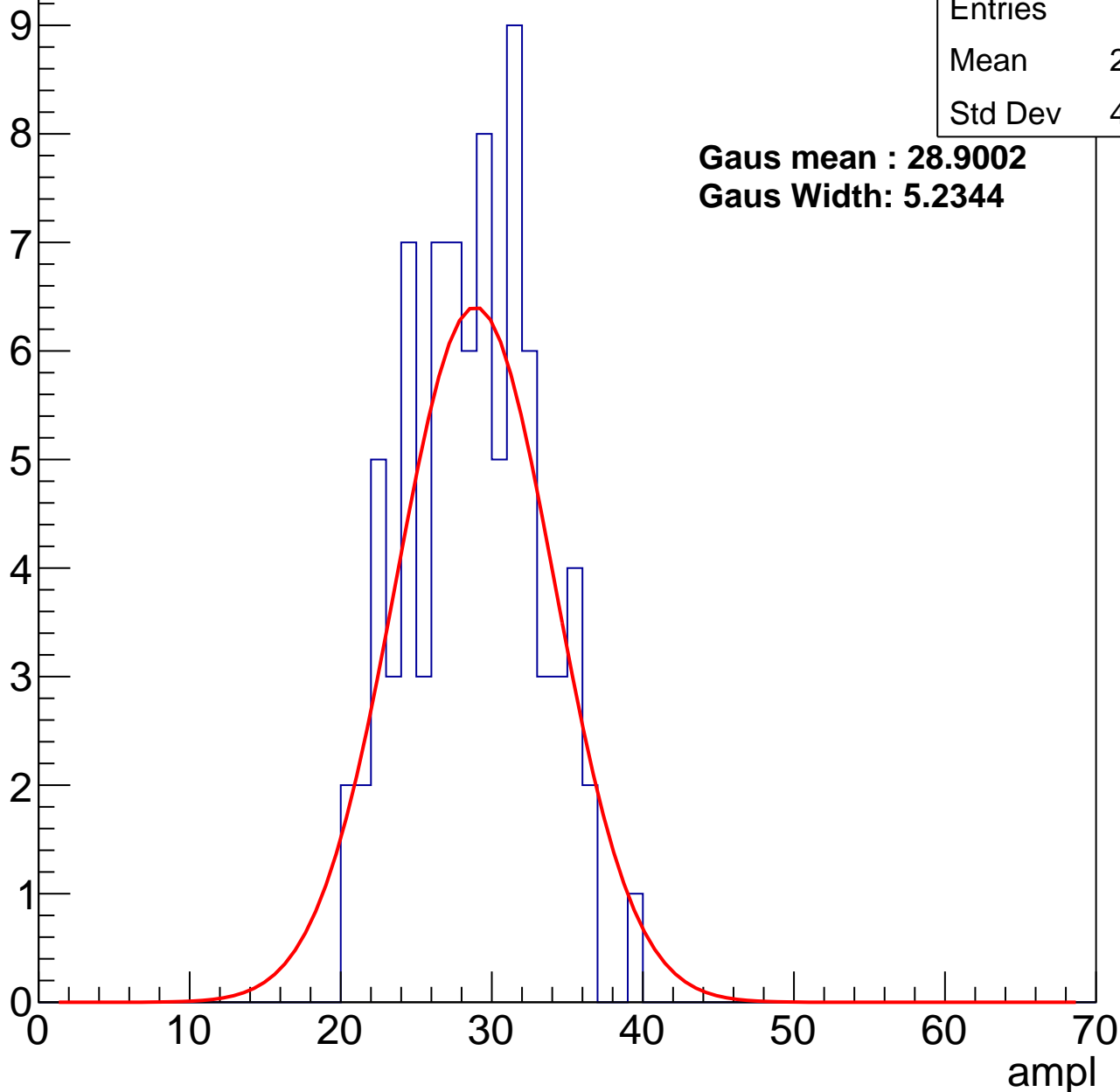
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	28.29
Std Dev	4.224

Gaus mean : 28.9002

Gaus Width: 5.2344



B1L103S, U12-ch126, adc1

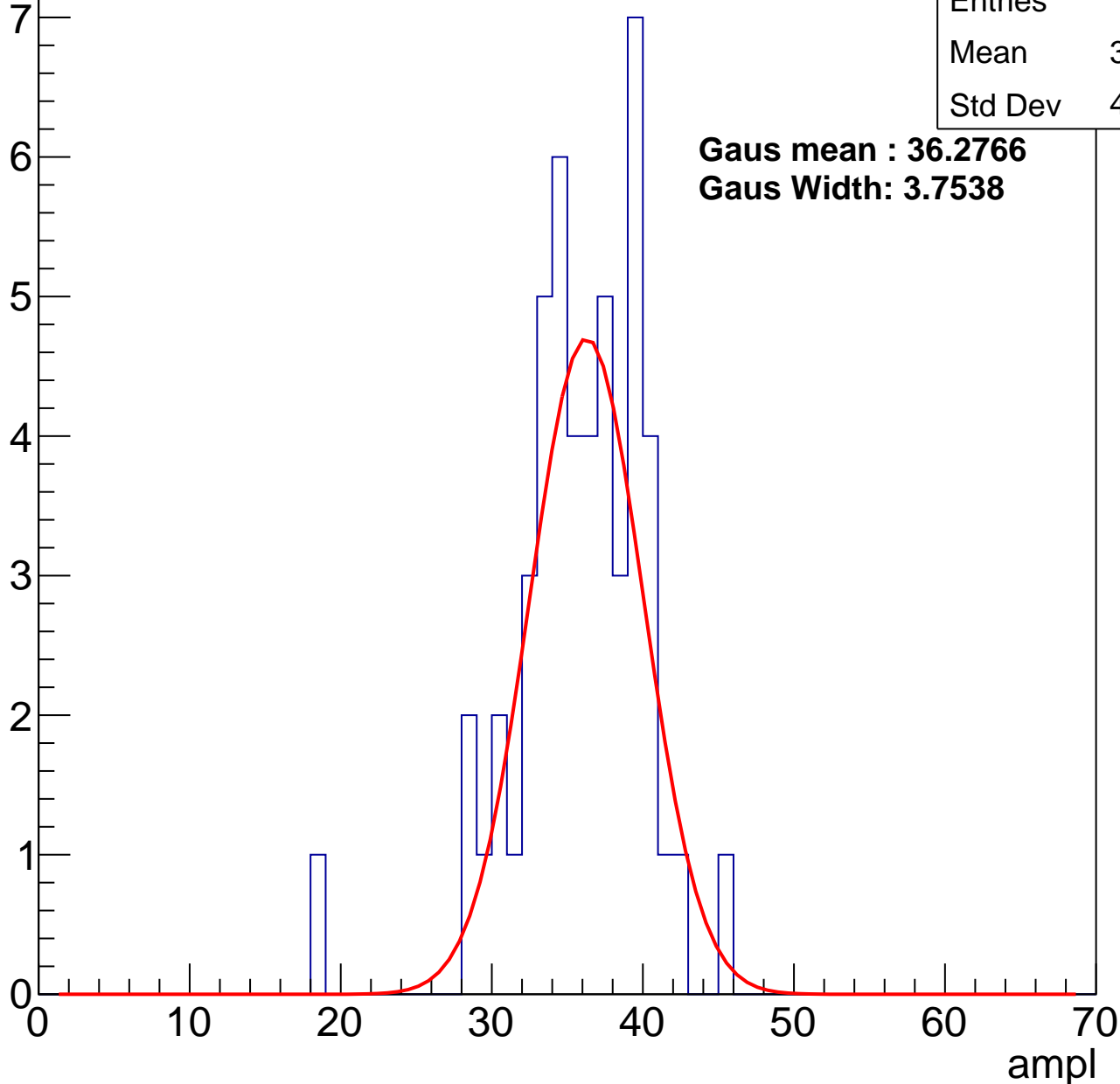
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	35.35
Std Dev	4.392

Gaus mean : 36.2766

Gaus Width: 3.7538



B1L103S, U12-ch126, adc2

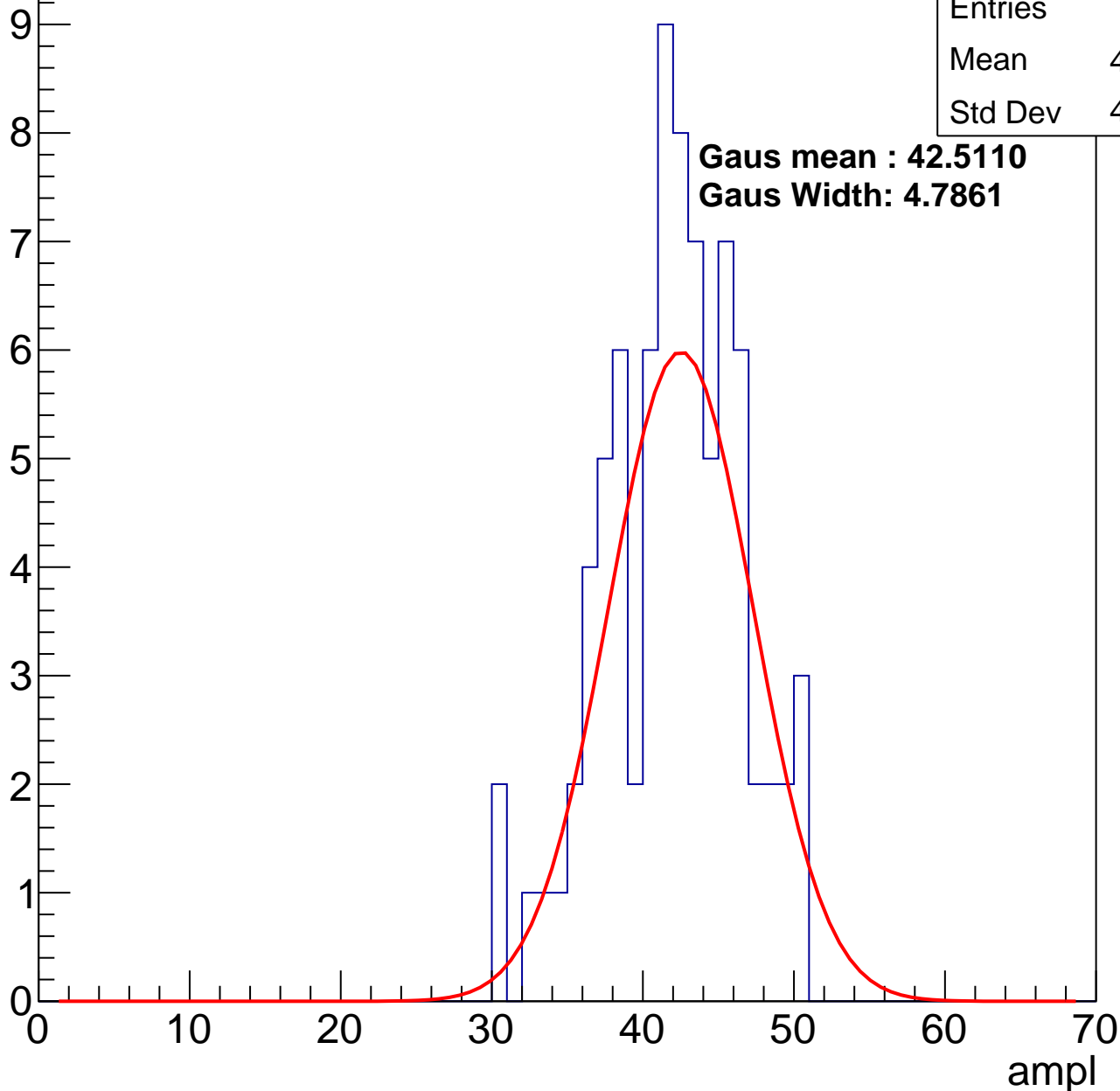
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	41.47
Std Dev	4.475

Gaus mean : 42.5110

Gaus Width: 4.7861

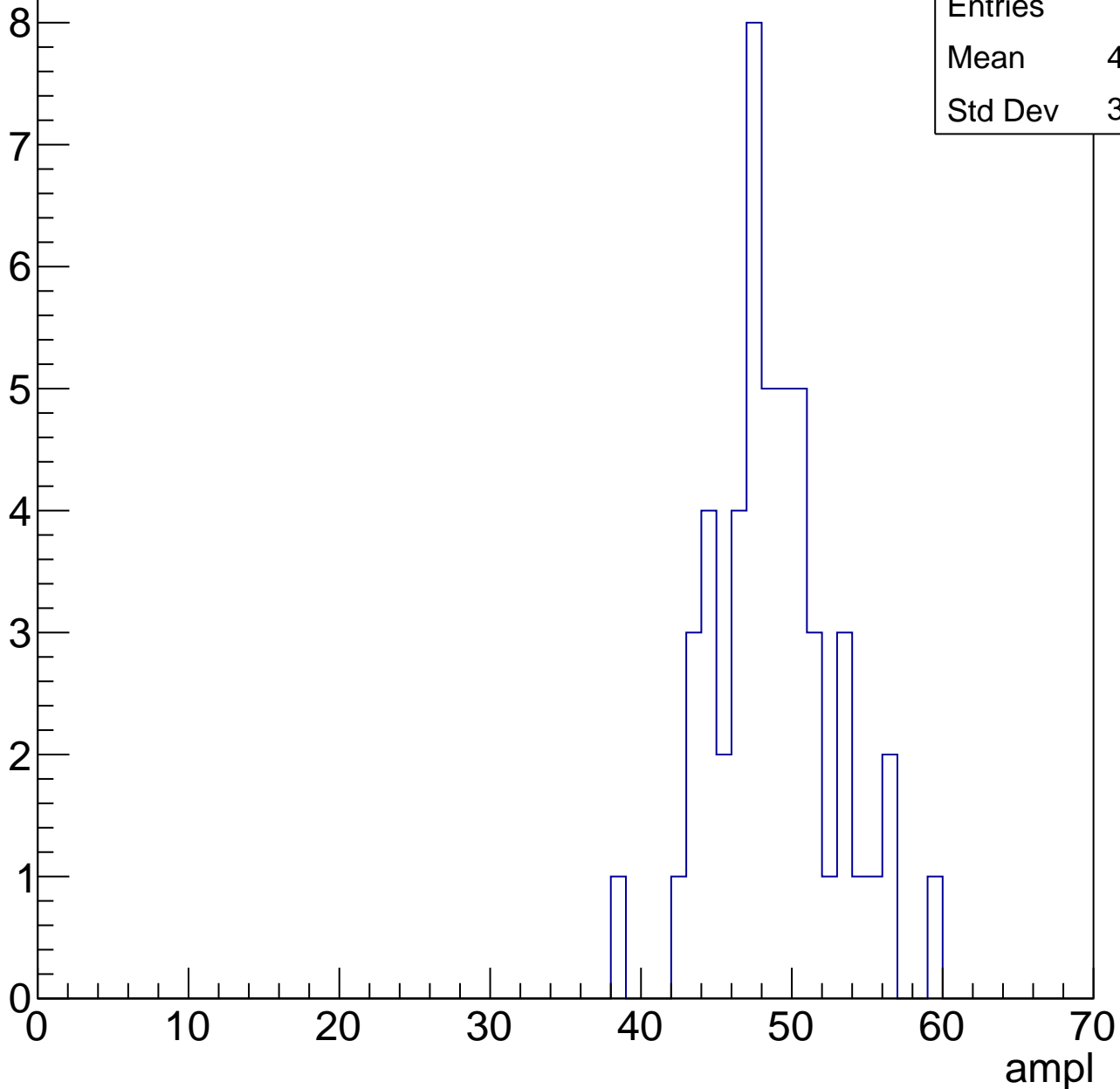


B1L103S, U12-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

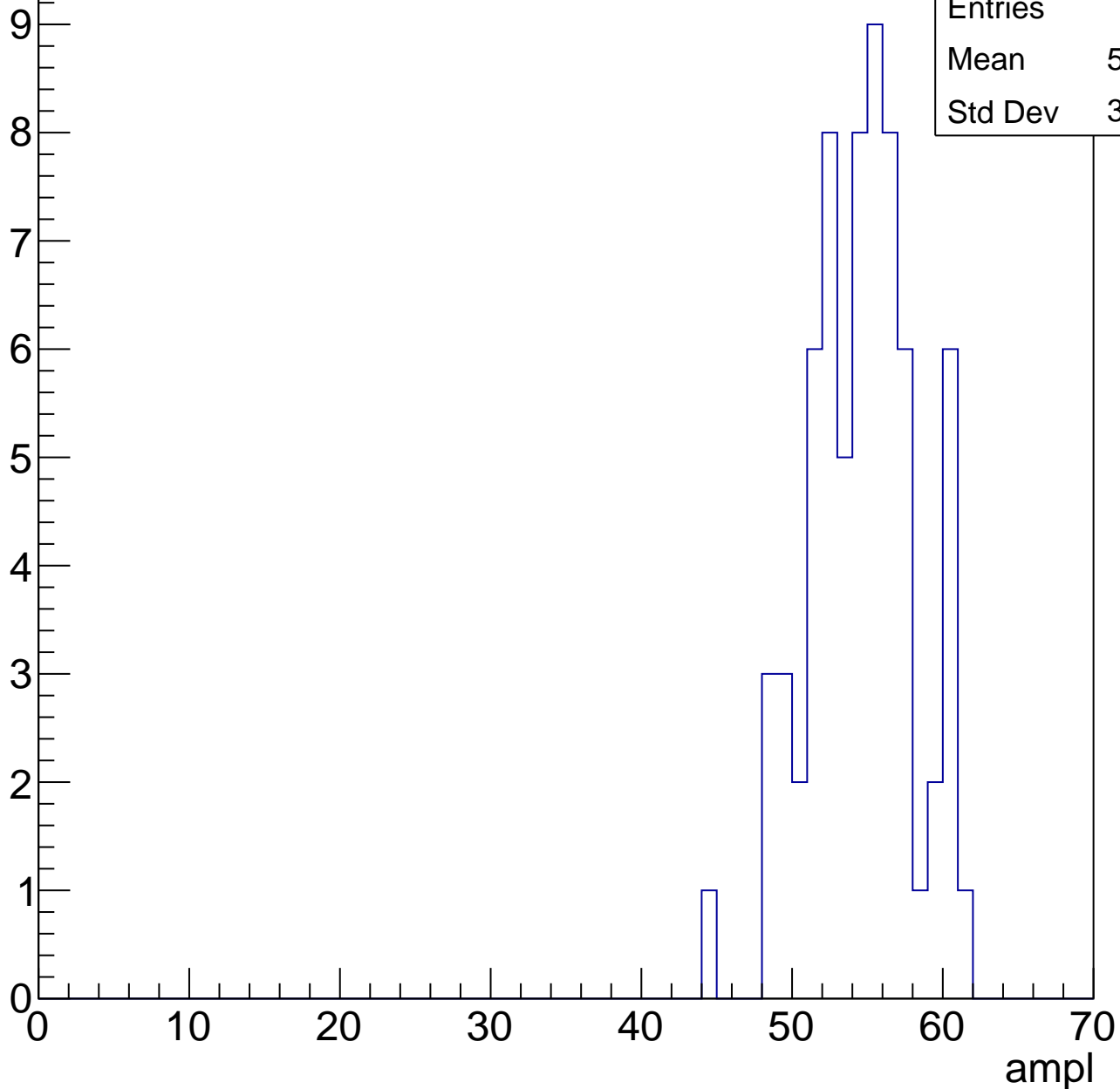
Entries	50
Mean	48.28
Std Dev	3.975



B1L103S, U12-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



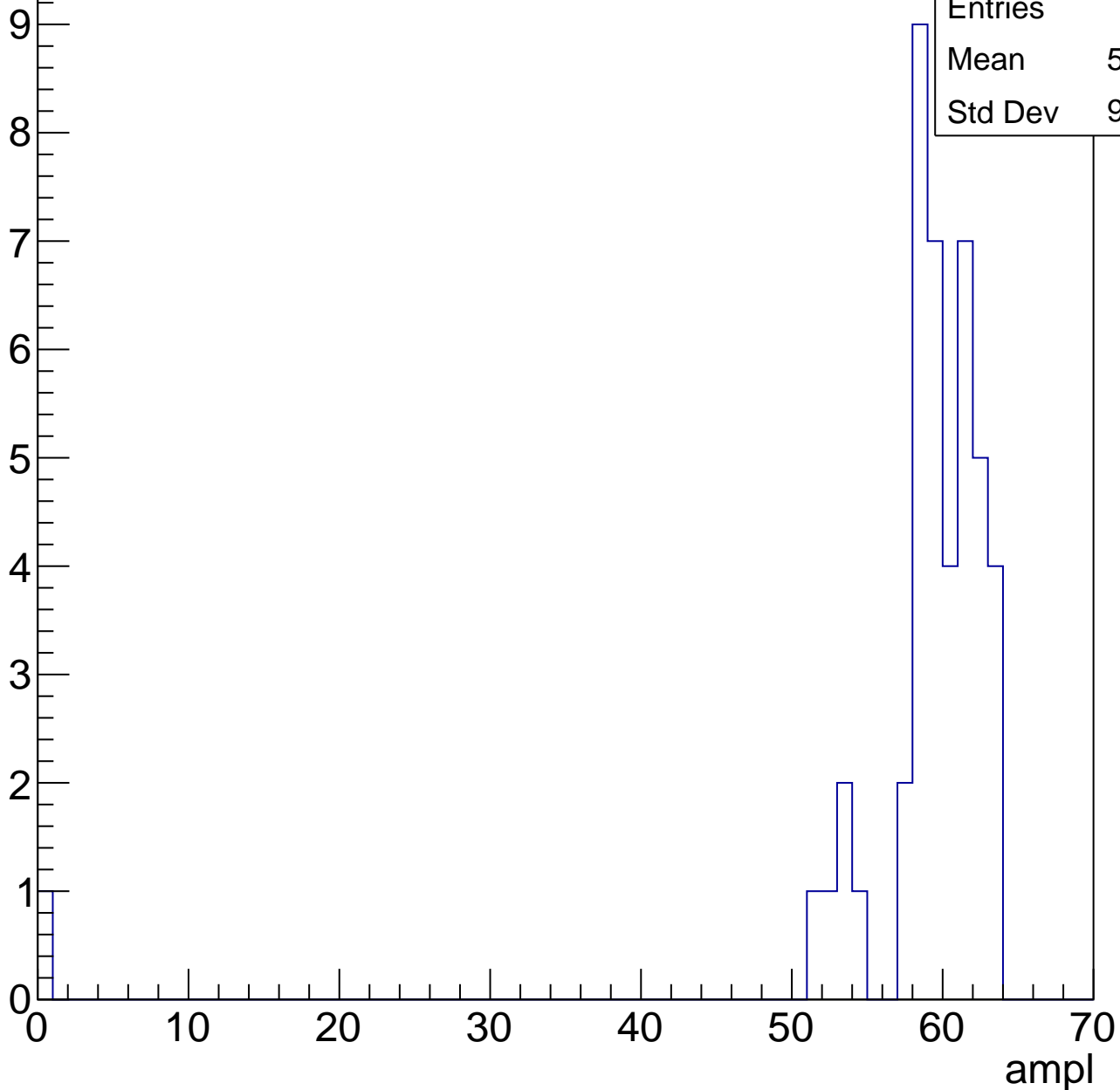
Entries	69
Mean	54.14
Std Dev	3.469

B1L103S, U12-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

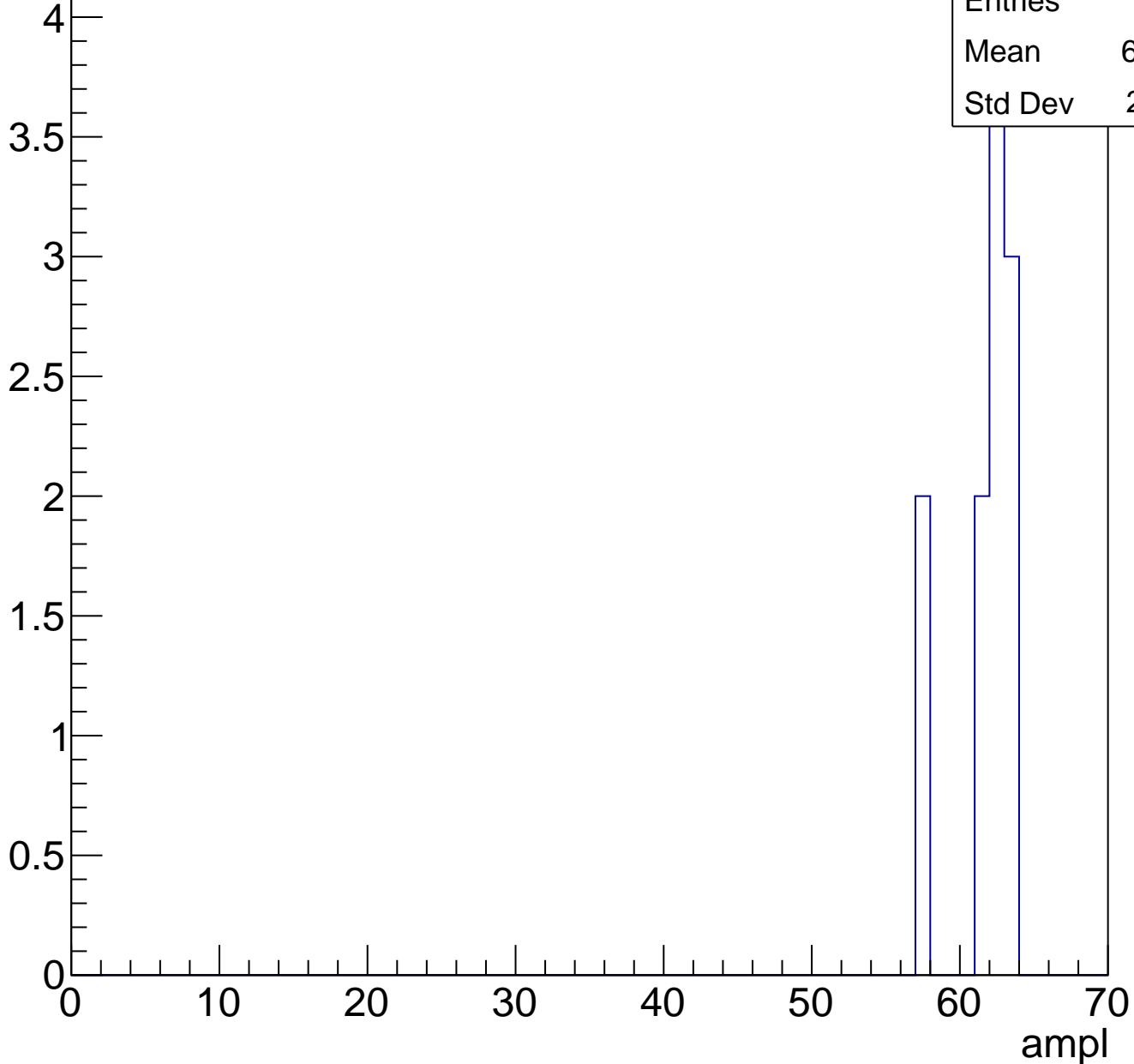
Entries	44
Mean	57.75
Std Dev	9.269



B1L103S, U12-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

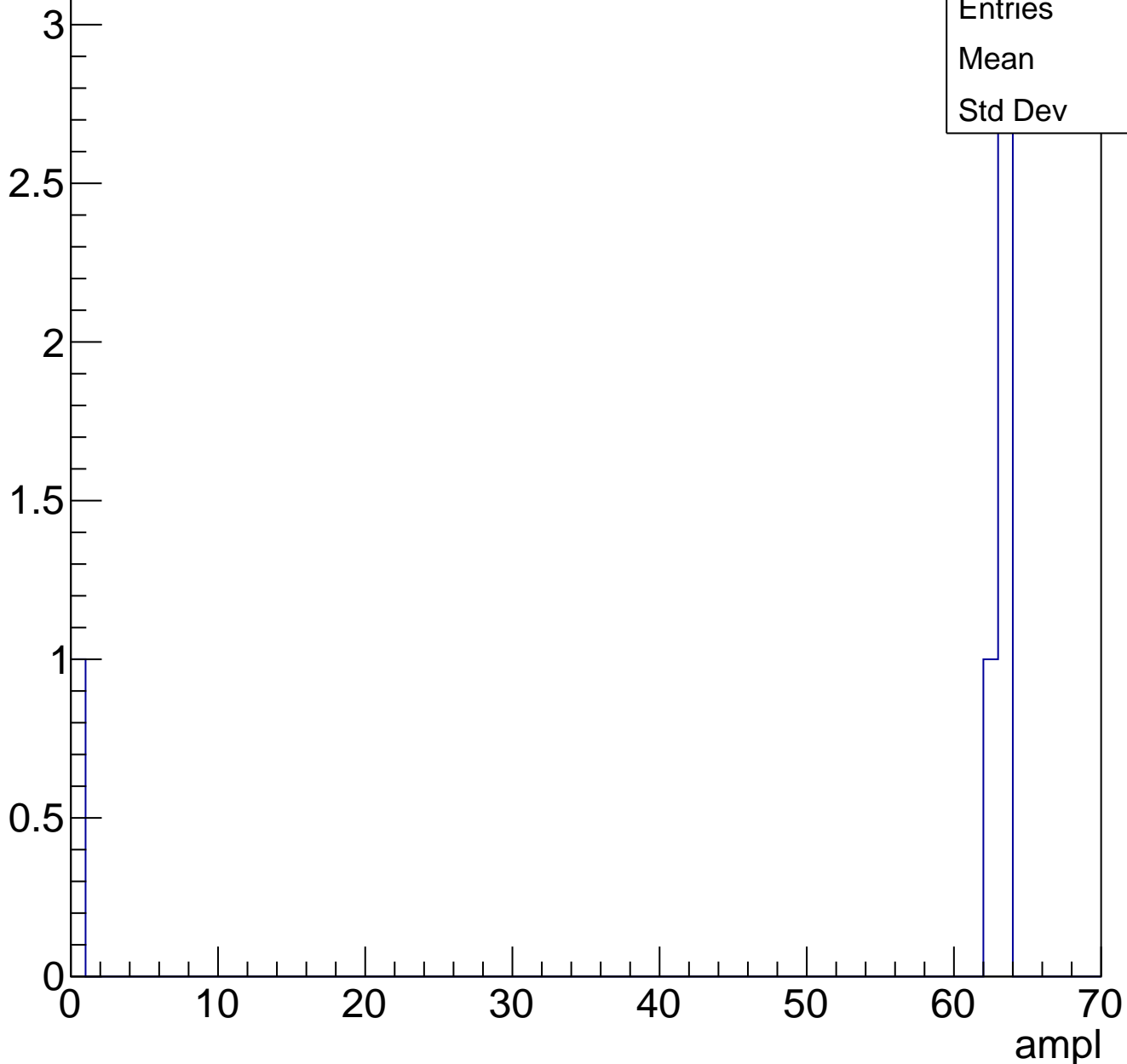
Entry



B1L103S, U12-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U12-ch127, adc0

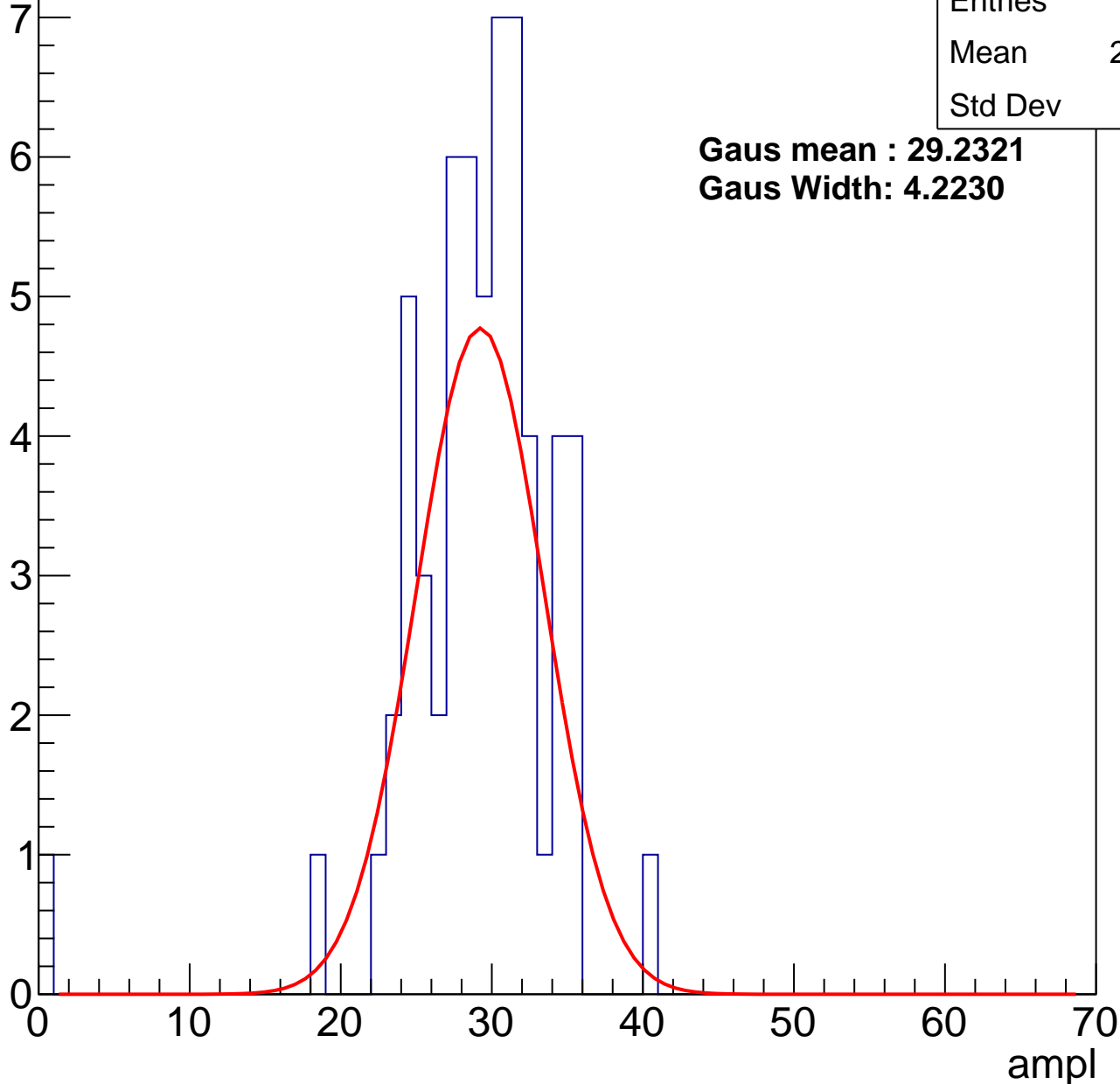
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.53
Std Dev	5.39

Gaus mean : 29.2321

Gaus Width: 4.2230



B1L103S, U12-ch127, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	93
Mean	35.39
Std Dev	6.776

Gaus mean : 37.3042

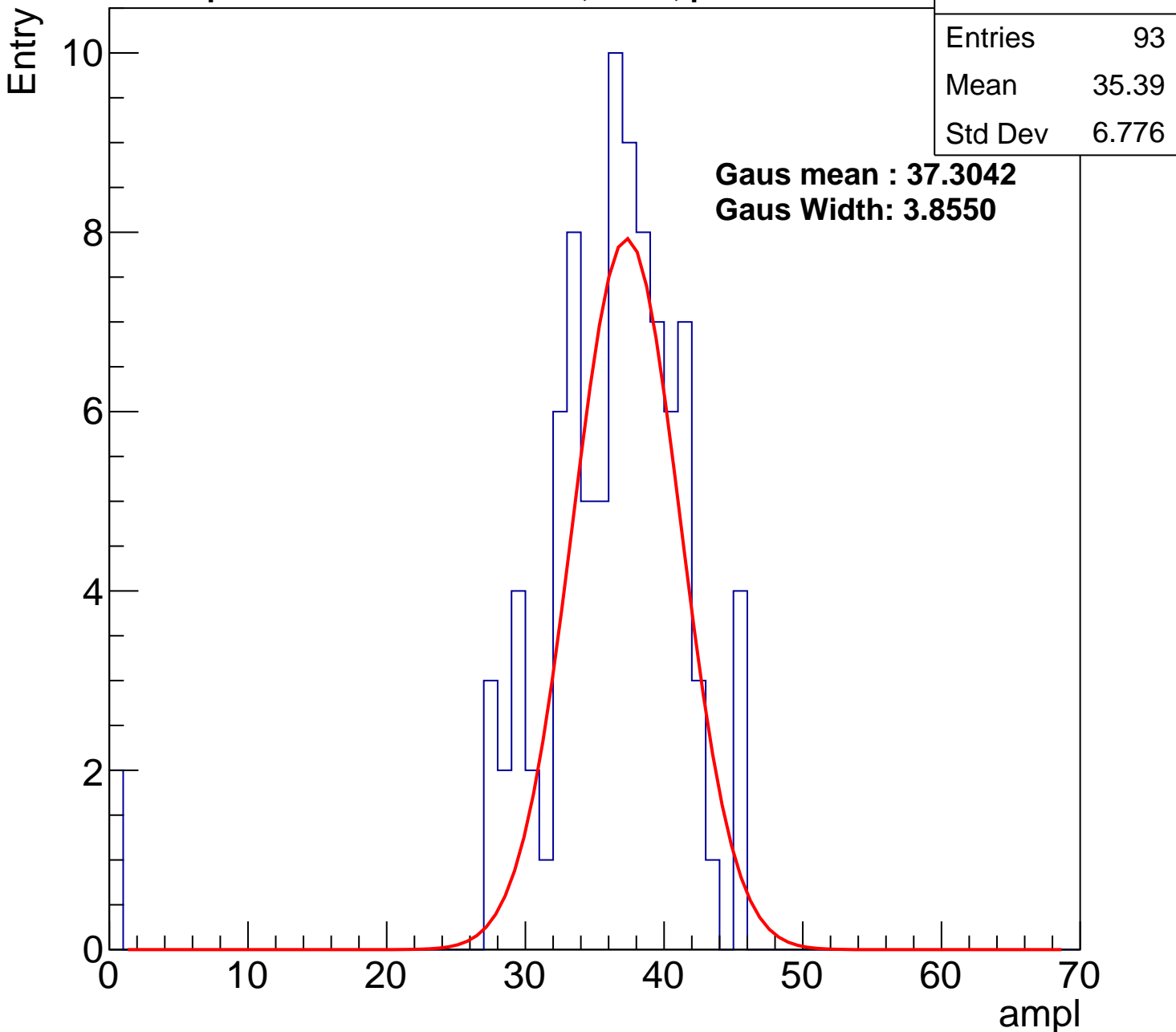
Gaus Width: 3.8550

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U12-ch127, adc2

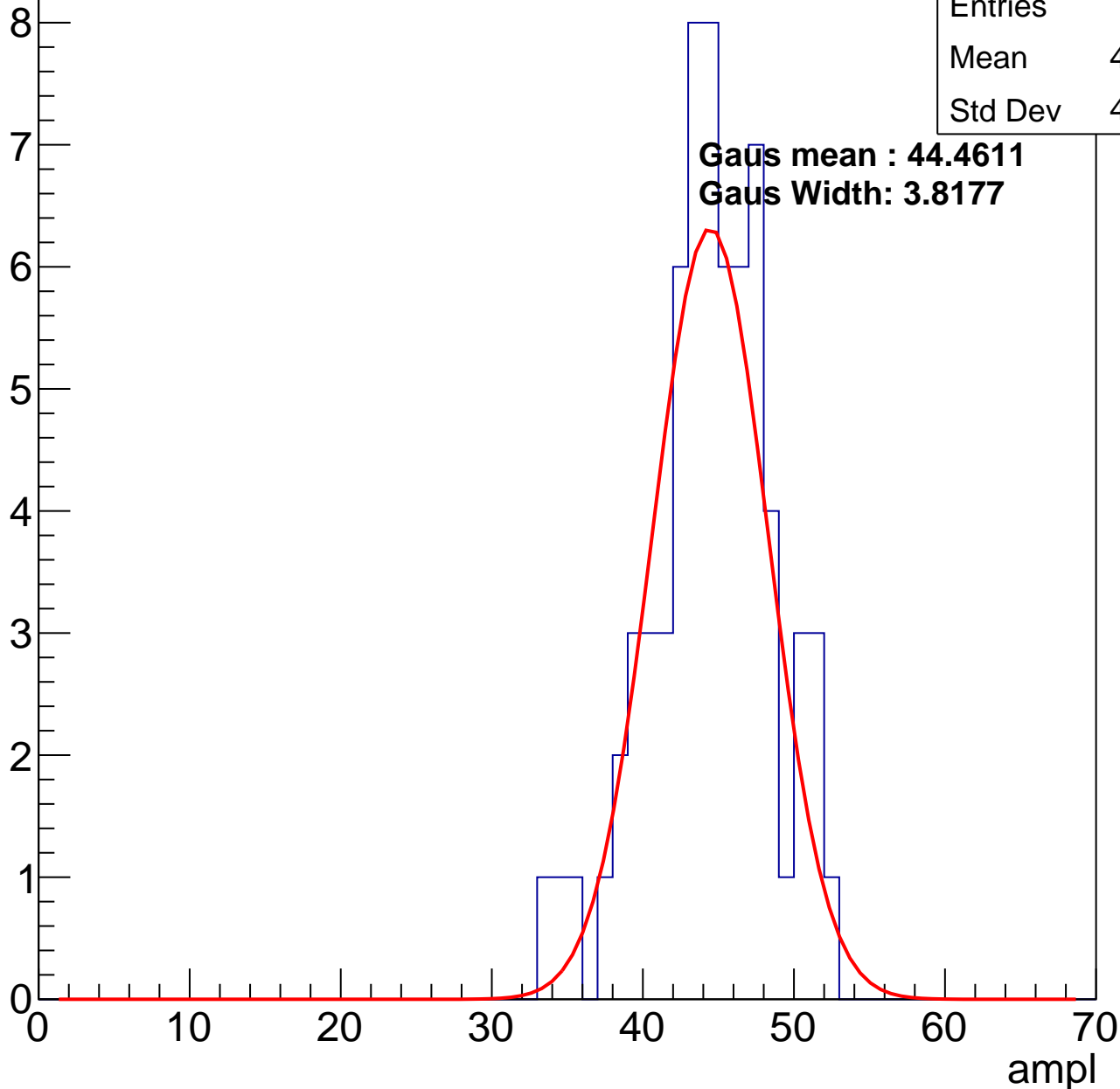
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	44.03
Std Dev	4.033

Gaus mean : 44.4611

Gaus Width: 3.8177

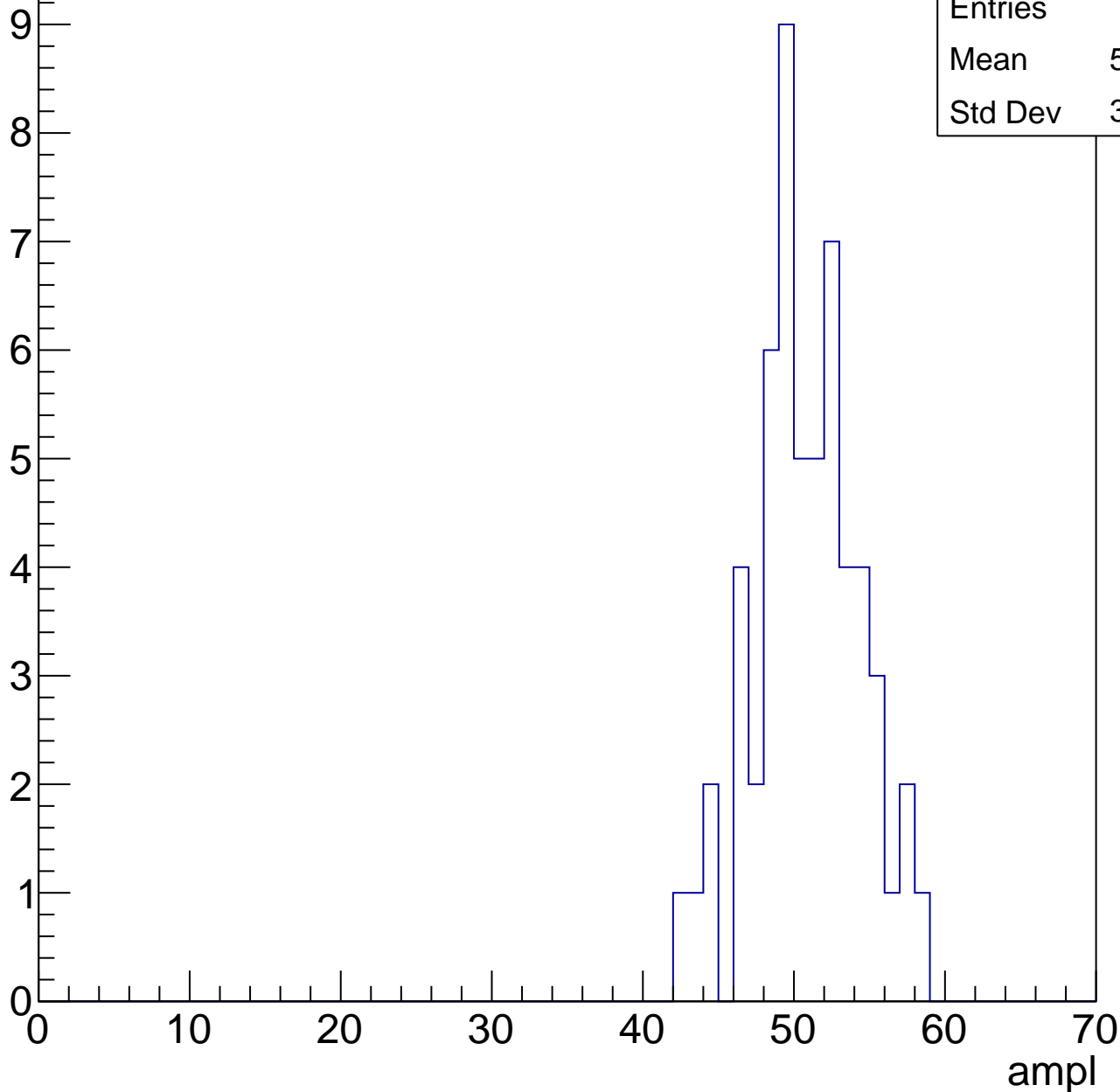


B1L103S, U12-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	50.35
Std Dev	3.502

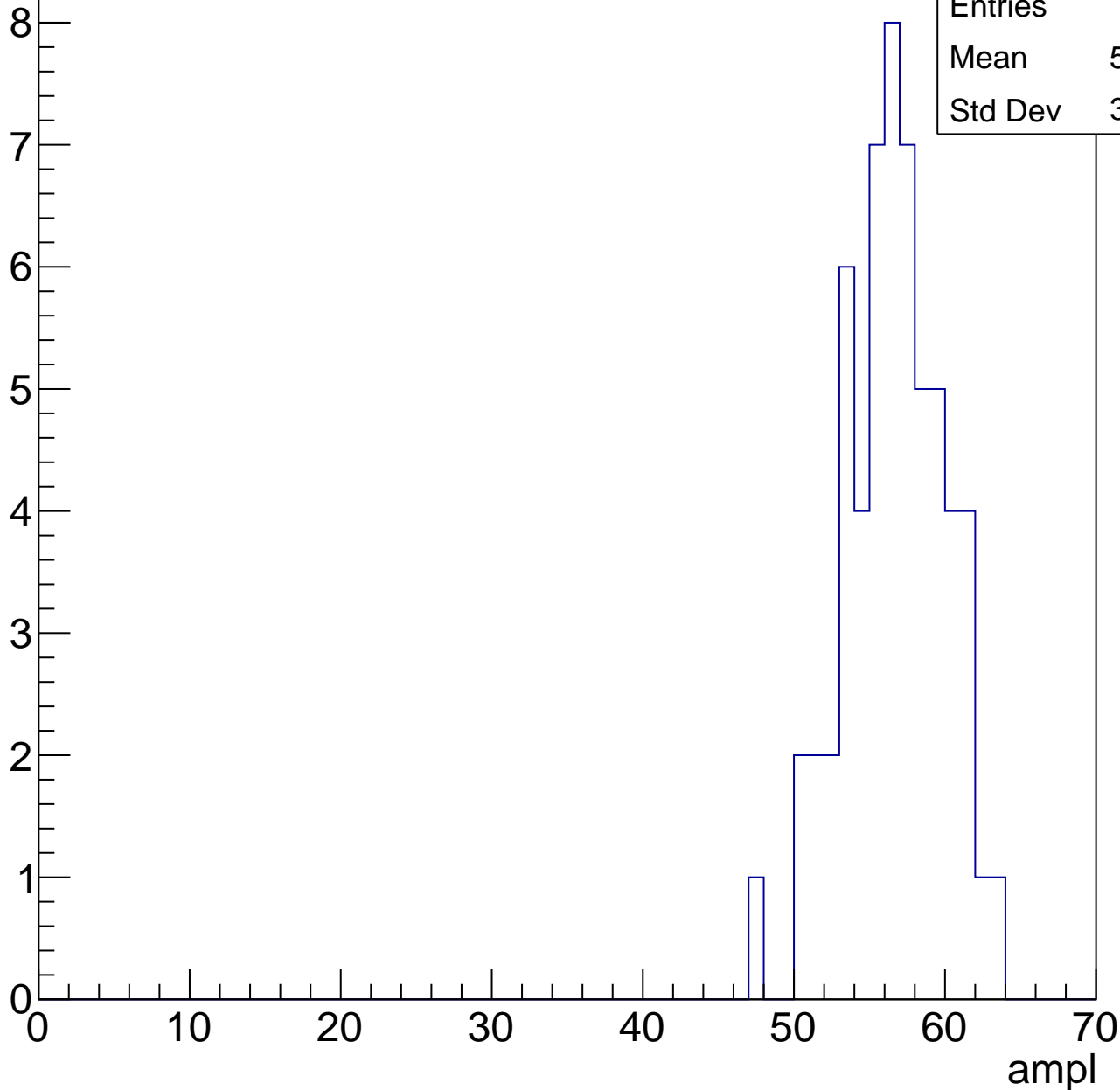


B1L103S, U12-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	56.15
Std Dev	3.272

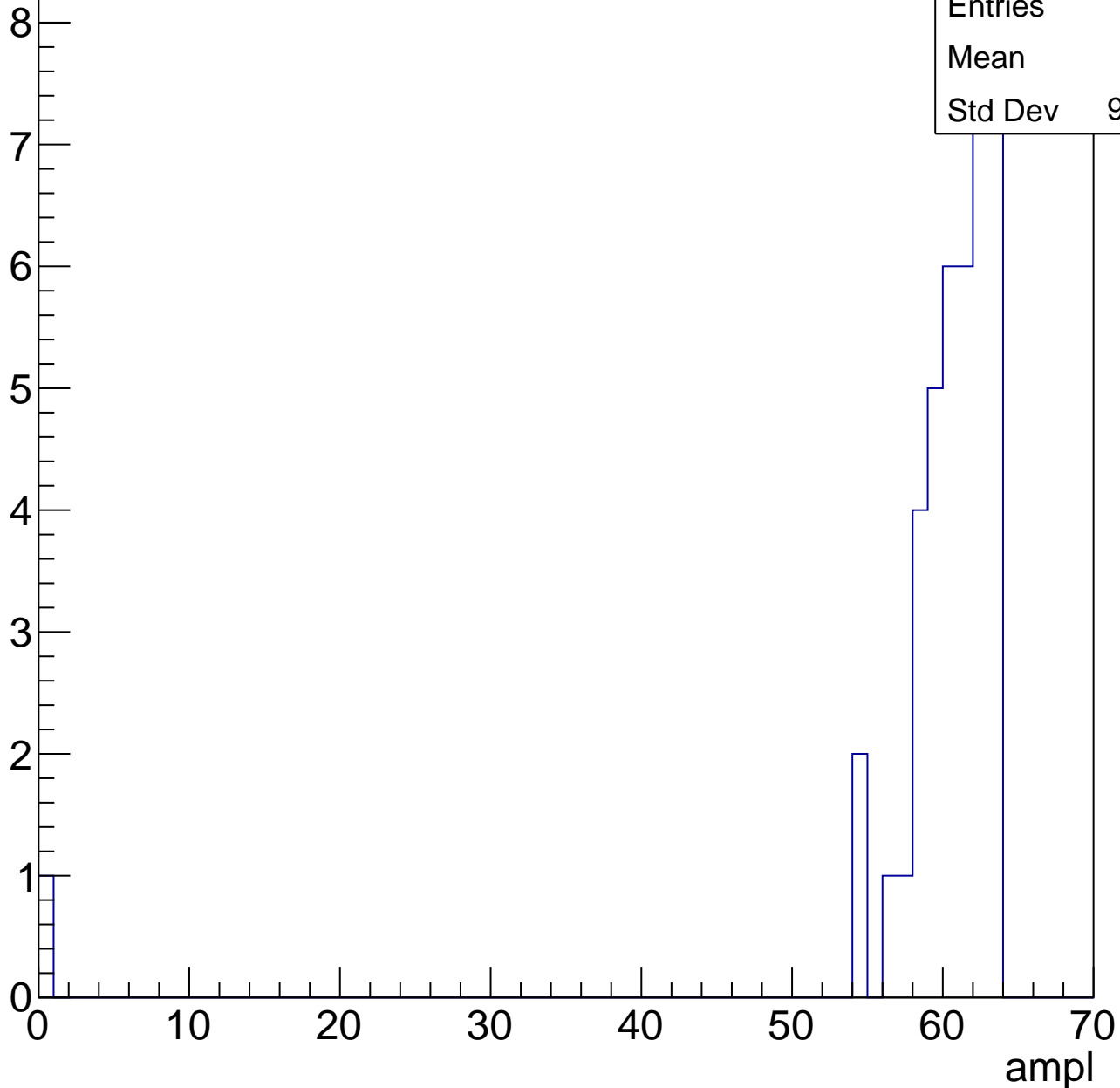


B1L103S, U12-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.9
Std Dev	9.484



B1L103S, U12-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

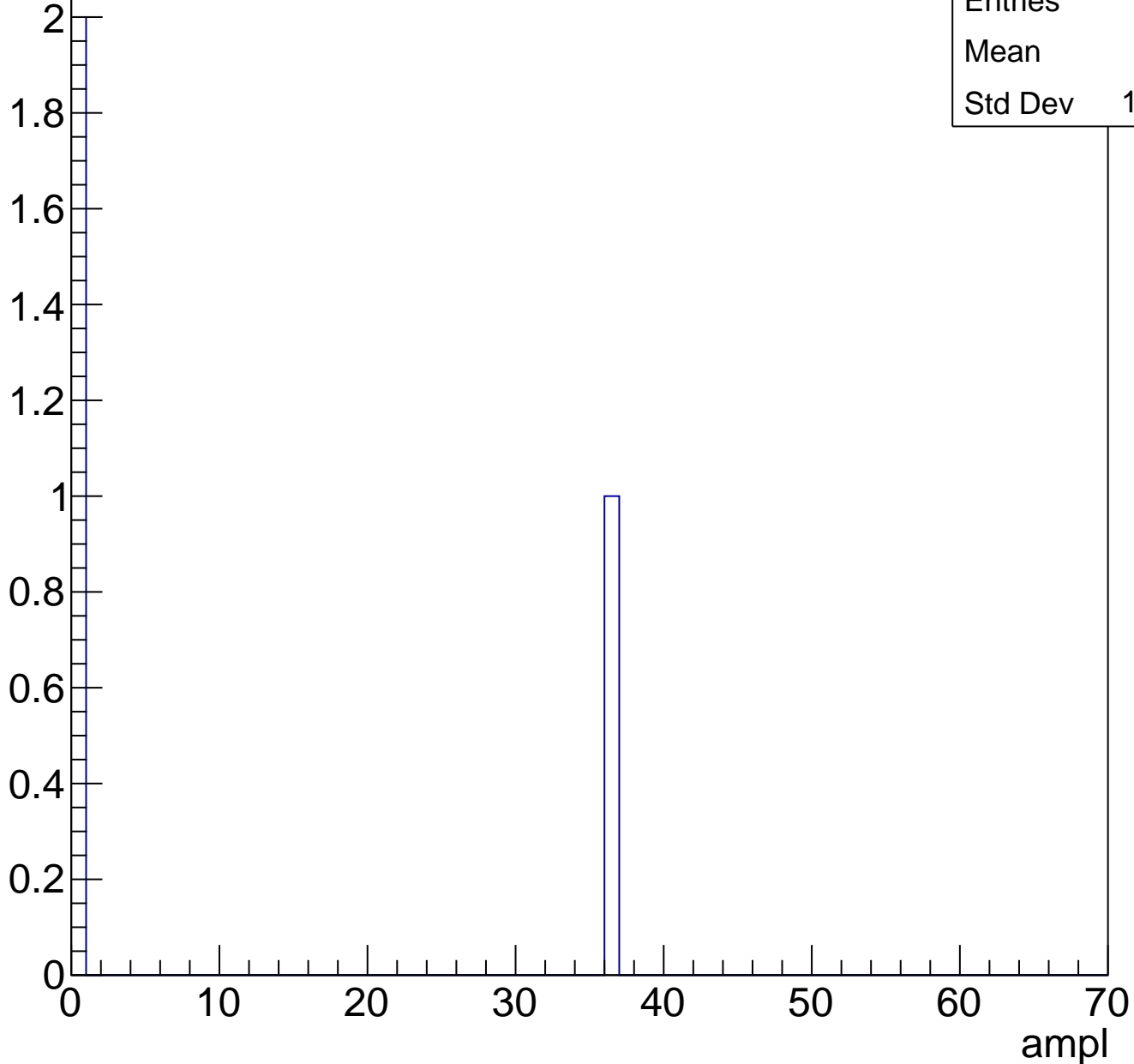
Entry



B1L103S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

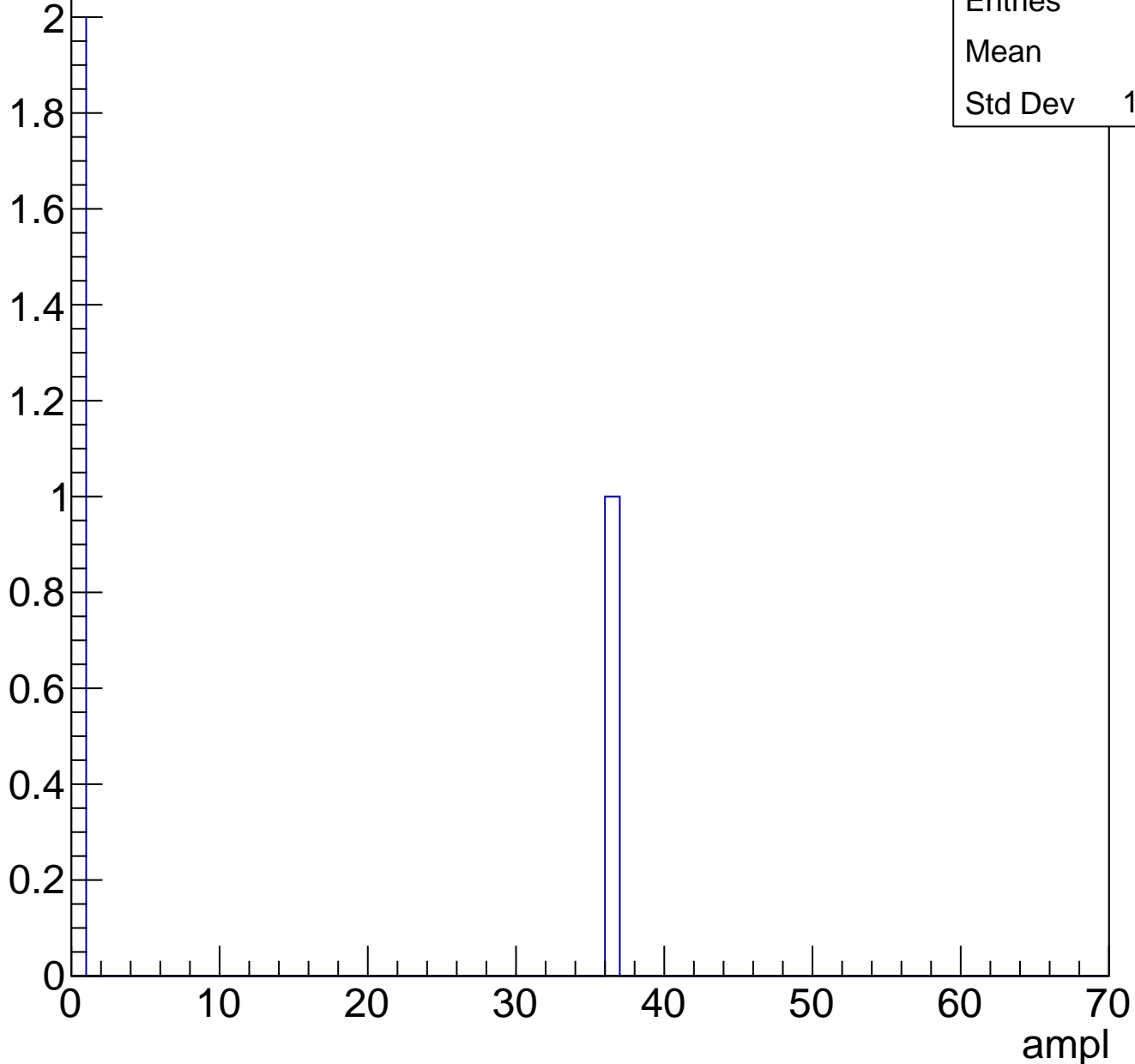


Entries	3
Mean	12
Std Dev	16.97

B1L103S, U12-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	12
Std Dev	16.97