



# B1L003S, U24-ch0

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 44.02 |
| Std Dev | 11.81 |

Turn on : 26.4430

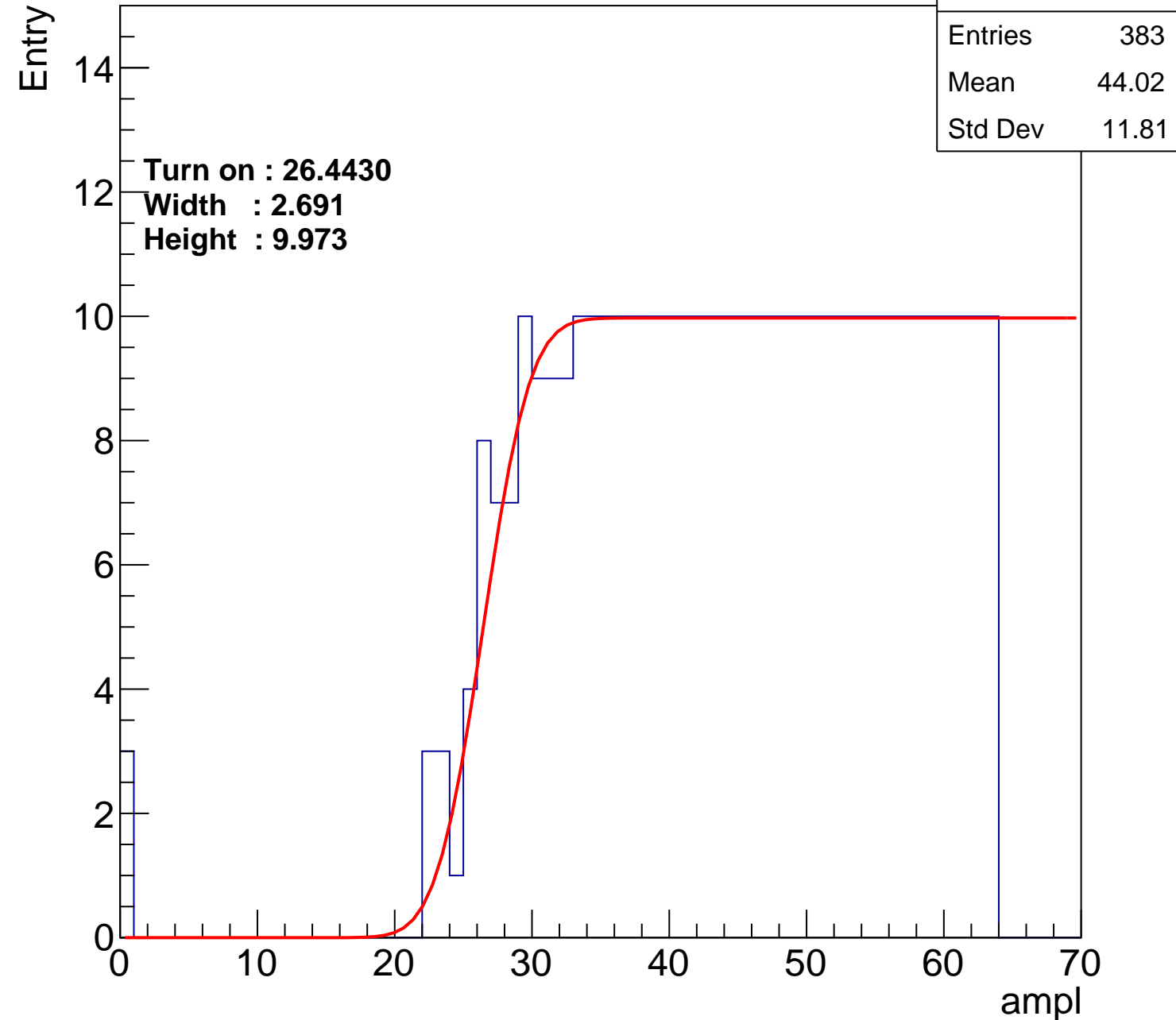
Width : 2.691

Height : 9.973

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch1

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.89 |
| Std Dev | 11.34 |

Turn on : 27.8796

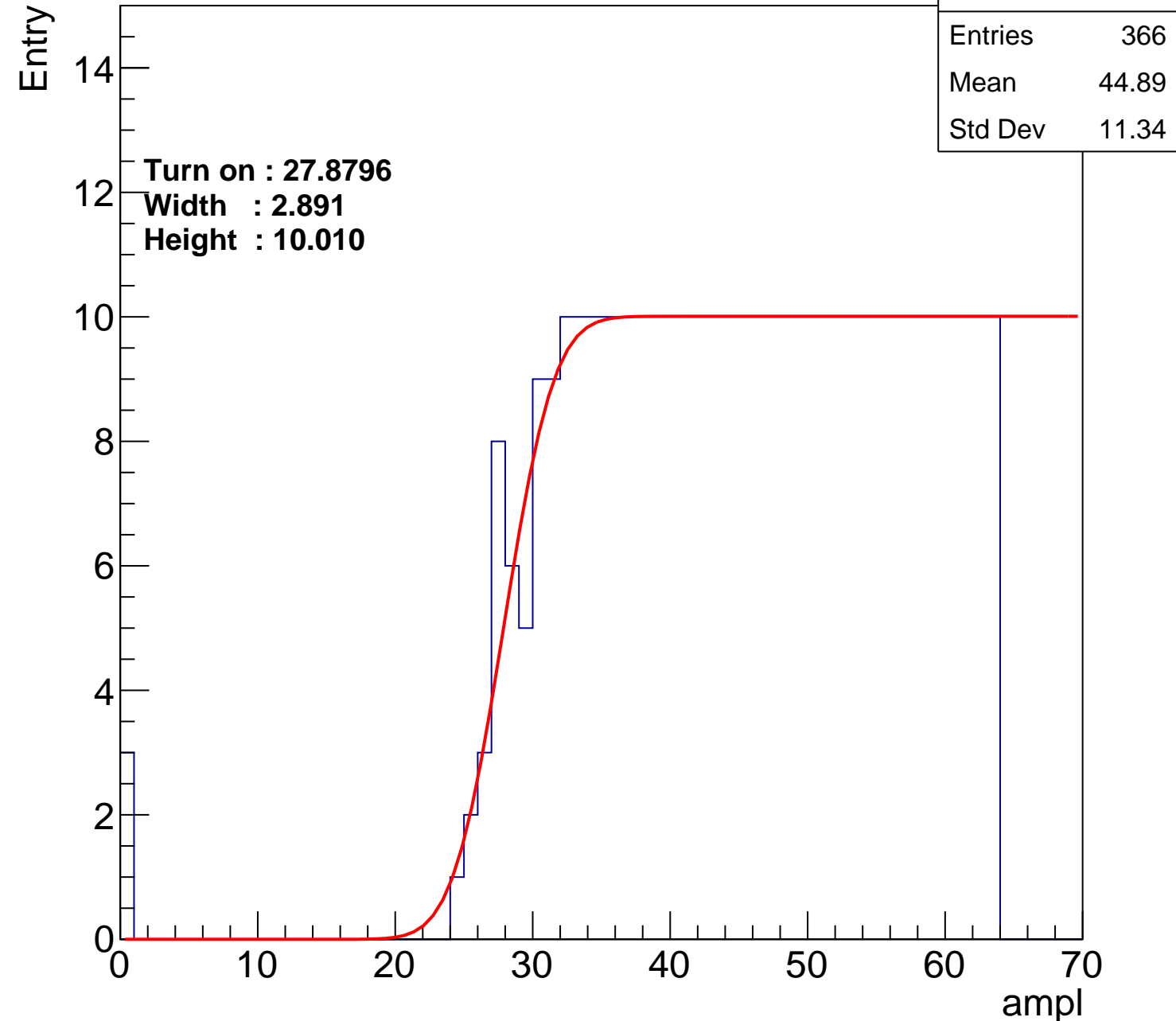
Width : 2.891

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch2

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.81 |
| Std Dev | 11.24 |

Turn on : 28.0396

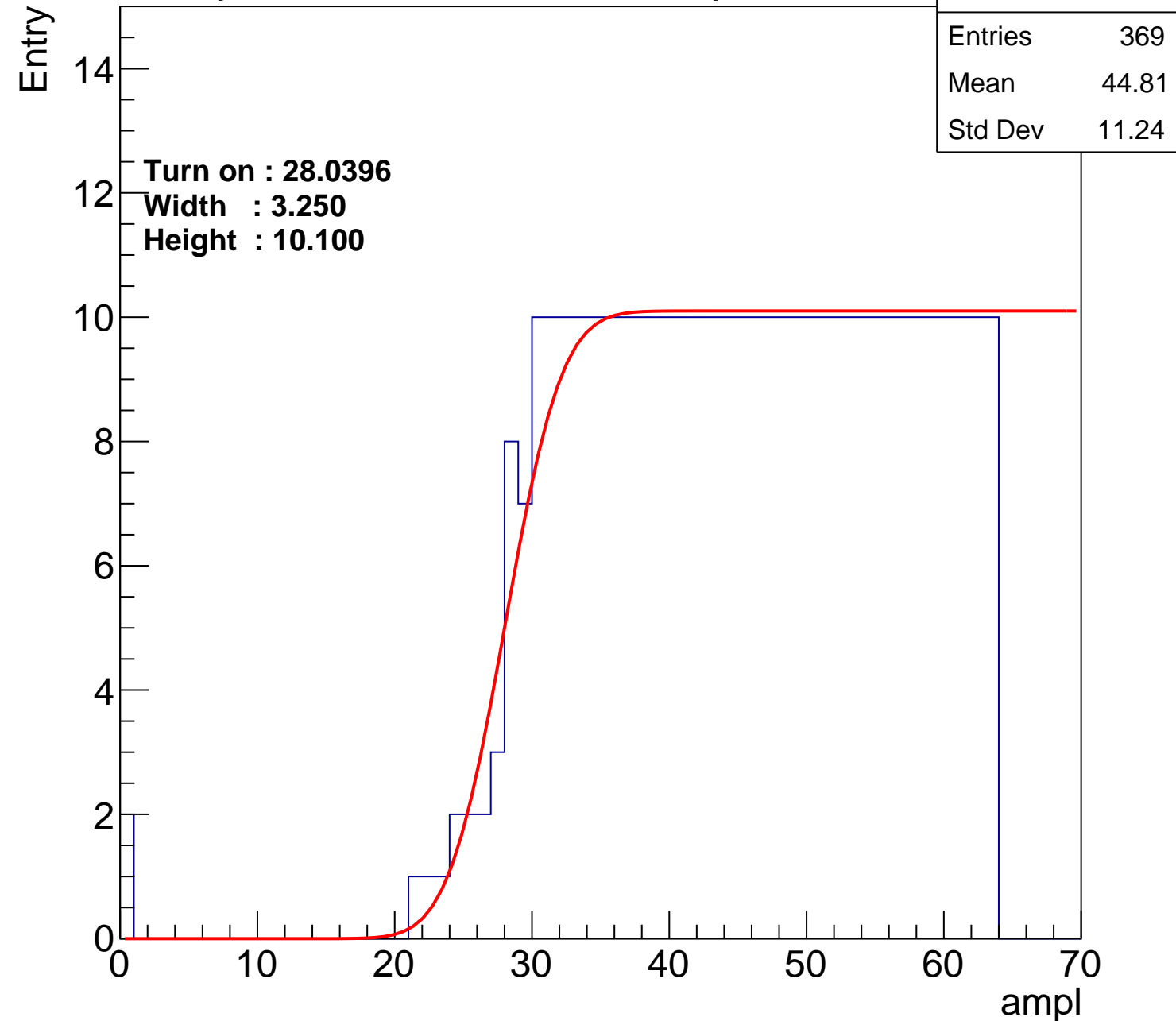
Width : 3.250

Height : 10.100

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch3

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.26 |
| Std Dev | 11.52 |

Turn on : 25.7902

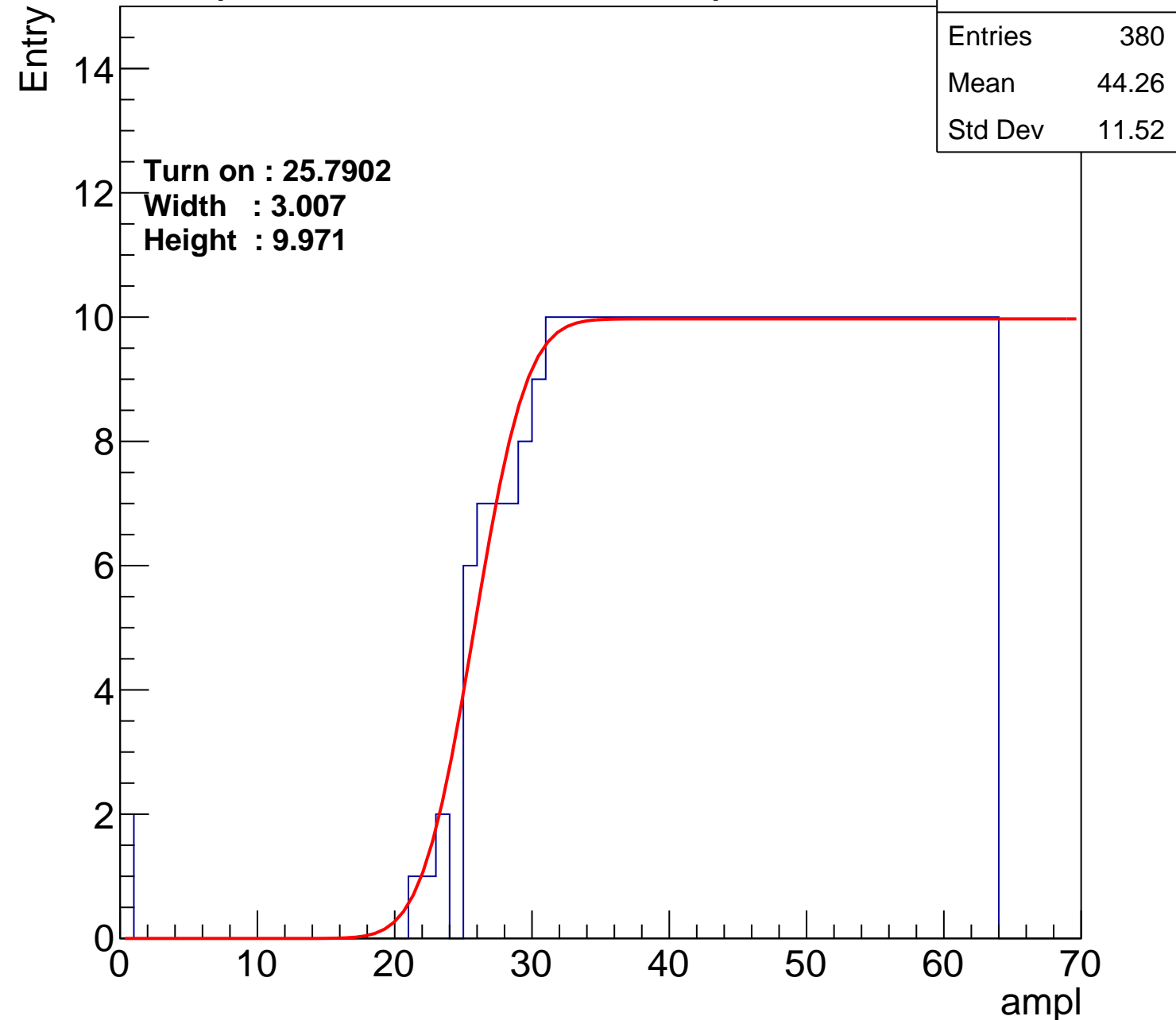
Width : 3.007

Height : 9.971

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch4

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.49 |
| Std Dev | 11.27 |

Turn on : 26.5027

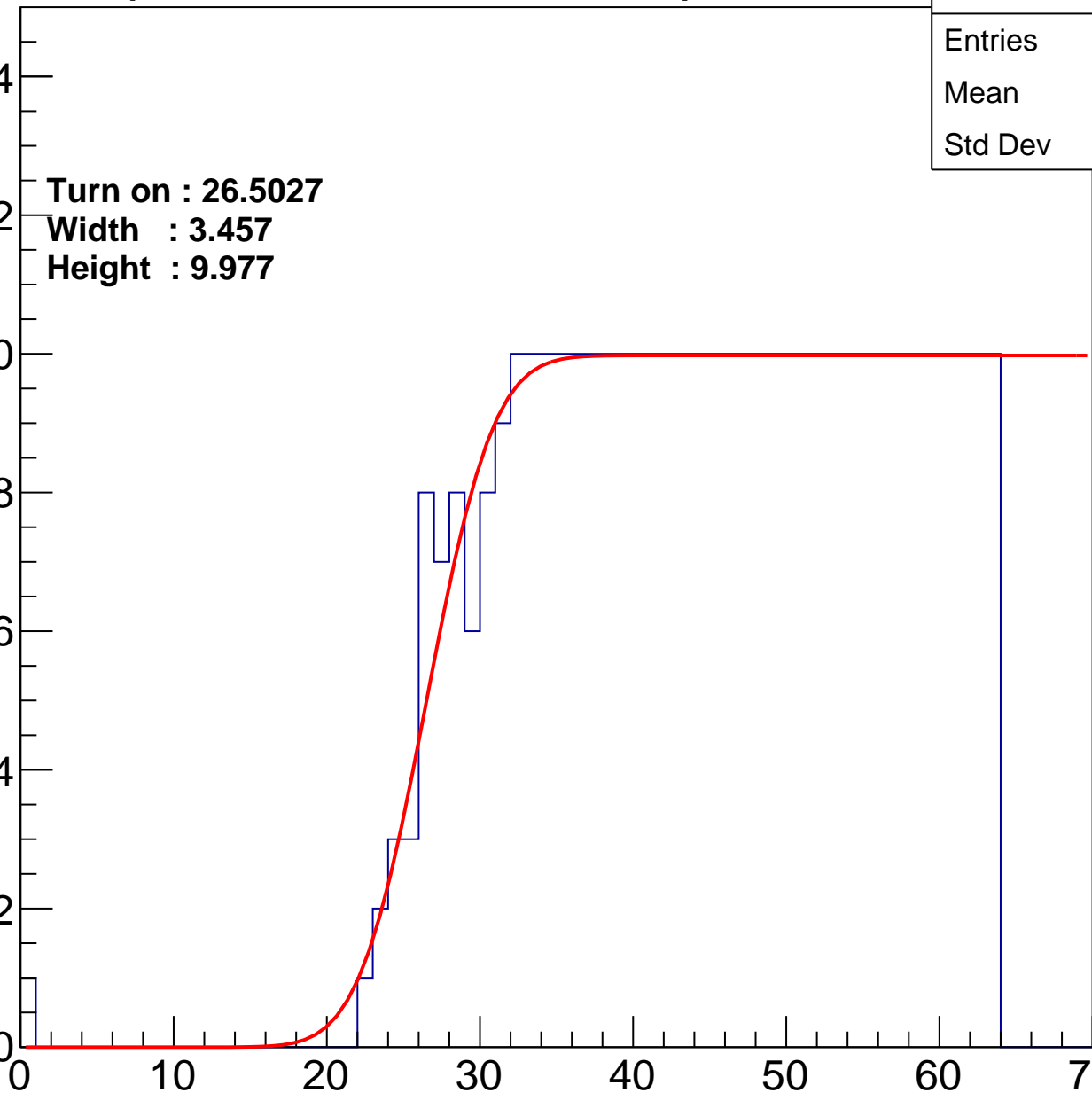
Width : 3.457

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch5

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.71 |
| Std Dev | 11.42 |

Turn on : 27.6053

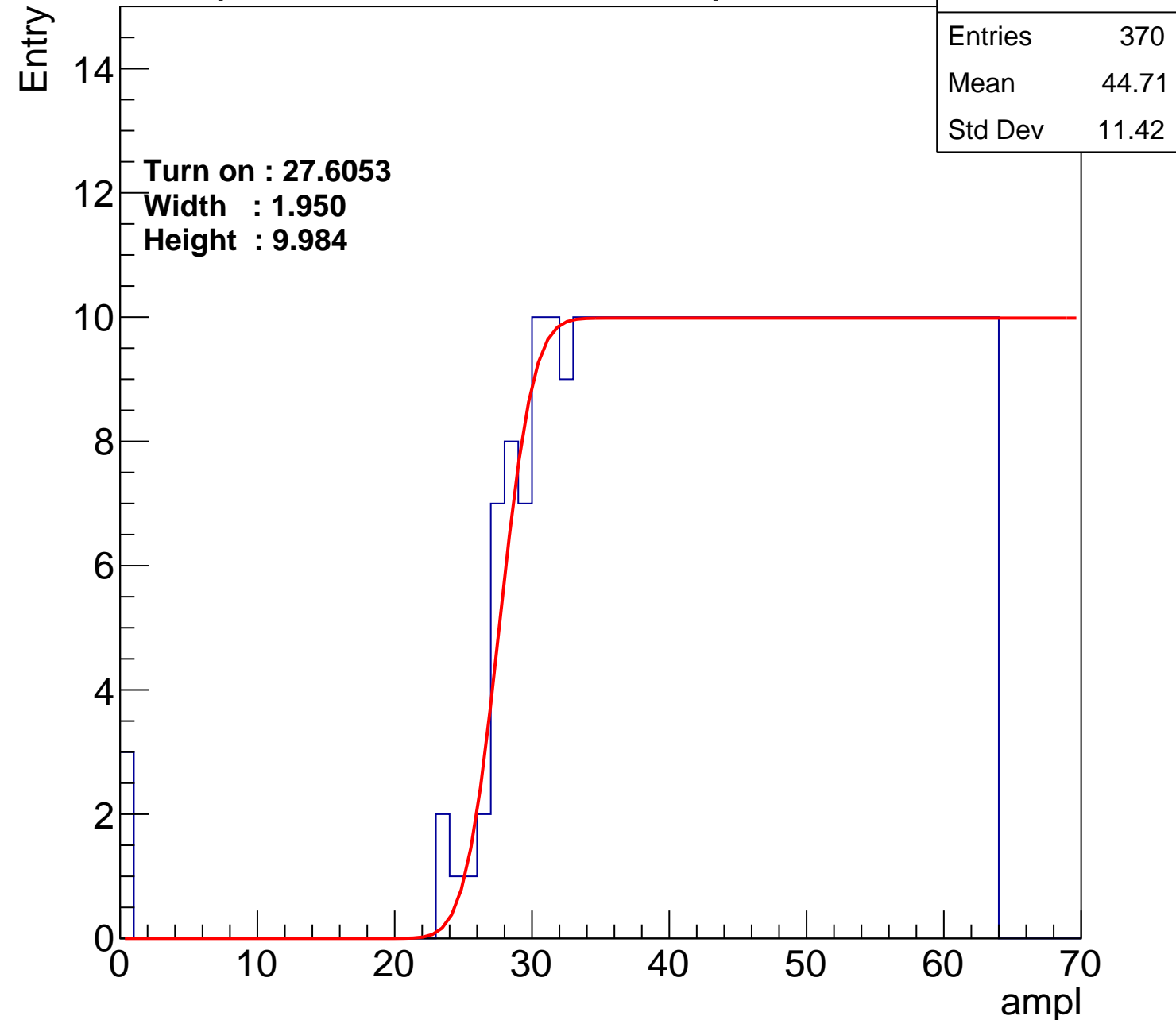
Width : 1.950

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch6

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 44.08 |
| Std Dev | 11.42 |

Turn on : 25.5898

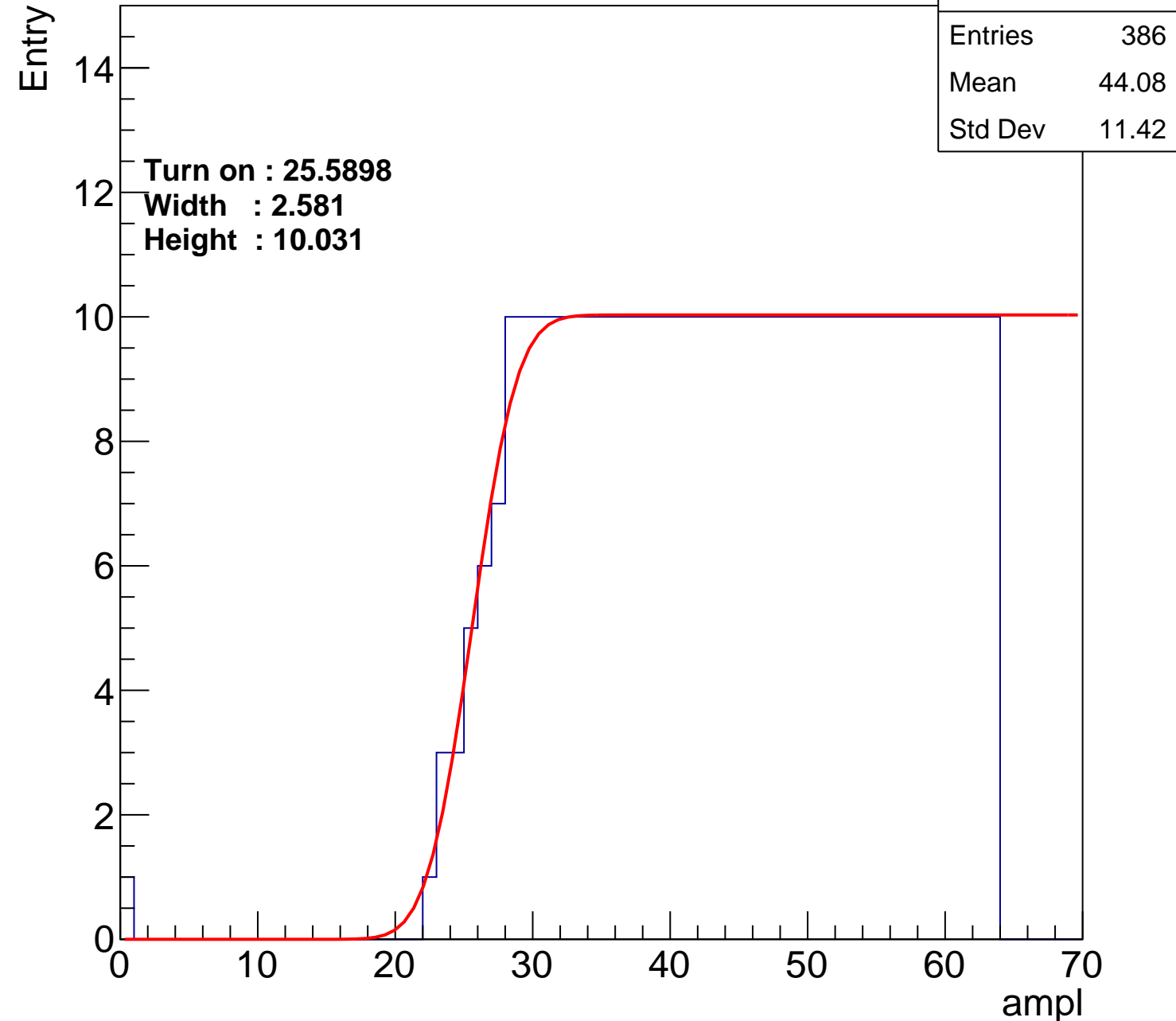
Width : 2.581

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch7

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.93 |
| Std Dev | 10.99 |

Turn on : 27.3535

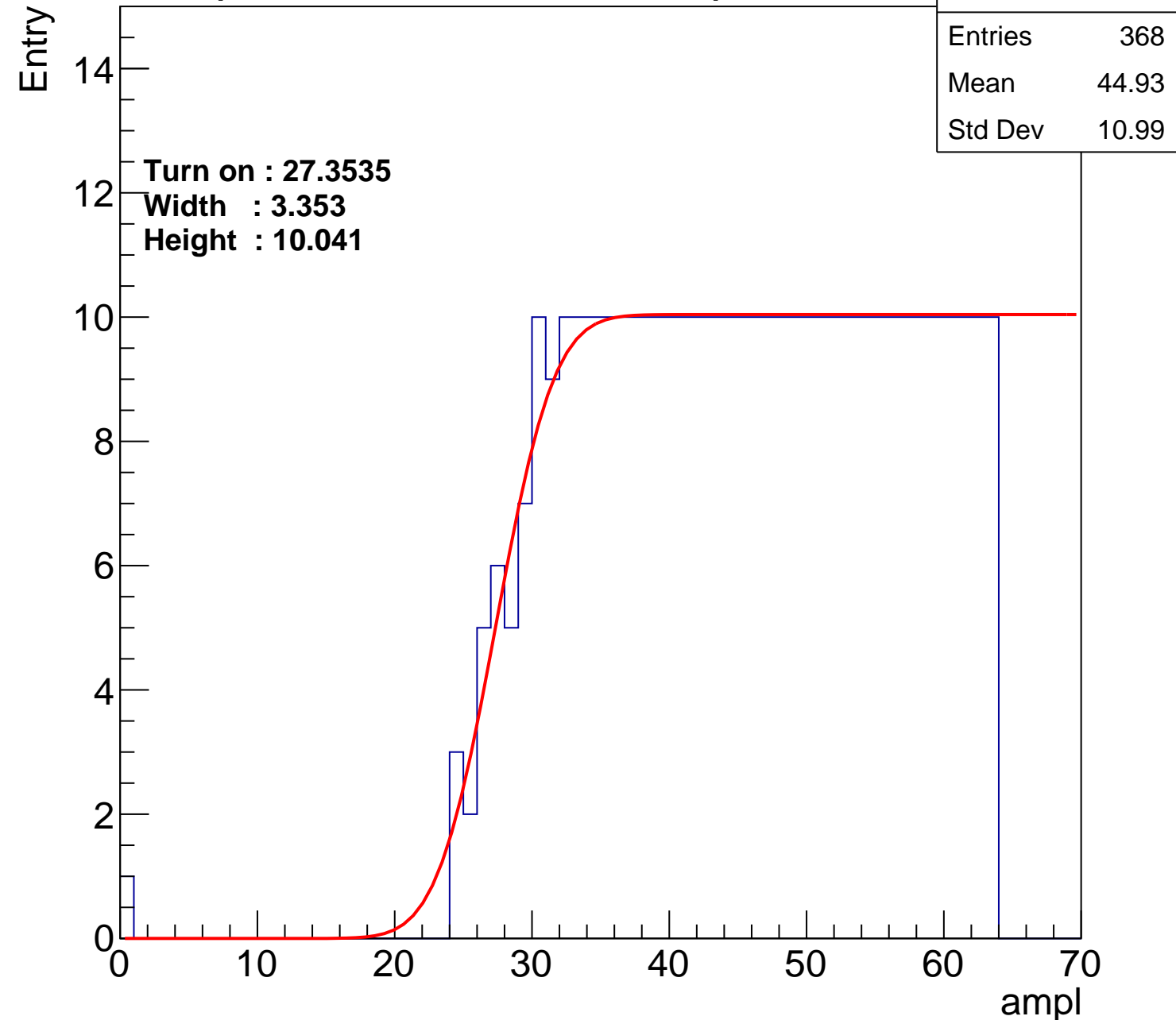
Width : 3.353

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch8

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.26 |
| Std Dev | 11.69 |

Turn on : 26.3491

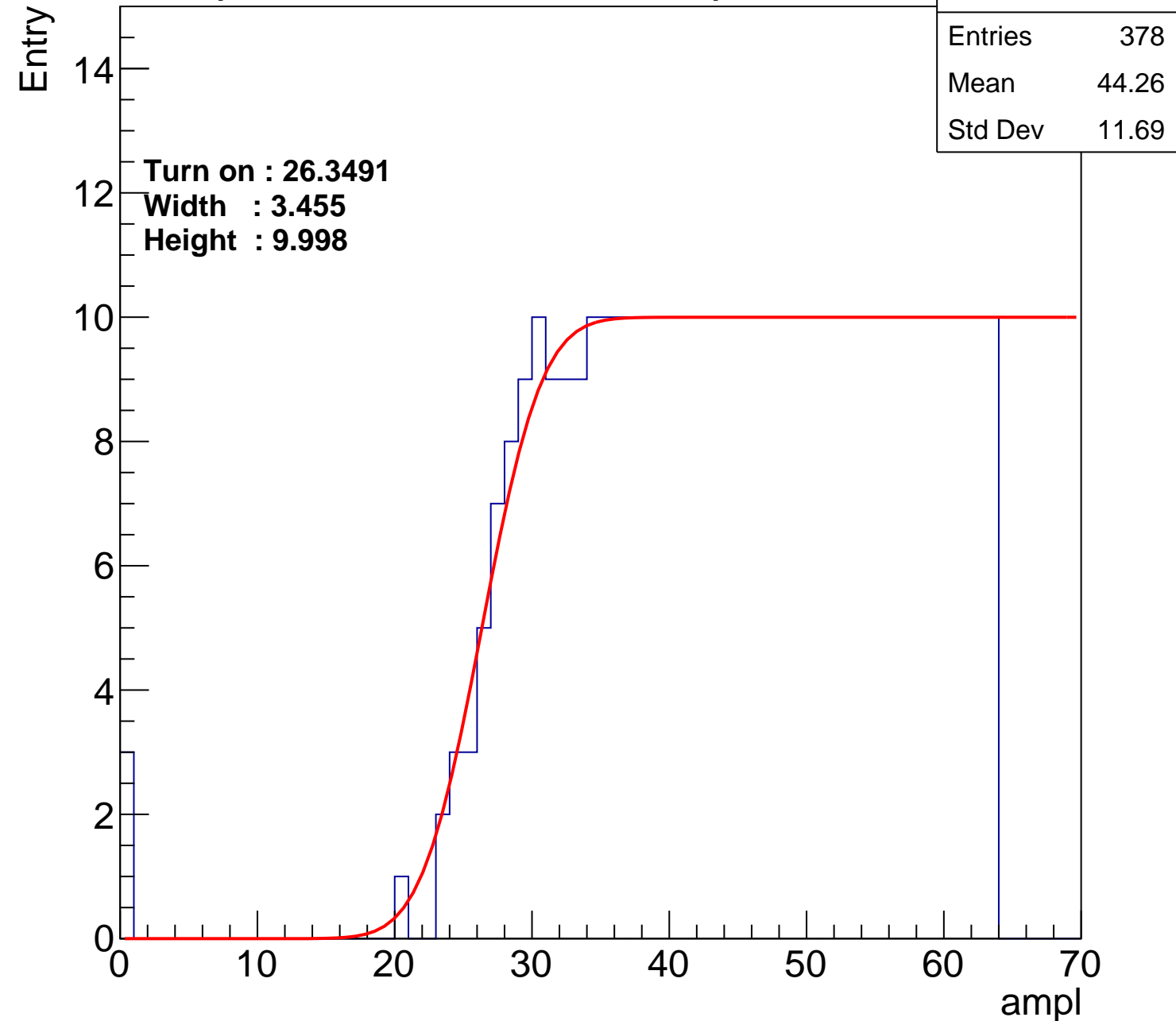
Width : 3.455

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch9

calib\_packv5\_042523\_0143.root, FC#13, port D2

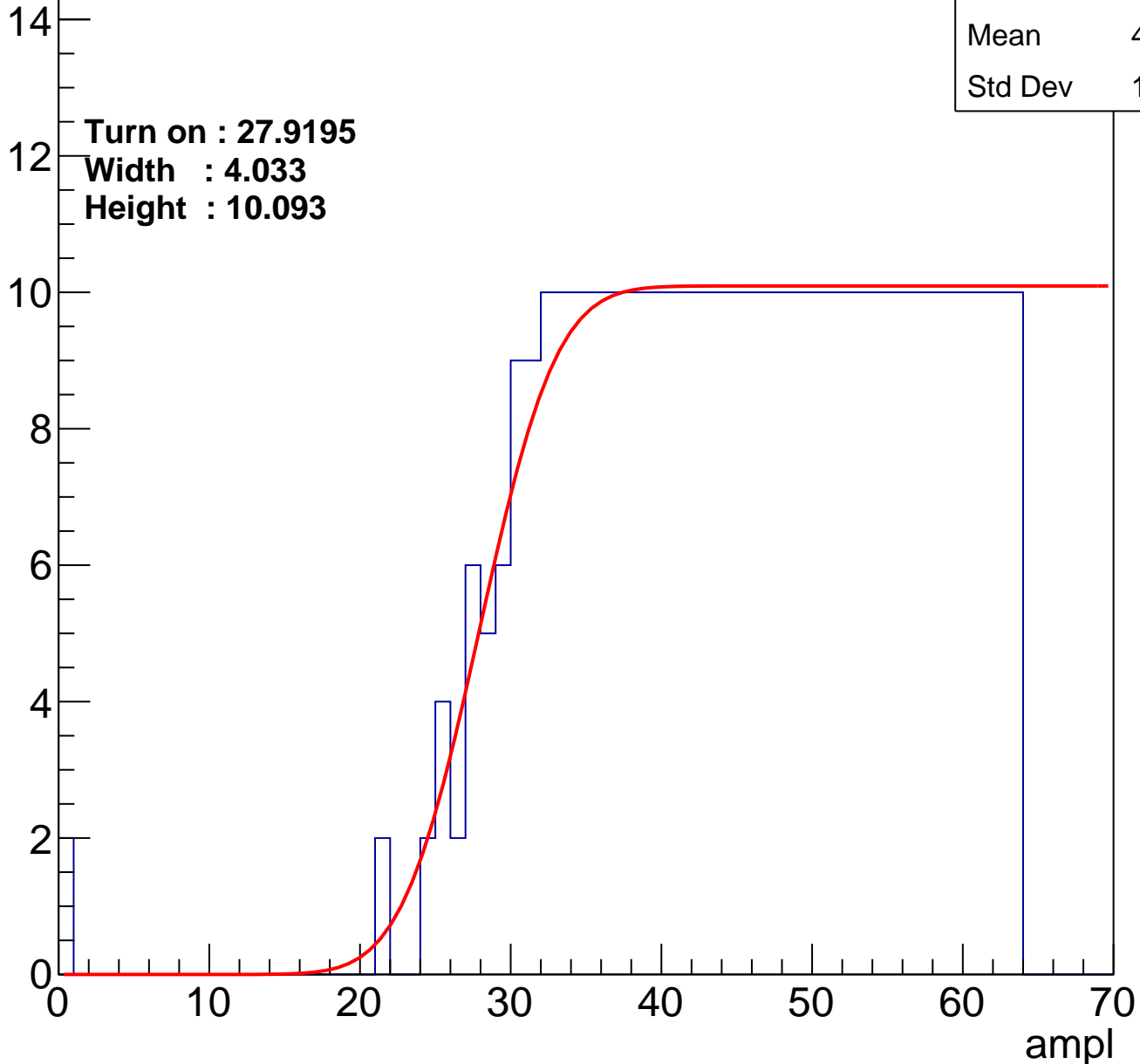
|         |       |
|---------|-------|
| Entries | 367   |
| Mean    | 44.87 |
| Std Dev | 11.24 |

Turn on : 27.9195

Width : 4.033

Height : 10.093

Entry



# B1L003S, U24-ch10

calib\_packv5\_042523\_0143.root, FC#13, port D2

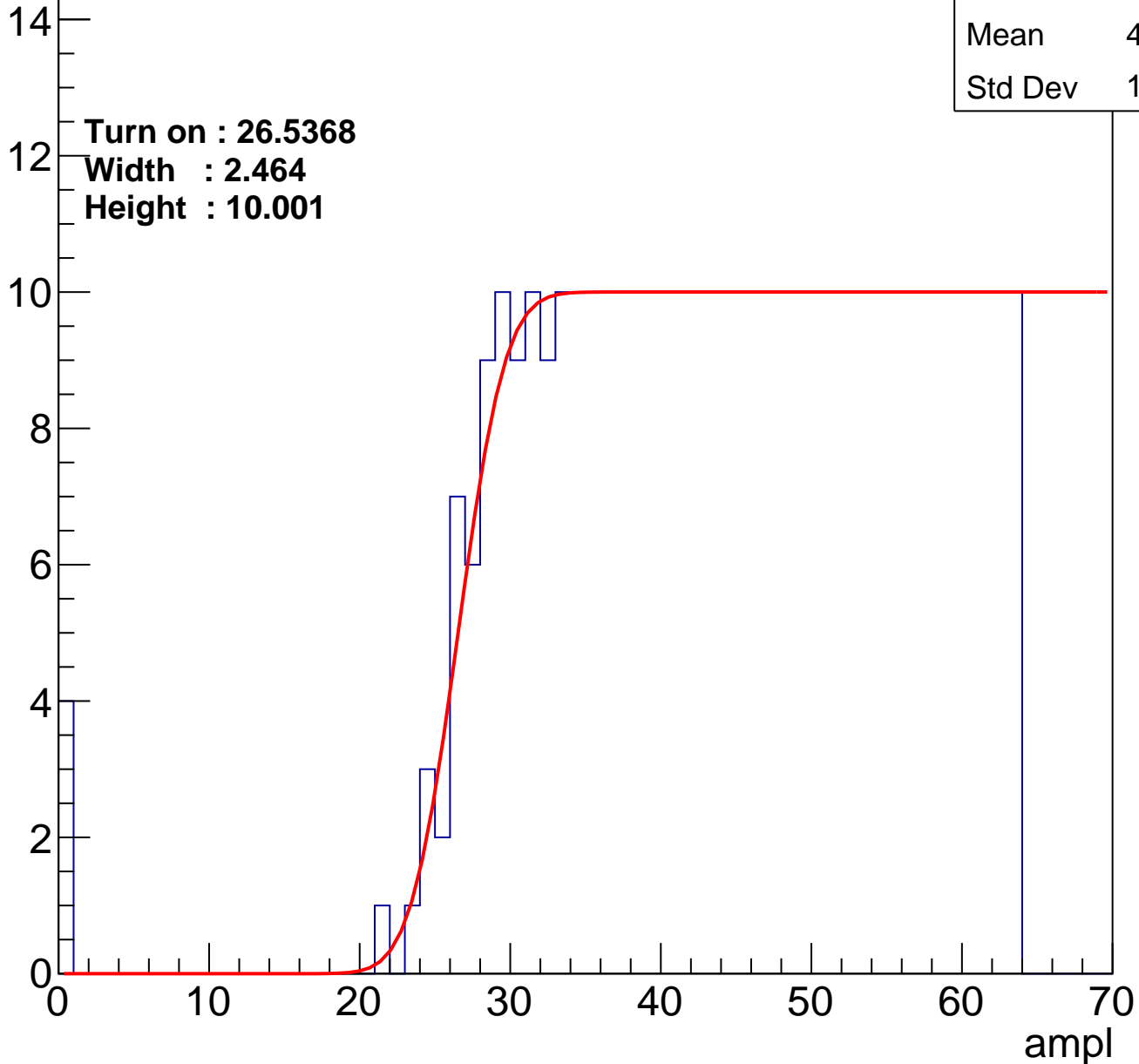
|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 44.09 |
| Std Dev | 11.87 |

Turn on : 26.5368

Width : 2.464

Height : 10.001

Entry



# B1L003S, U24-ch11

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 355   |
| Mean    | 45.31 |
| Std Dev | 11.37 |

Turn on : 29.5948

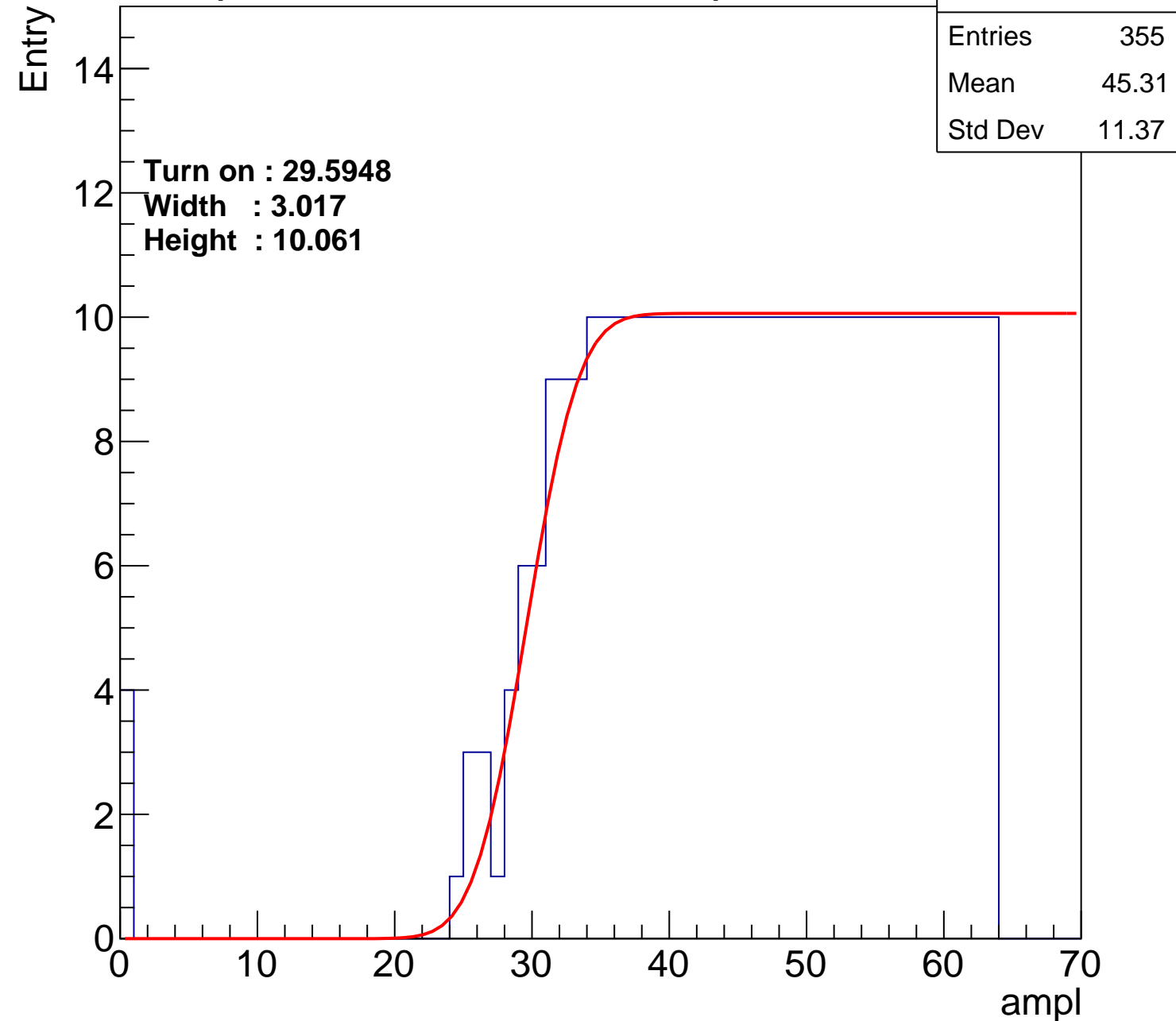
Width : 3.017

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch12

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.92 |
| Std Dev | 11.21 |

Turn on : 28.3207

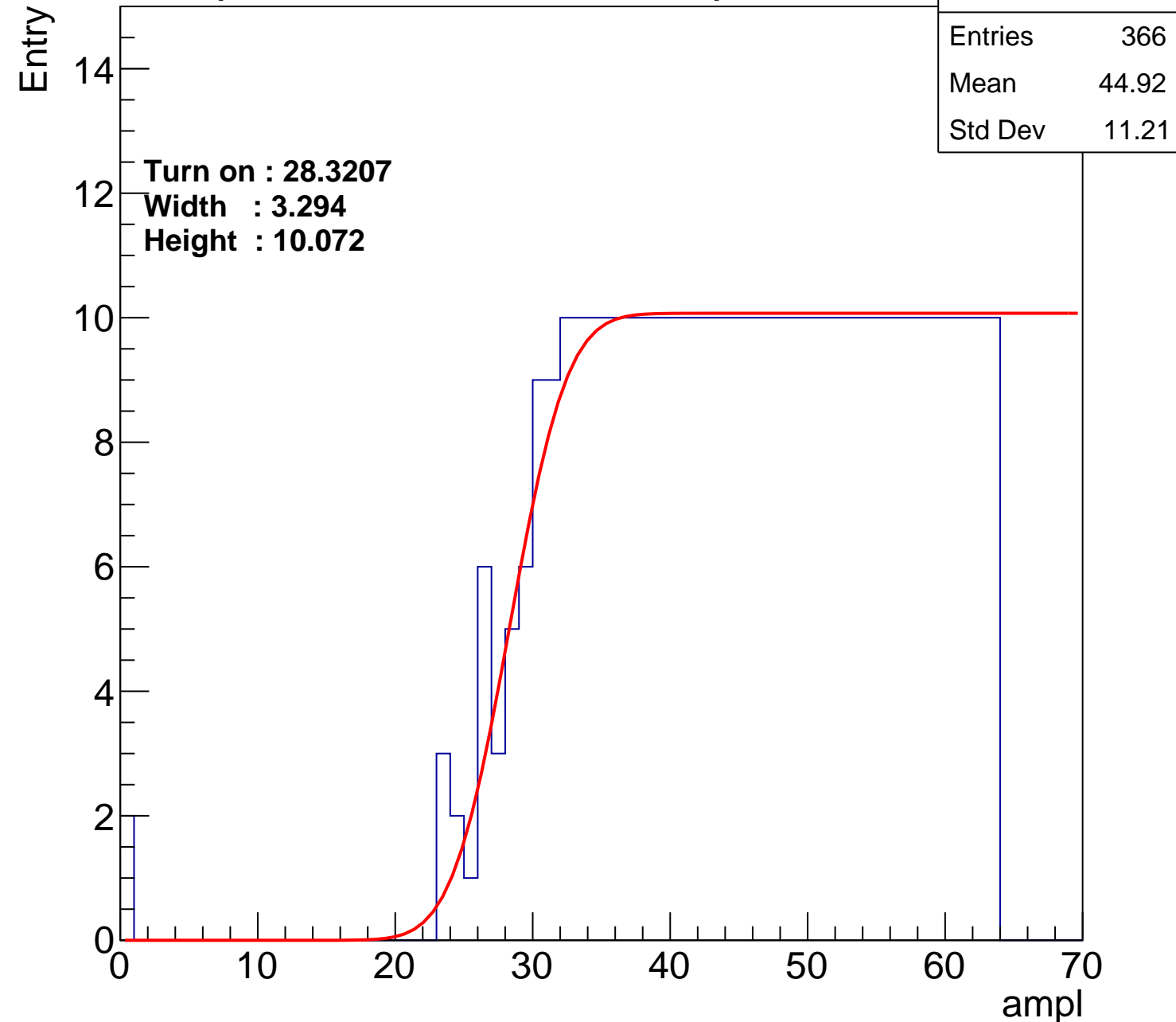
Width : 3.294

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch13

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.35 |
| Std Dev | 10.74 |

Turn on : 27.6403

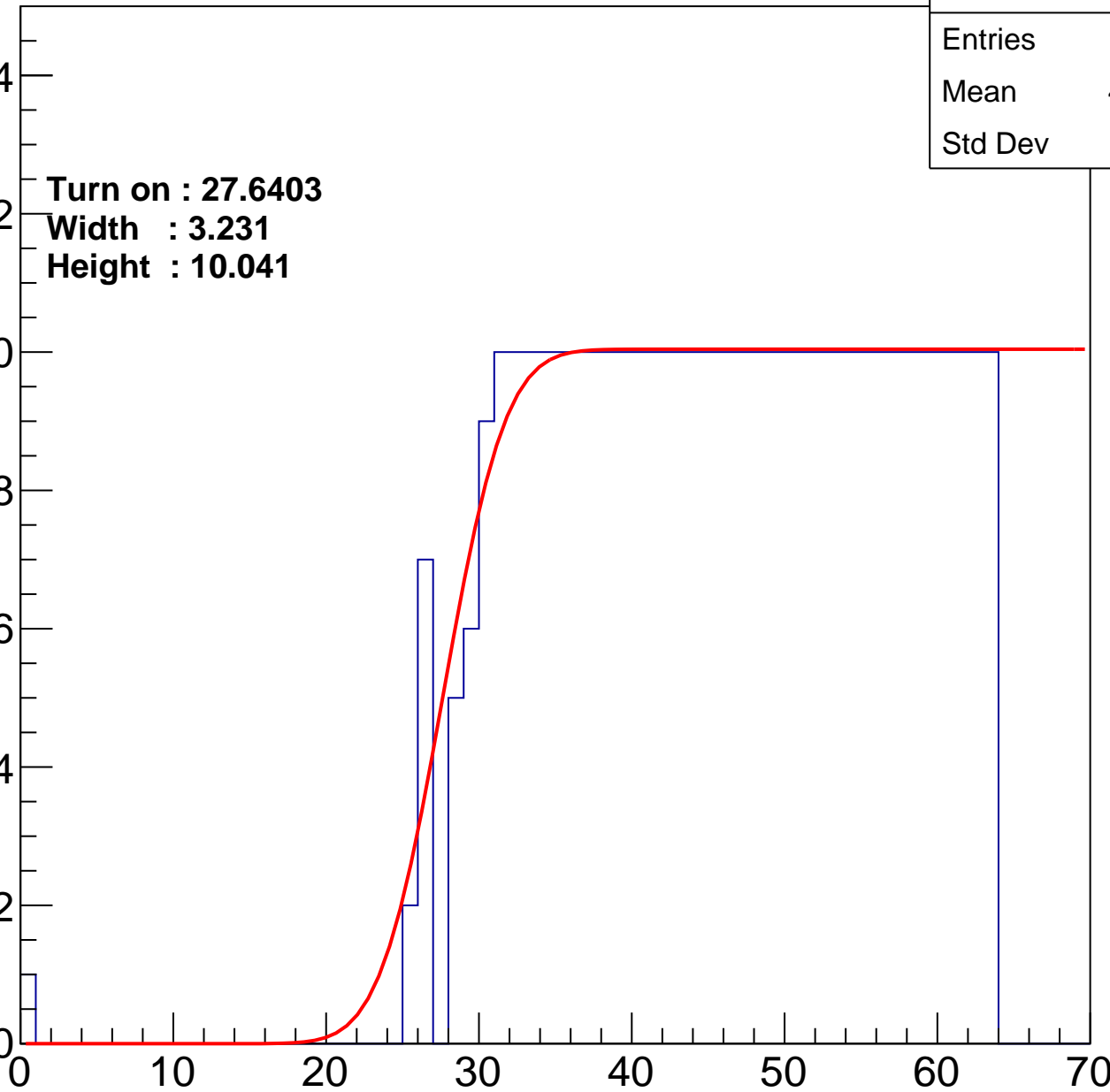
Width : 3.231

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch14

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.43 |
| Std Dev | 11.6  |

Turn on : 27.2101

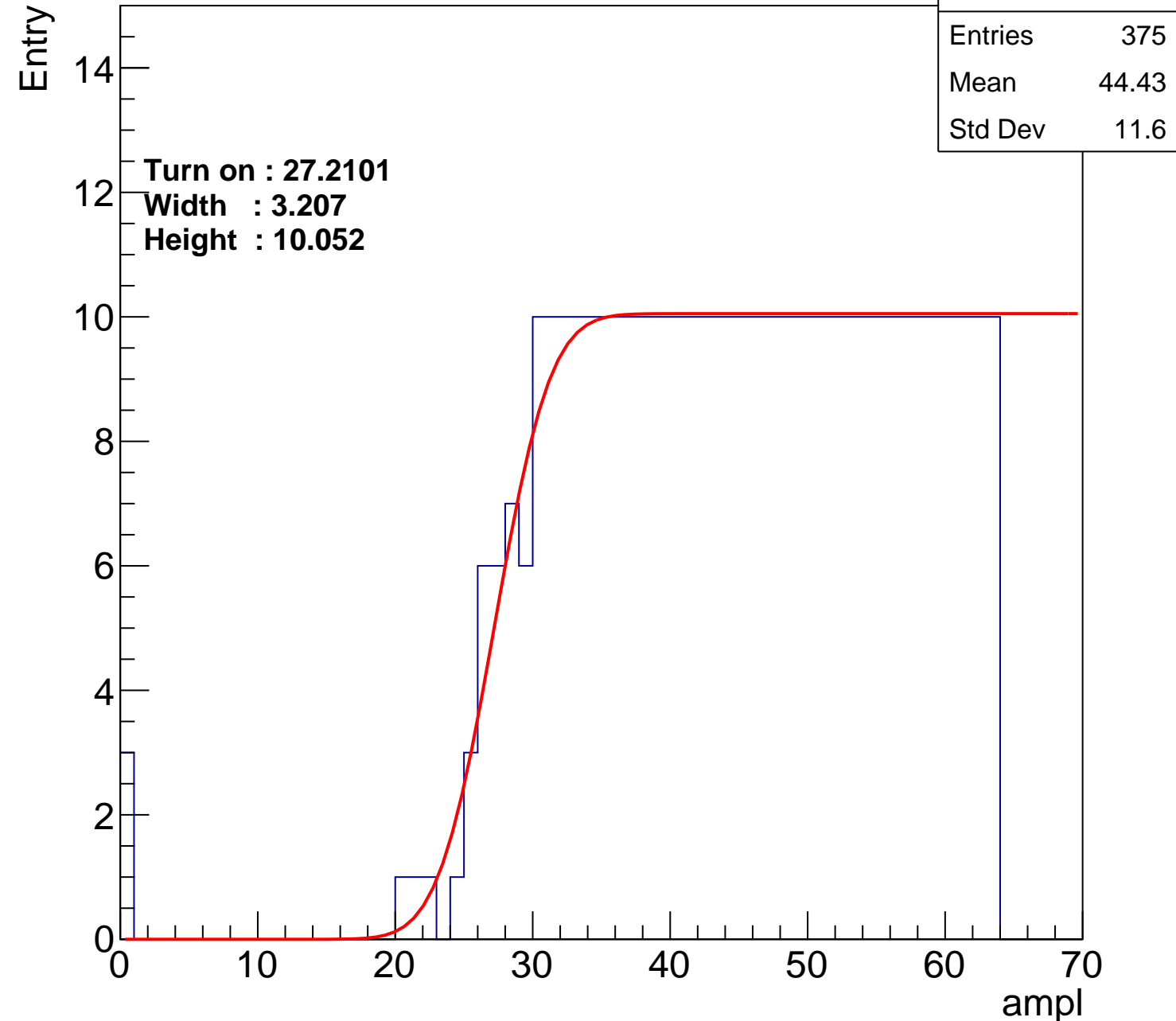
Width : 3.207

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch15

calib\_packv5\_042523\_0143.root, FC#13, port D2

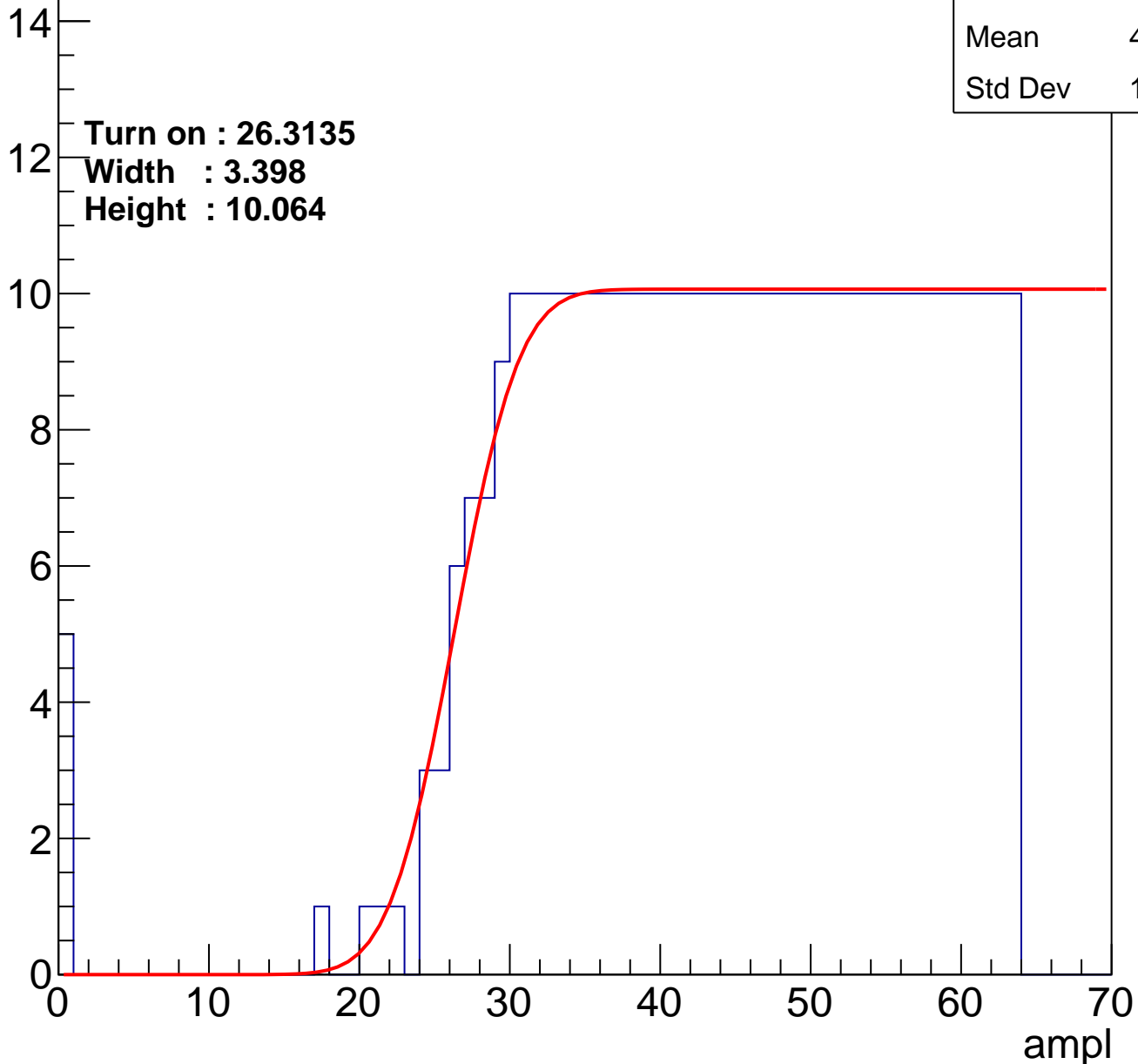
|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 43.85 |
| Std Dev | 12.17 |

Turn on : 26.3135

Width : 3.398

Height : 10.064

Entry



# B1L003S, U24-ch16

calib\_packv5\_042523\_0143.root, FC#13, port D2

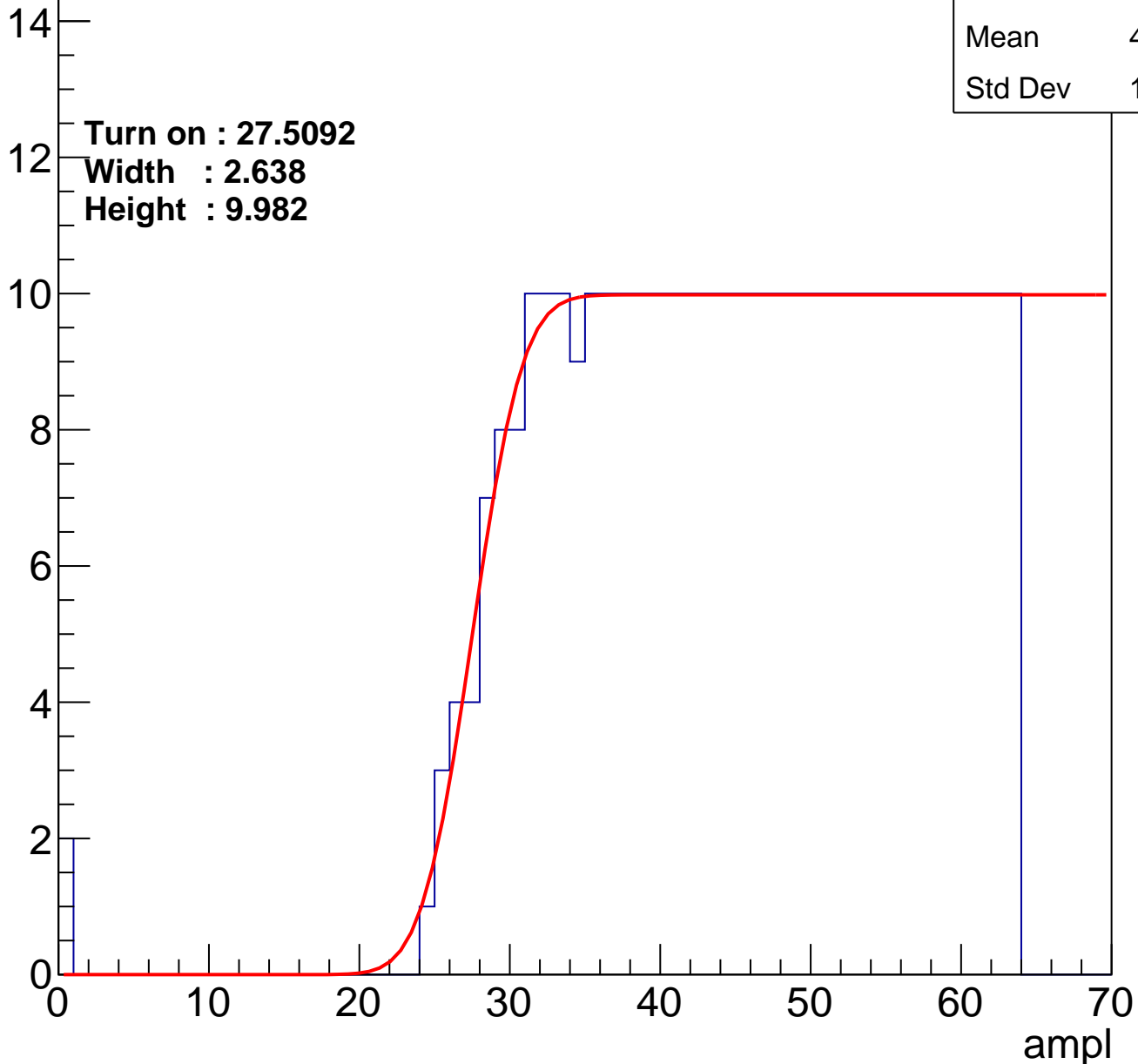
|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.96 |
| Std Dev | 11.14 |

Turn on : 27.5092

Width : 2.638

Height : 9.982

Entry



# B1L003S, U24-ch17

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.24 |
| Std Dev | 11.54 |

Turn on : 26.3345

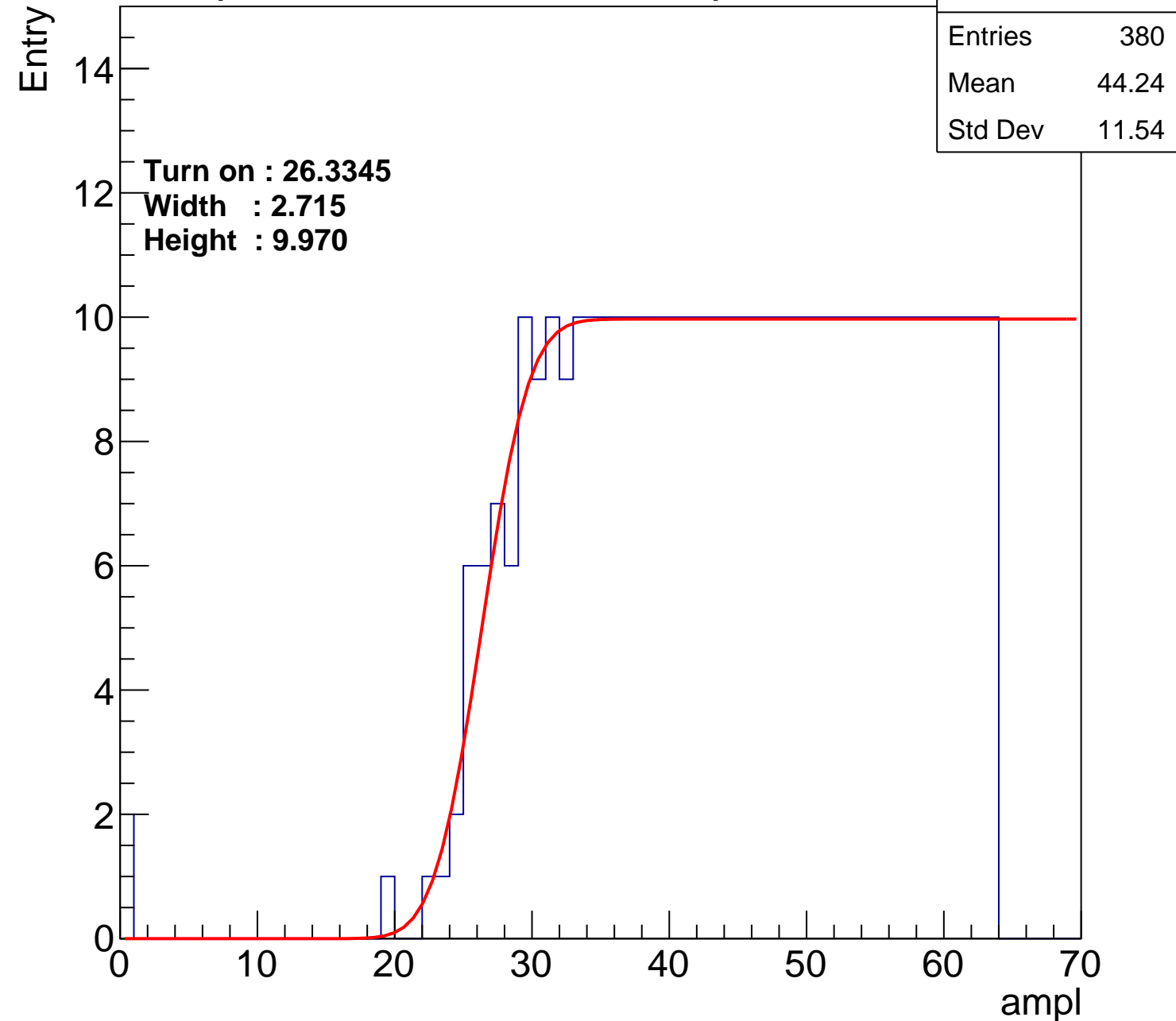
Width : 2.715

Height : 9.970

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch18

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 382   |
| Mean    | 44.12 |
| Std Dev | 11.71 |

Turn on : 26.5580

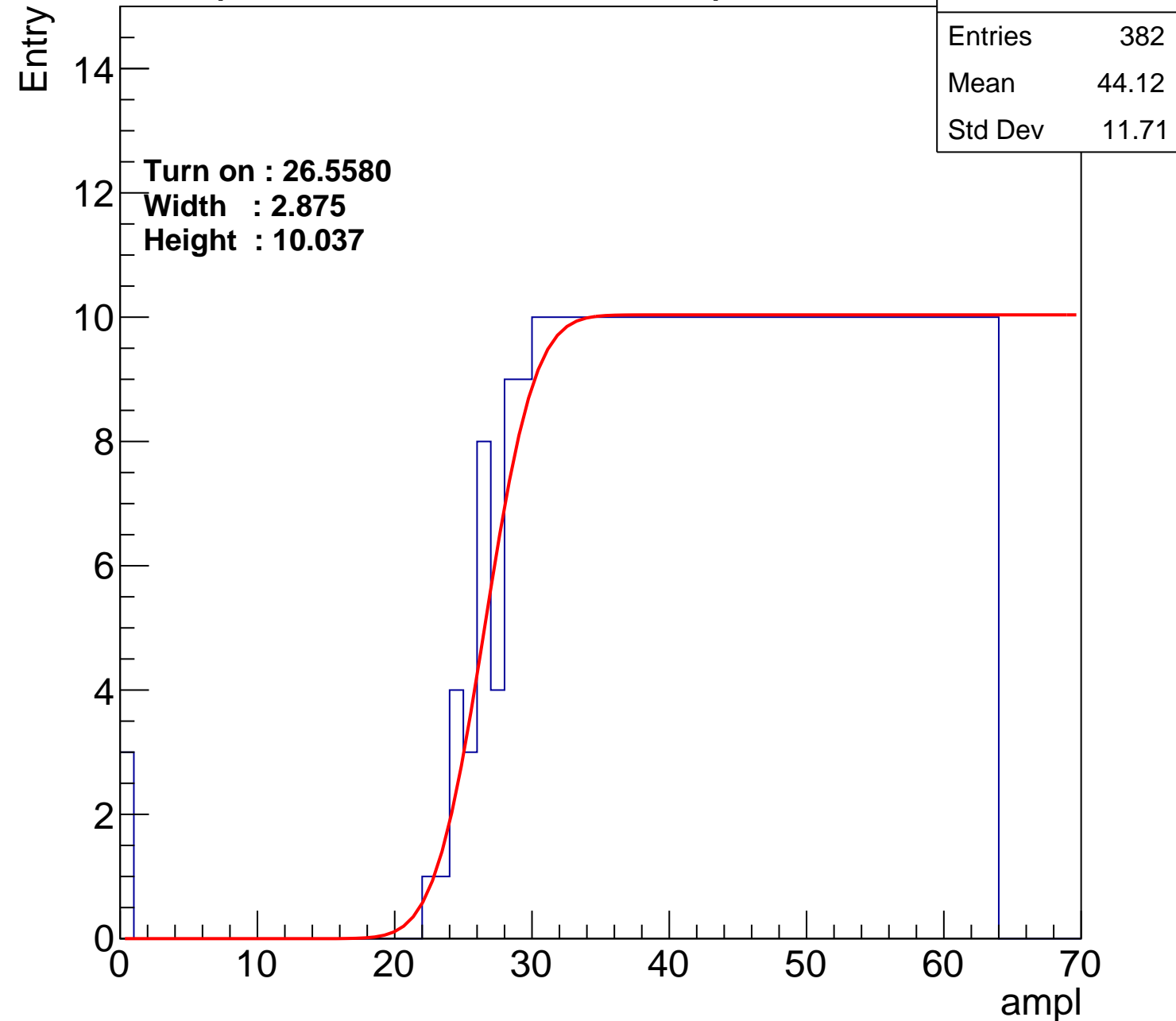
Width : 2.875

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch19

calib\_packv5\_042523\_0143.root, FC#13, port D2

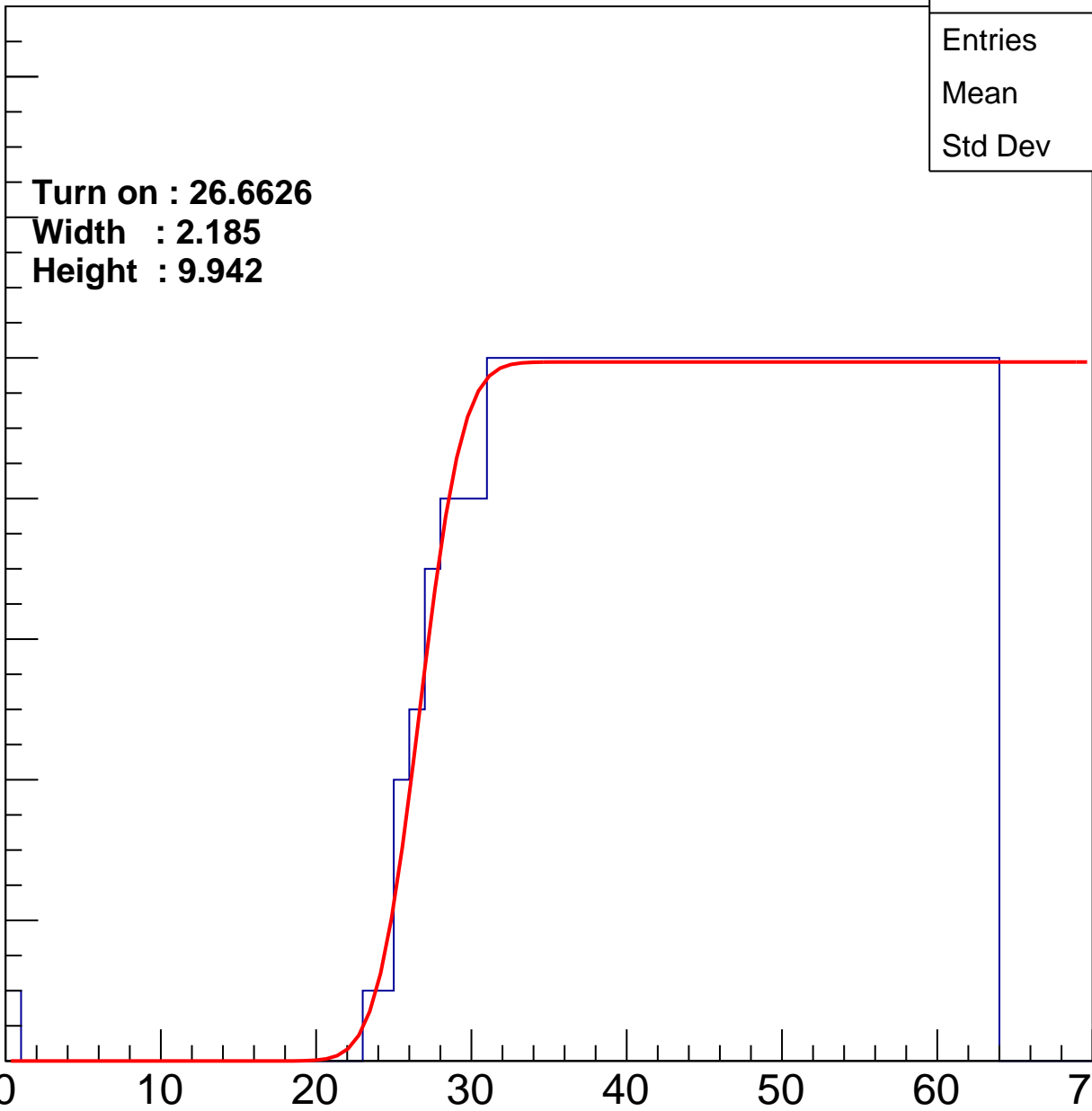
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.6626  
Width : 2.185  
Height : 9.942

|         |      |
|---------|------|
| Entries | 373  |
| Mean    | 44.7 |
| Std Dev | 11.1 |

ampl



# B1L003S, U24-ch20

calib\_packv5\_042523\_0143.root, FC#13, port D2

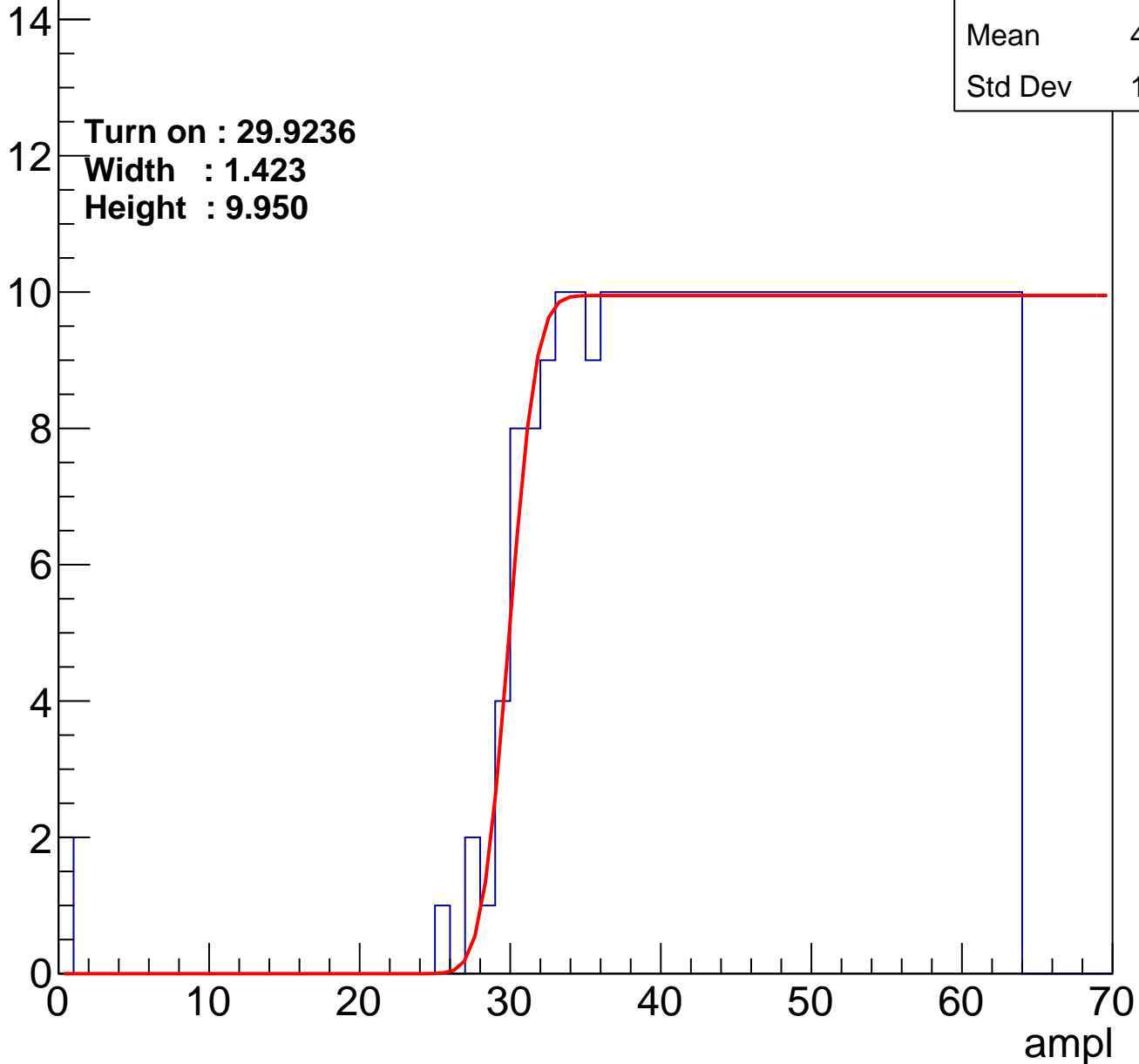
|         |       |
|---------|-------|
| Entries | 344   |
| Mean    | 46.06 |
| Std Dev | 10.57 |

**Turn on : 29.9236**

**Width : 1.423**

**Height : 9.950**

Entry



# B1L003S, U24-ch21

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.09 |
| Std Dev | 11.3  |

**Turn on : 28.3746**

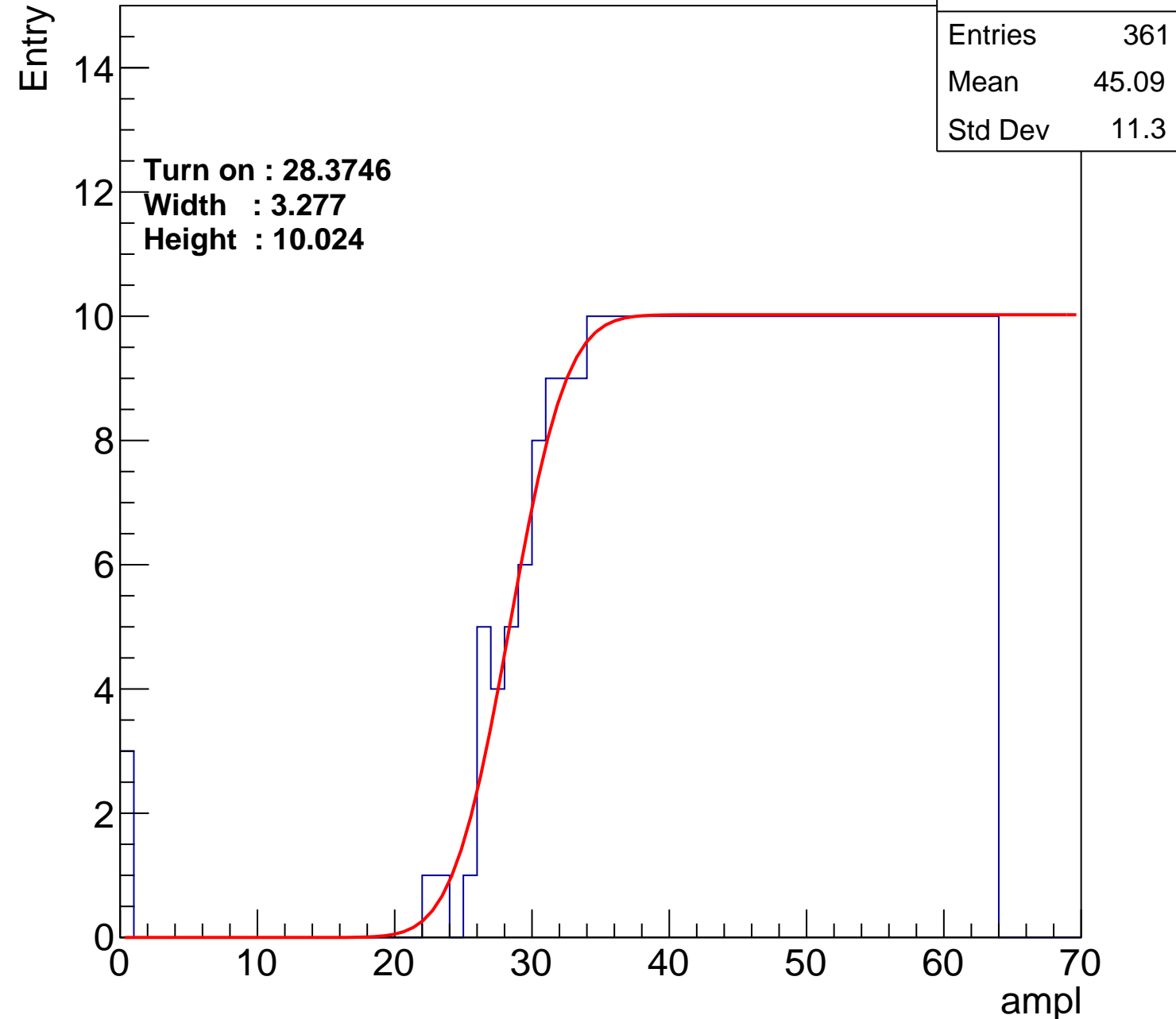
**Width : 3.277**

**Height : 10.024**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch22

calib\_packv5\_042523\_0143.root, FC#13, port D2

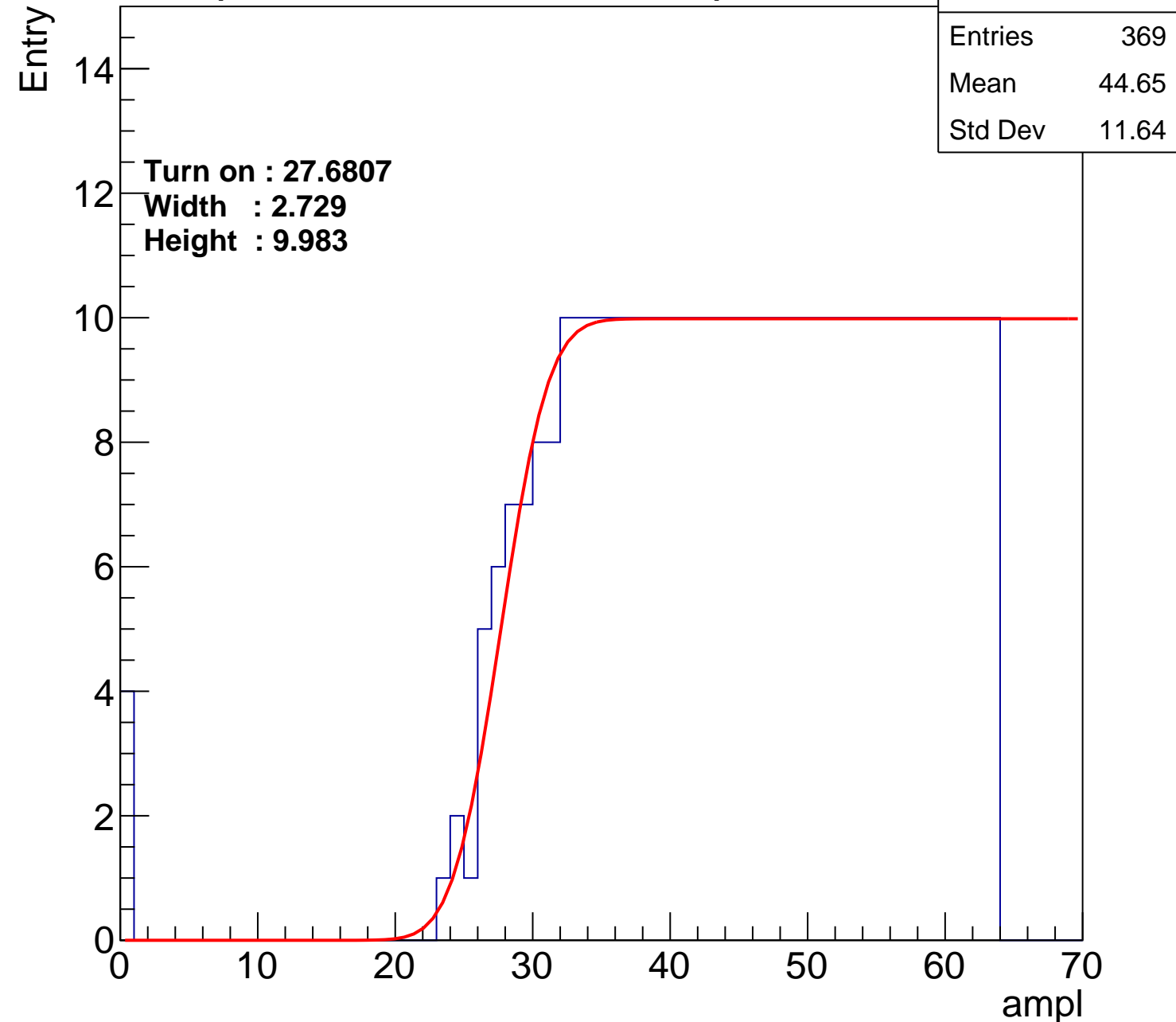
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.6807**  
**Width : 2.729**  
**Height : 9.983**

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.65 |
| Std Dev | 11.64 |

ampl





# B1L003S, U24-ch23

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.67 |
| Std Dev | 11.75 |

Turn on : 27.7777

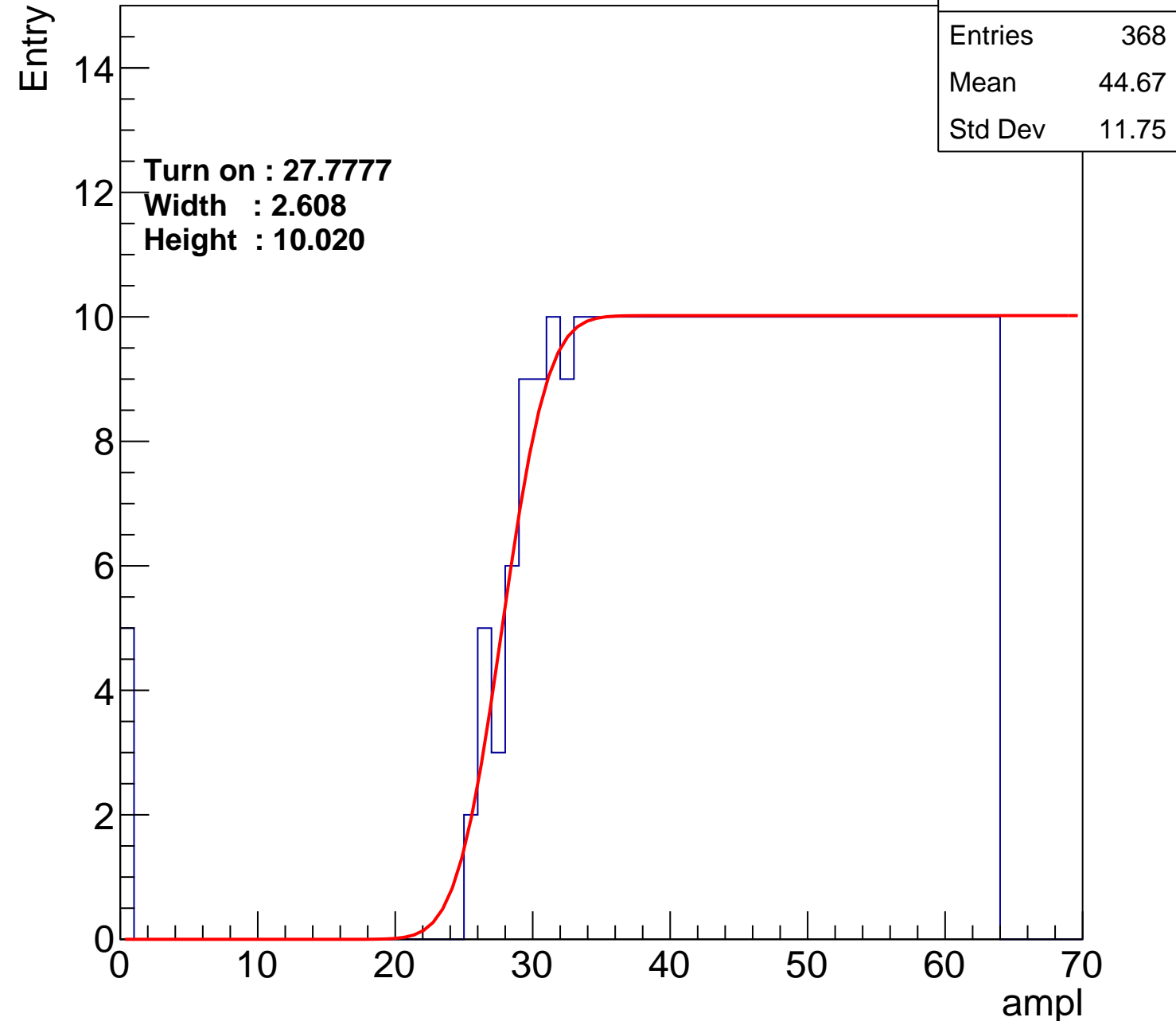
Width : 2.608

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch24

calib\_packv5\_042523\_0143.root, FC#13, port D2

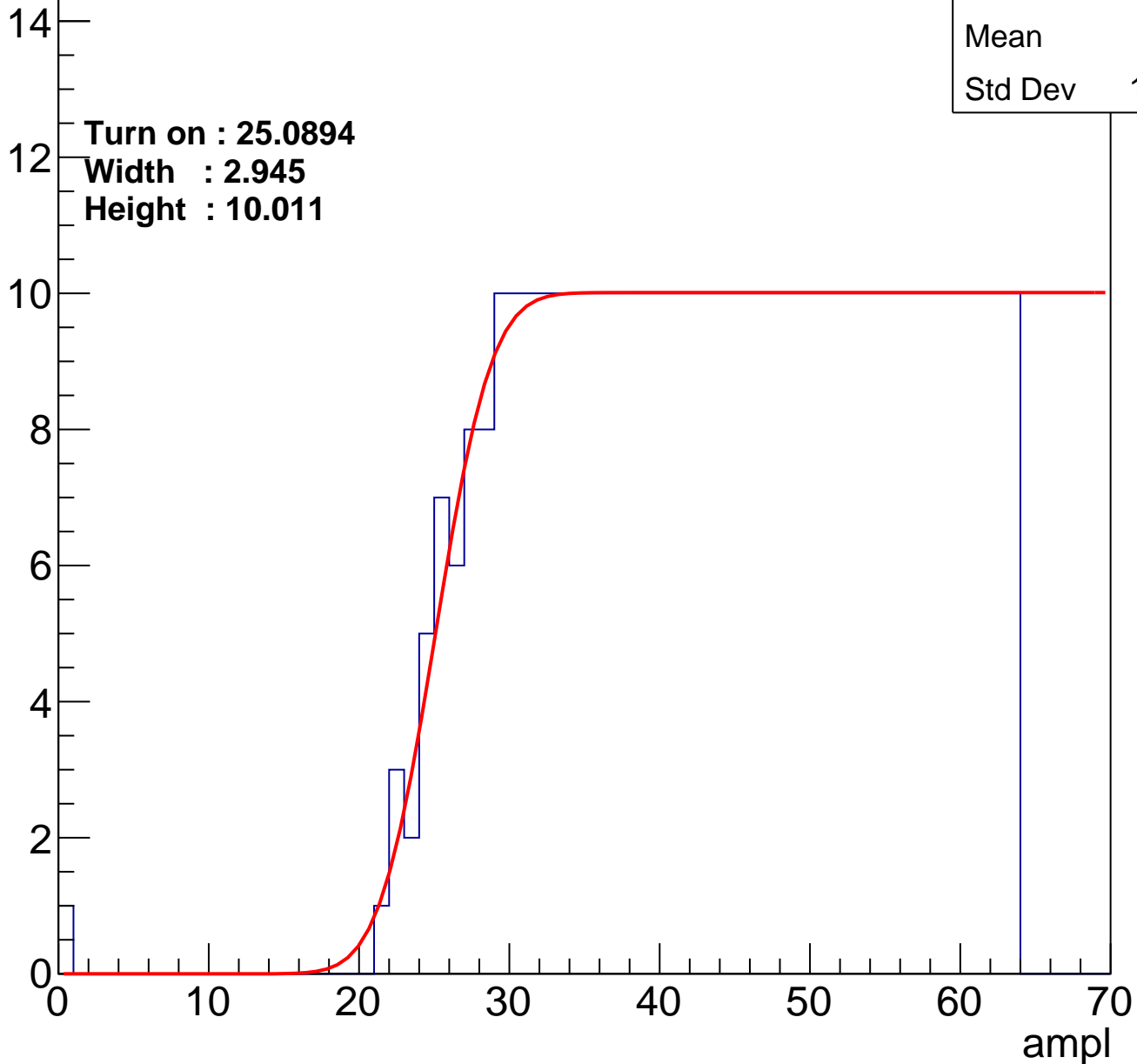
|         |       |
|---------|-------|
| Entries | 391   |
| Mean    | 43.8  |
| Std Dev | 11.61 |

Turn on : 25.0894

Width : 2.945

Height : 10.011

Entry



# B1L003S, U24-ch25

calib\_packv5\_042523\_0143.root, FC#13, port D2

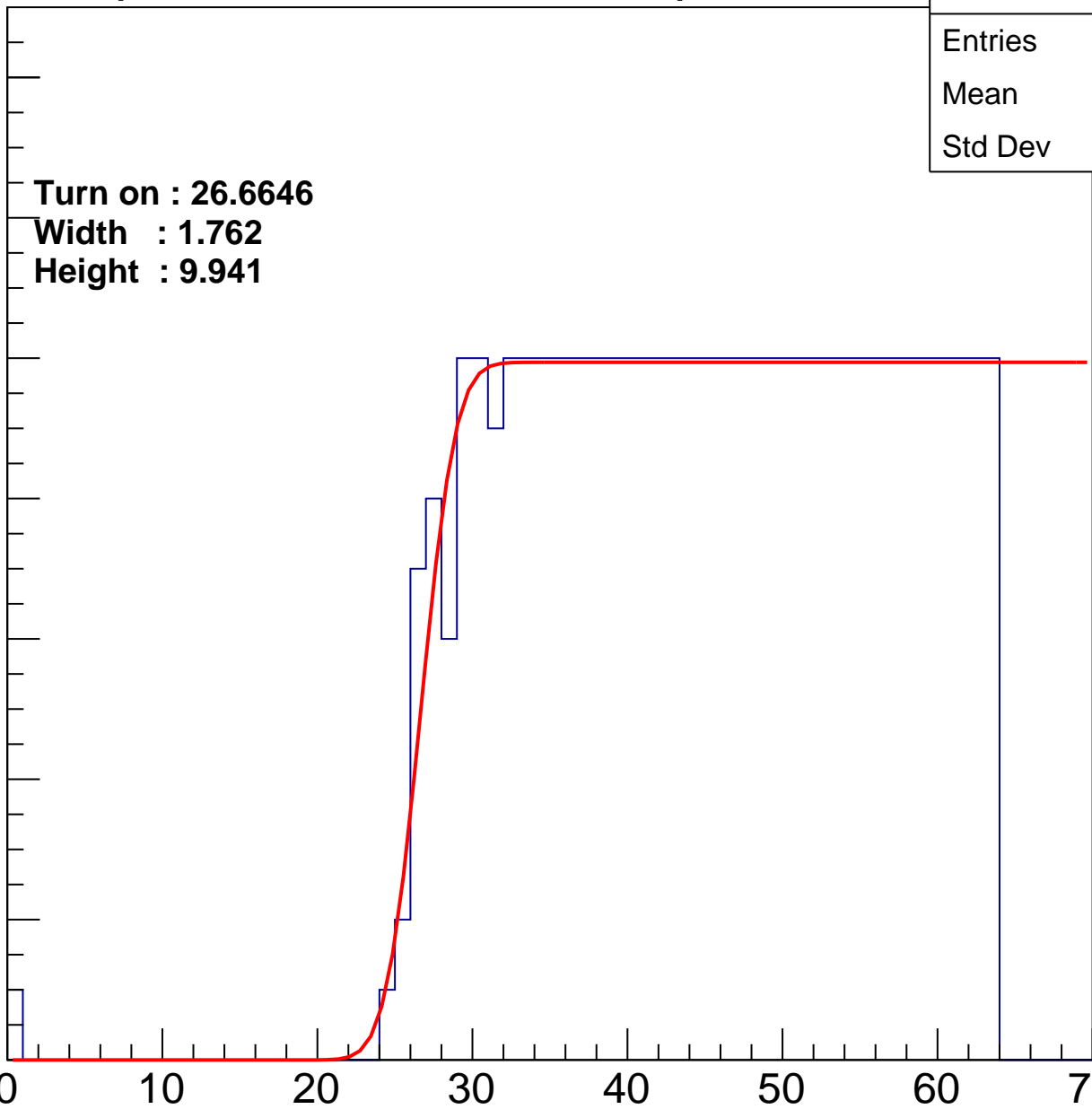
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.6646  
Width : 1.762  
Height : 9.941

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.68 |
| Std Dev | 11.08 |

ampl



# B1L003S, U24-ch26

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.6  |
| Std Dev | 11.35 |

Turn on : 26.9113

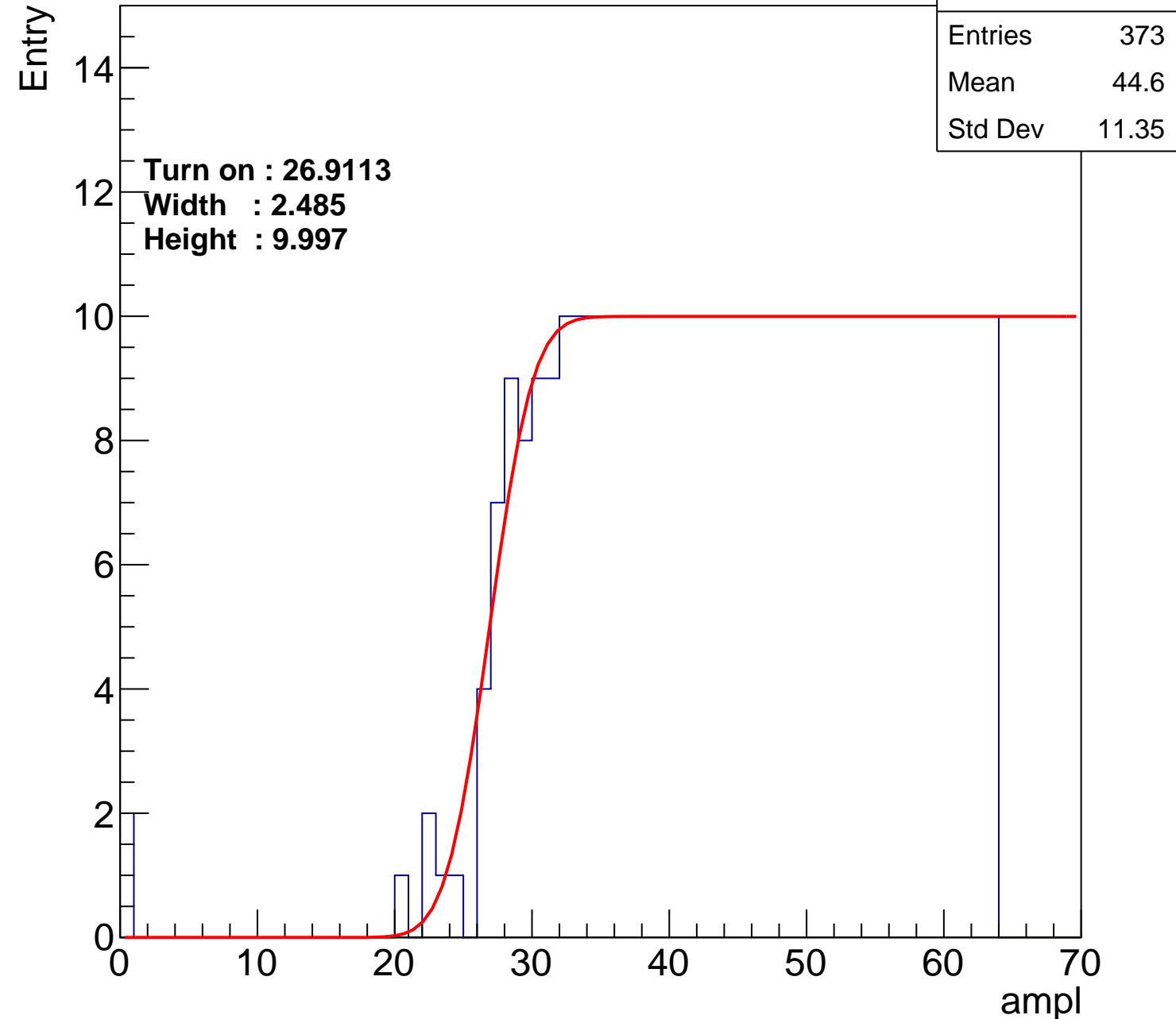
Width : 2.485

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch27

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.62 |
| Std Dev | 11.44 |

Turn on : 26.6529

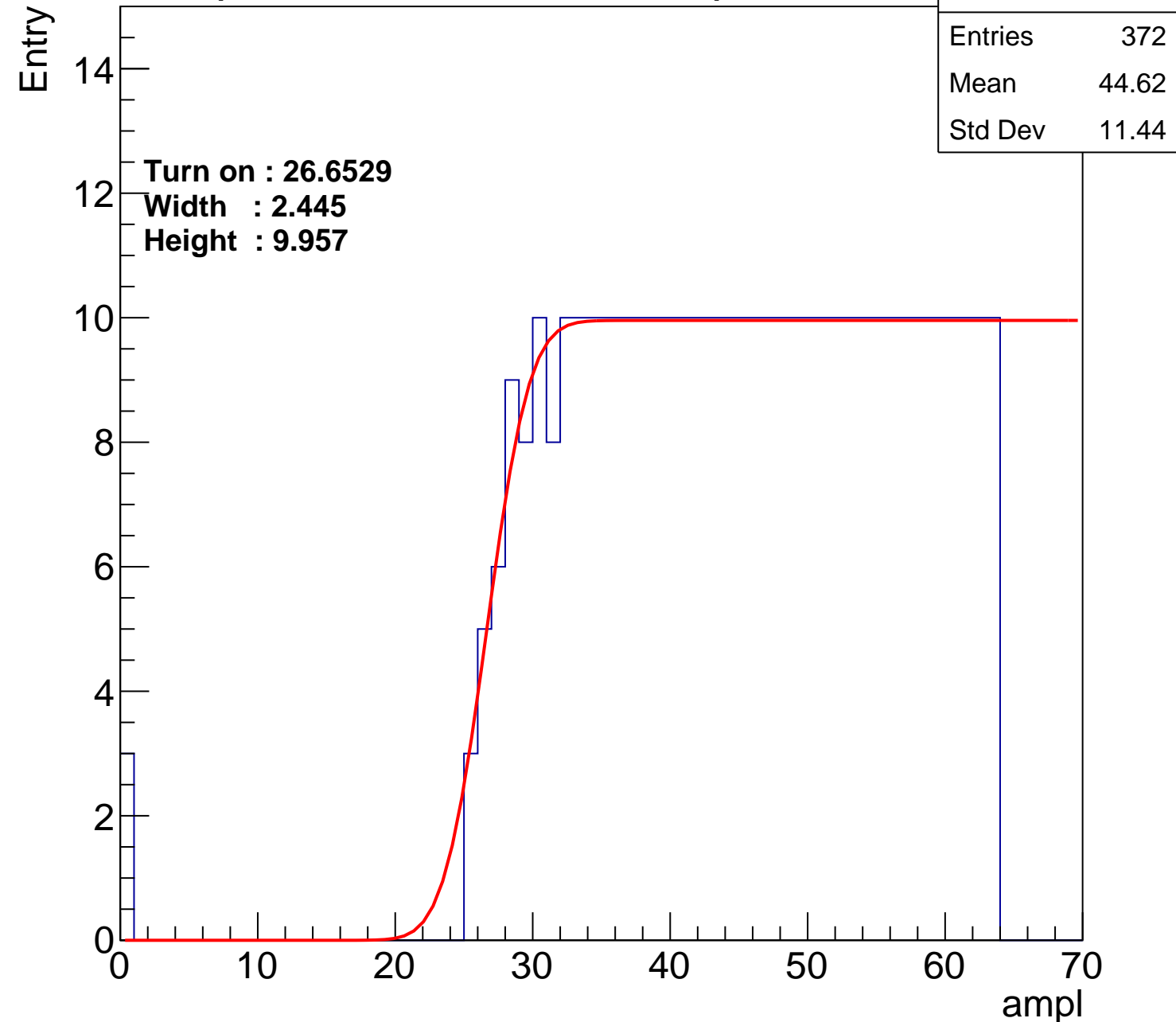
Width : 2.445

Height : 9.957

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch28

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.62 |
| Std Dev | 11.5  |

Turn on : 27.1533

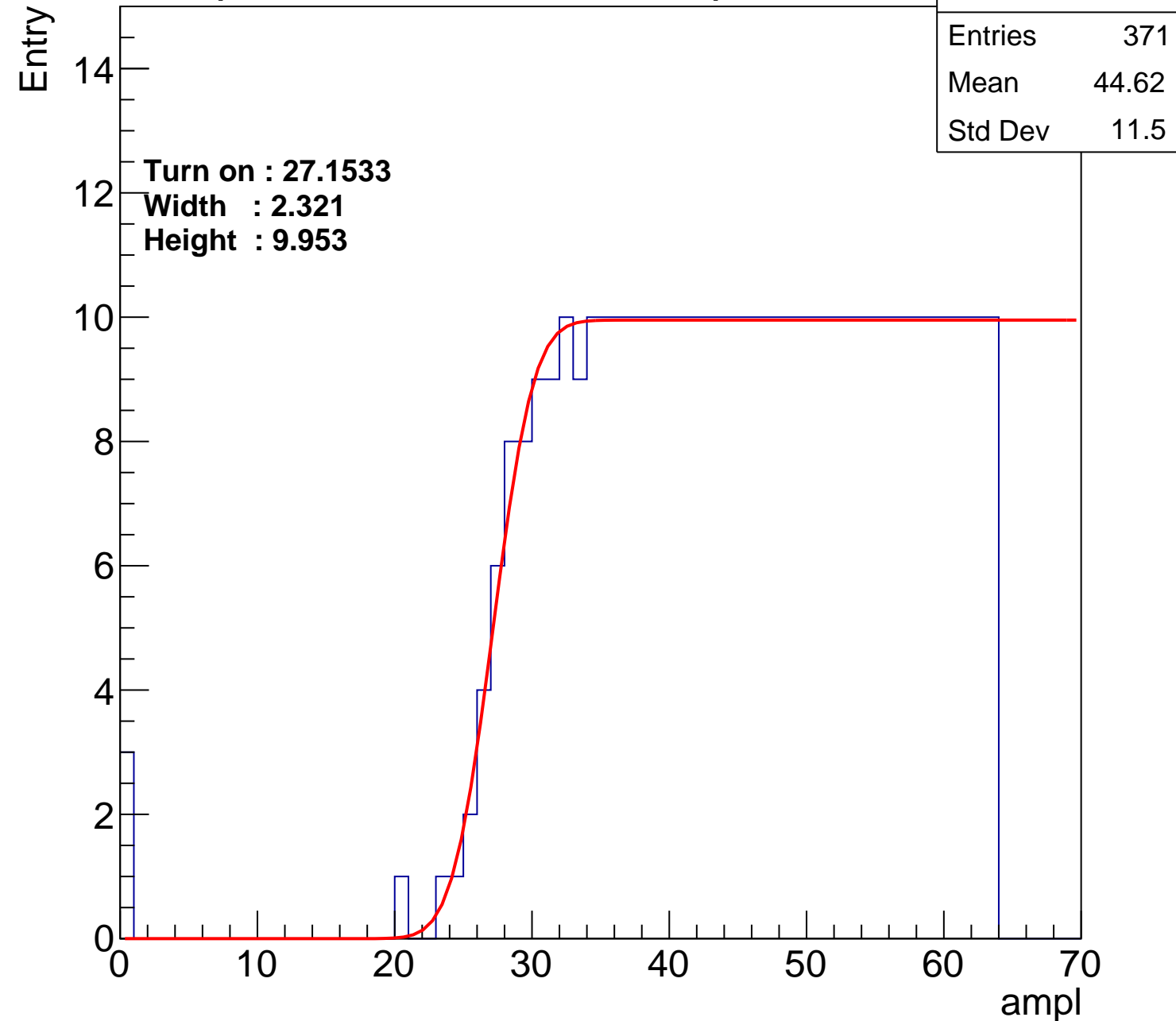
Width : 2.321

Height : 9.953

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch29

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 353   |
| Mean    | 45.56 |
| Std Dev | 10.87 |

Turn on : 28.7796

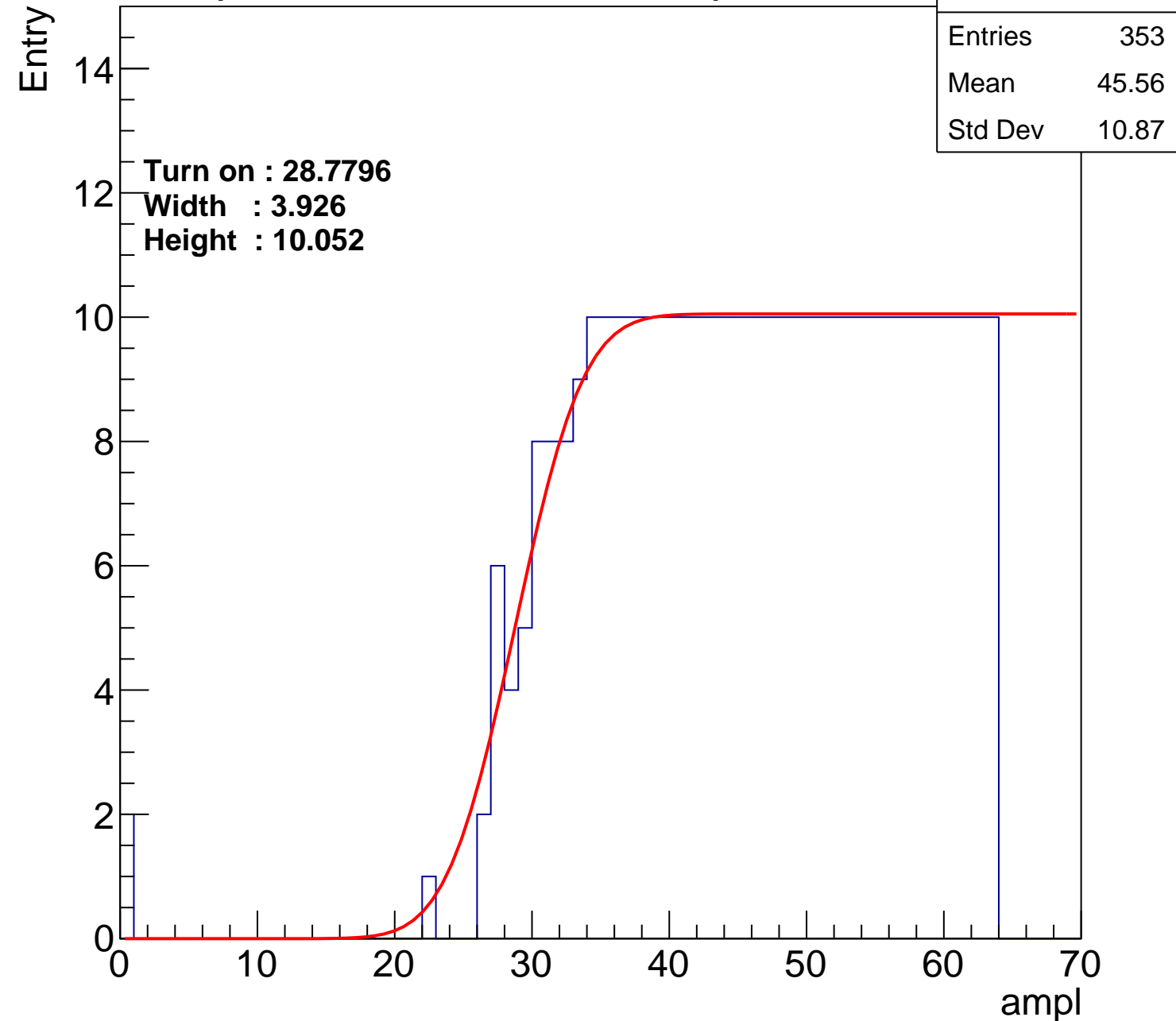
Width : 3.926

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch30

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 44.05 |
| Std Dev | 11.46 |

**Turn on : 25.8908**

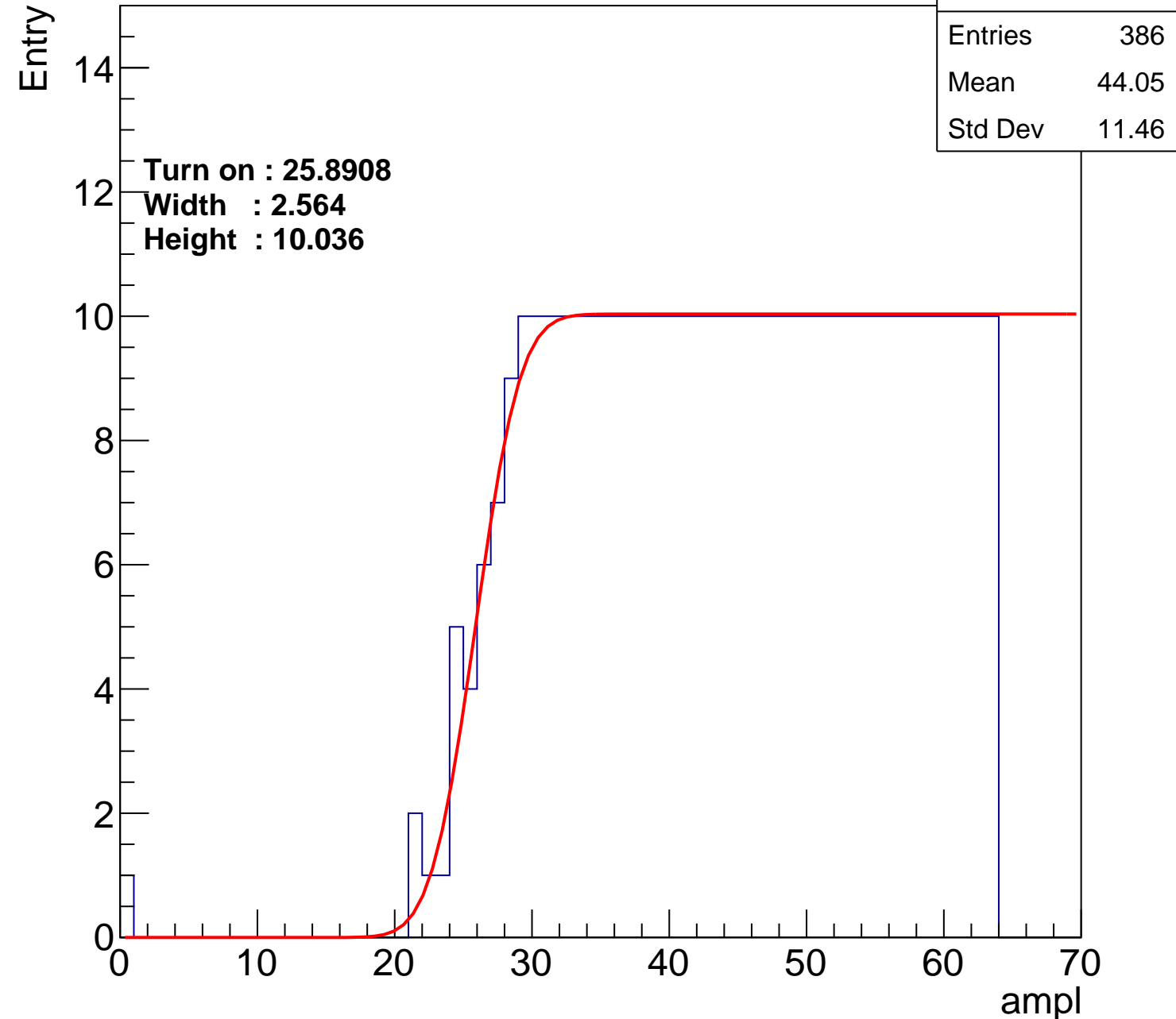
**Width : 2.564**

**Height : 10.036**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch31

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 43.98 |
| Std Dev | 11.95 |

Turn on : 25.7437

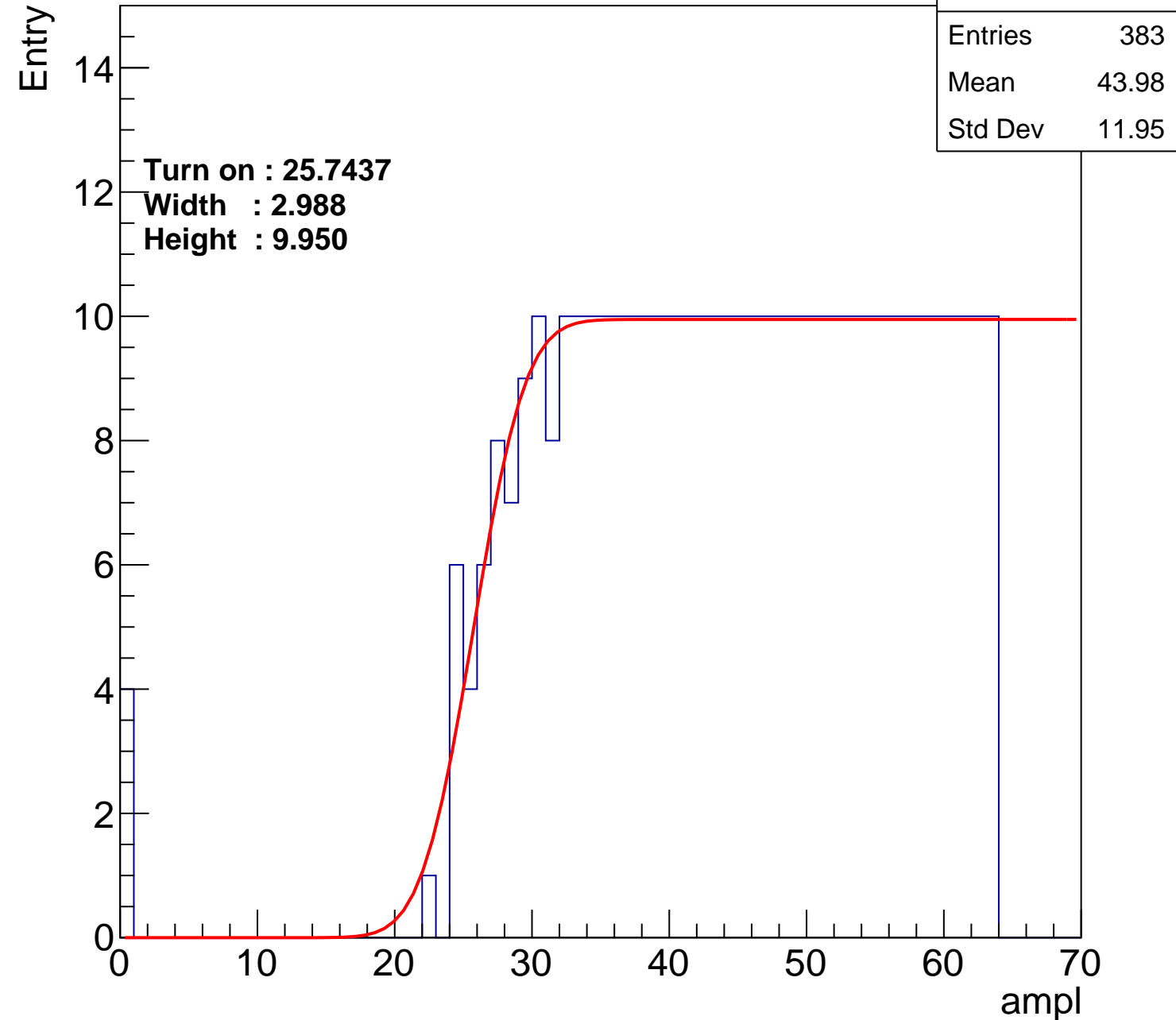
Width : 2.988

Height : 9.950

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch32

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 44.01 |
| Std Dev | 11.78 |

Turn on : 26.3476

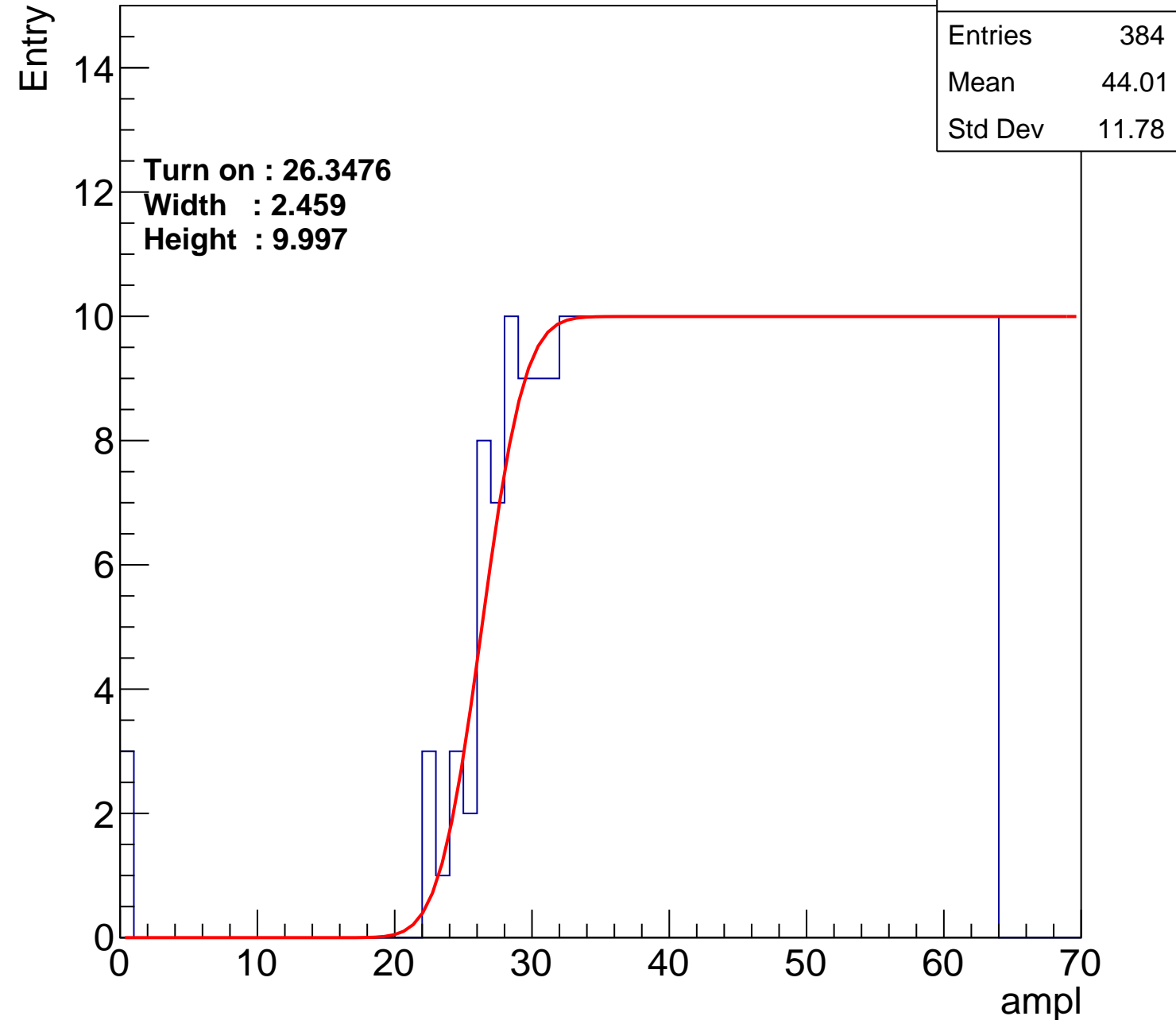
Width : 2.459

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch33

calib\_packv5\_042523\_0143.root, FC#13, port D2

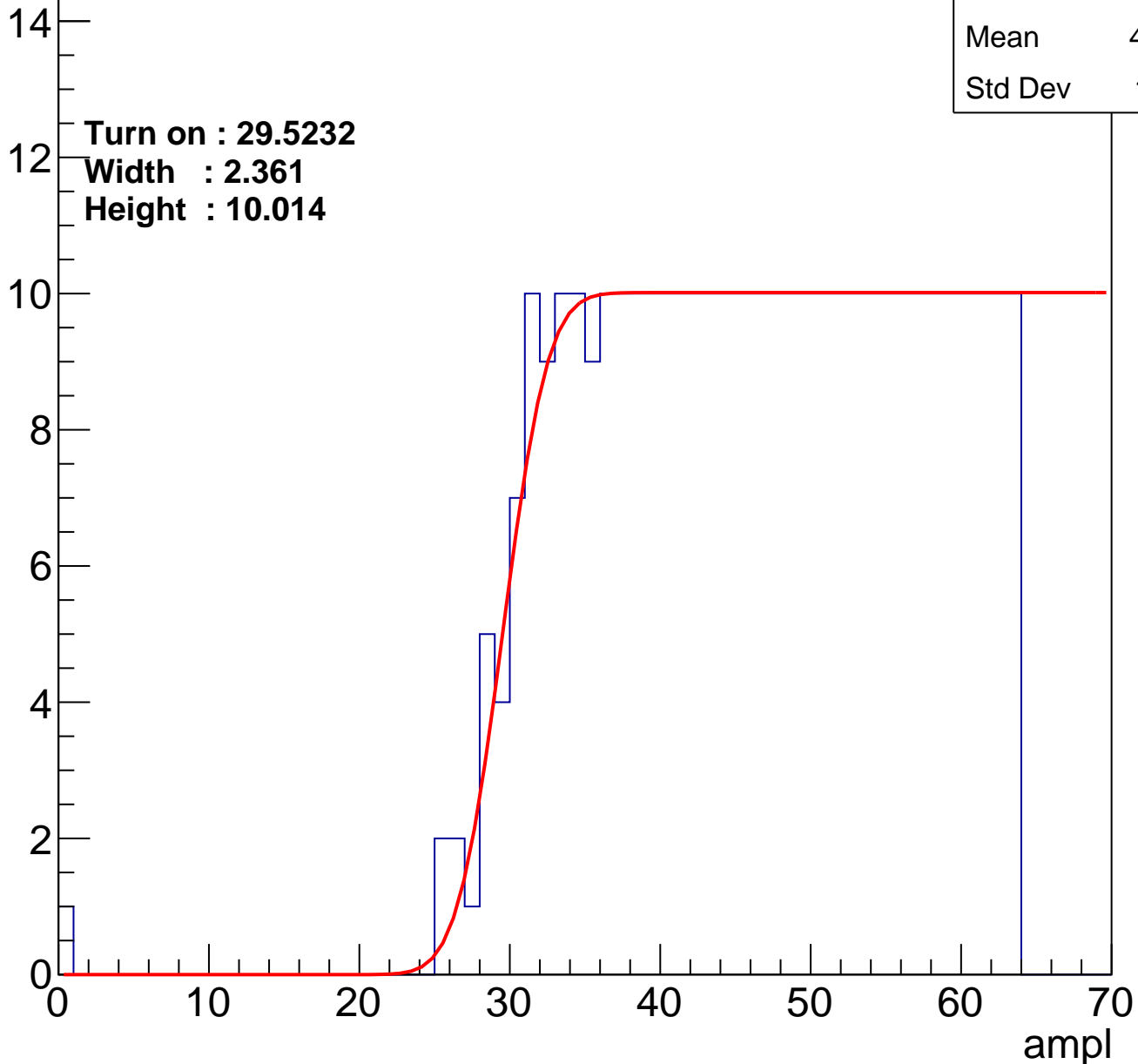
|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.82 |
| Std Dev | 10.51 |

Turn on : 29.5232

Width : 2.361

Height : 10.014

Entry



# B1L003S, U24-ch34

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 387   |
| Mean    | 43.93 |
| Std Dev | 11.68 |

Turn on : 25.7188

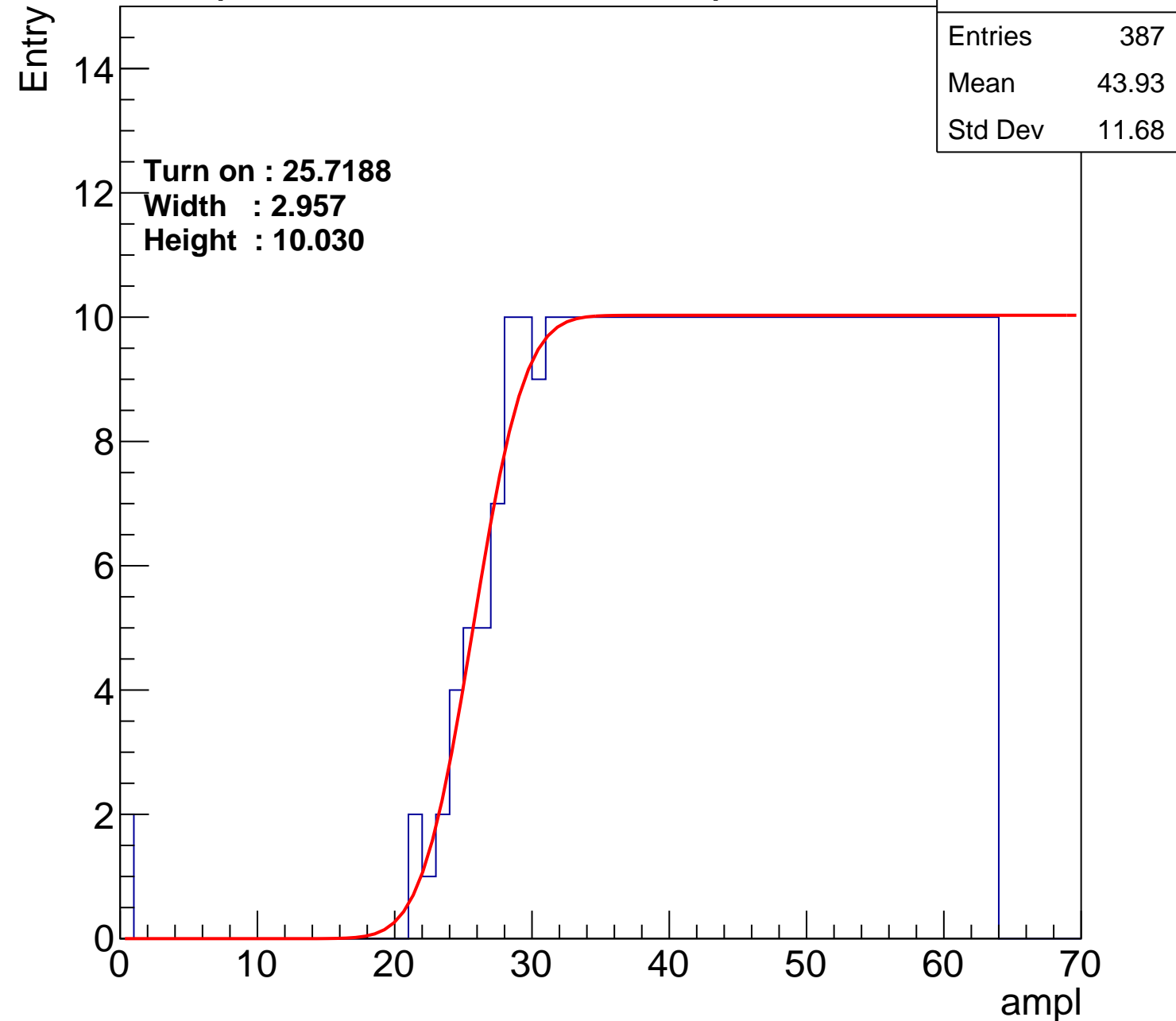
Width : 2.957

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch35

calib\_packv5\_042523\_0143.root, FC#13, port D2

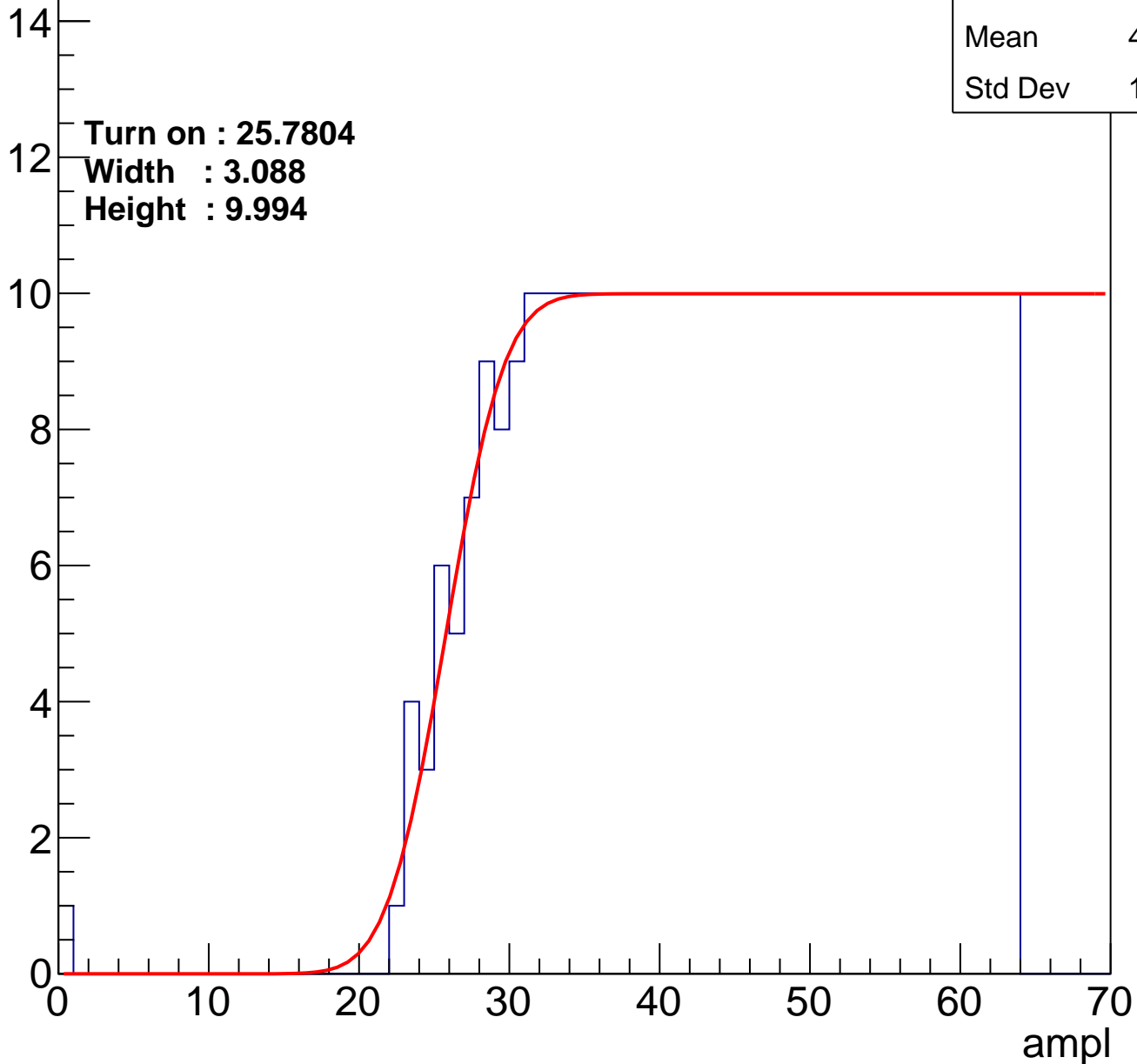
|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 44.17 |
| Std Dev | 11.42 |

Turn on : 25.7804

Width : 3.088

Height : 9.994

Entry



# B1L003S, U24-ch36

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.15 |
| Std Dev | 12.01 |

Turn on : 26.8476

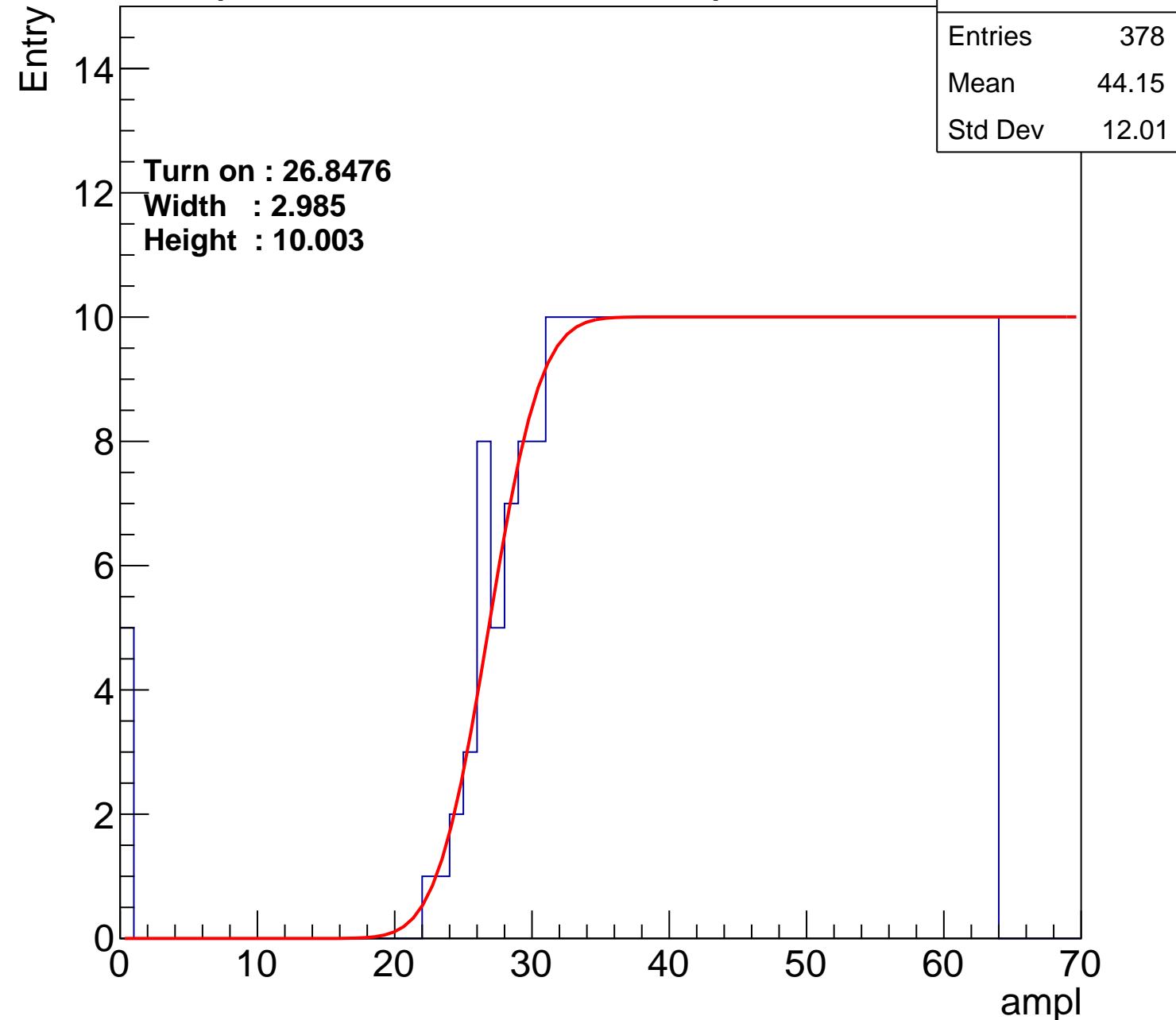
Width : 2.985

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch37

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.12 |
| Std Dev | 10.91 |

Turn on : 27.5439

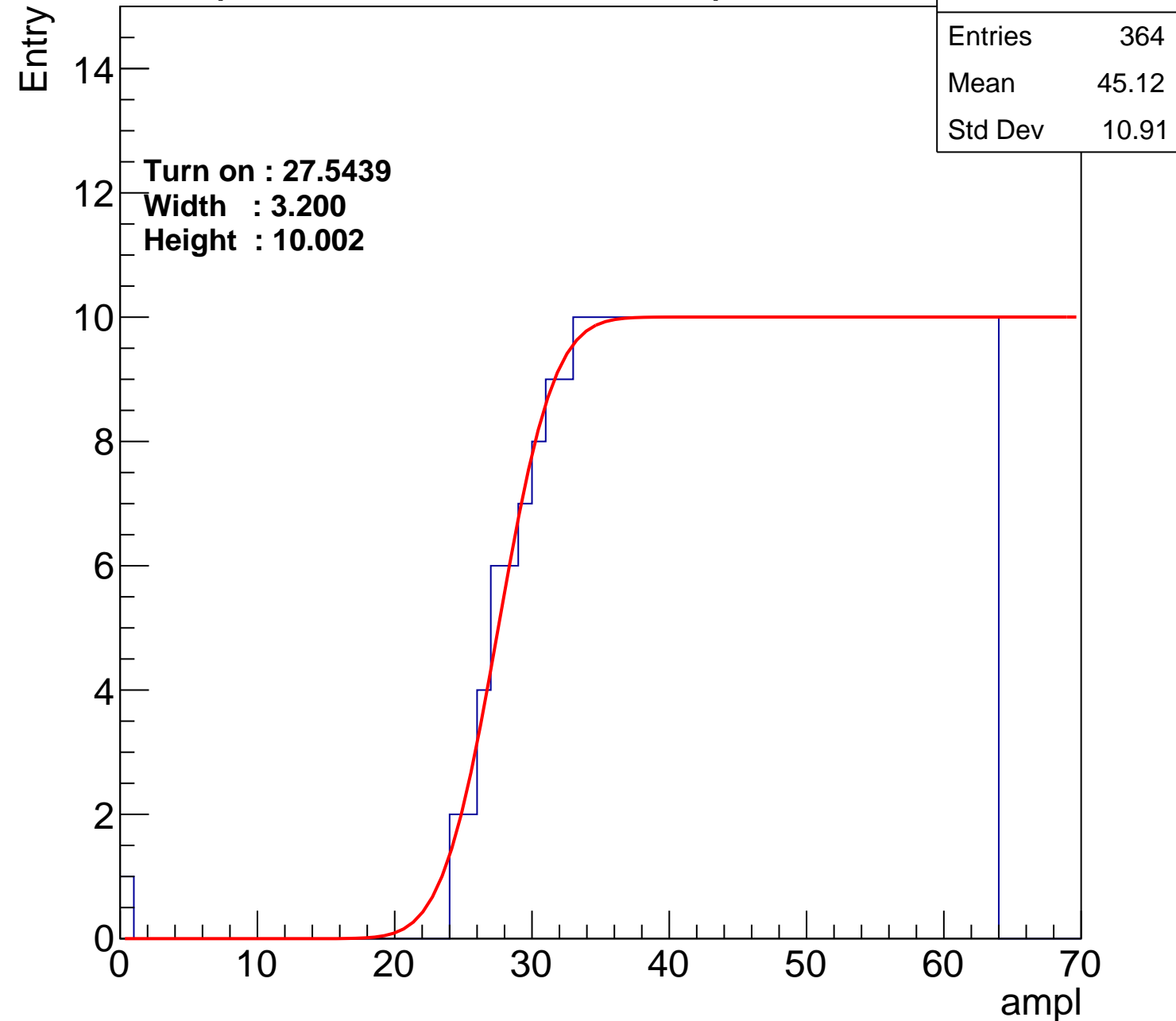
Width : 3.200

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch38

calib\_packv5\_042523\_0143.root, FC#13, port D2

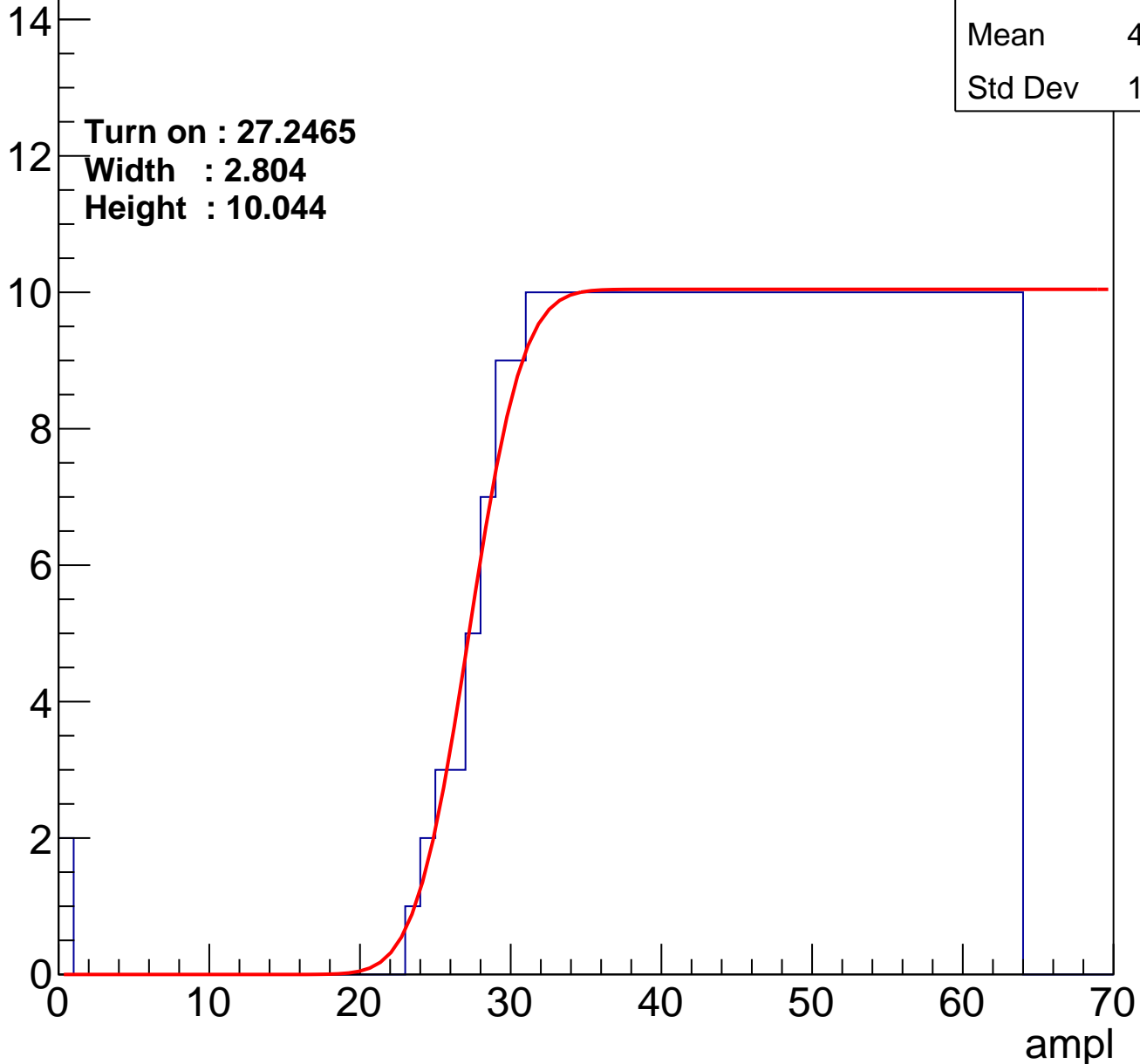
|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.73 |
| Std Dev | 11.24 |

Turn on : 27.2465

Width : 2.804

Height : 10.044

Entry





# B1L003S, U24-ch39

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 382   |
| Mean    | 44.14 |
| Std Dev | 11.67 |

Turn on : 25.7733

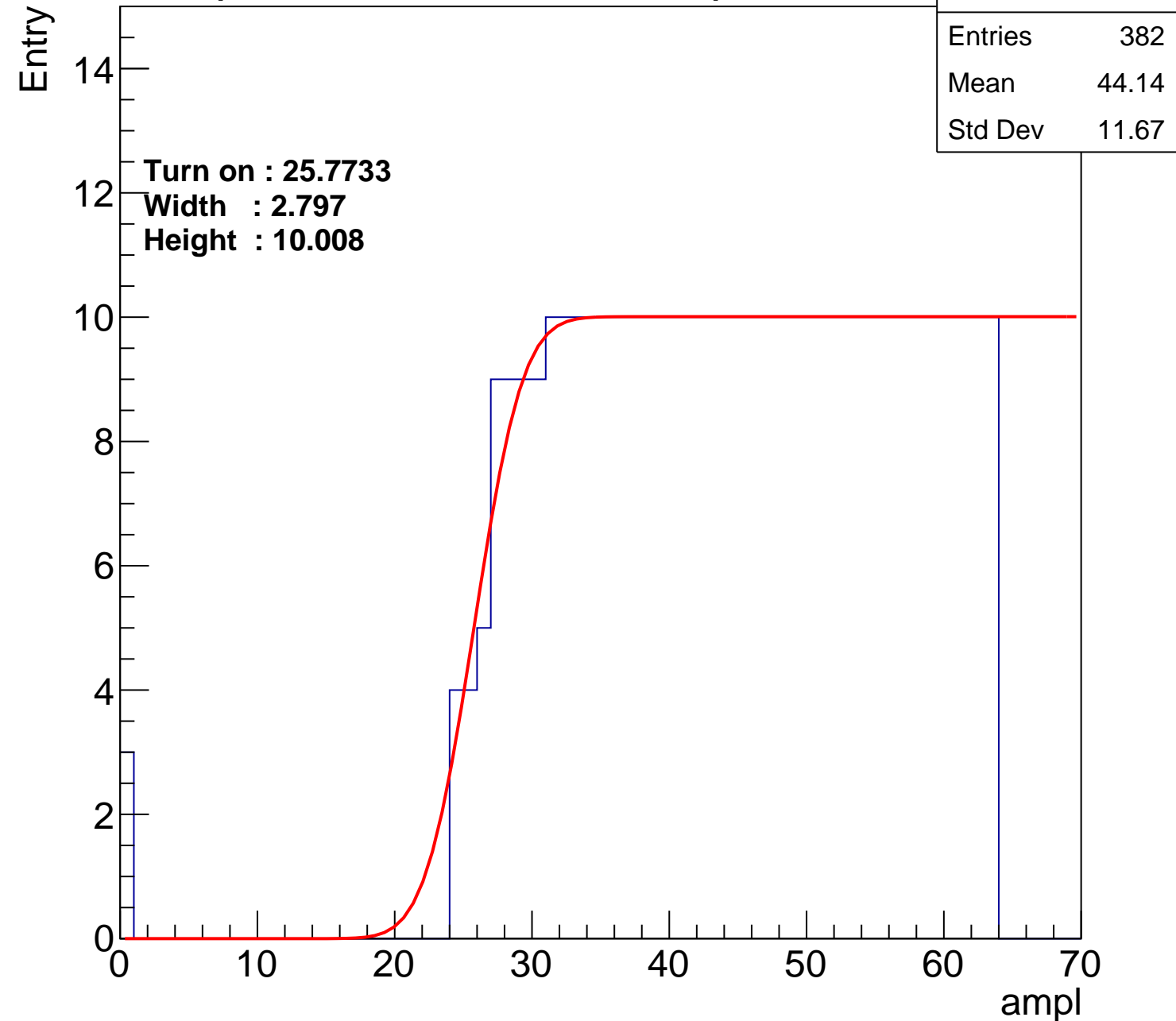
Width : 2.797

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch40

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 388   |
| Mean    | 43.87 |
| Std Dev | 11.72 |

**Turn on : 25.3048**

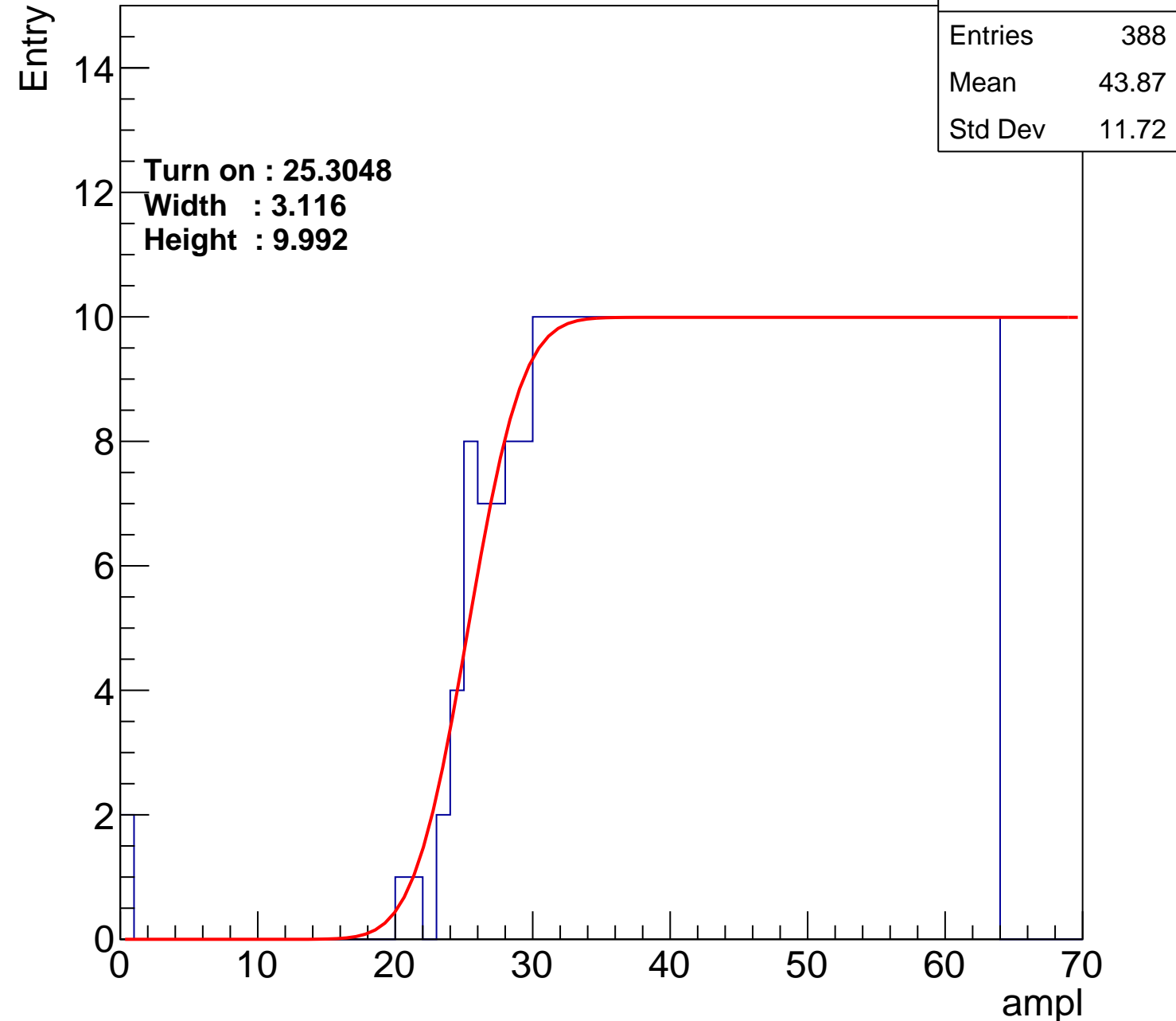
**Width : 3.116**

**Height : 9.992**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch41

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.29 |
| Std Dev | 11.14 |

Turn on : 28.5991

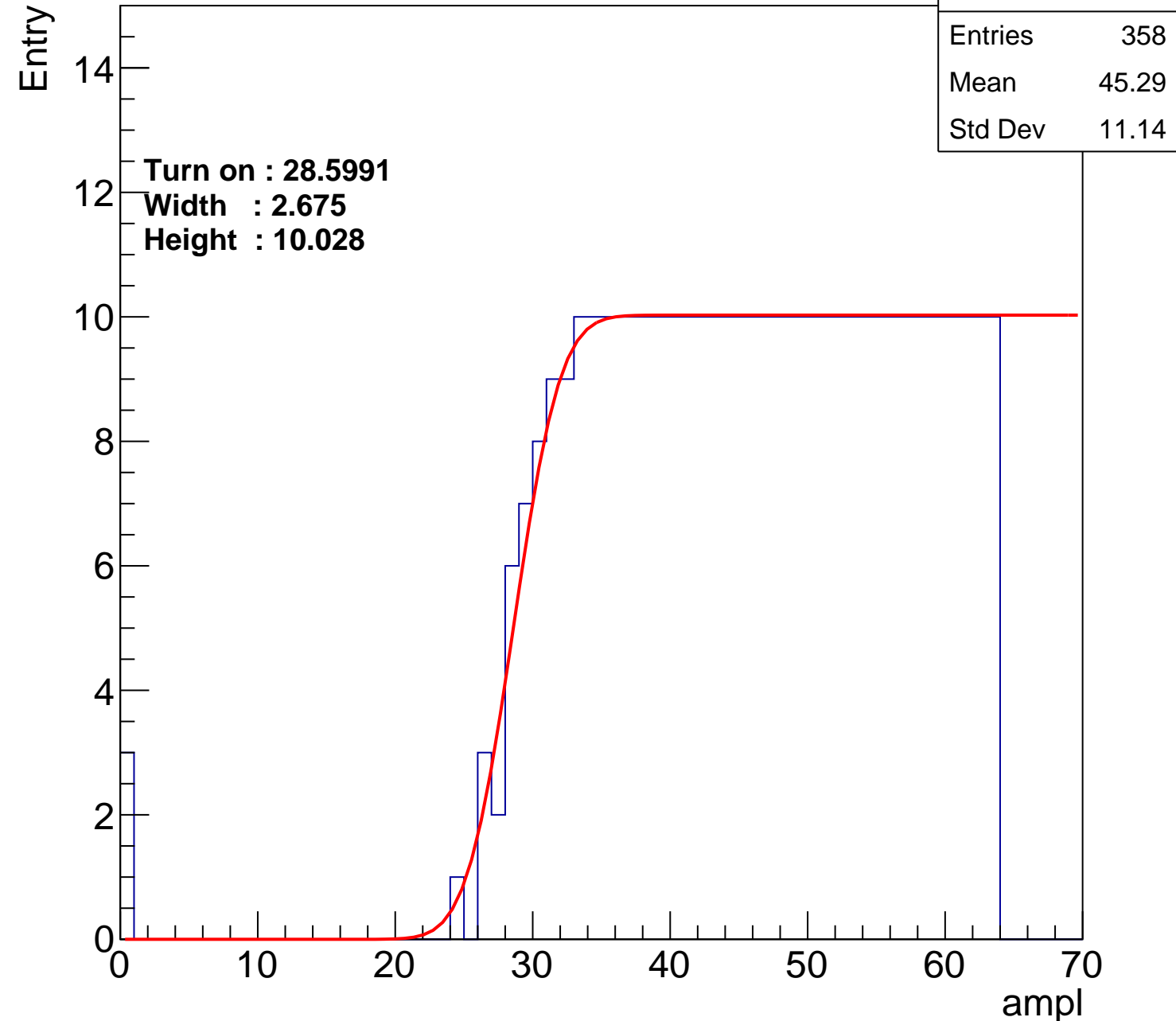
Width : 2.675

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch42

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 389   |
| Mean    | 43.88 |
| Std Dev | 11.59 |

Turn on : 25.6554

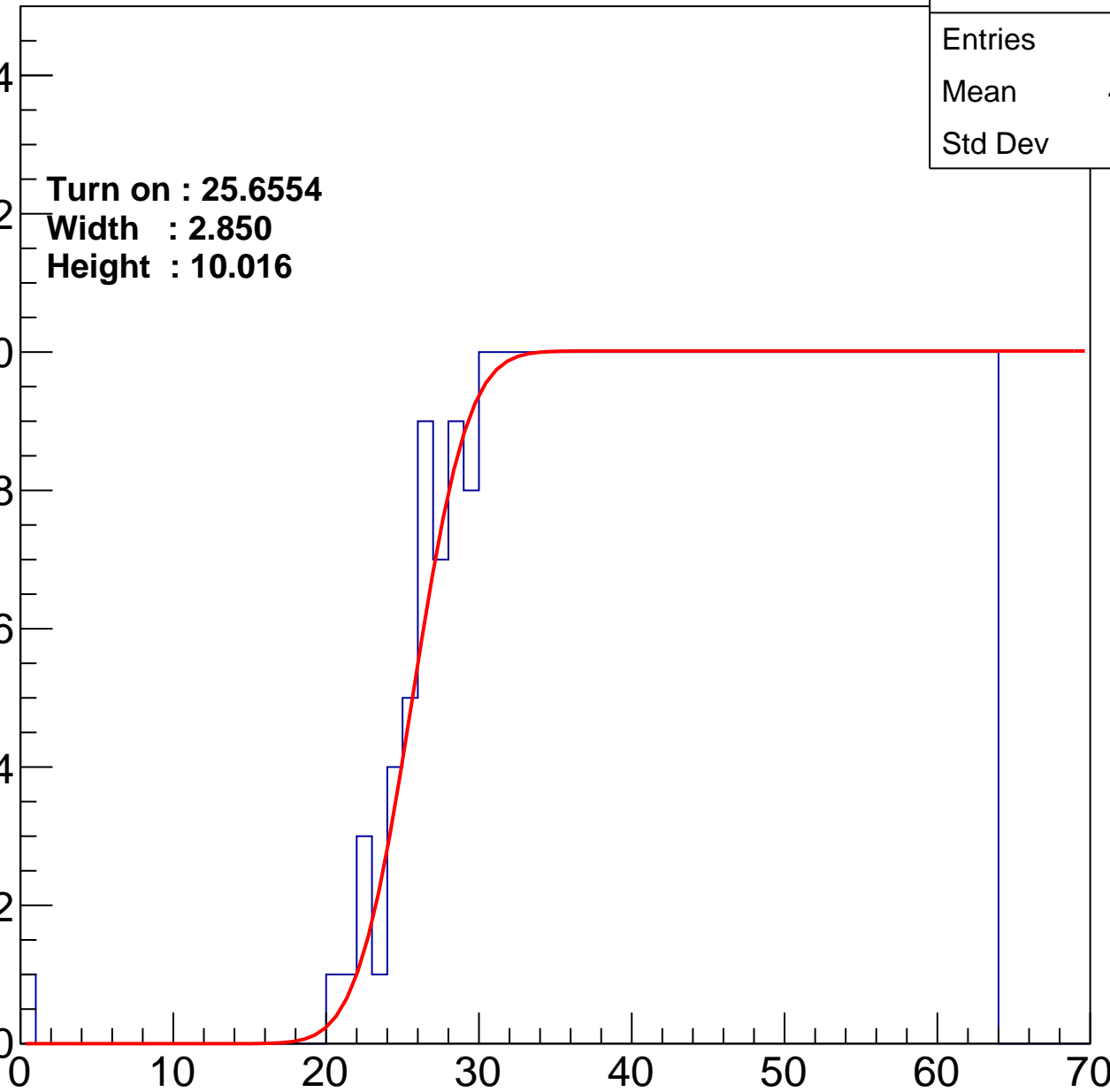
Width : 2.850

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch43

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.33 |
| Std Dev | 10.78 |

Turn on : 28.3124

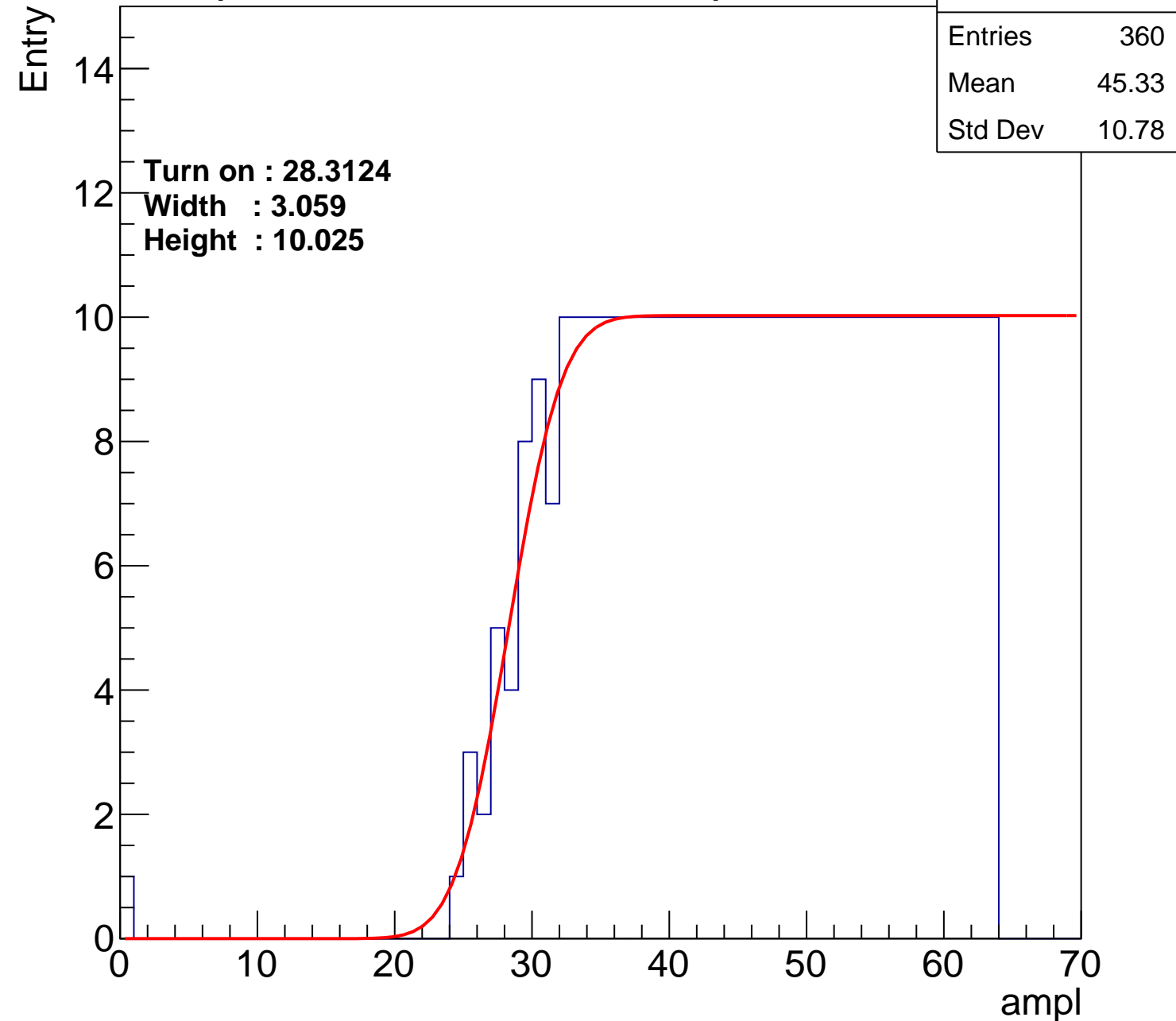
Width : 3.059

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch44

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.41 |
| Std Dev | 11.92 |

Turn on : 27.4745

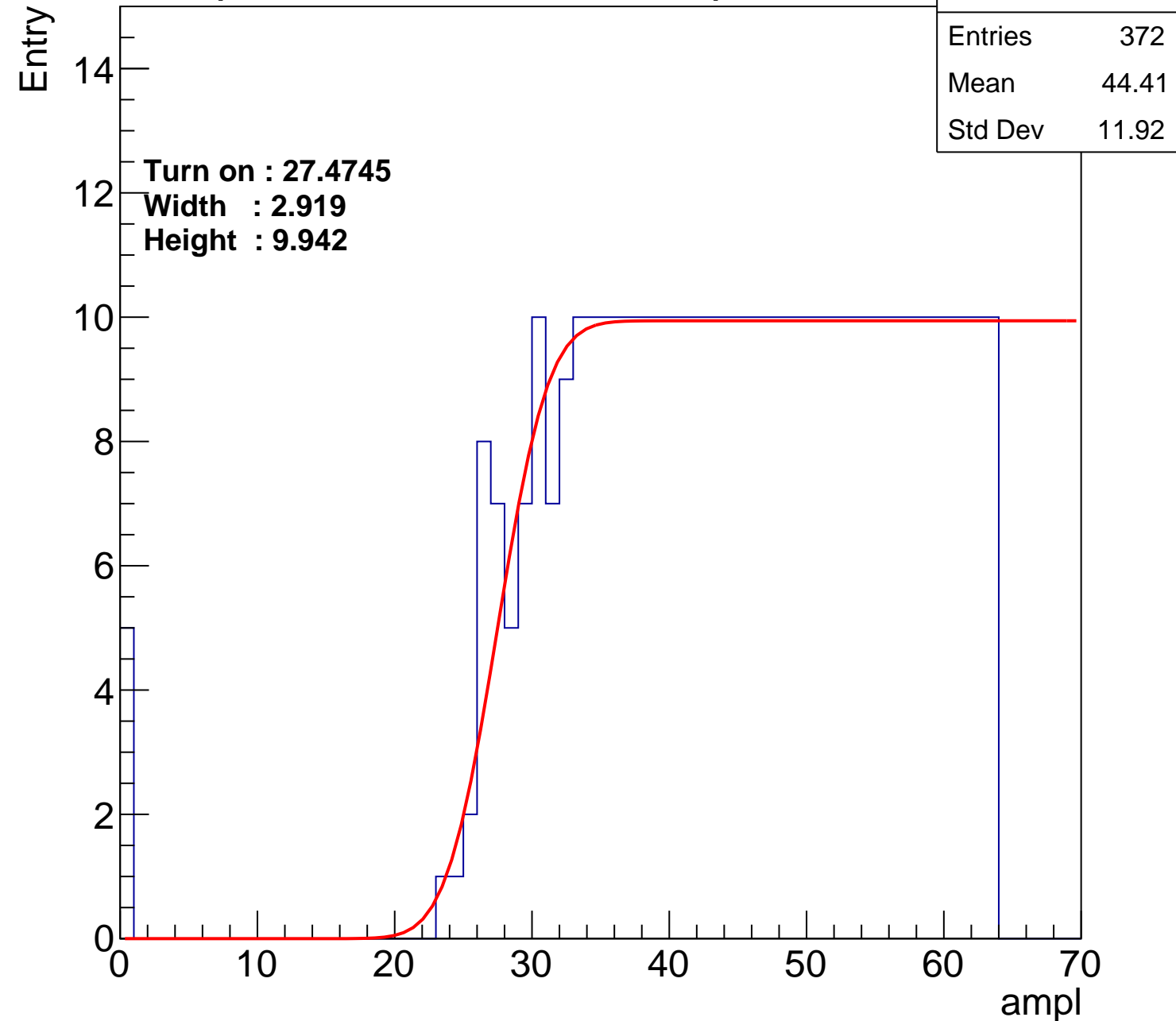
Width : 2.919

Height : 9.942

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch45

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.42 |
| Std Dev | 11.79 |

Turn on : 27.4616

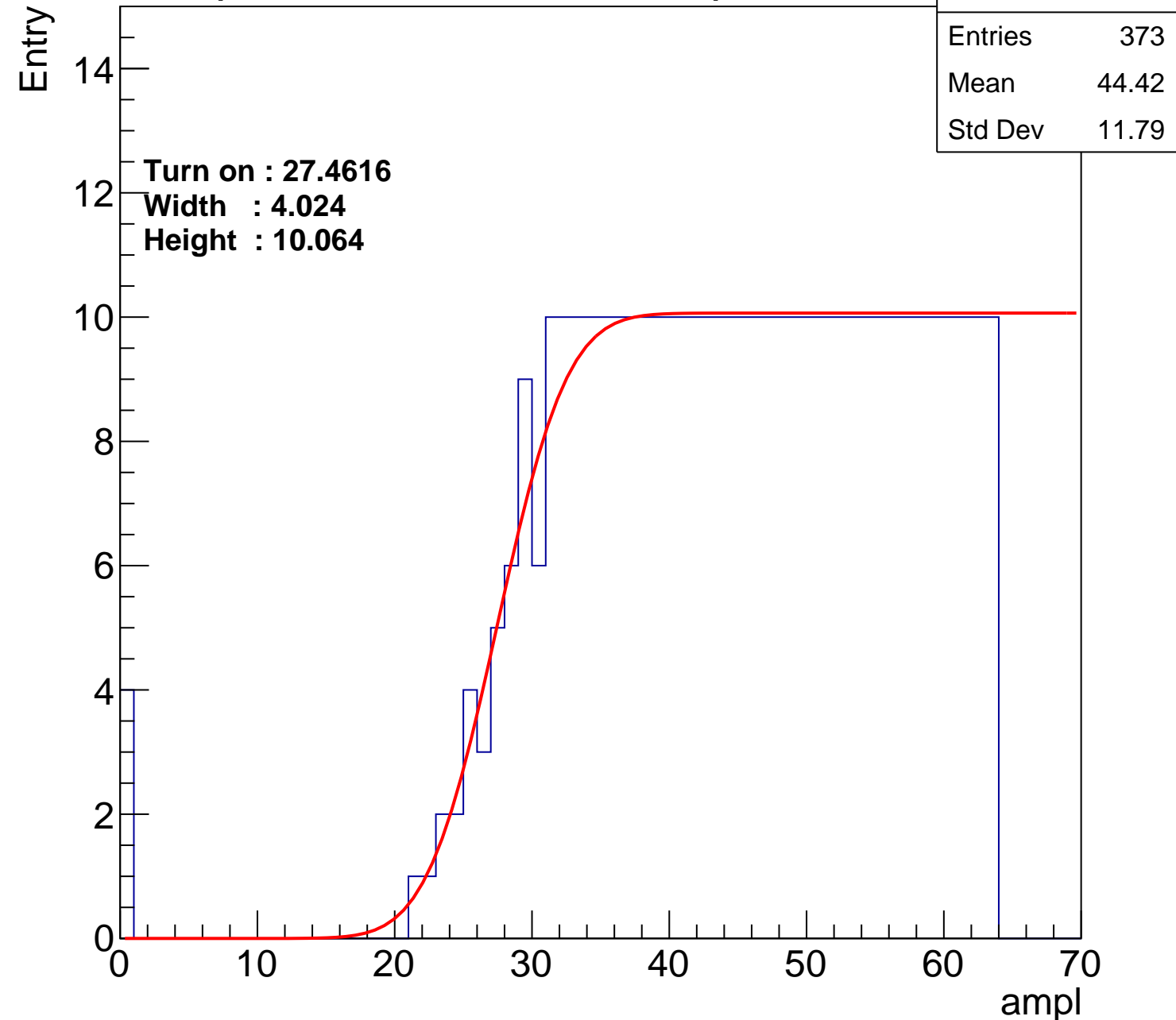
Width : 4.024

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch46

calib\_packv5\_042523\_0143.root, FC#13, port D2

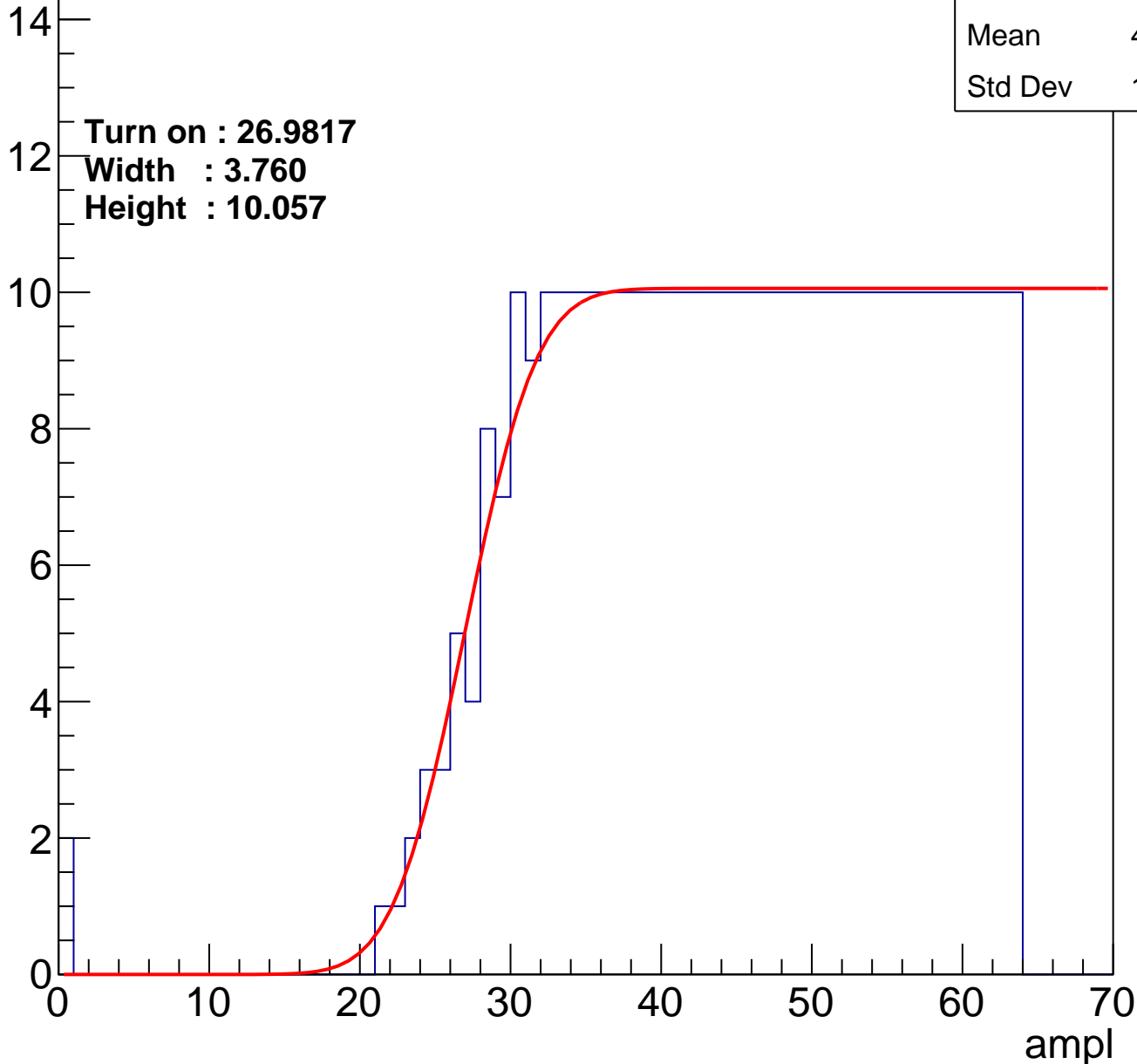
|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.48 |
| Std Dev | 11.44 |

Turn on : 26.9817

Width : 3.760

Height : 10.057

Entry





# B1L003S, U24-ch47

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.66 |
| Std Dev | 11.3  |

Turn on : 27.1647

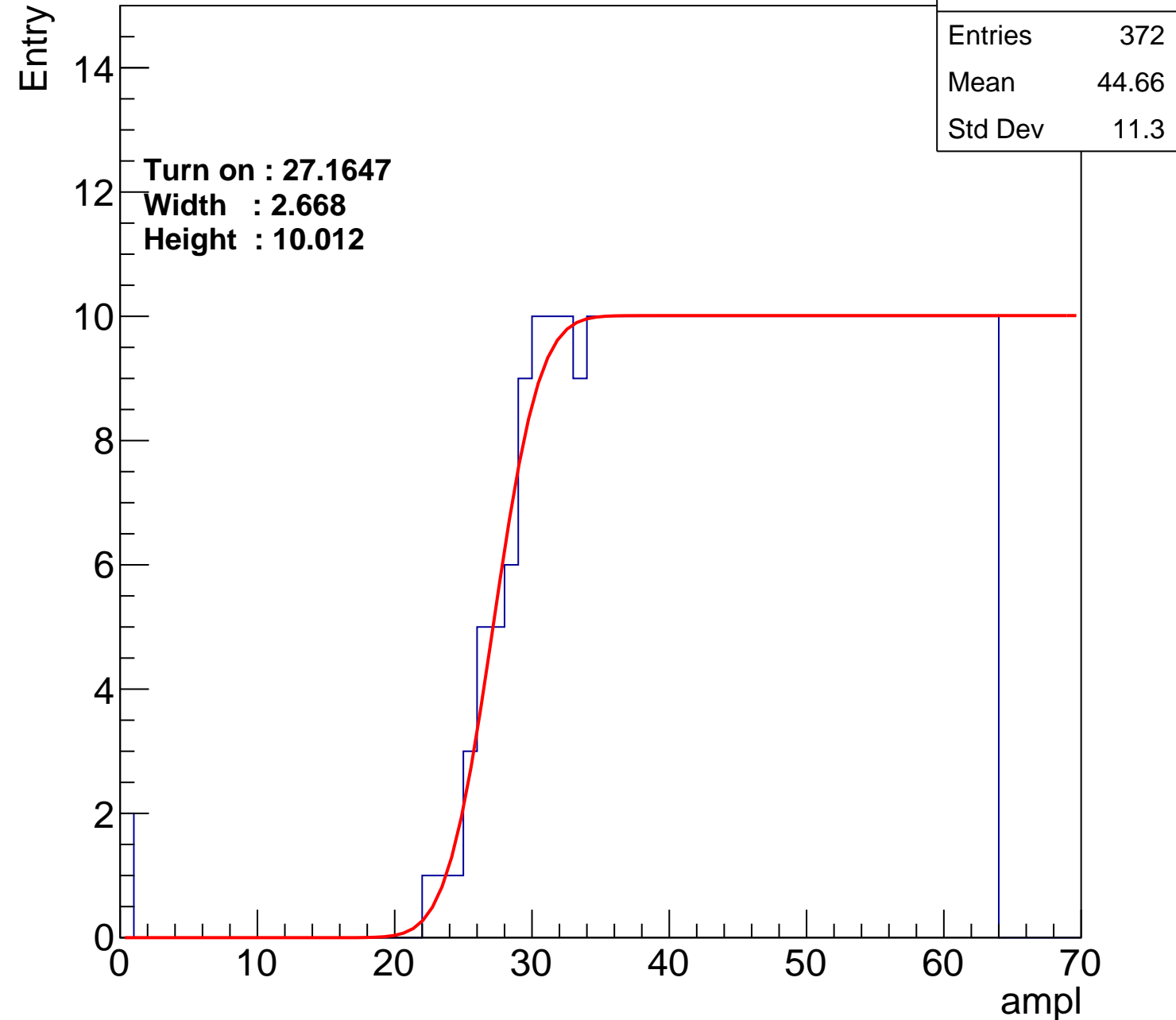
Width : 2.668

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch48

calib\_packv5\_042523\_0143.root, FC#13, port D2

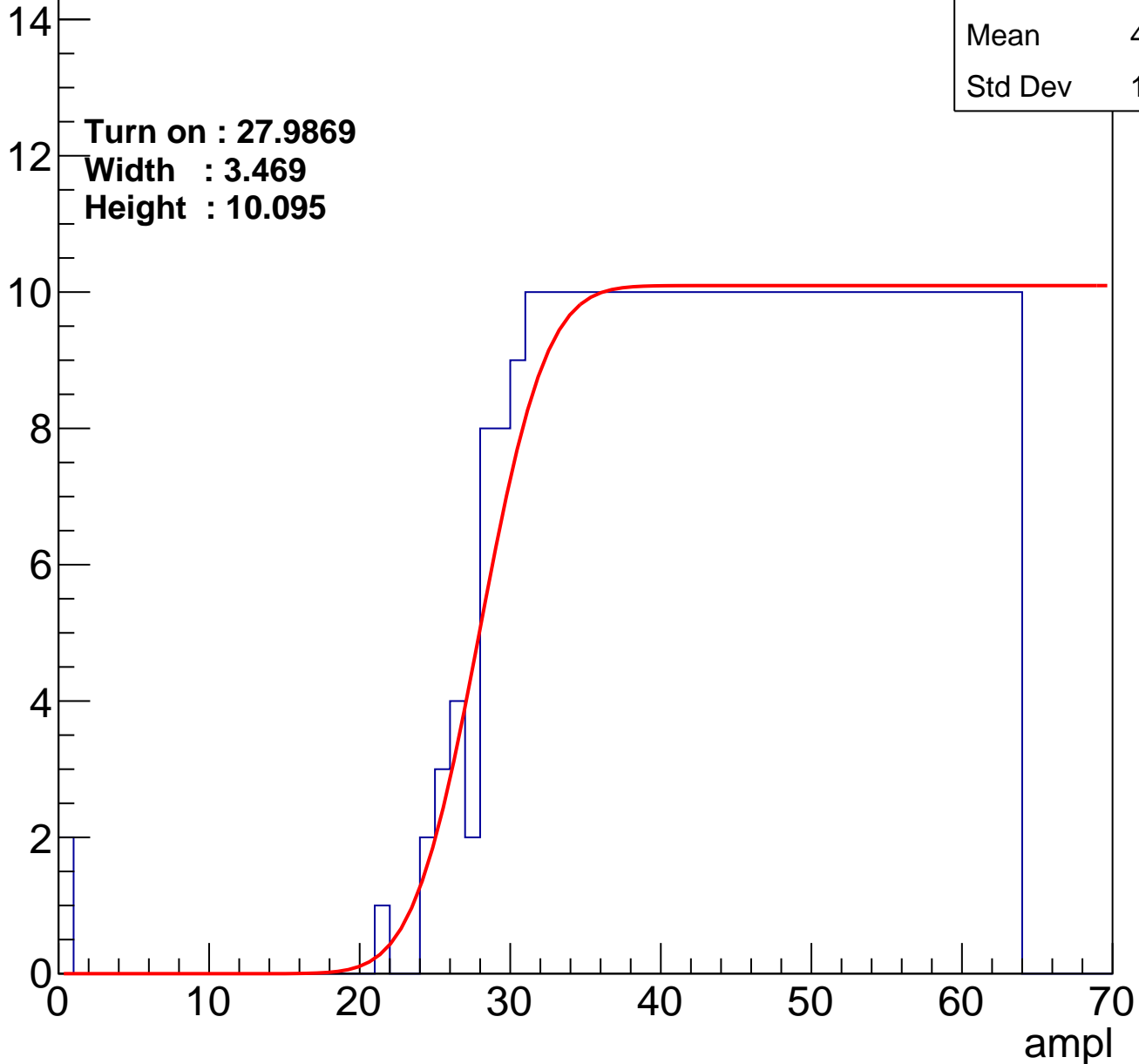
|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.82 |
| Std Dev | 11.22 |

Turn on : 27.9869

Width : 3.469

Height : 10.095

Entry



# B1L003S, U24-ch49

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 379   |
| Mean    | 44.22 |
| Std Dev | 11.78 |

Turn on : 26.0524

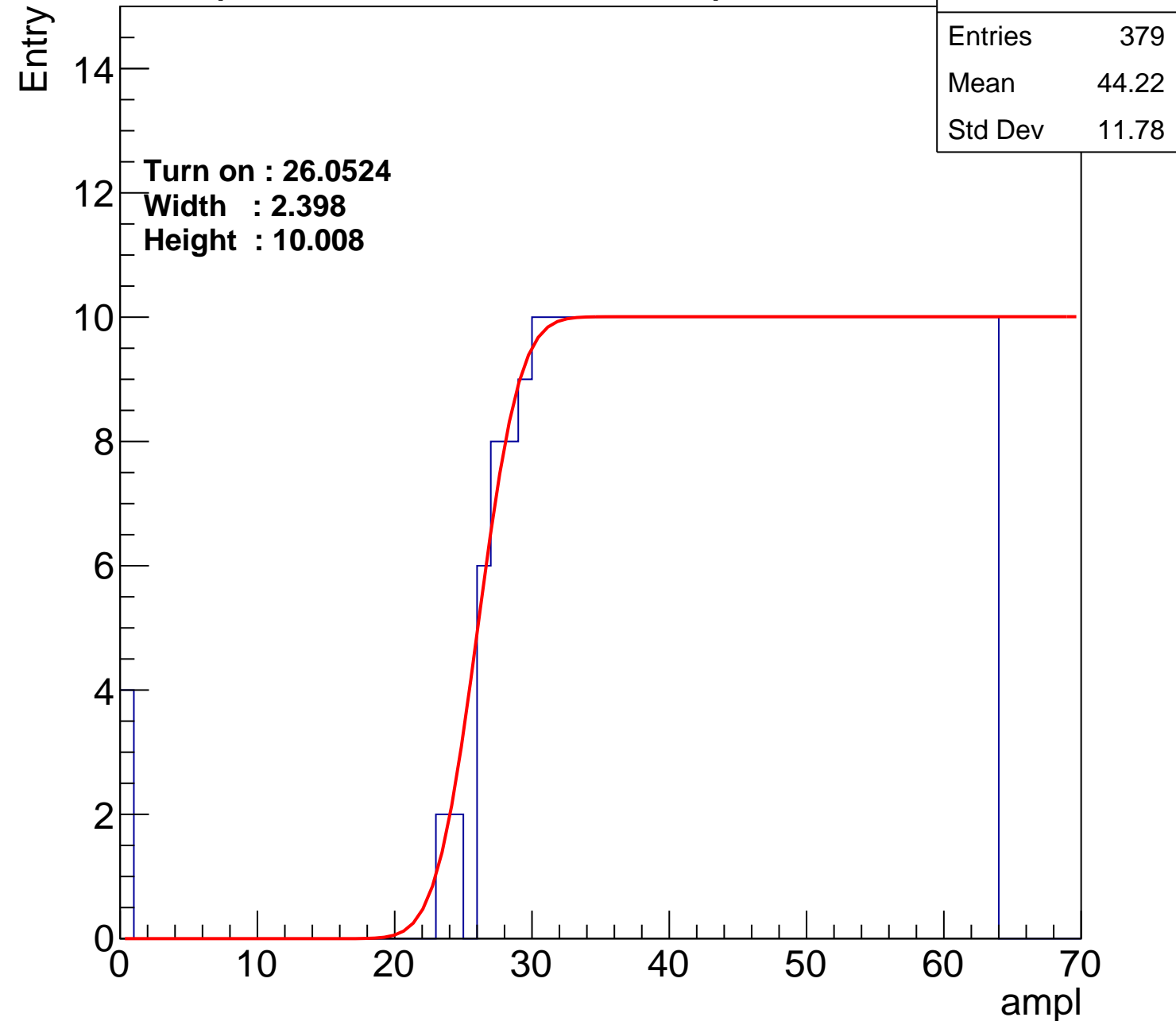
Width : 2.398

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch50

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 44.16 |
| Std Dev | 11.44 |

Turn on : 26.0159

Width : 2.685

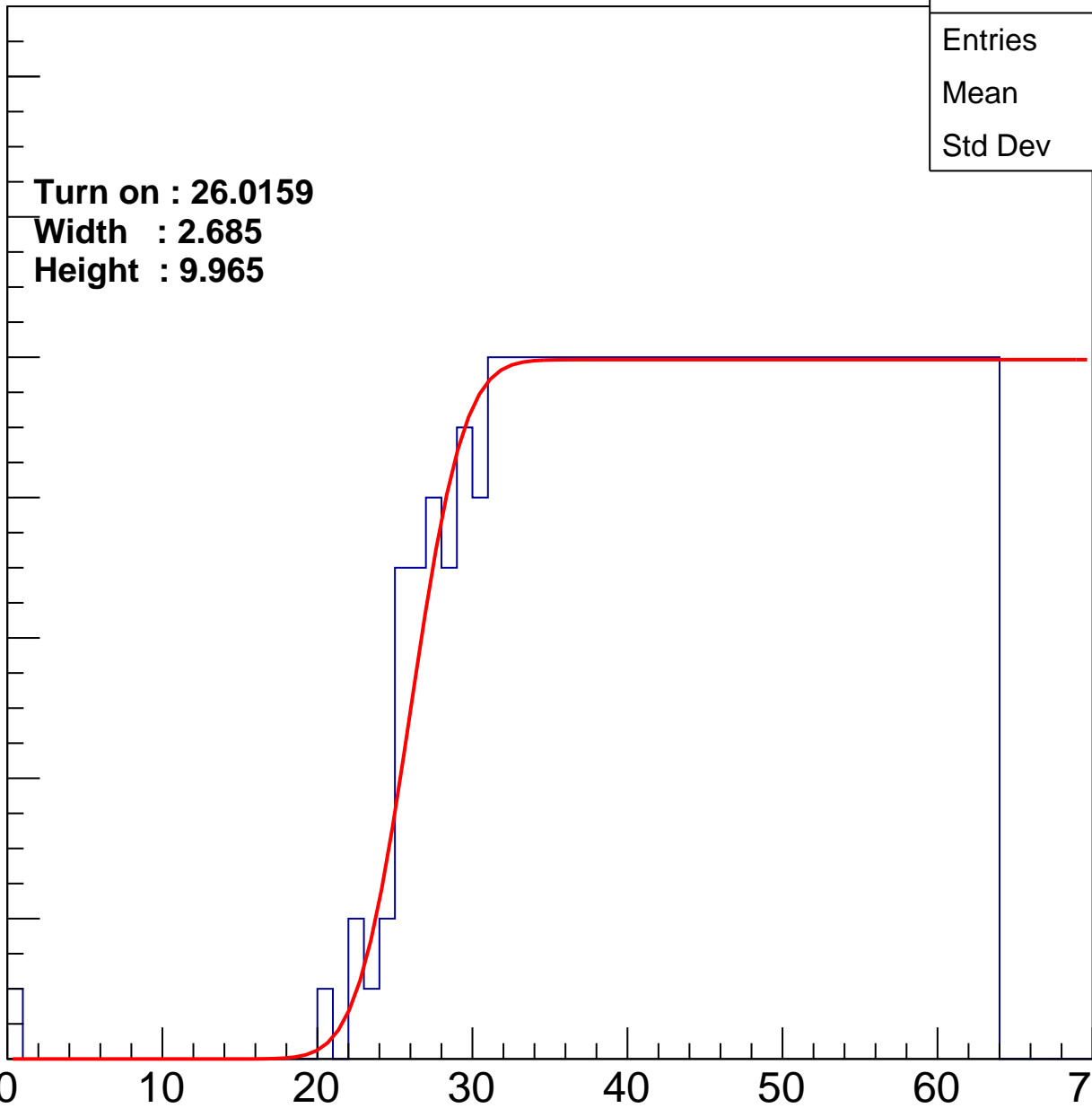
Height : 9.965

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U24-ch51

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.32 |
| Std Dev | 11.61 |

Turn on : 26.1884

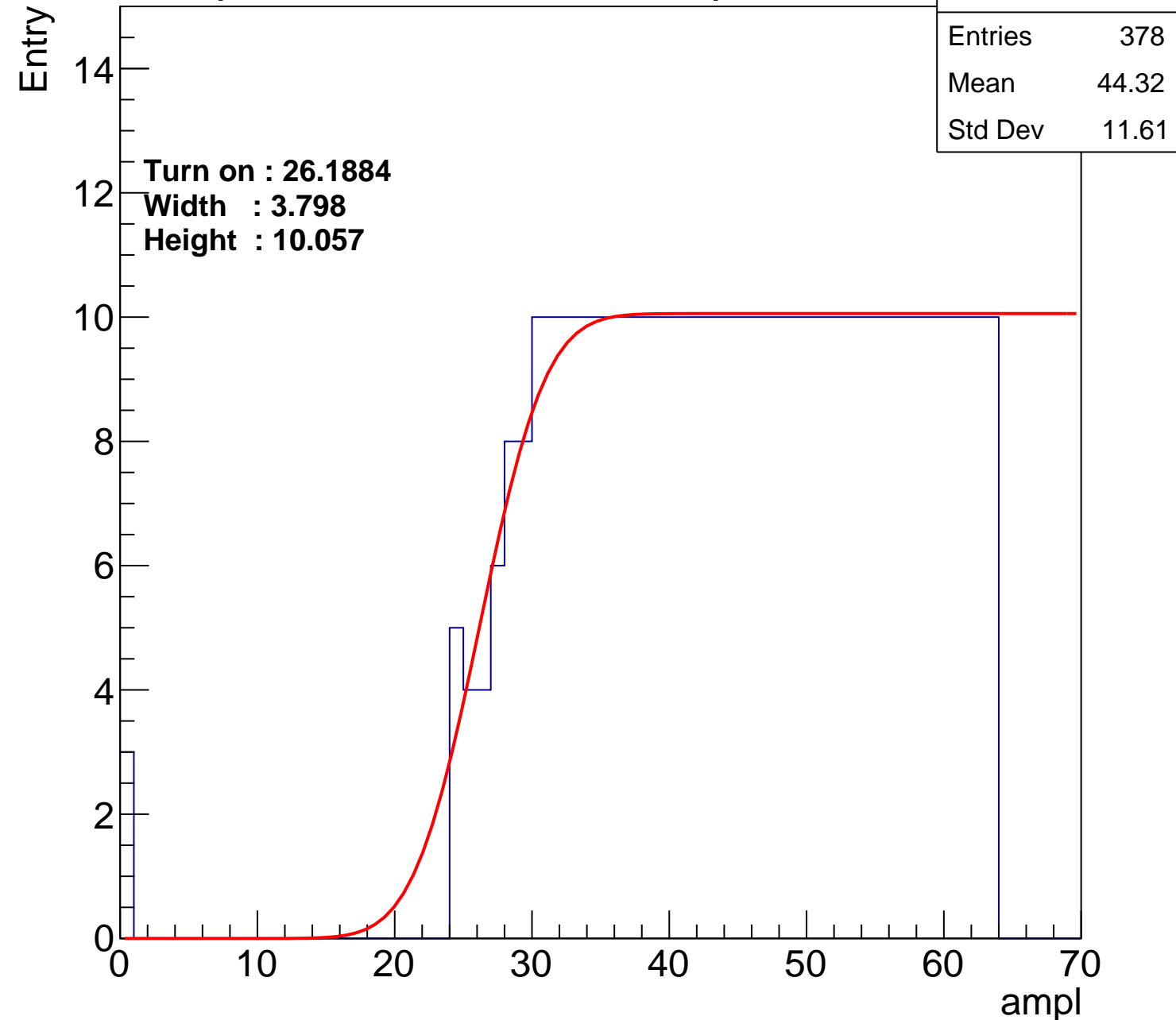
Width : 3.798

Height : 10.057

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch52

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.43 |
| Std Dev | 11.73 |

Turn on : 27.1100

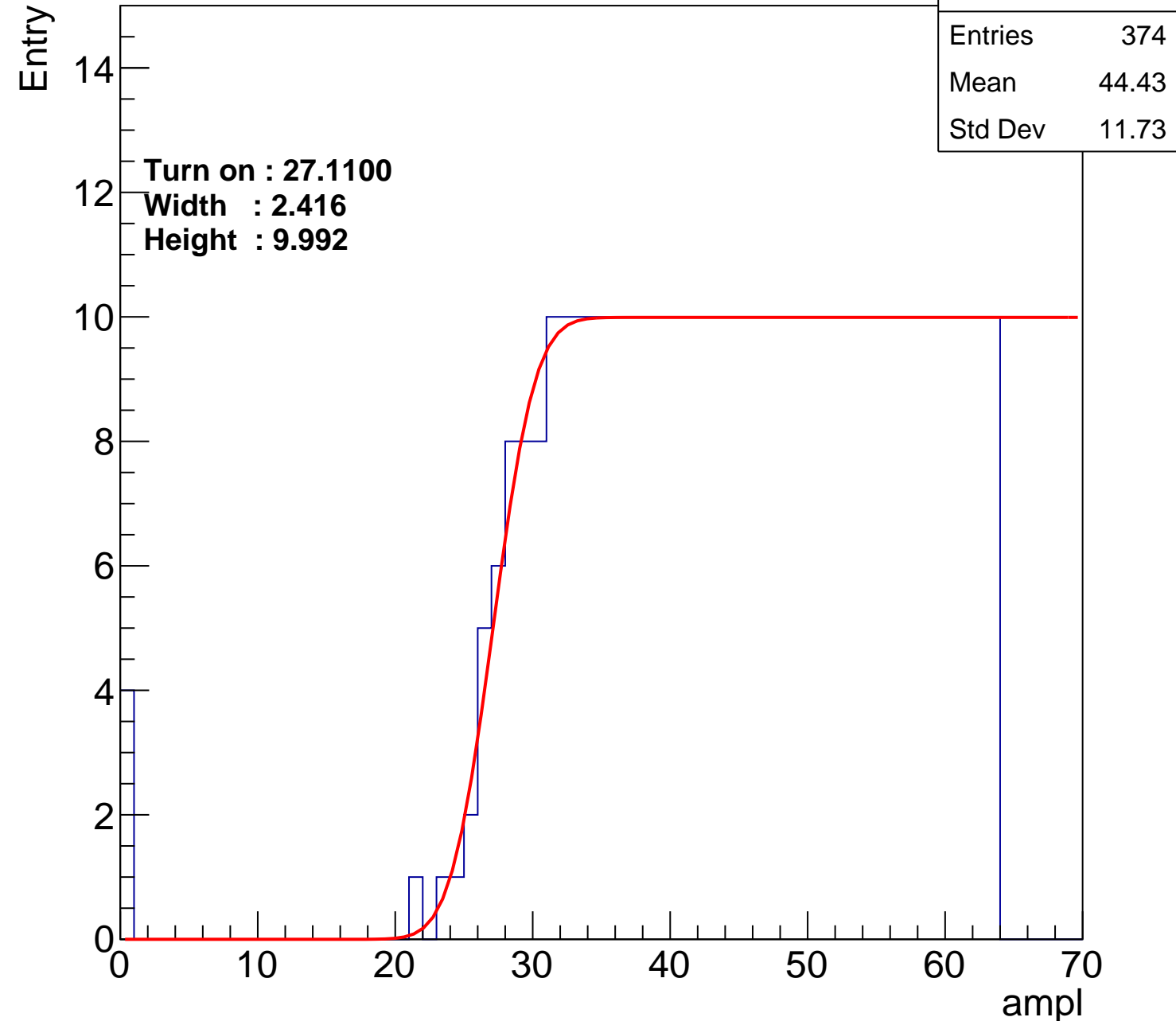
Width : 2.416

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch53

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.67 |
| Std Dev | 11.28 |

Turn on : 26.9368

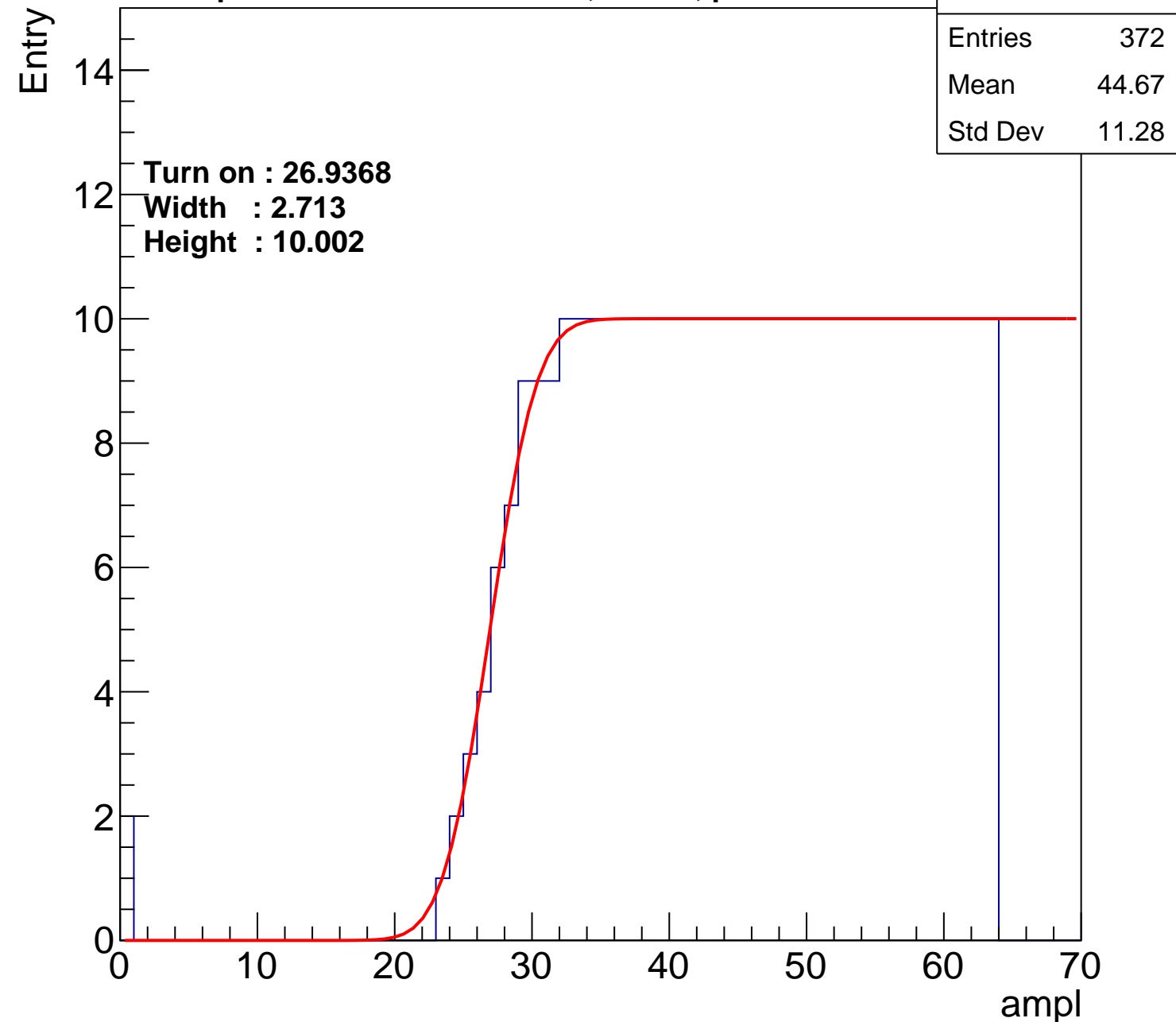
Width : 2.713

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch54

calib\_packv5\_042523\_0143.root, FC#13, port D2

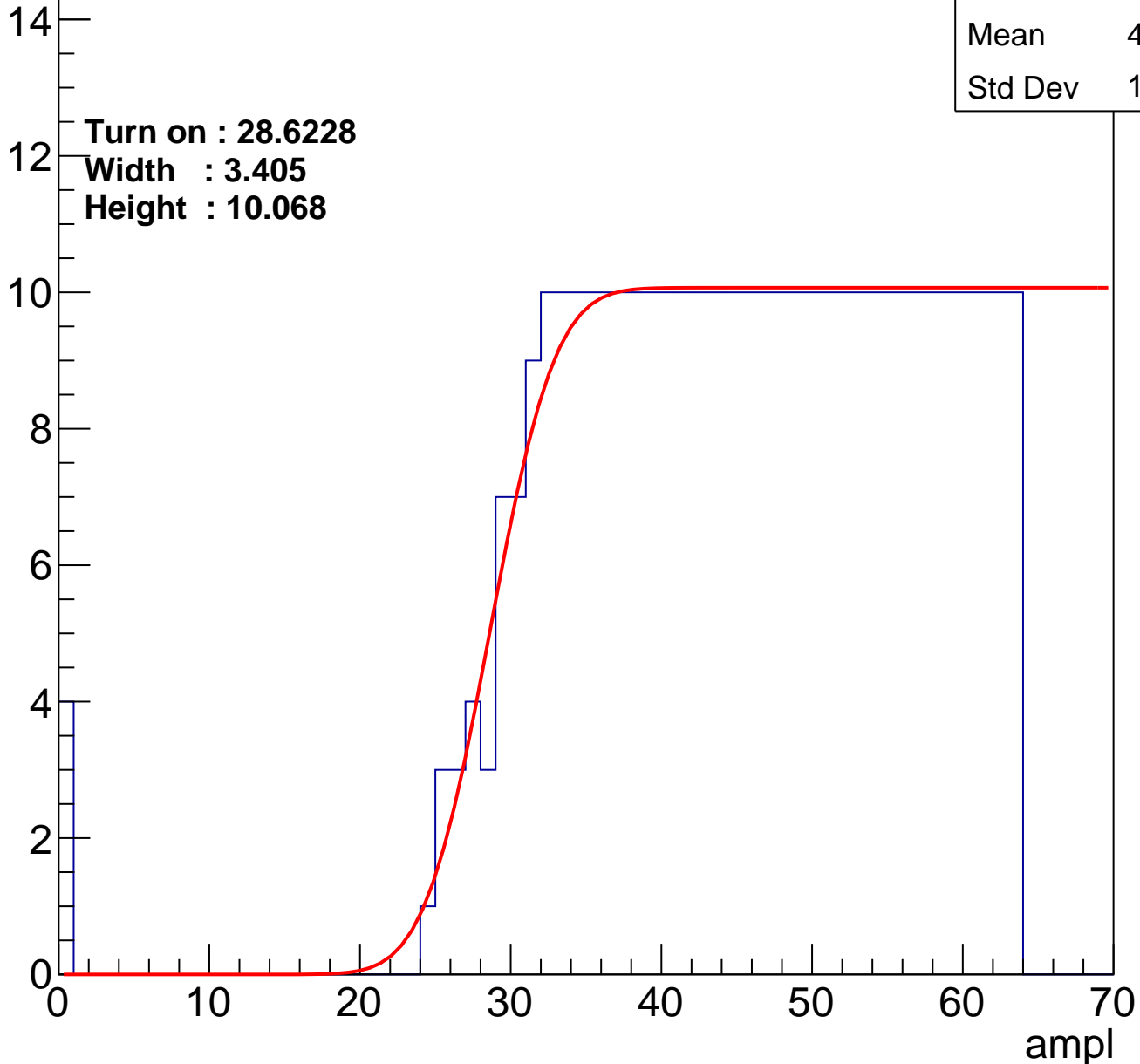
|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.04 |
| Std Dev | 11.46 |

**Turn on : 28.6228**

**Width : 3.405**

**Height : 10.068**

Entry





# B1L003S, U24-ch55

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 355   |
| Mean    | 45.5  |
| Std Dev | 10.87 |

Turn on : 29.1417

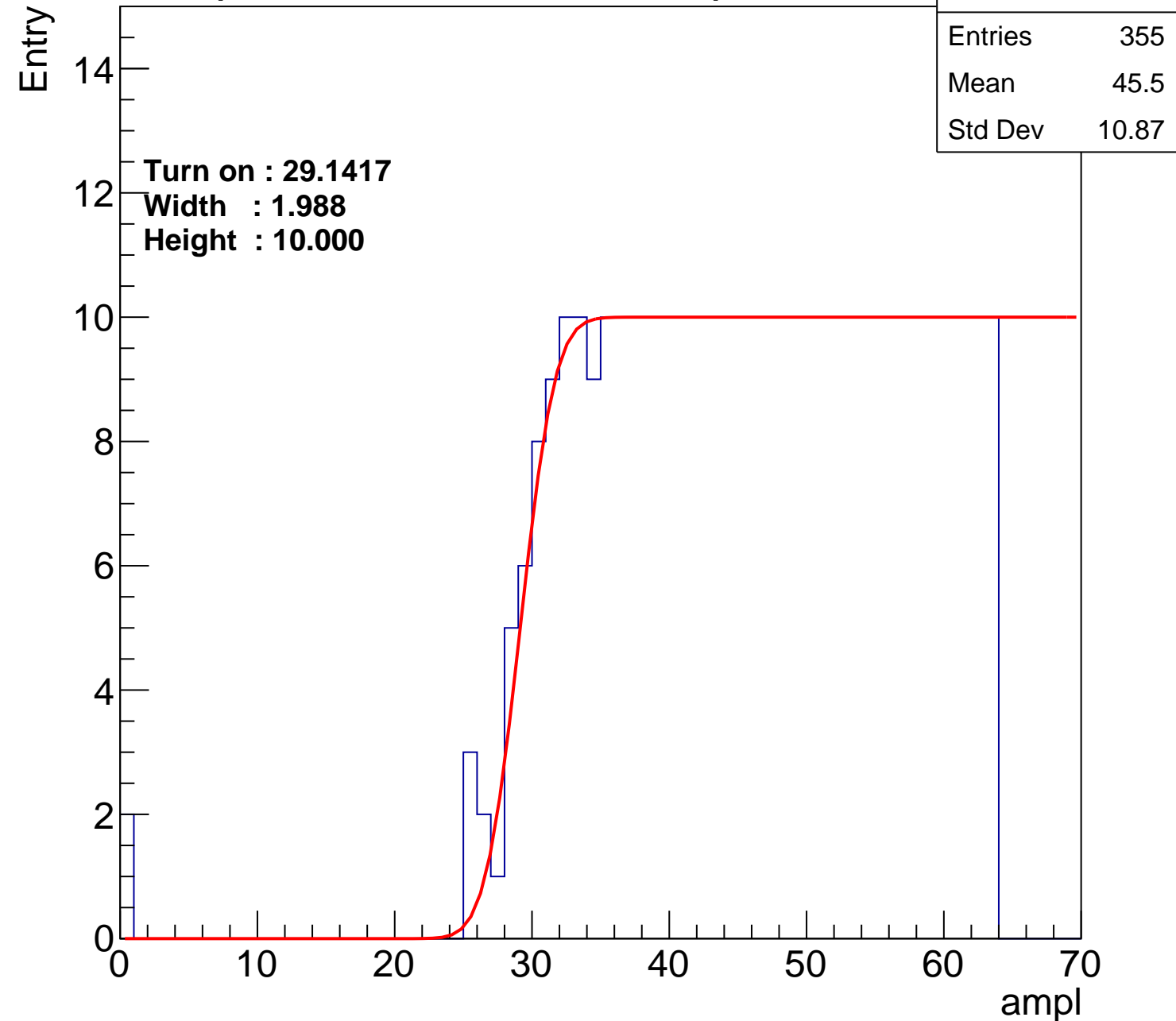
Width : 1.988

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch56

calib\_packv5\_042523\_0143.root, FC#13, port D2

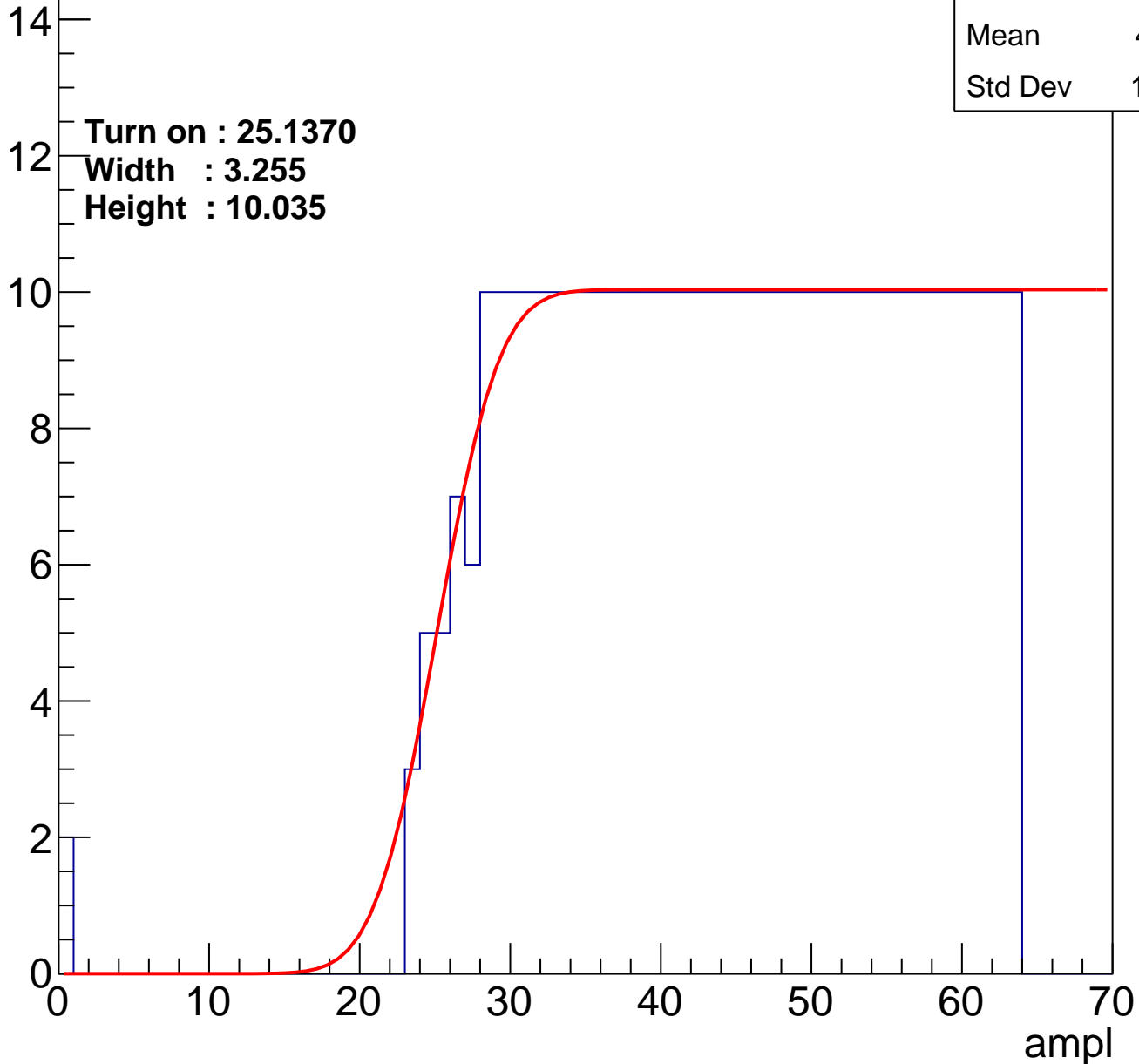
|         |       |
|---------|-------|
| Entries | 388   |
| Mean    | 43.91 |
| Std Dev | 11.65 |

Turn on : 25.1370

Width : 3.255

Height : 10.035

Entry



# B1L003S, U24-ch57

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.77 |
| Std Dev | 11.42 |

Turn on : 27.4131

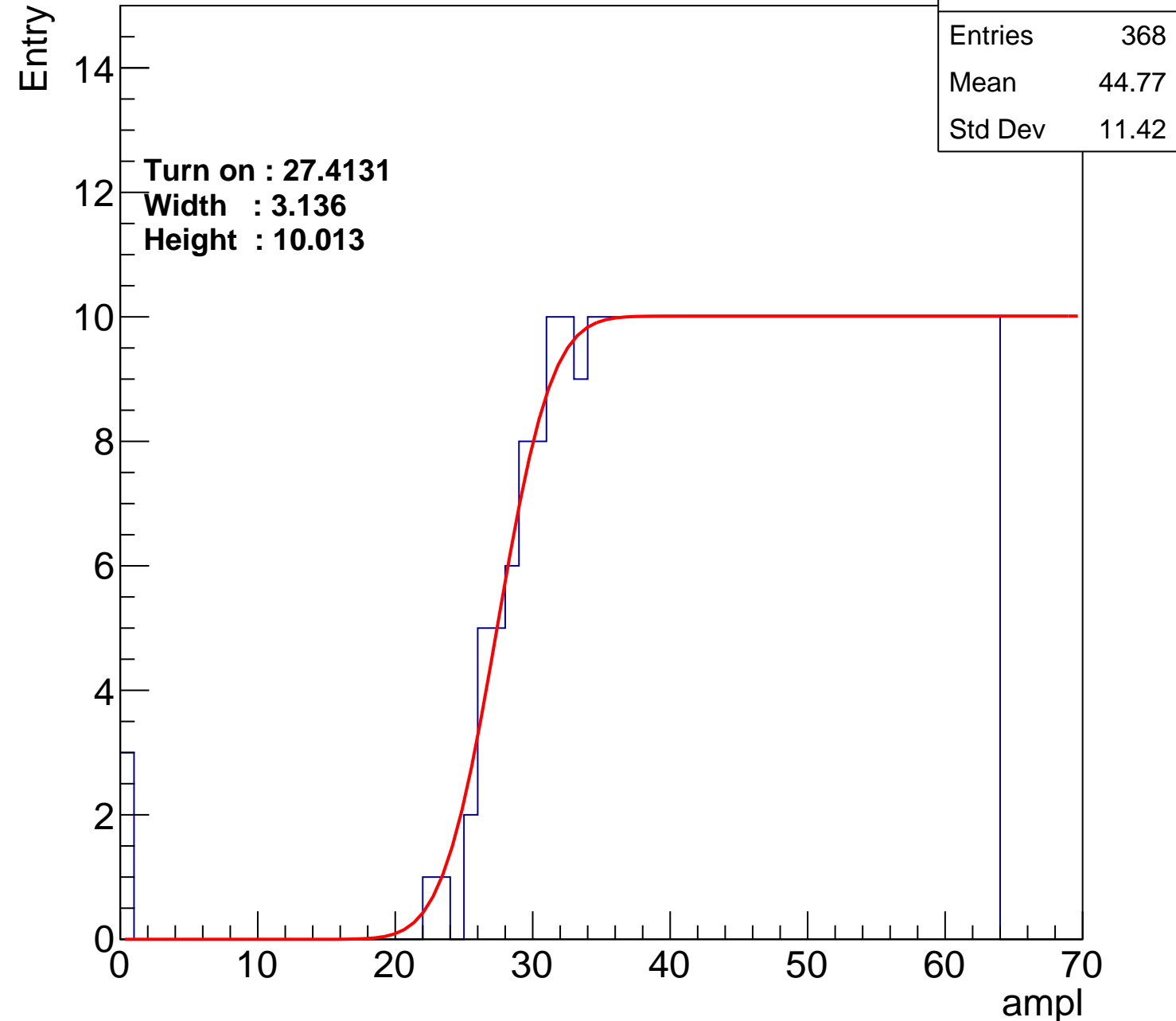
Width : 3.136

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch58

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 44.21 |
| Std Dev | 11.54 |

Turn on : 25.8746

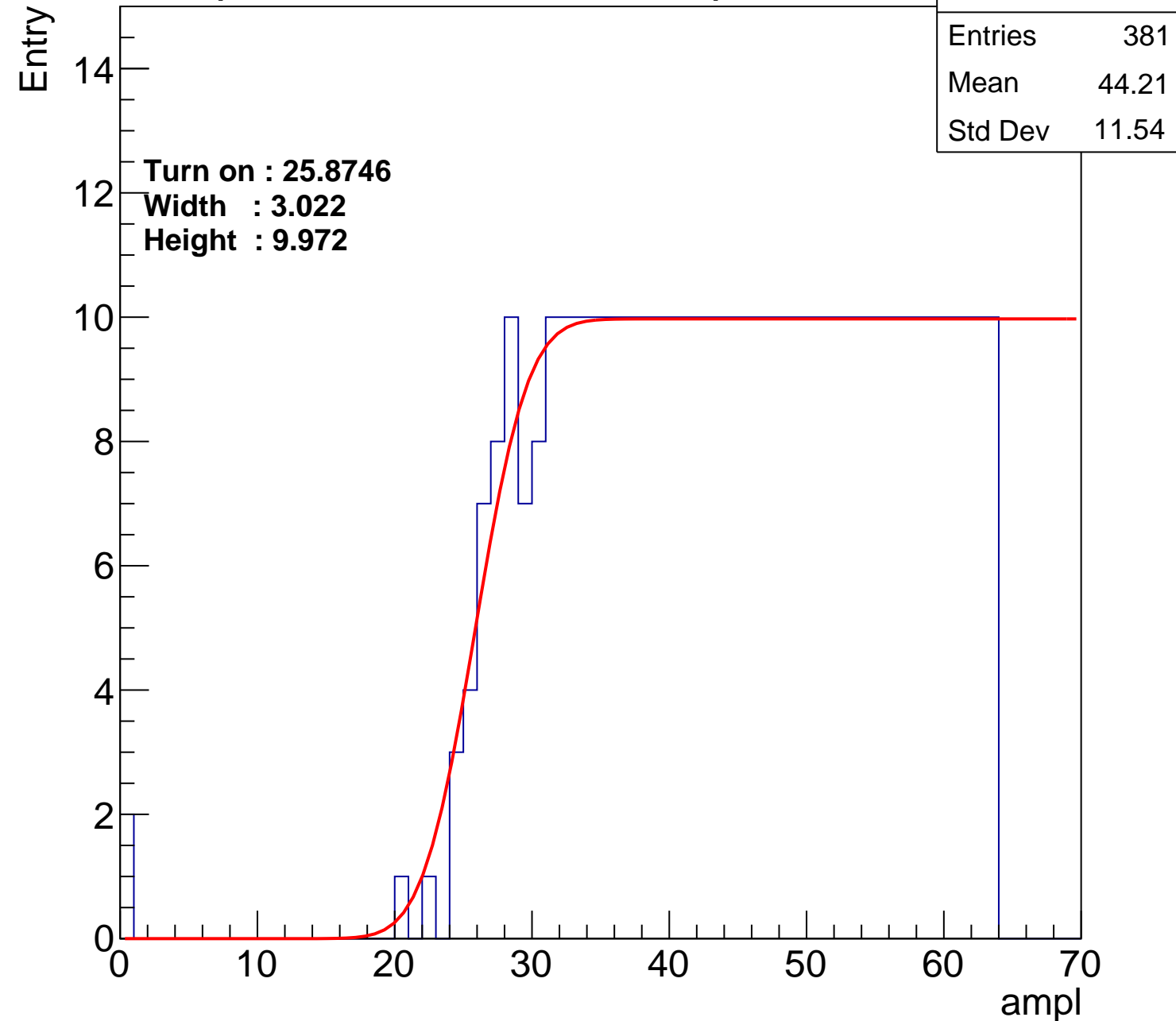
Width : 3.022

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch59

calib\_packv5\_042523\_0143.root, FC#13, port D2

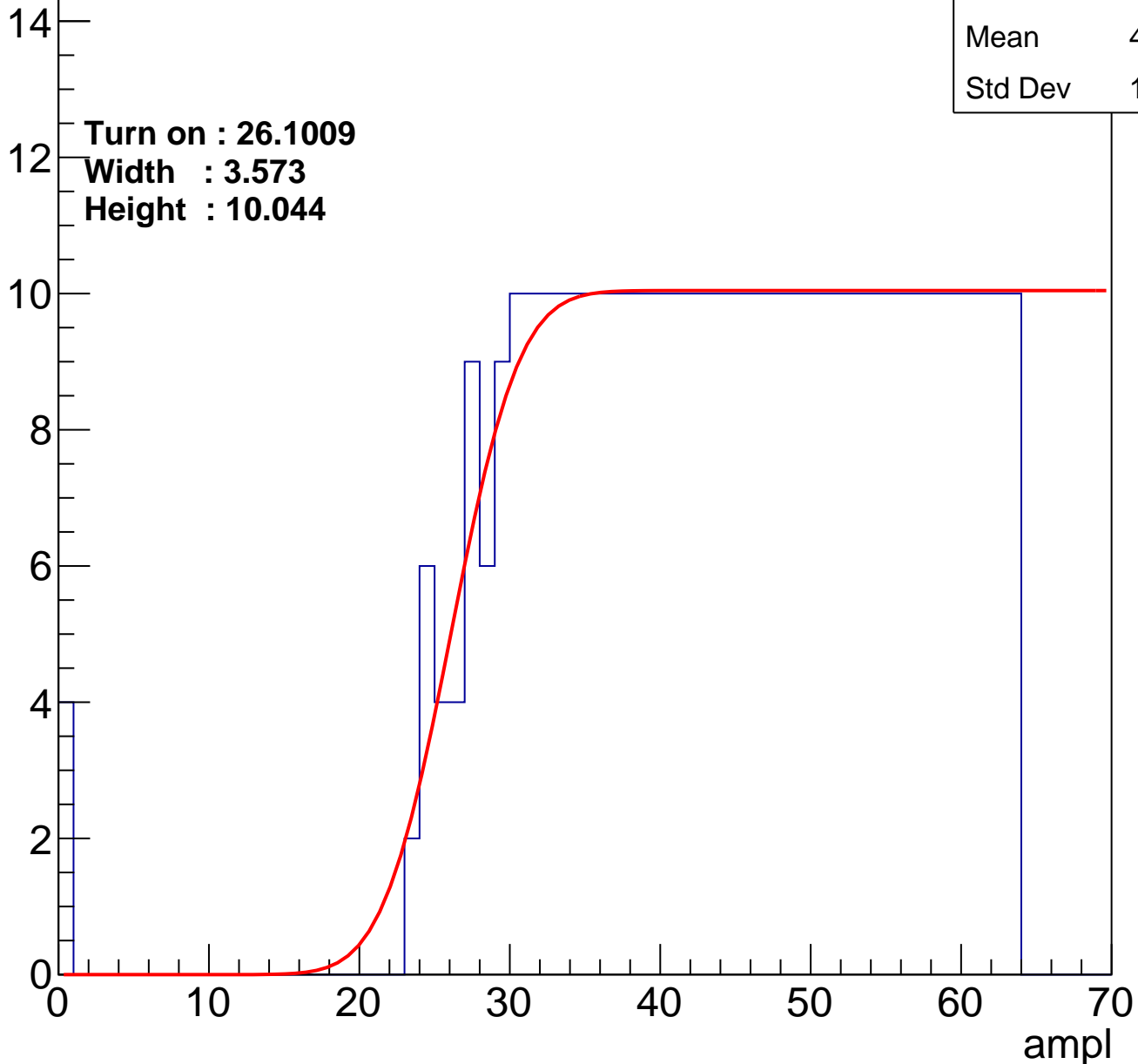
|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 43.95 |
| Std Dev | 11.94 |

Turn on : 26.1009

Width : 3.573

Height : 10.044

Entry



# B1L003S, U24-ch60

calib\_packv5\_042523\_0143.root, FC#13, port D2

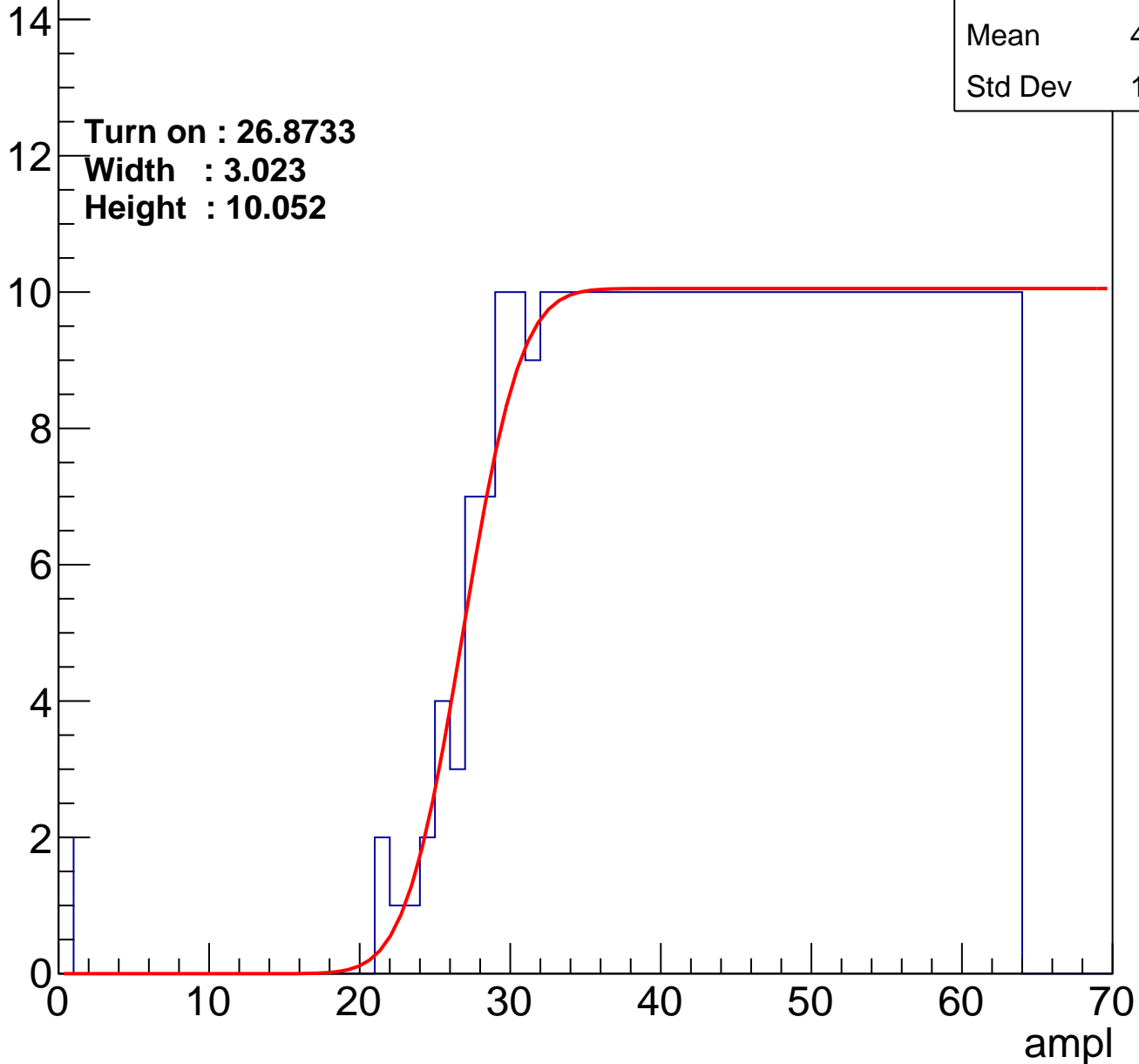
Entry

|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.36 |
| Std Dev | 11.47 |

Turn on : 26.8733

Width : 3.023

Height : 10.052



# B1L003S, U24-ch61

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.59 |
| Std Dev | 11.17 |

Turn on : 26.8229

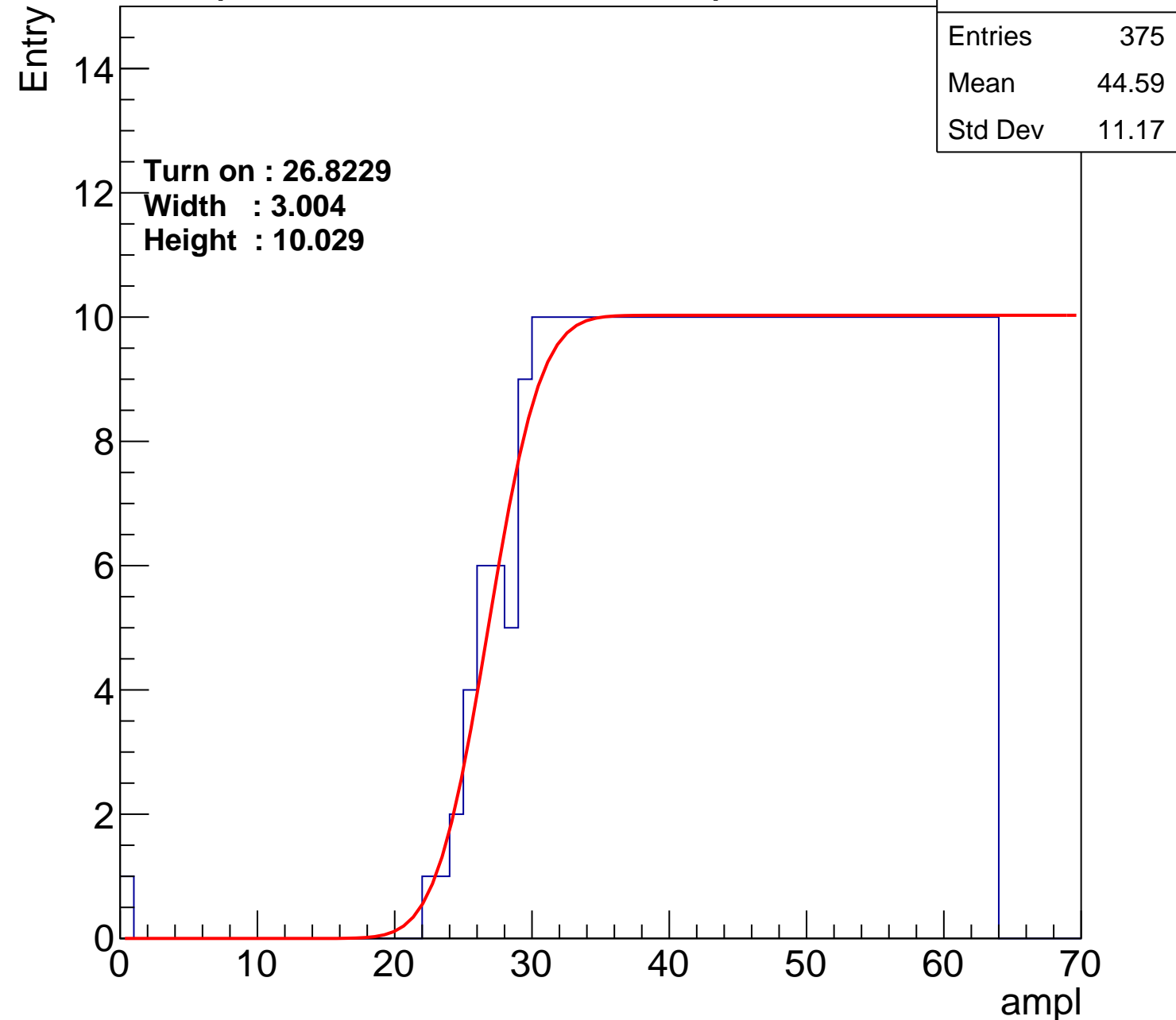
Width : 3.004

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch62

calib\_packv5\_042523\_0143.root, FC#13, port D2

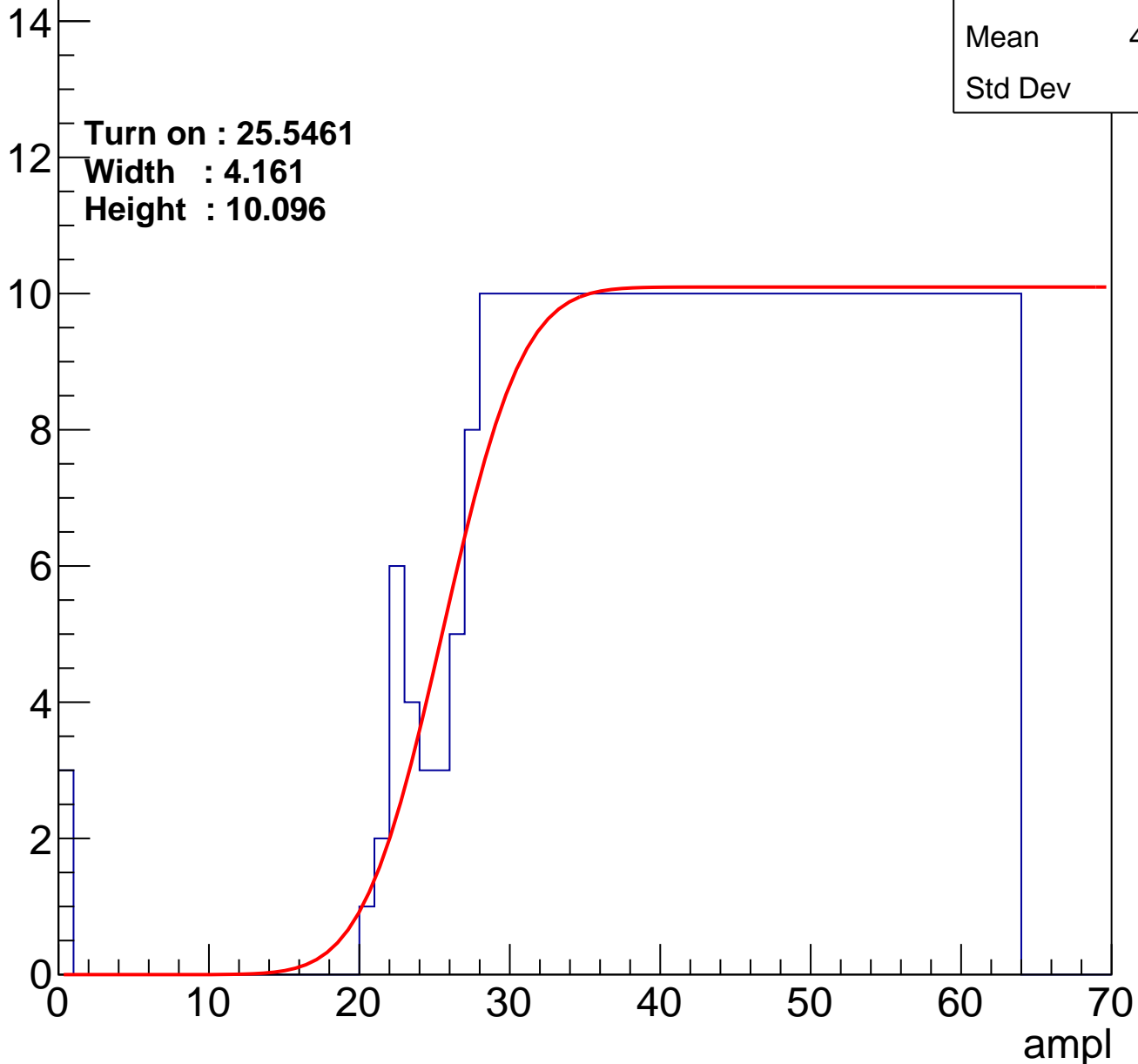
|         |       |
|---------|-------|
| Entries | 395   |
| Mean    | 43.44 |
| Std Dev | 12.1  |

Turn on : 25.5461

Width : 4.161

Height : 10.096

Entry





# B1L003S, U24-ch63

calib\_packv5\_042523\_0143.root, FC#13, port D2

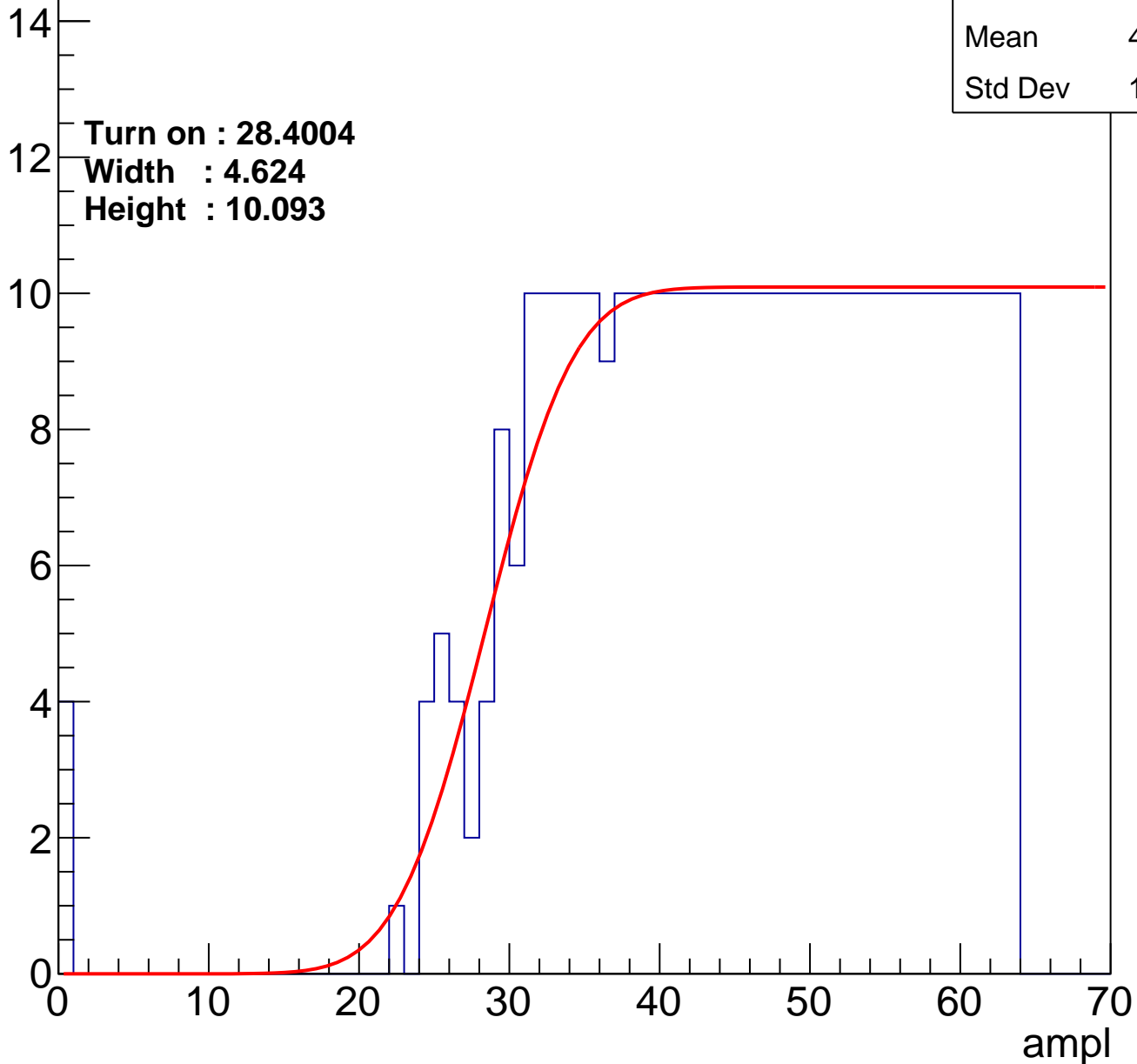
|         |       |
|---------|-------|
| Entries | 367   |
| Mean    | 44.68 |
| Std Dev | 11.69 |

Turn on : 28.4004

Width : 4.624

Height : 10.093

Entry



# B1L003S, U24-ch64

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.89 |
| Std Dev | 11.06 |

Turn on : 27.2783

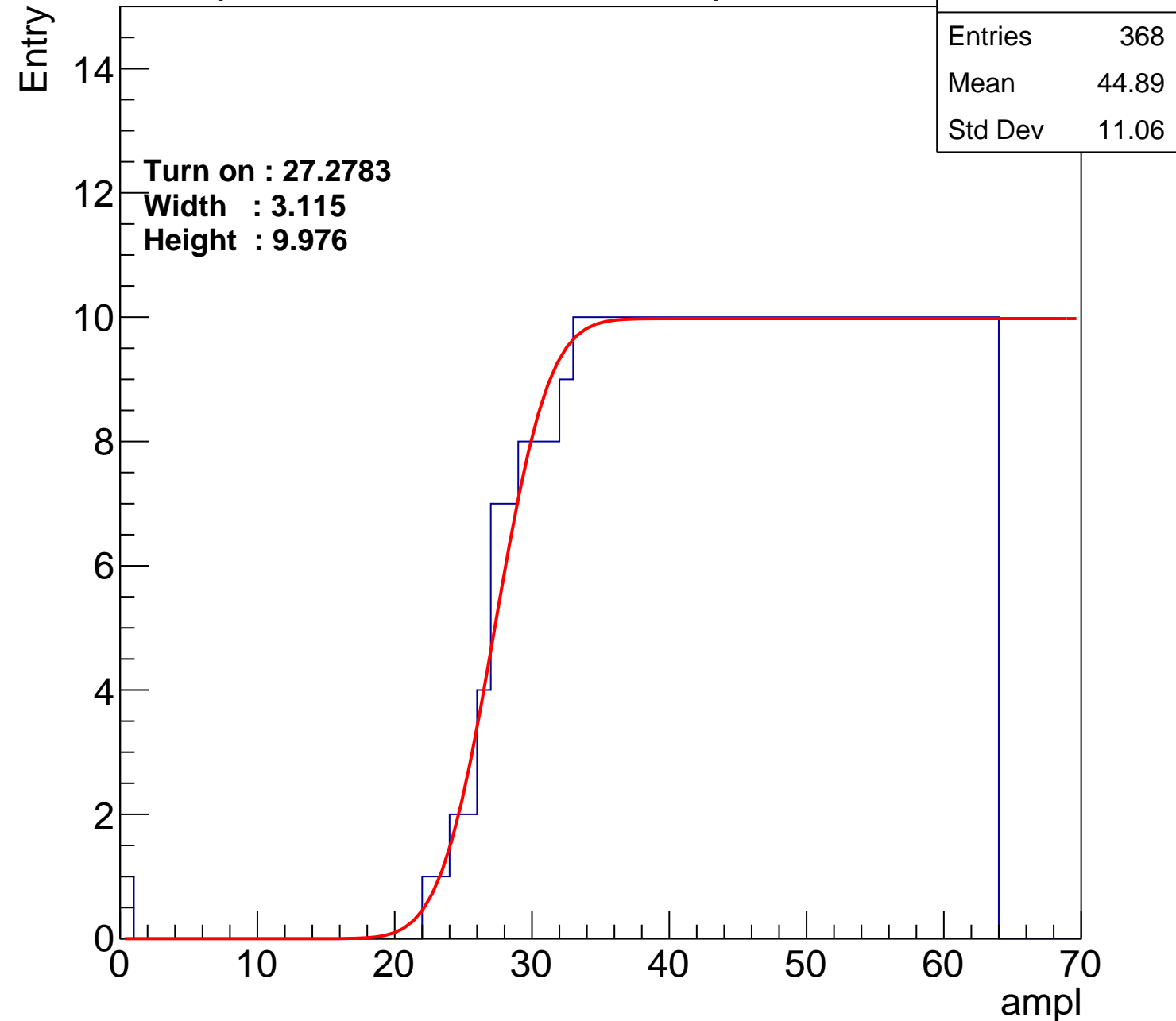
Width : 3.115

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

Turn on : 28.2999  
Width : 2.352  
Height : 10.030



# B1L003S, U24-ch66

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.44 |
| Std Dev | 11.58 |

Turn on : 26.6502

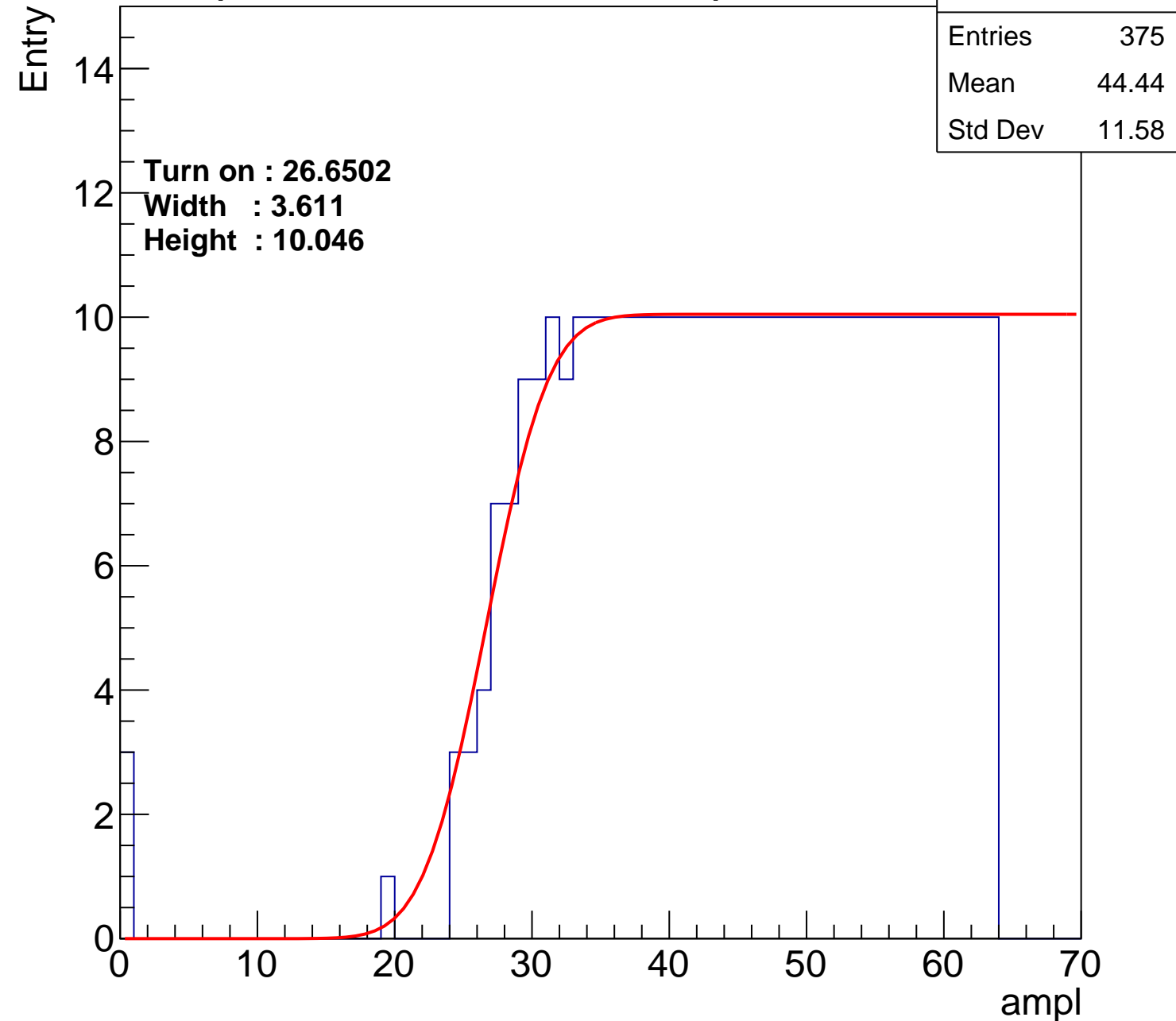
Width : 3.611

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch67

calib\_packv5\_042523\_0143.root, FC#13, port D2

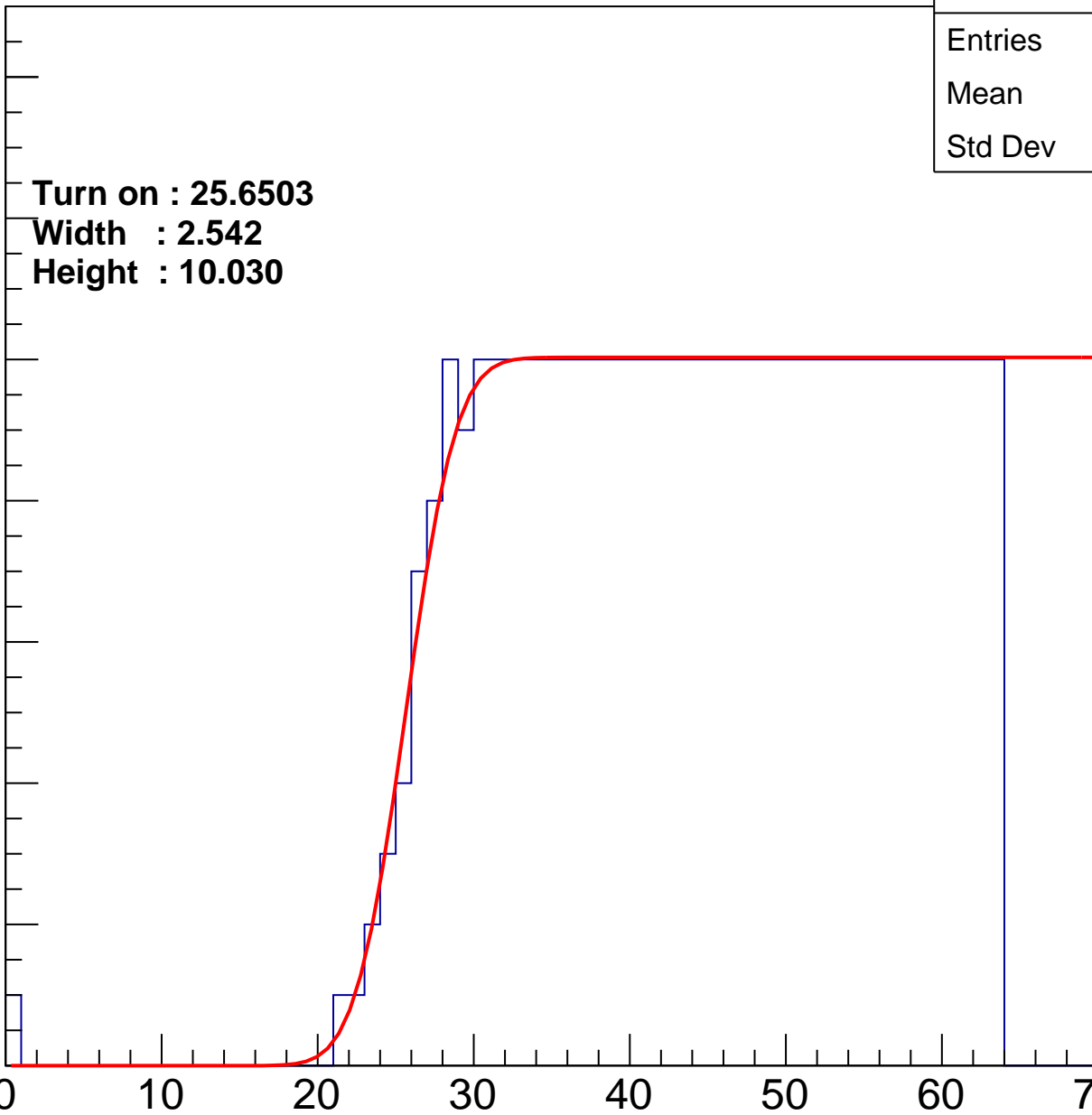
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.6503  
Width : 2.542  
Height : 10.030

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 44.07 |
| Std Dev | 11.44 |

ampl

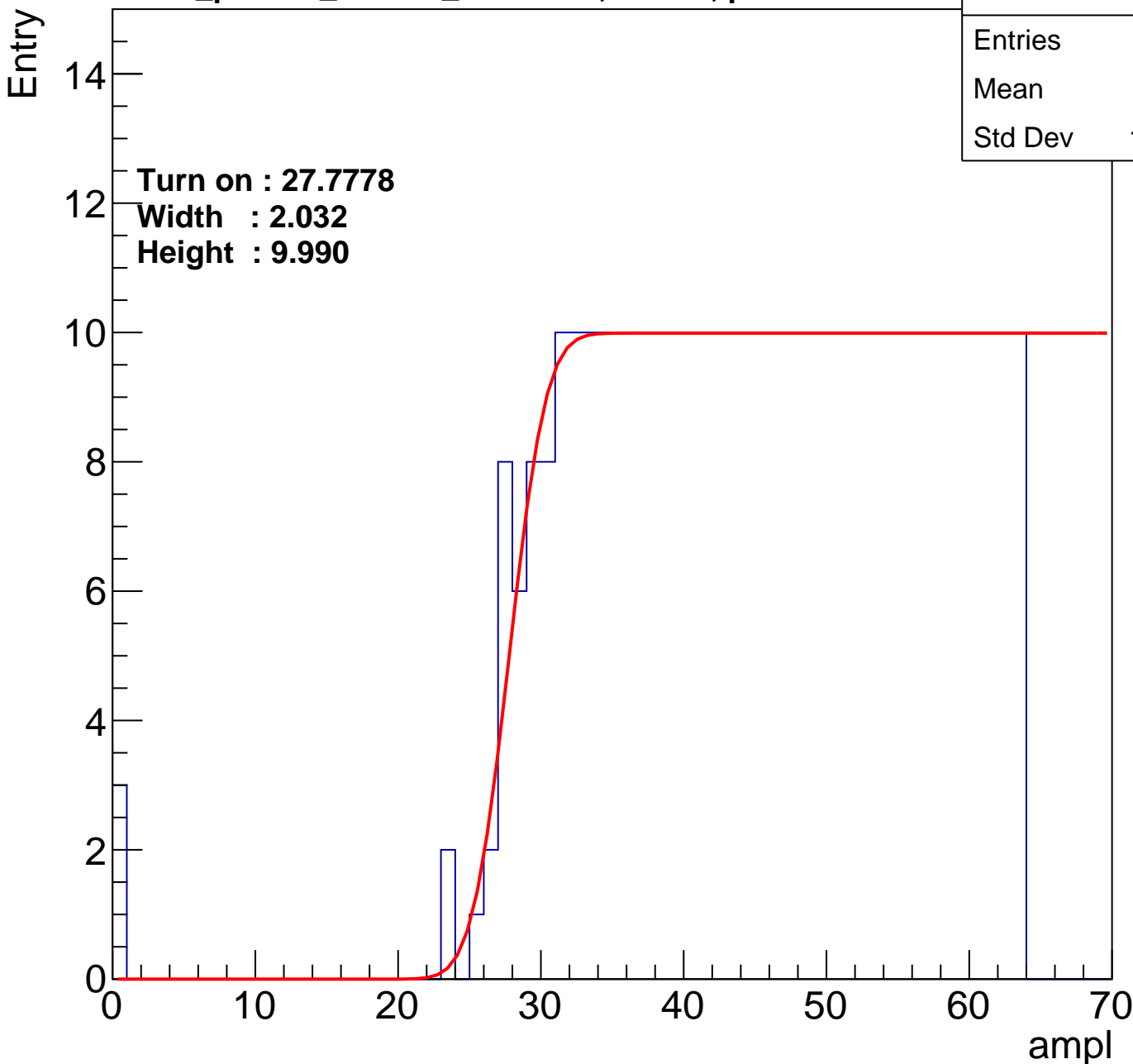


**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.81 |
| Std Dev | 11.37 |

**Height : 9.990**



# B1L003S, U24-ch69

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 43.99 |
| Std Dev | 11.92 |

Turn on : 26.0702

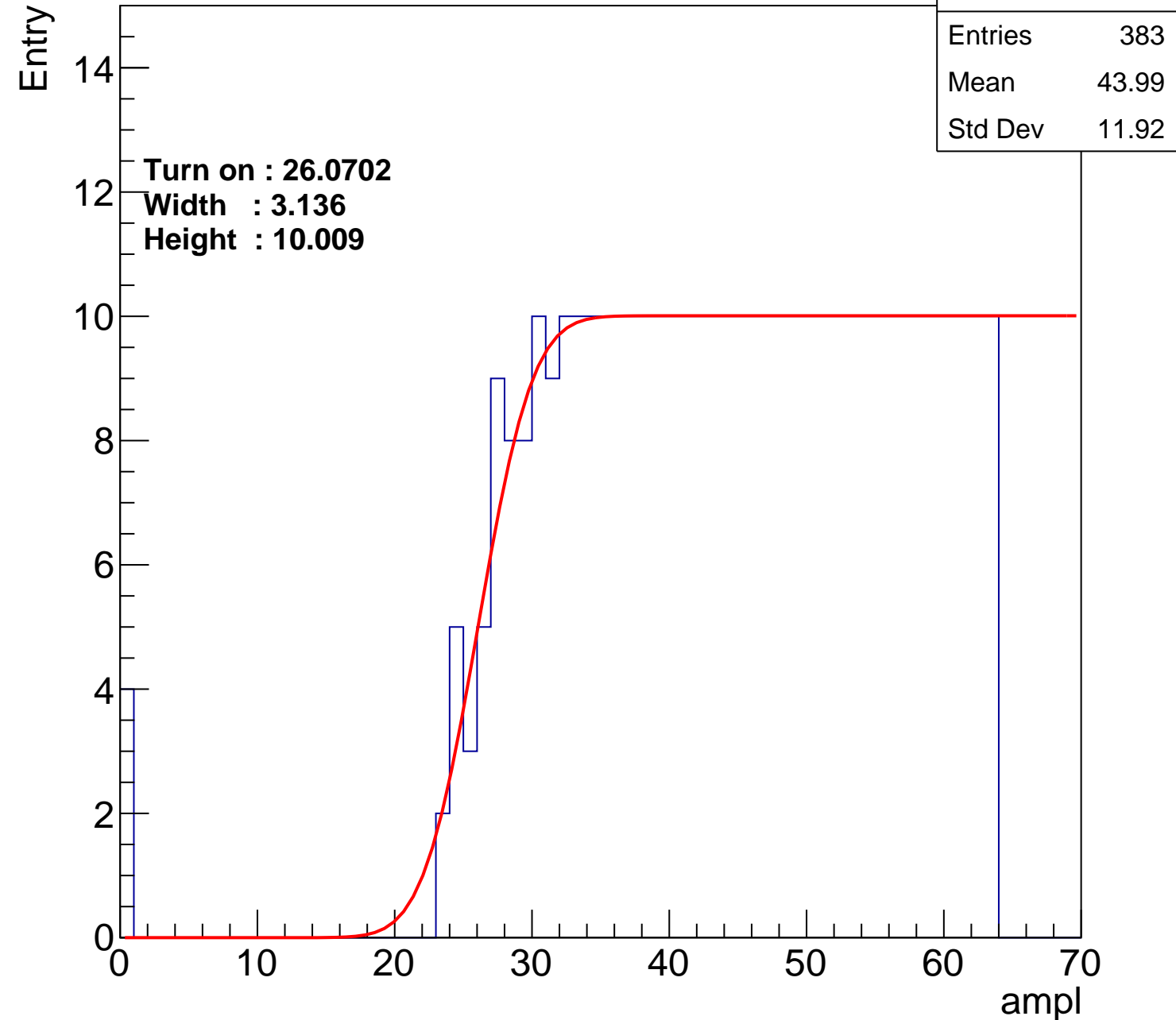
Width : 3.136

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch70

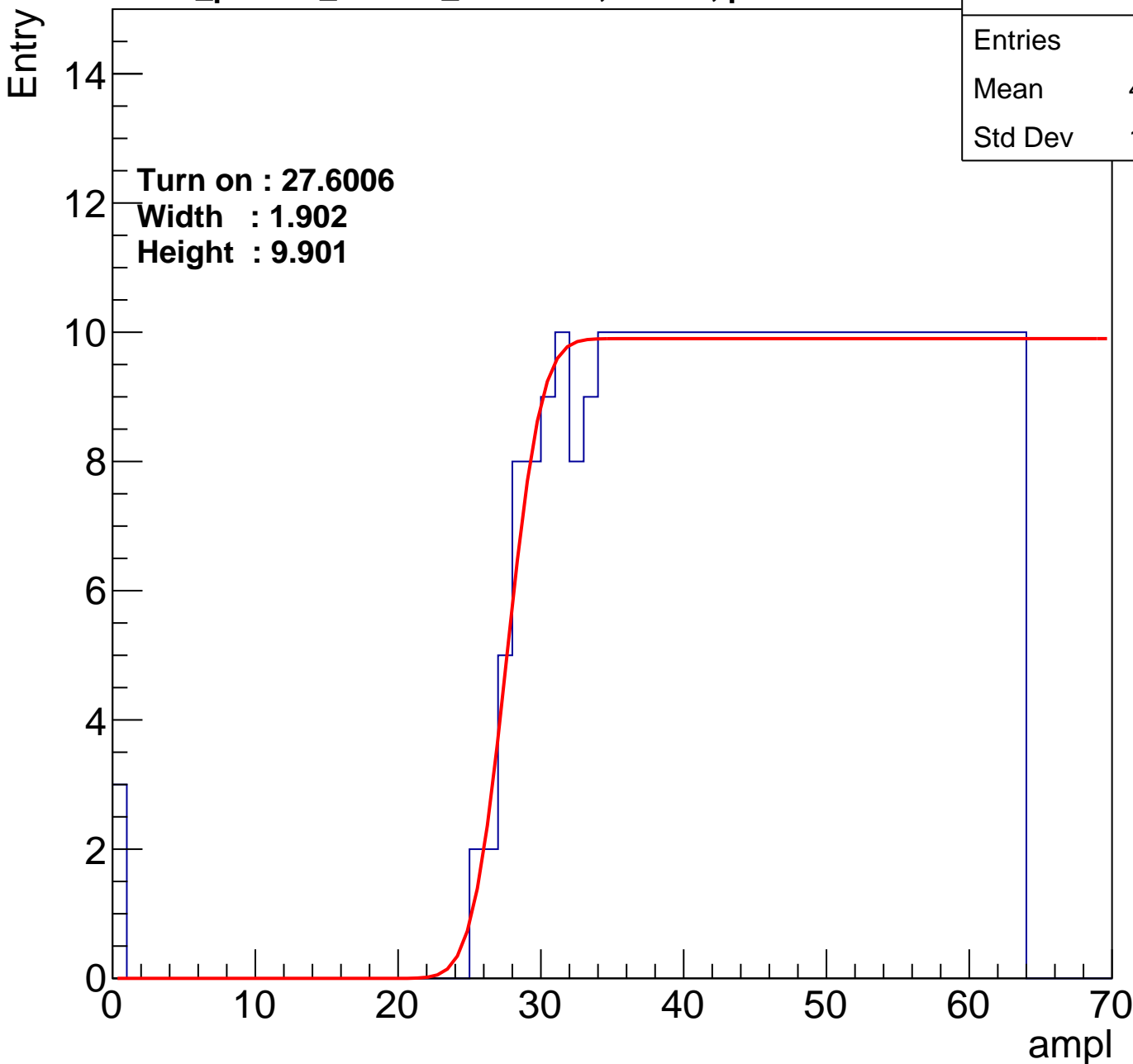
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 44.99 |
| Std Dev | 11.29 |

**Turn on : 27.6006**

**Width : 1.902**

**Height : 9.901**





# B1L003S, U24-ch71

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.84 |
| Std Dev | 11.03 |

Turn on : 26.9265

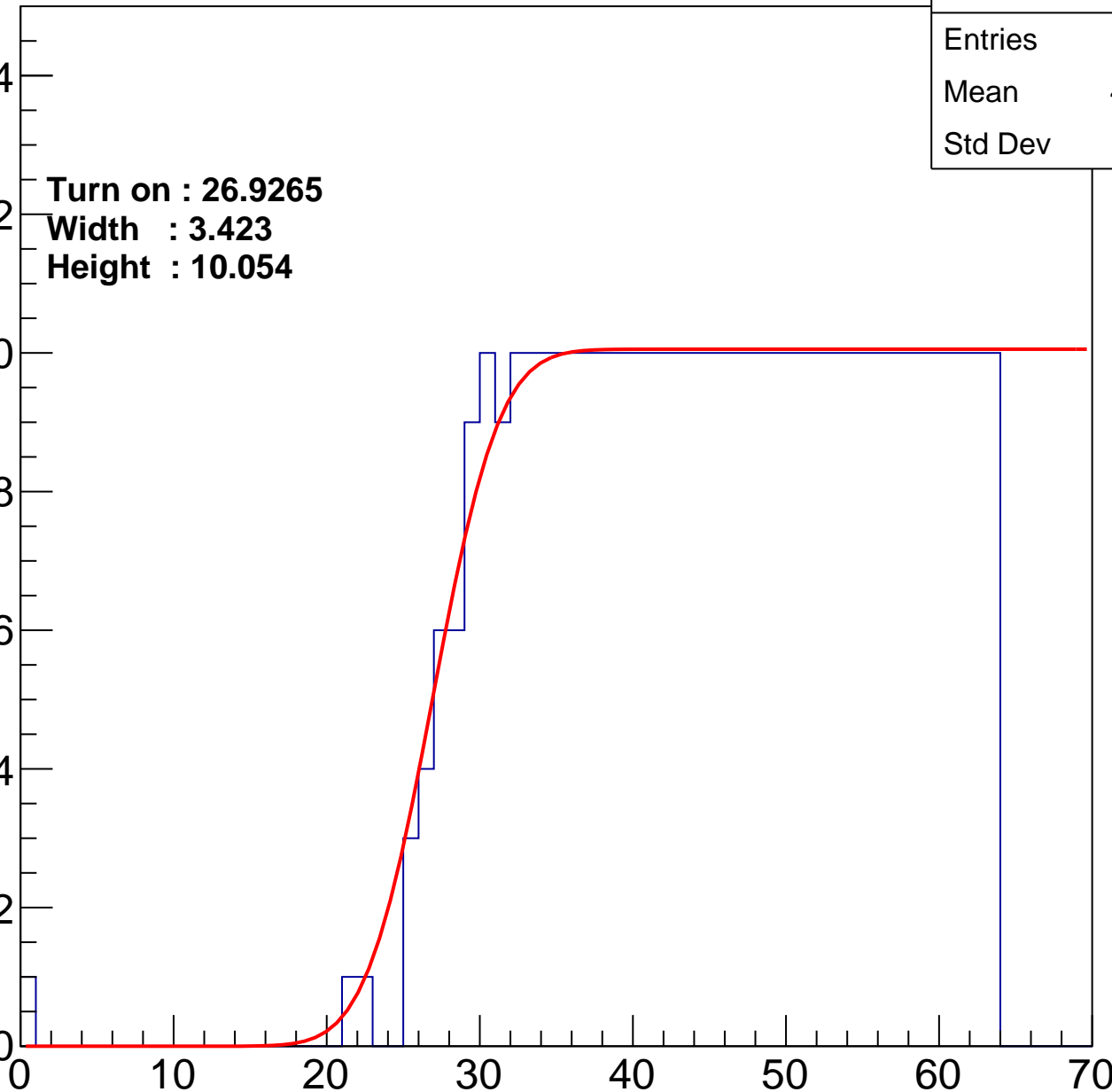
Width : 3.423

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch72

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 379   |
| Mean    | 44.23 |
| Std Dev | 11.7  |

Turn on : 27.3149

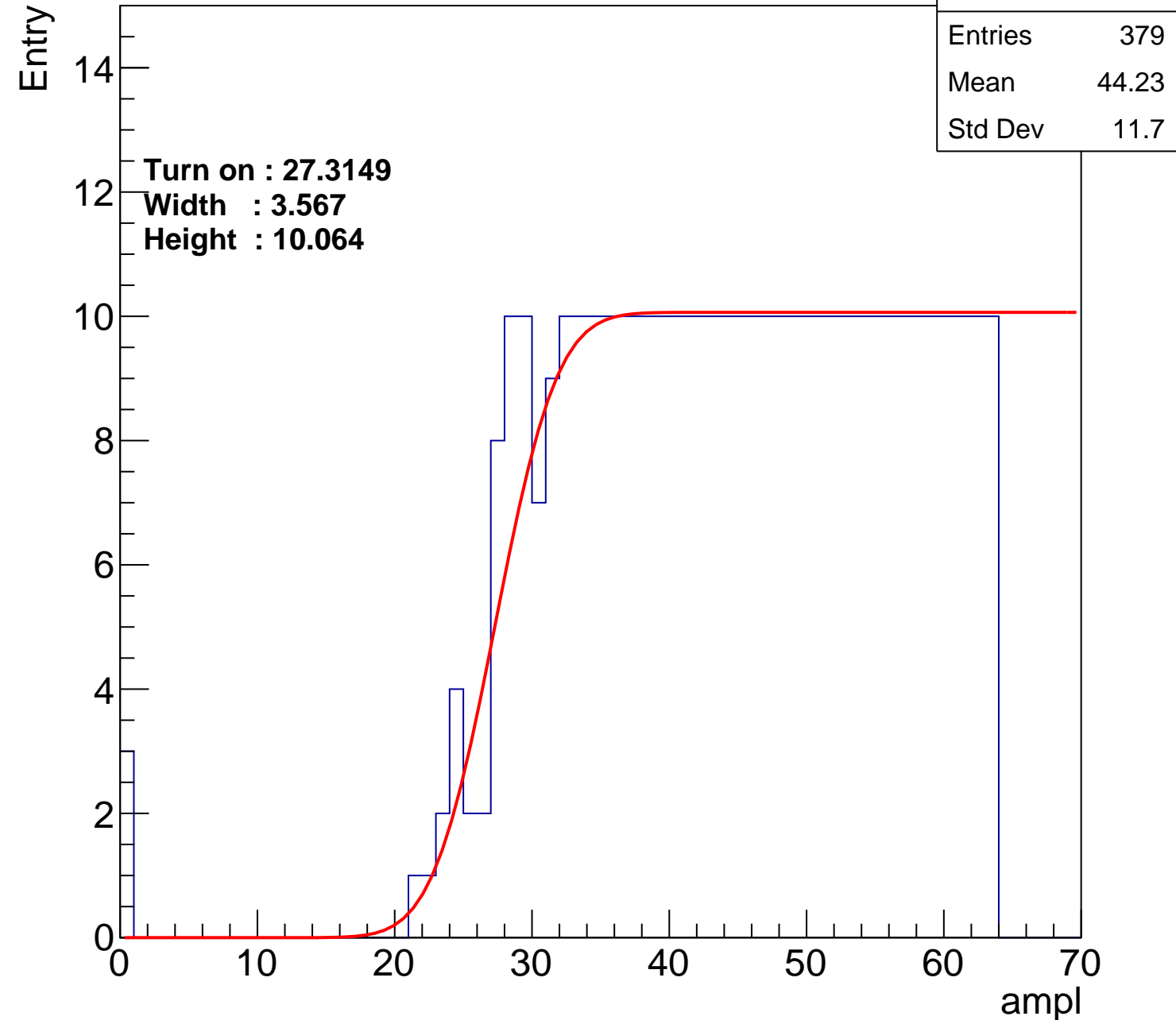
Width : 3.567

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch73

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.14 |
| Std Dev | 11.42 |

Turn on : 28.7868

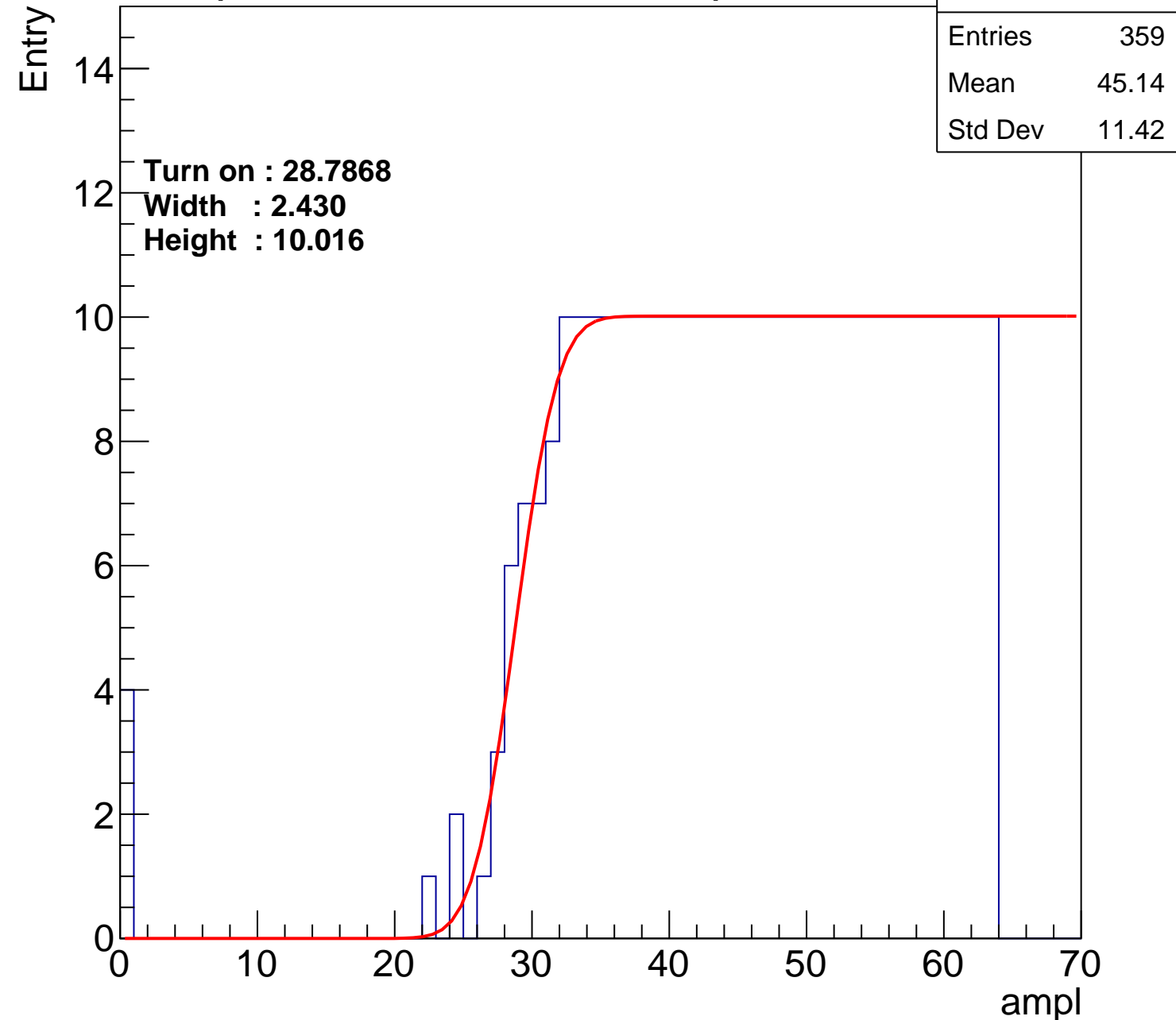
Width : 2.430

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch74

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 352   |
| Mean    | 45.62 |
| Std Dev | 10.84 |

Turn on : 29.8158

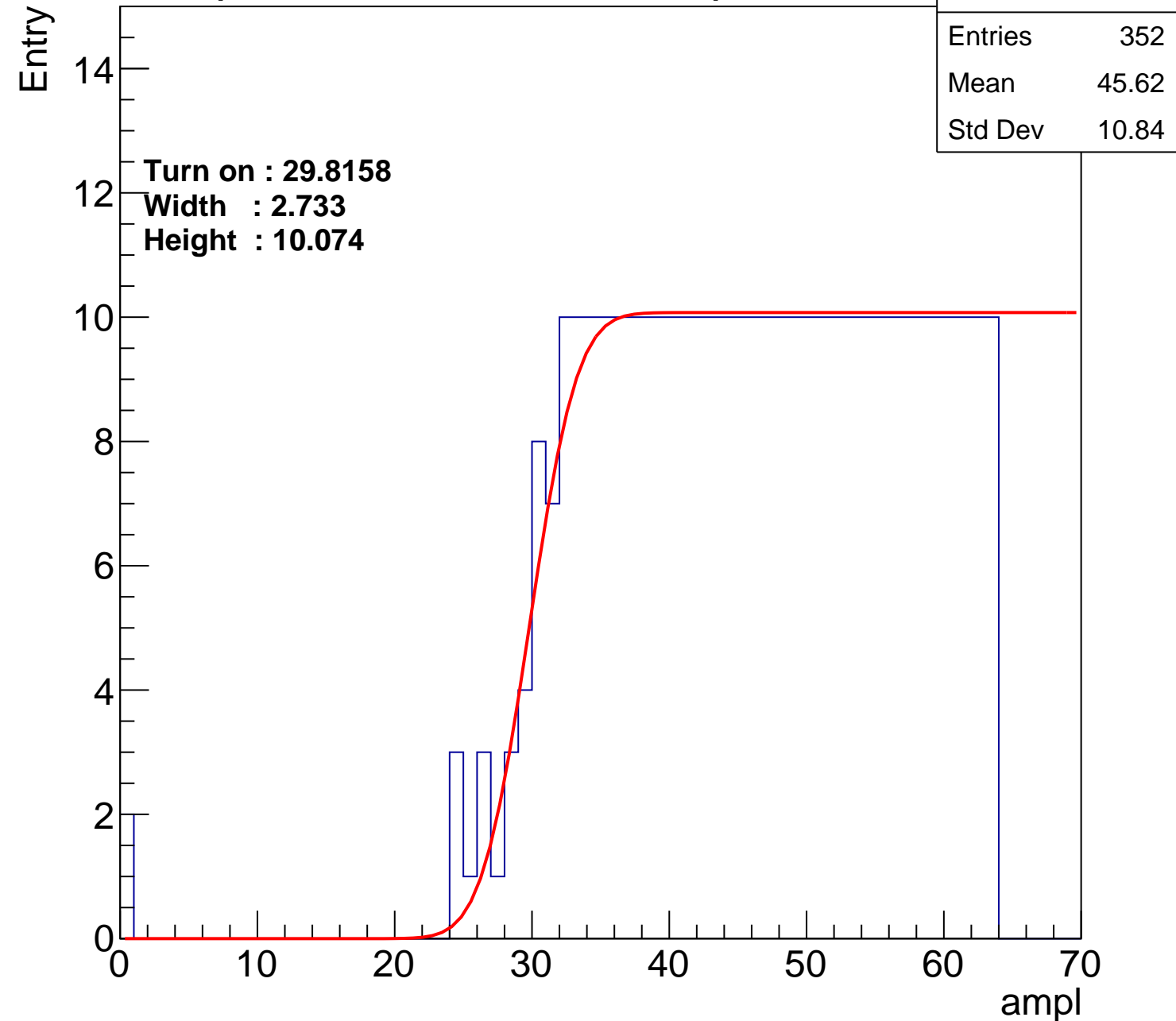
Width : 2.733

Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch75

calib\_packv5\_042523\_0143.root, FC#13, port D2

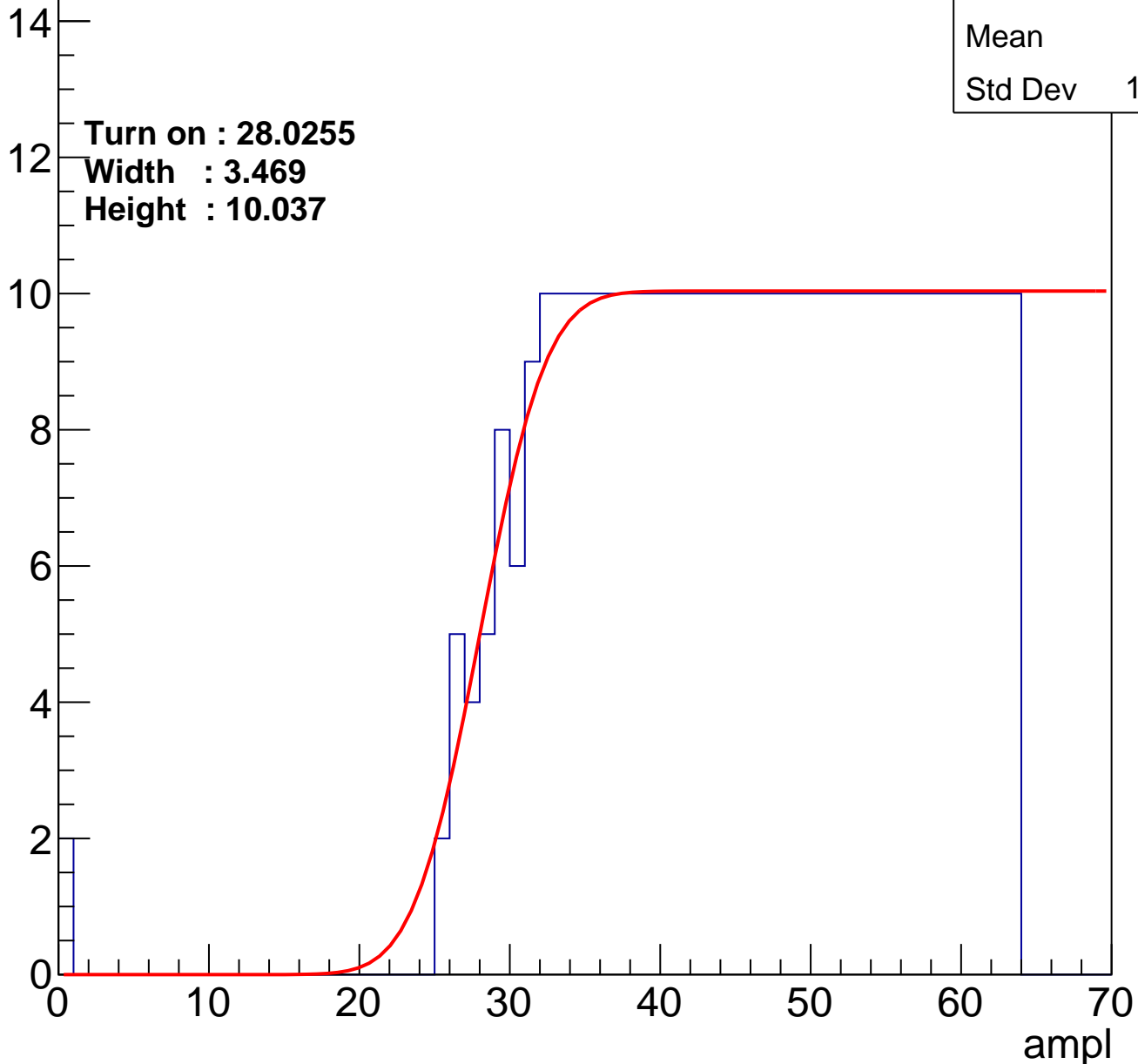
|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.2  |
| Std Dev | 11.02 |

Turn on : 28.0255

Width : 3.469

Height : 10.037

Entry



# B1L003S, U24-ch76

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.65 |
| Std Dev | 11.96 |

Turn on : 28.2563

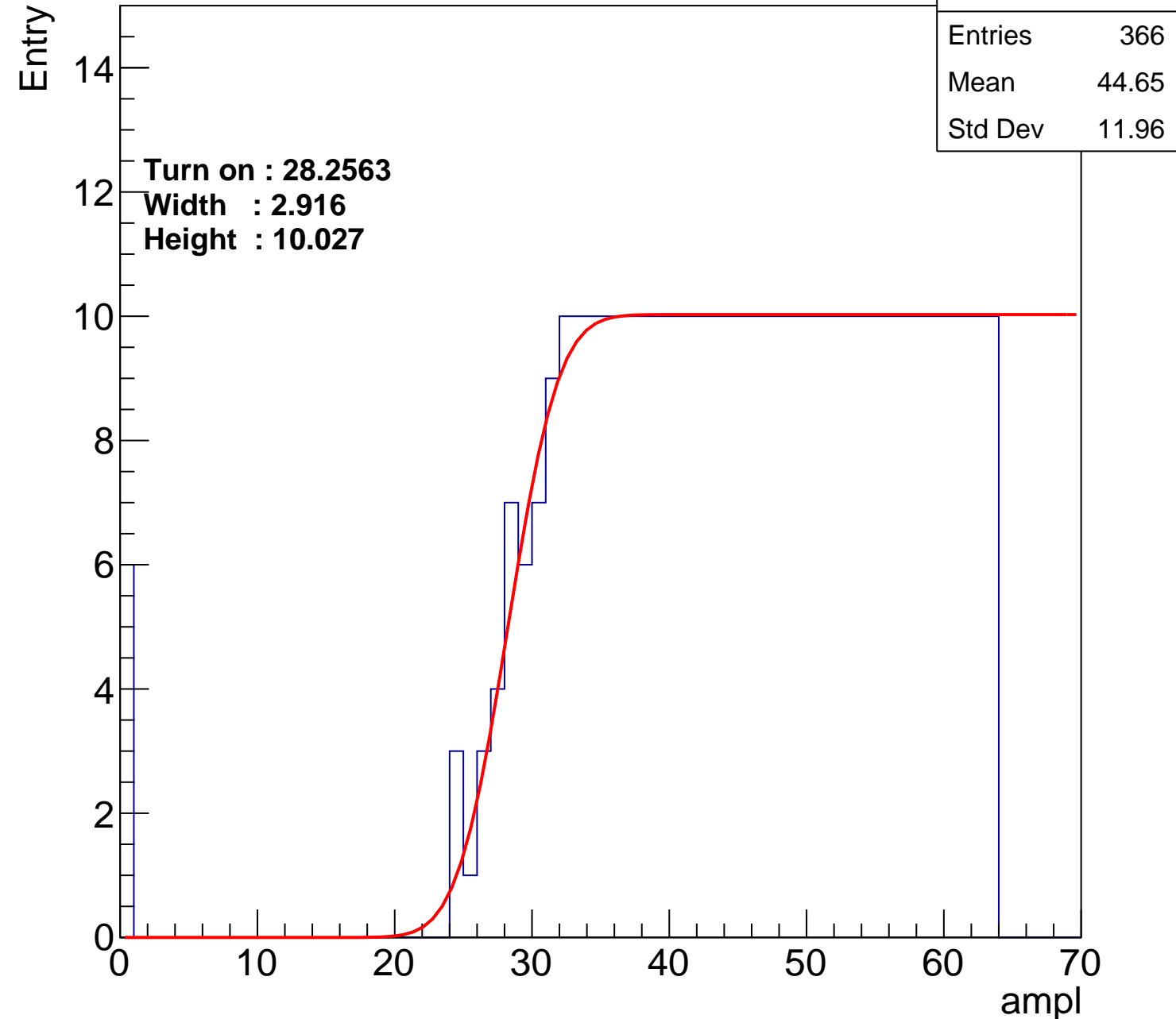
Width : 2.916

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch77

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 44.02 |
| Std Dev | 11.77 |

Turn on : 26.6756

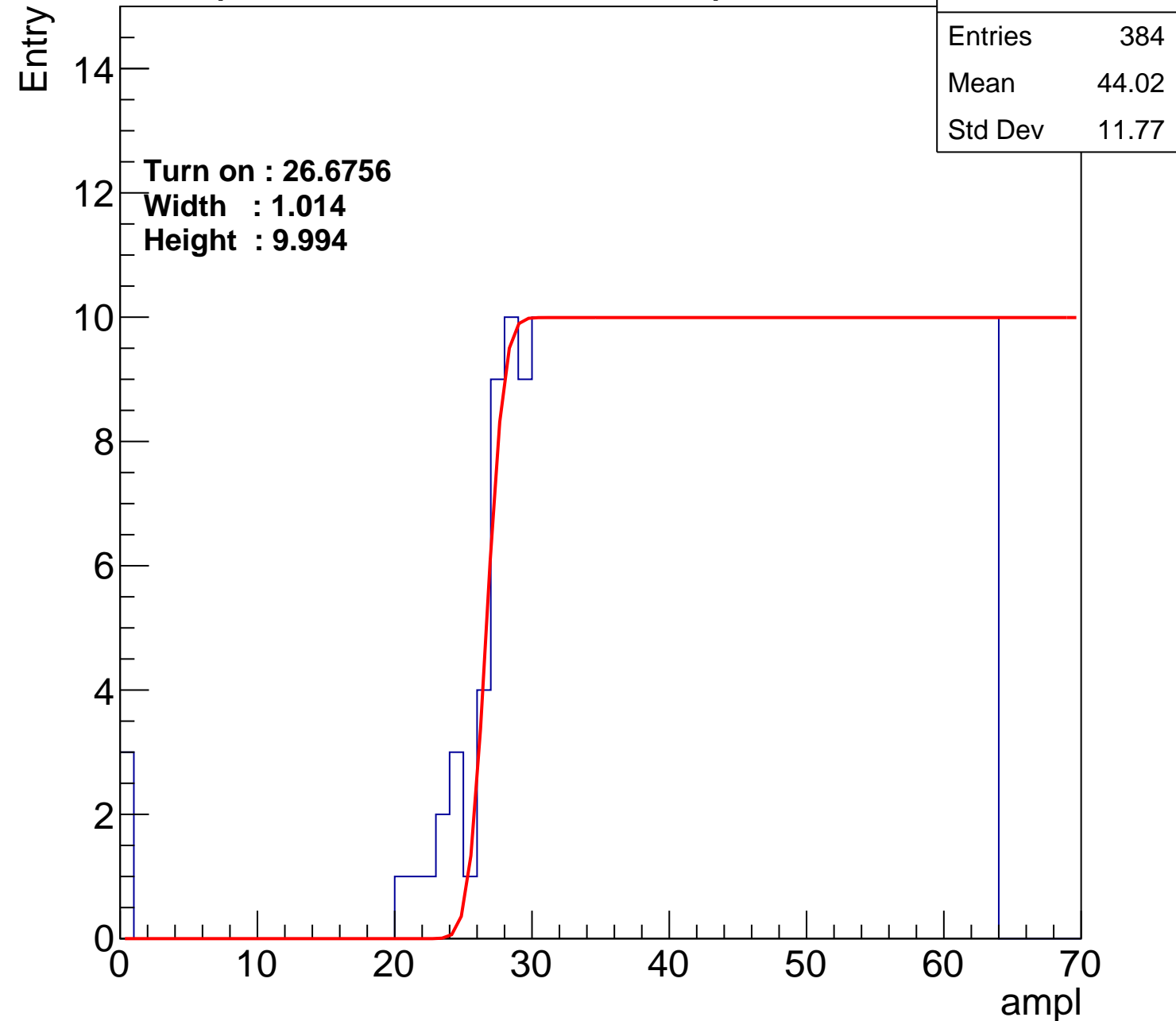
Width : 1.014

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch78

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.34 |
| Std Dev | 11.62 |

Turn on : 26.8192

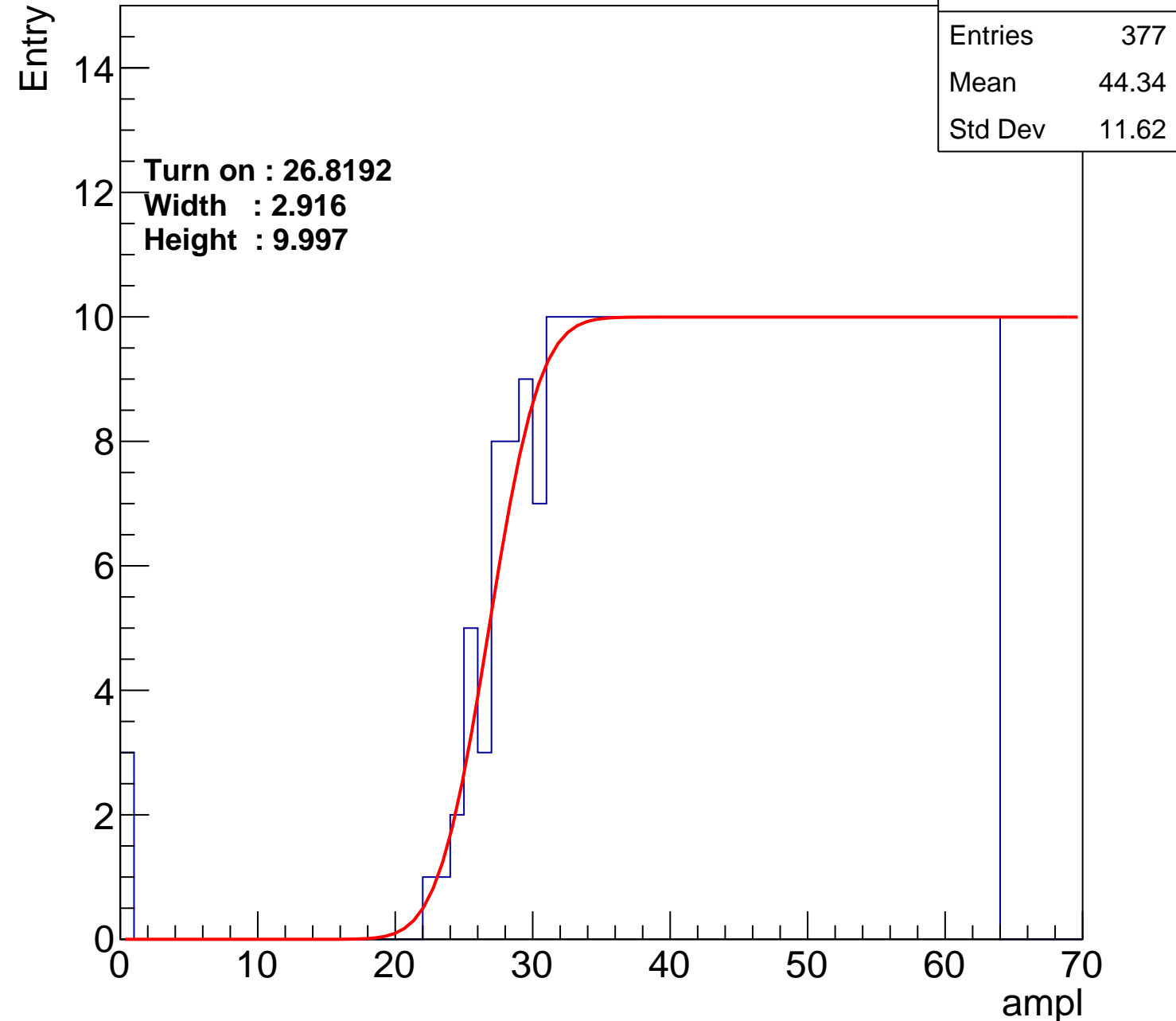
Width : 2.916

Height : 9.997

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch79

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.14 |
| Std Dev | 11.41 |

Turn on : 28.7891

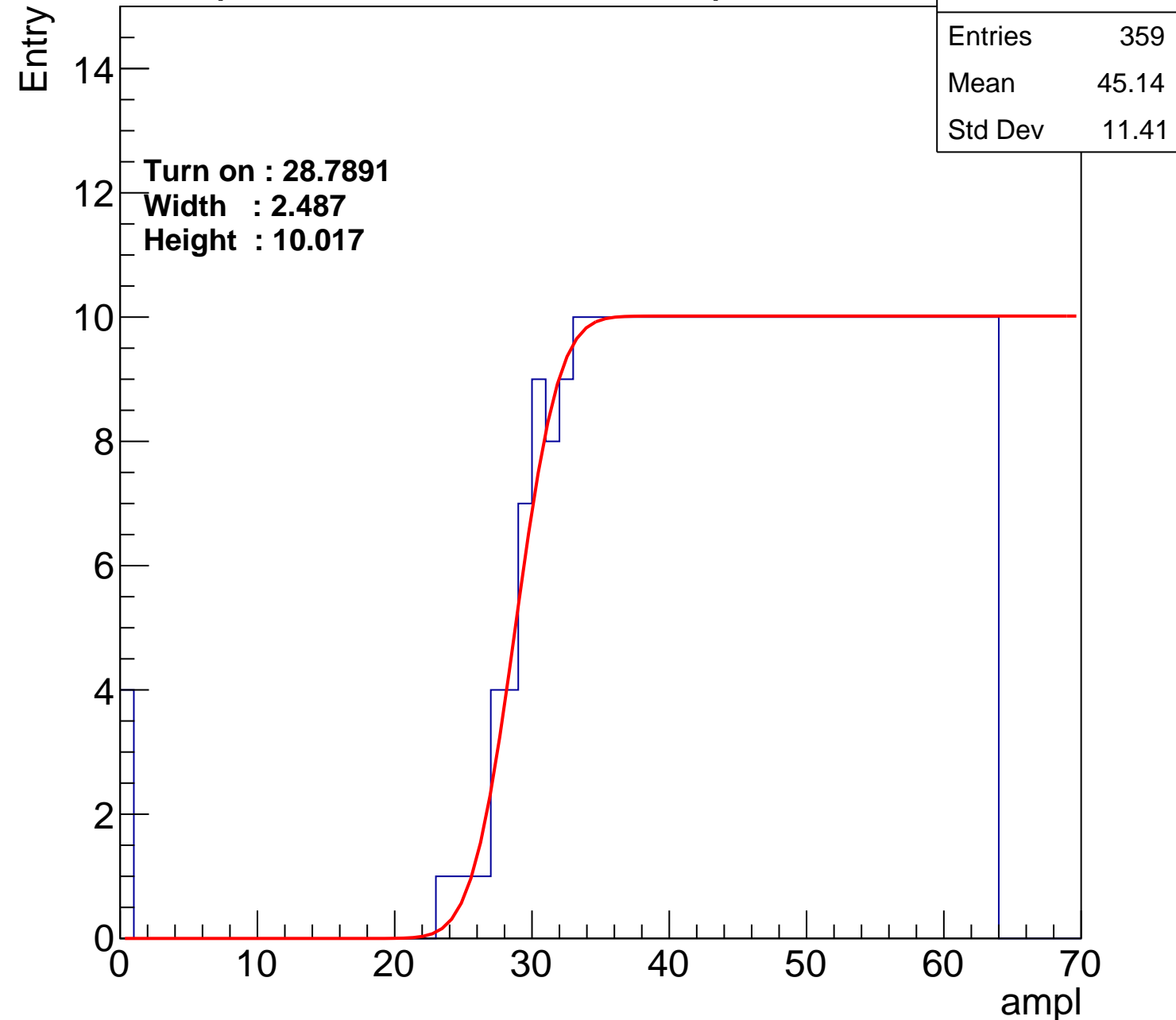
Width : 2.487

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch80

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 44.98 |
| Std Dev | 11.26 |

Turn on : 27.9798

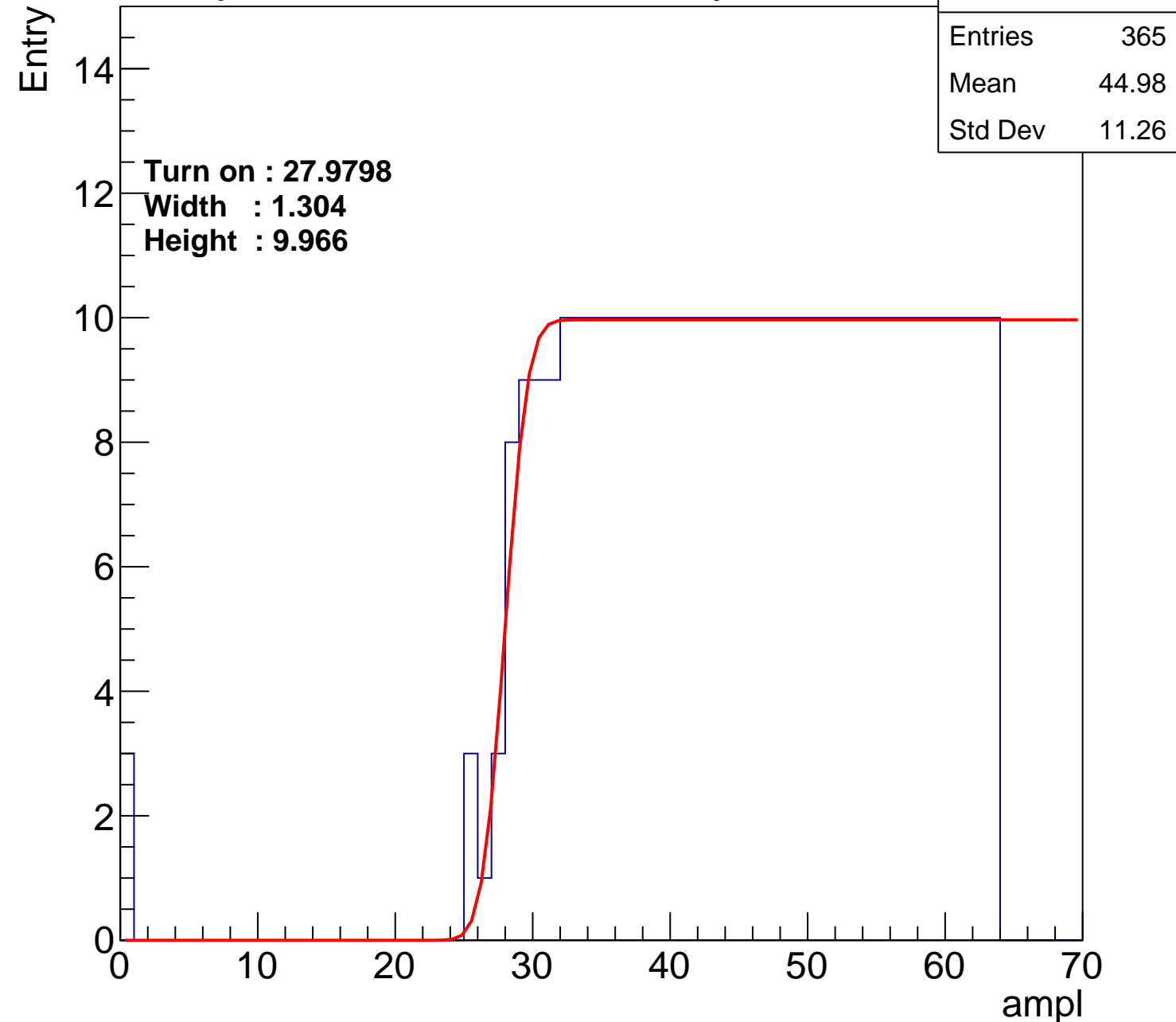
Width : 1.304

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch81

calib\_packv5\_042523\_0143.root, FC#13, port D2

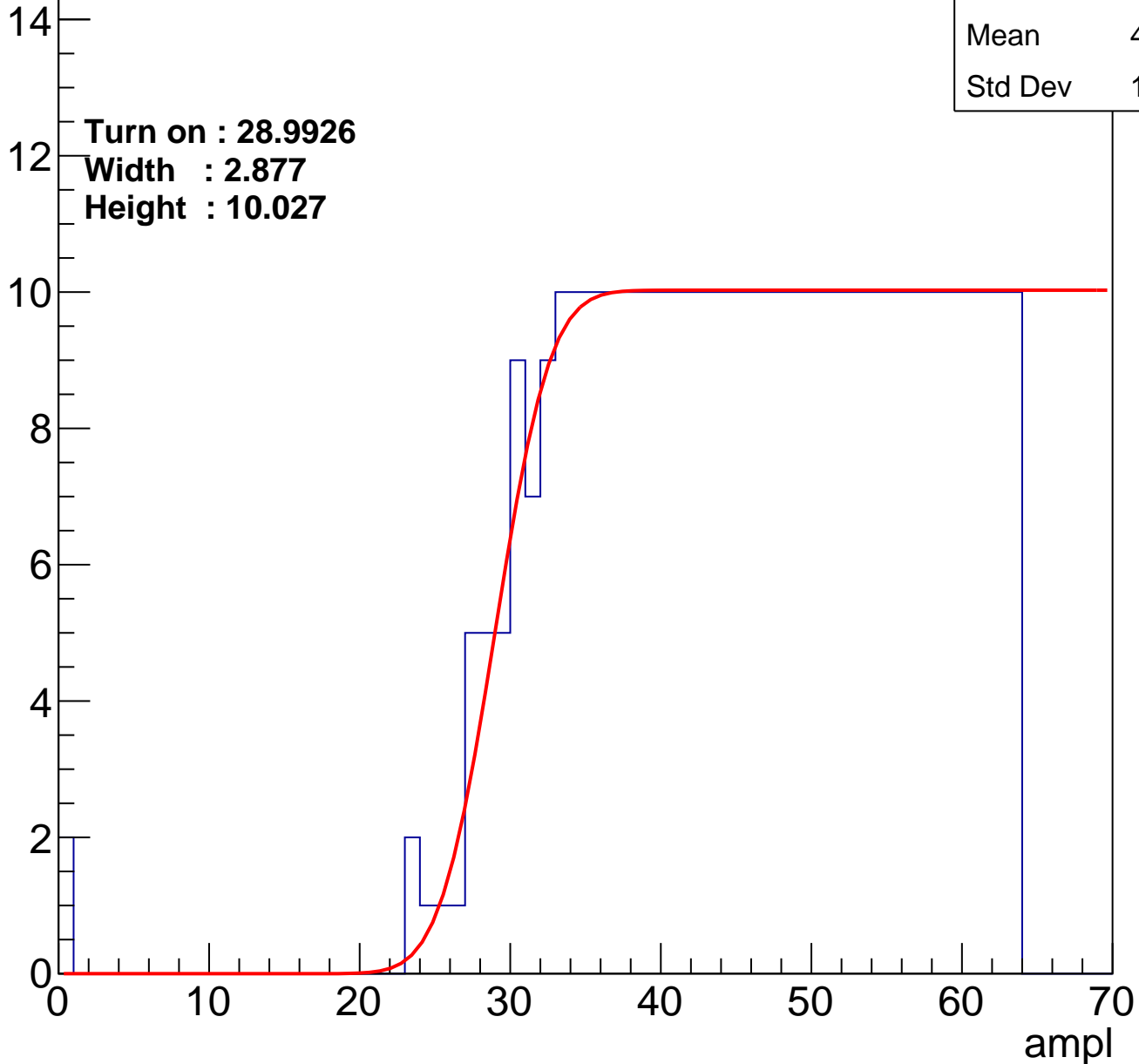
|         |       |
|---------|-------|
| Entries | 357   |
| Mean    | 45.37 |
| Std Dev | 10.98 |

Turn on : 28.9926

Width : 2.877

Height : 10.027

Entry



# B1L003S, U24-ch82

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 45.09 |
| Std Dev | 10.89 |

Turn on : 27.8426

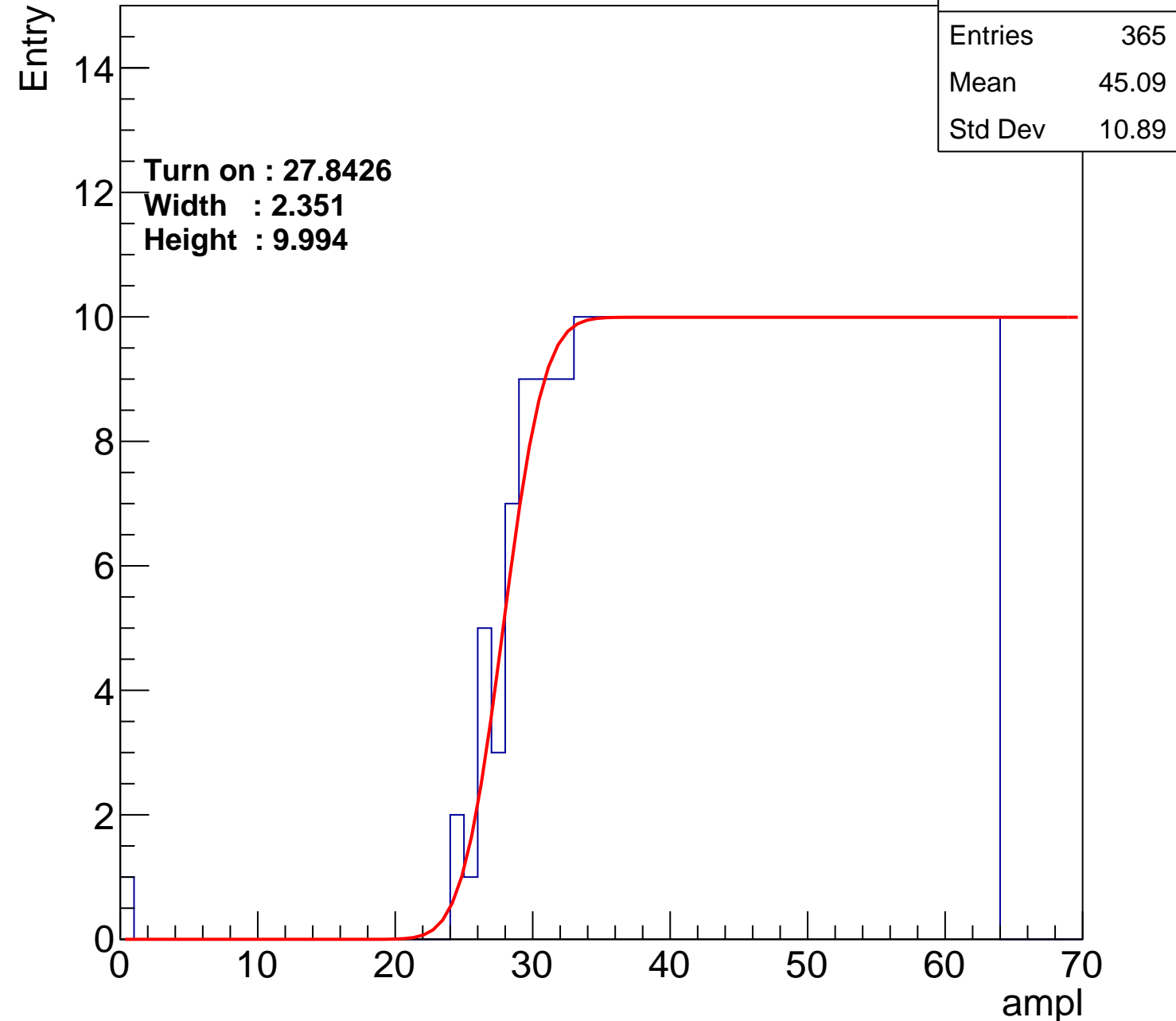
Width : 2.351

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch83

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.9  |
| Std Dev | 10.99 |

Turn on : 27.4382

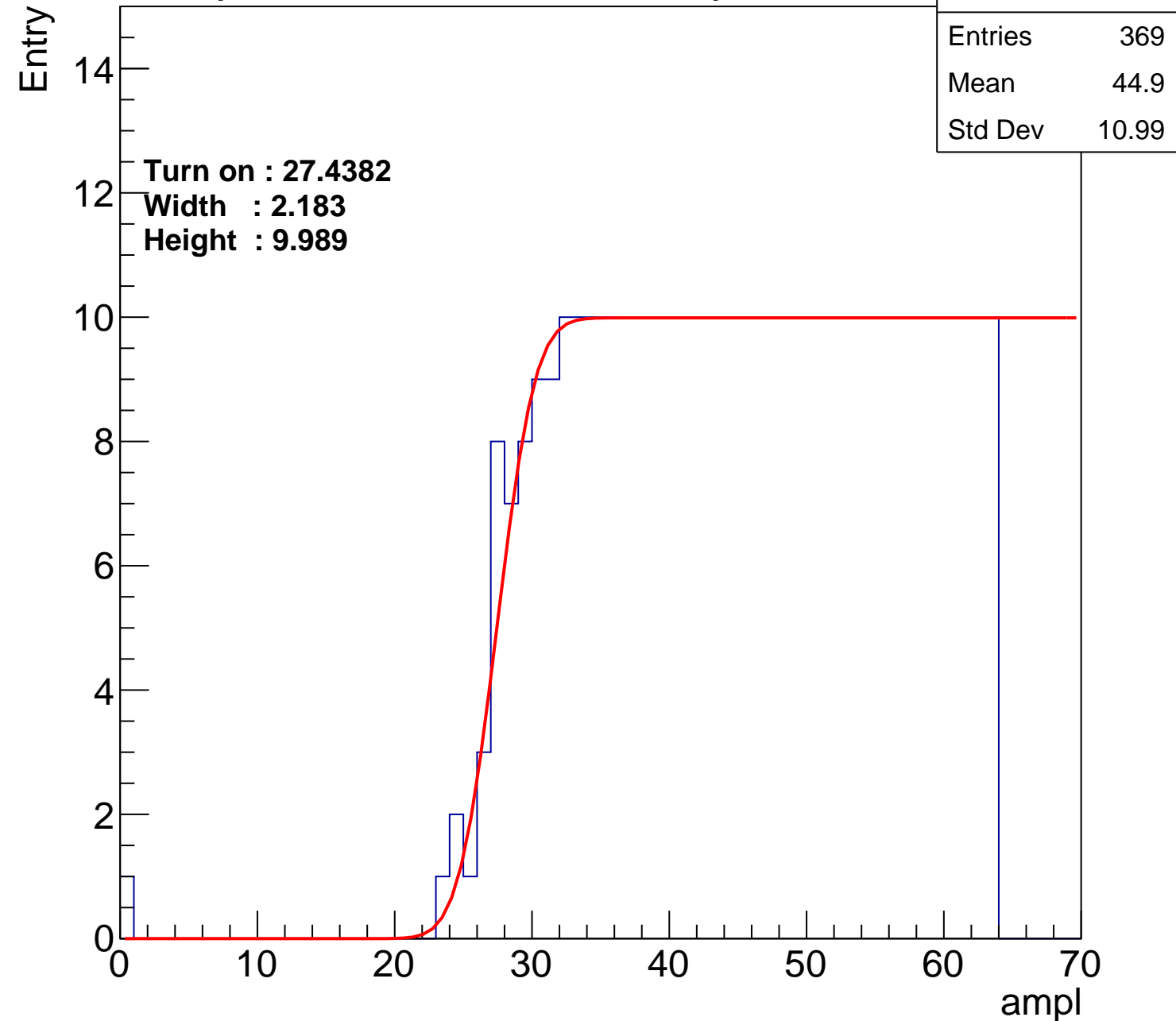
Width : 2.183

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch84

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.6  |
| Std Dev | 11.35 |

Turn on : 26.9217

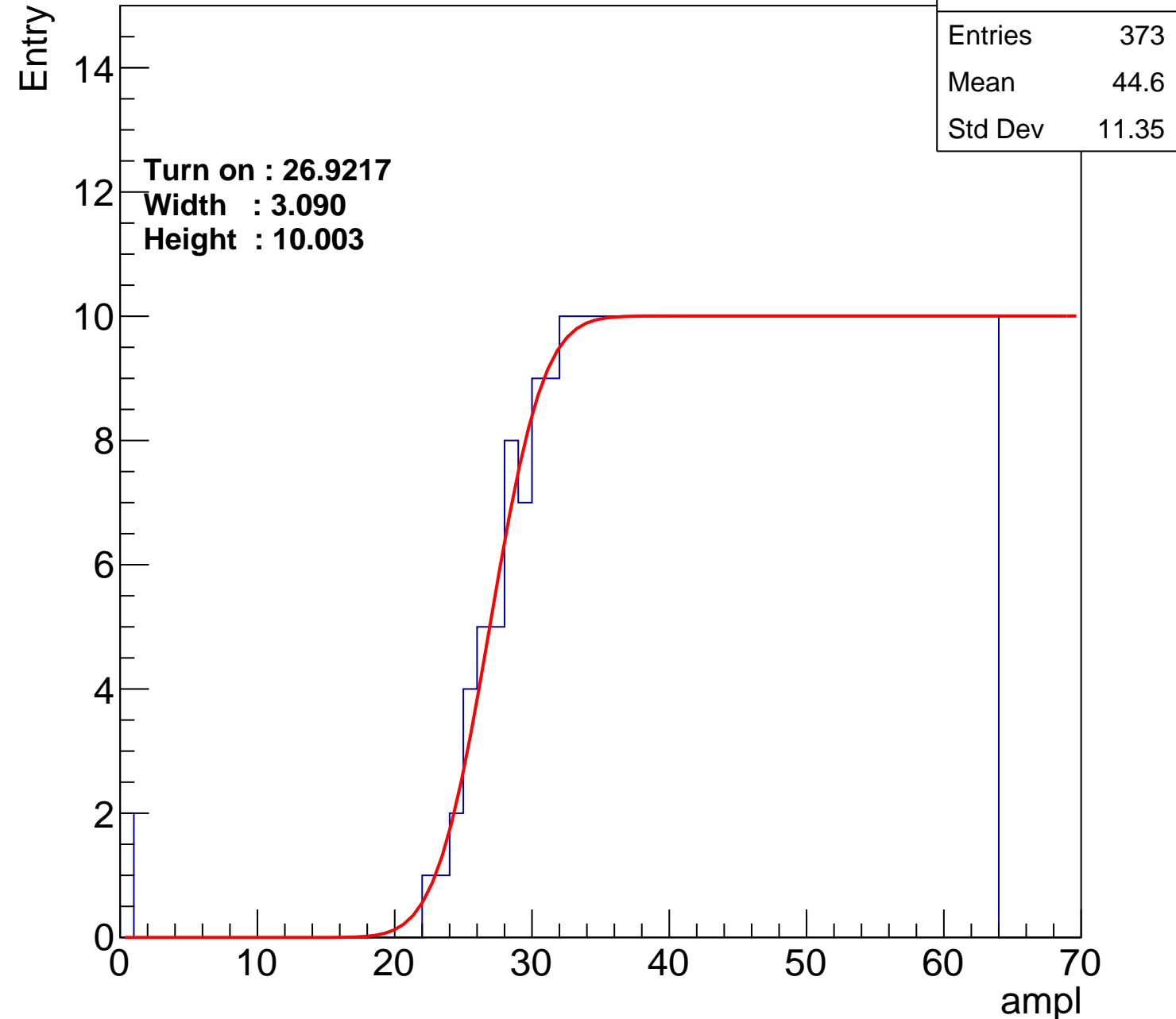
Width : 3.090

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch85

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.86 |
| Std Dev | 11.06 |

Turn on : 27.1469

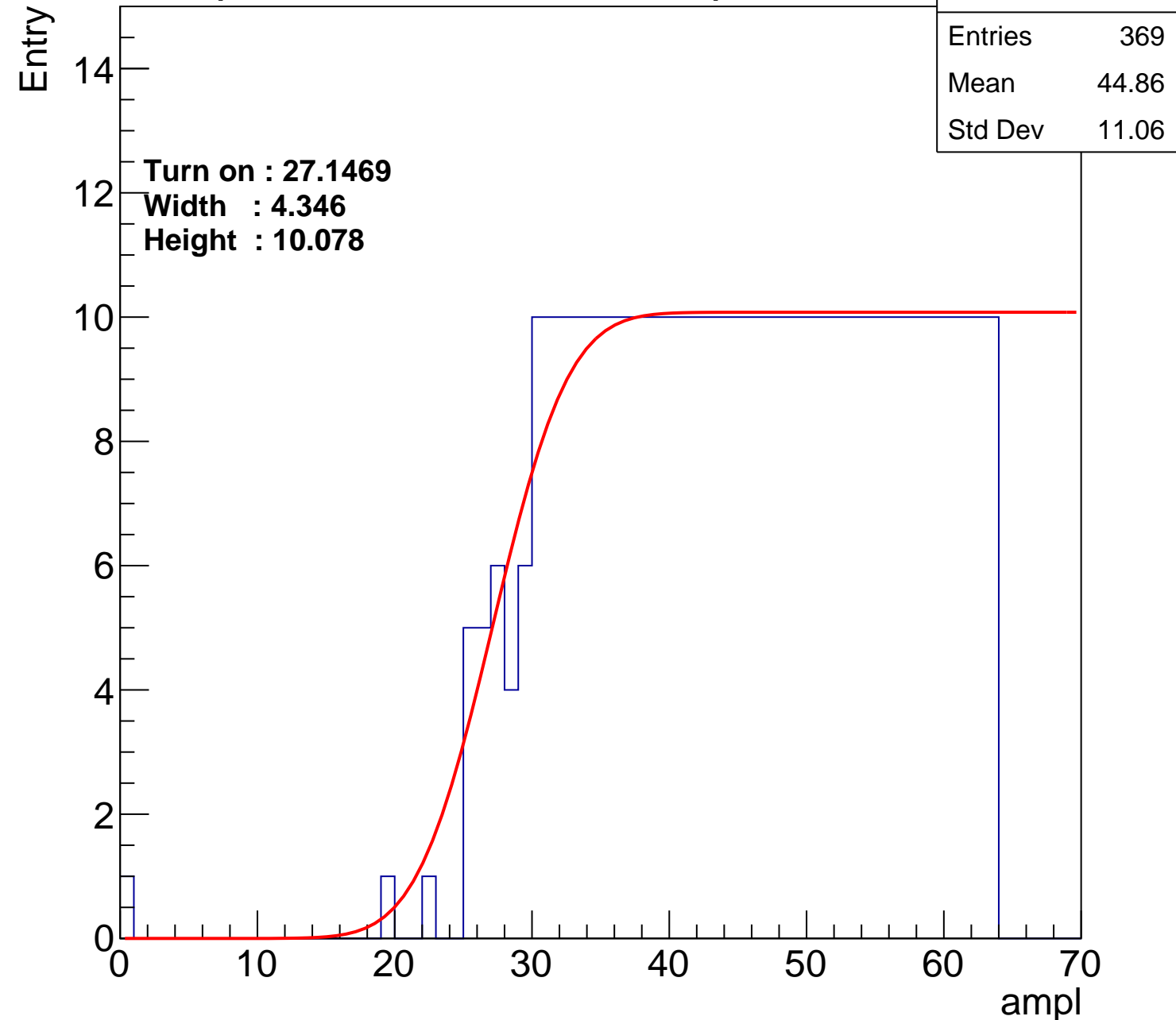
Width : 4.346

Height : 10.078

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch86

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 342   |
| Mean    | 46.22 |
| Std Dev | 10.3  |

Turn on : 30.0166

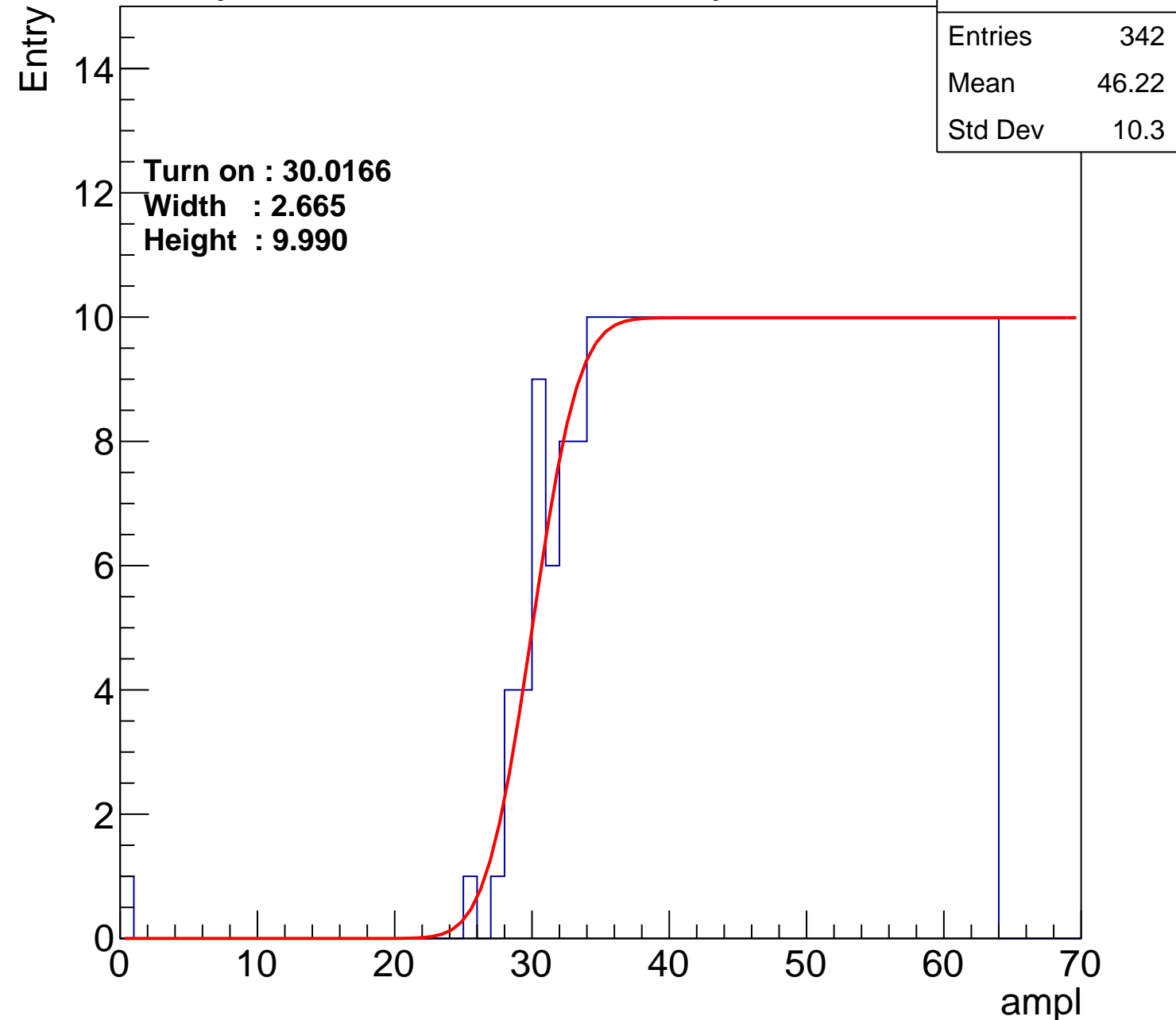
Width : 2.665

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch87

calib\_packv5\_042523\_0143.root, FC#13, port D2

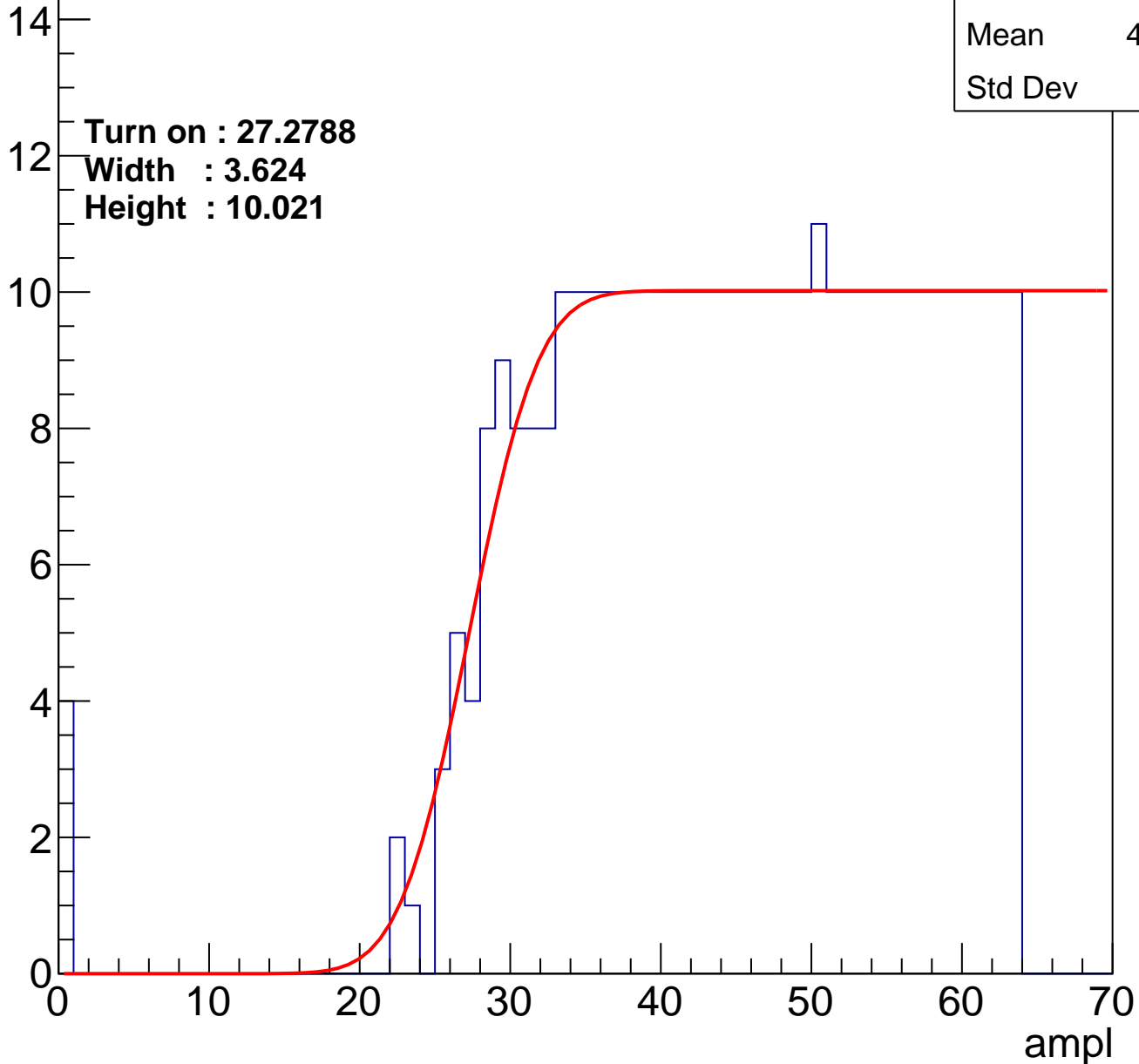
|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.58 |
| Std Dev | 11.7  |

Turn on : 27.2788

Width : 3.624

Height : 10.021

Entry



# B1L003S, U24-ch88

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.8  |
| Std Dev | 11.05 |

Turn on : 27.0684

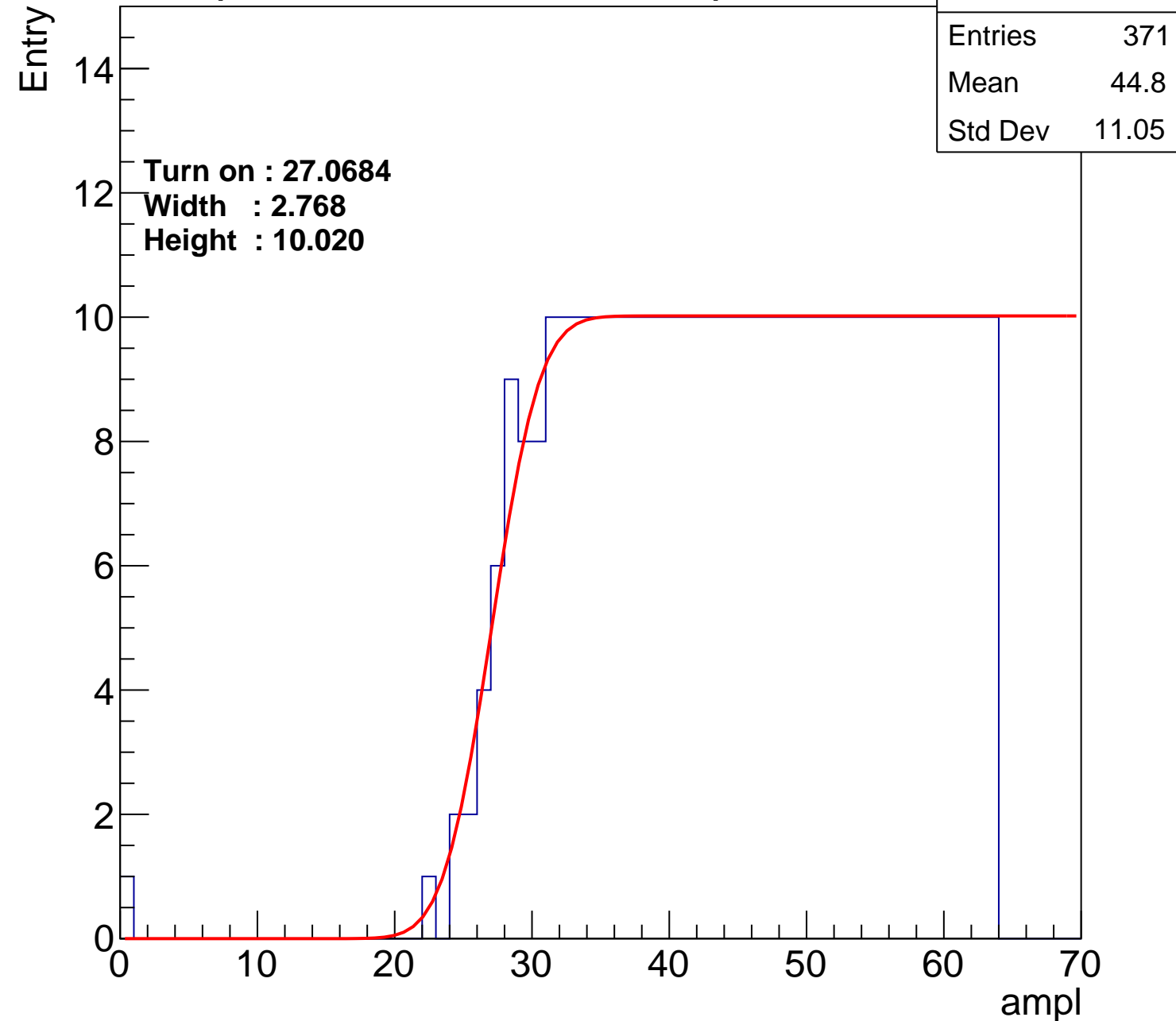
Width : 2.768

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch89

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.71 |
| Std Dev | 11.08 |

**Turn on : 26.7659**

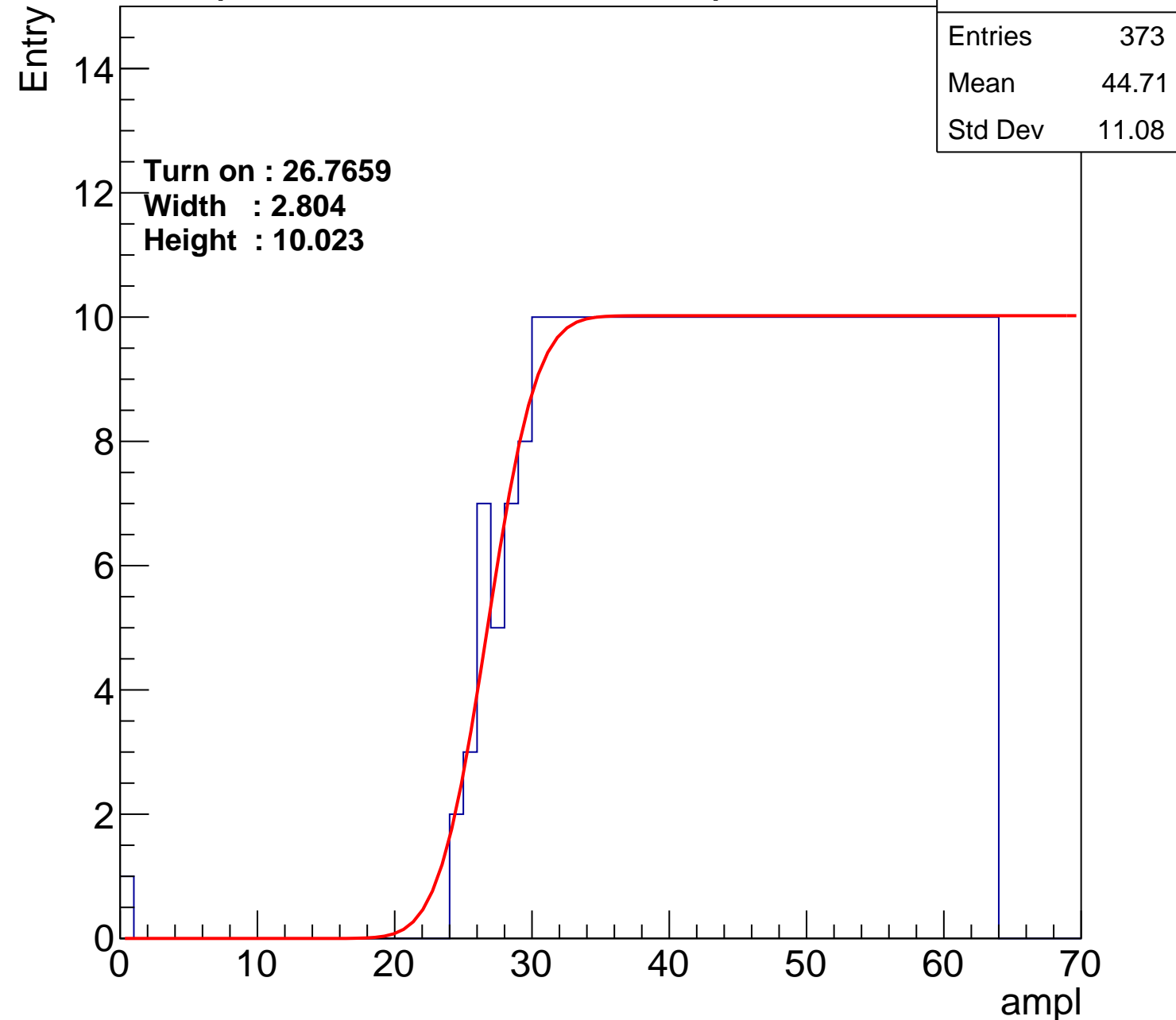
**Width : 2.804**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch90

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 363   |
| Mean    | 44.8  |
| Std Dev | 11.89 |

Turn on : 29.0869

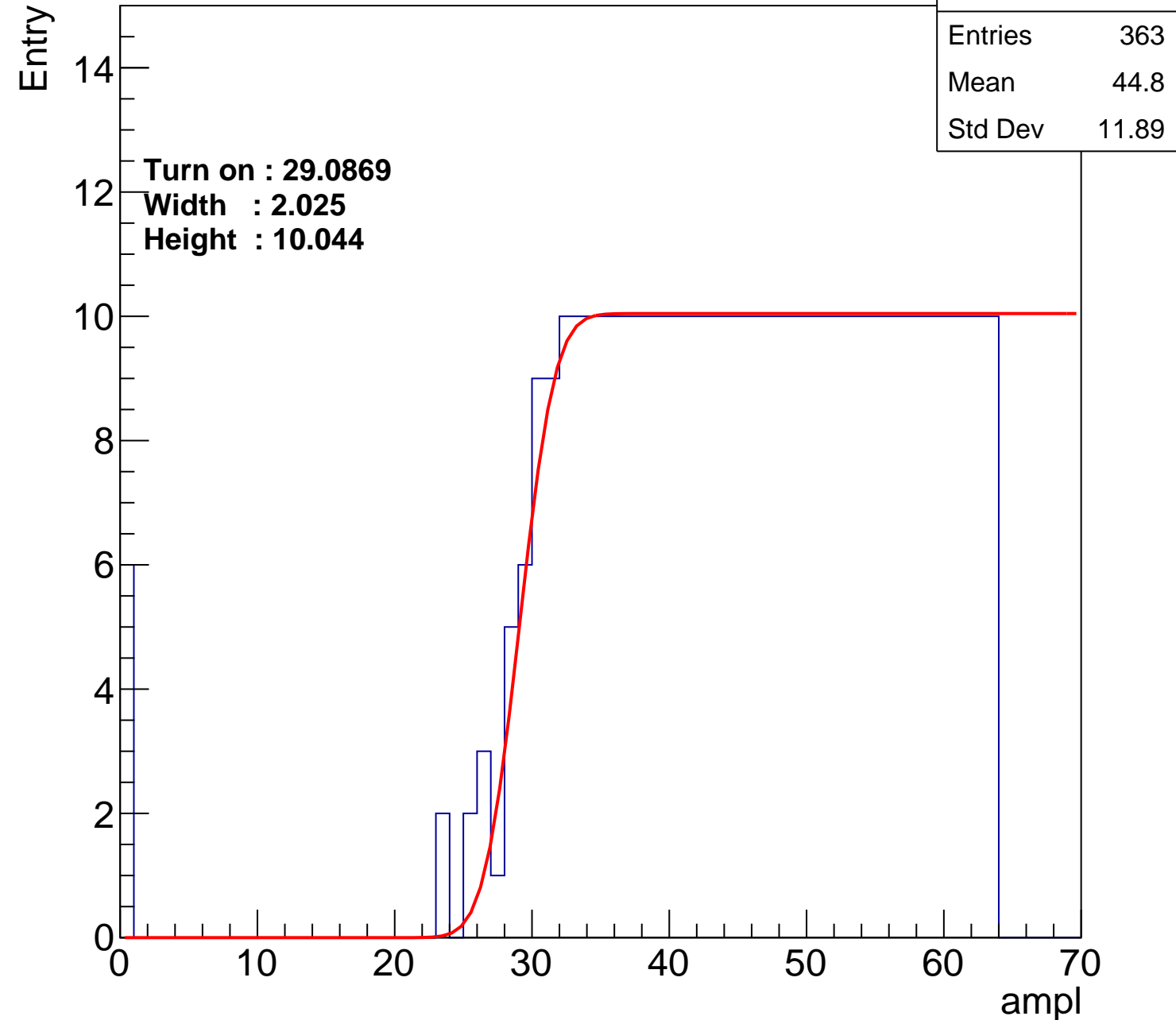
Width : 2.025

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch91

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 363   |
| Mean    | 45.18 |
| Std Dev | 10.89 |

Turn on : 27.6639

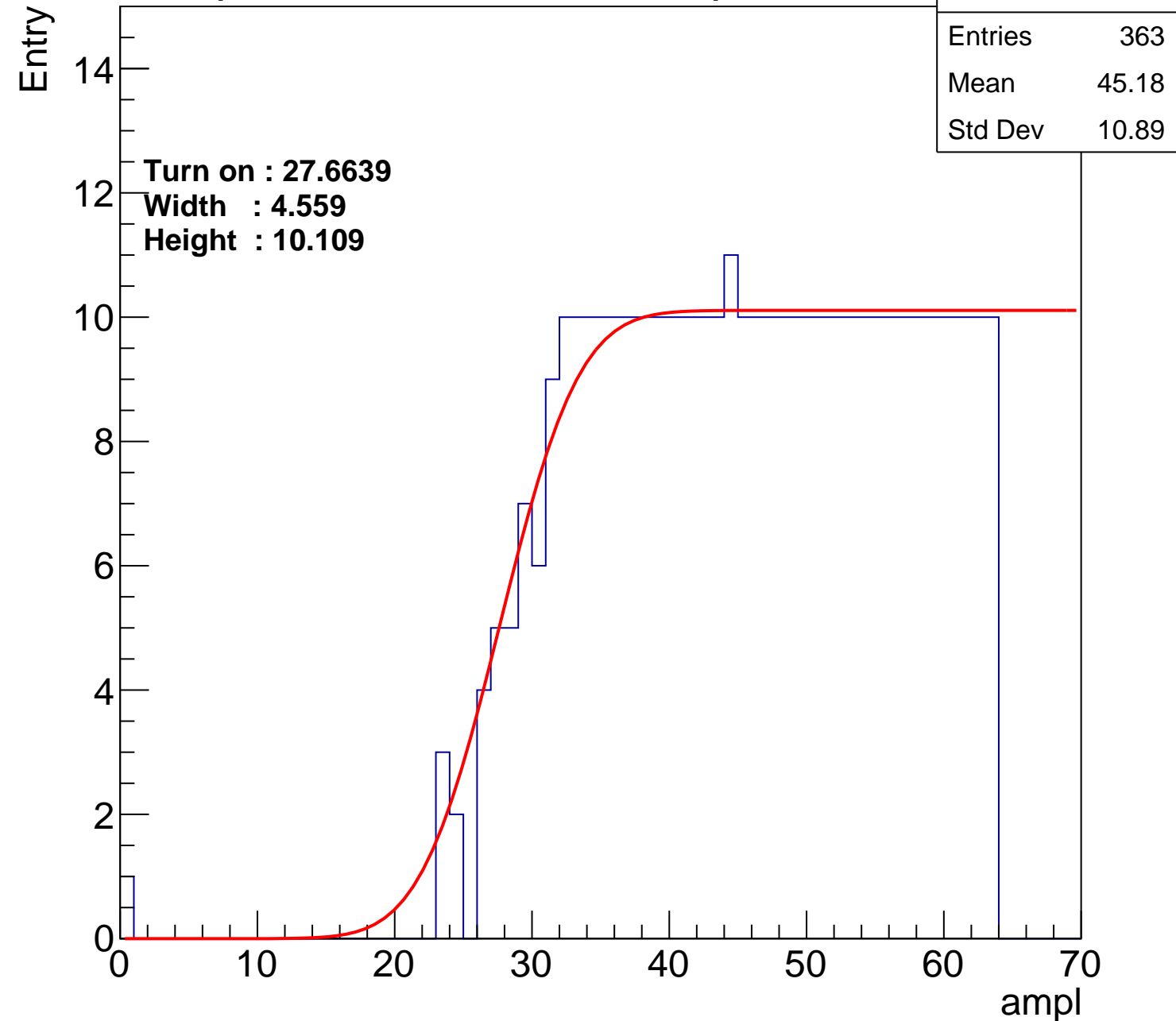
Width : 4.559

Height : 10.109

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch92

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**Turn on : 27.5634**

**Width : 2.005**

**Height : 9.900**

|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.06 |
| Std Dev | 11.08 |



# B1L003S, U24-ch93

calib\_packv5\_042523\_0143.root, FC#13, port D2

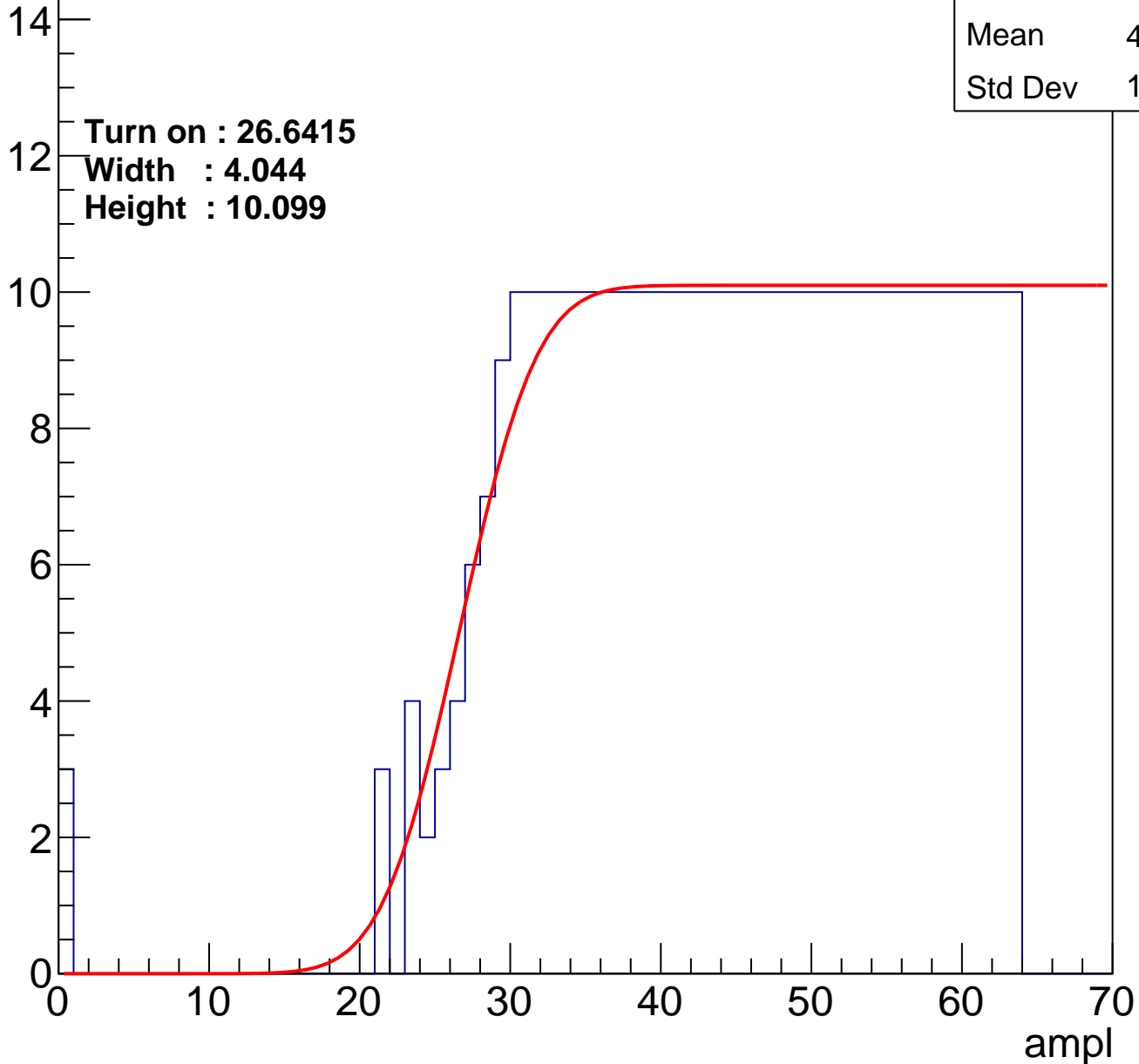
|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 44.12 |
| Std Dev | 11.76 |

Turn on : 26.6415

Width : 4.044

Height : 10.099

Entry



# B1L003S, U24-ch94

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.25 |
| Std Dev | 11.01 |

Turn on : 28.8465

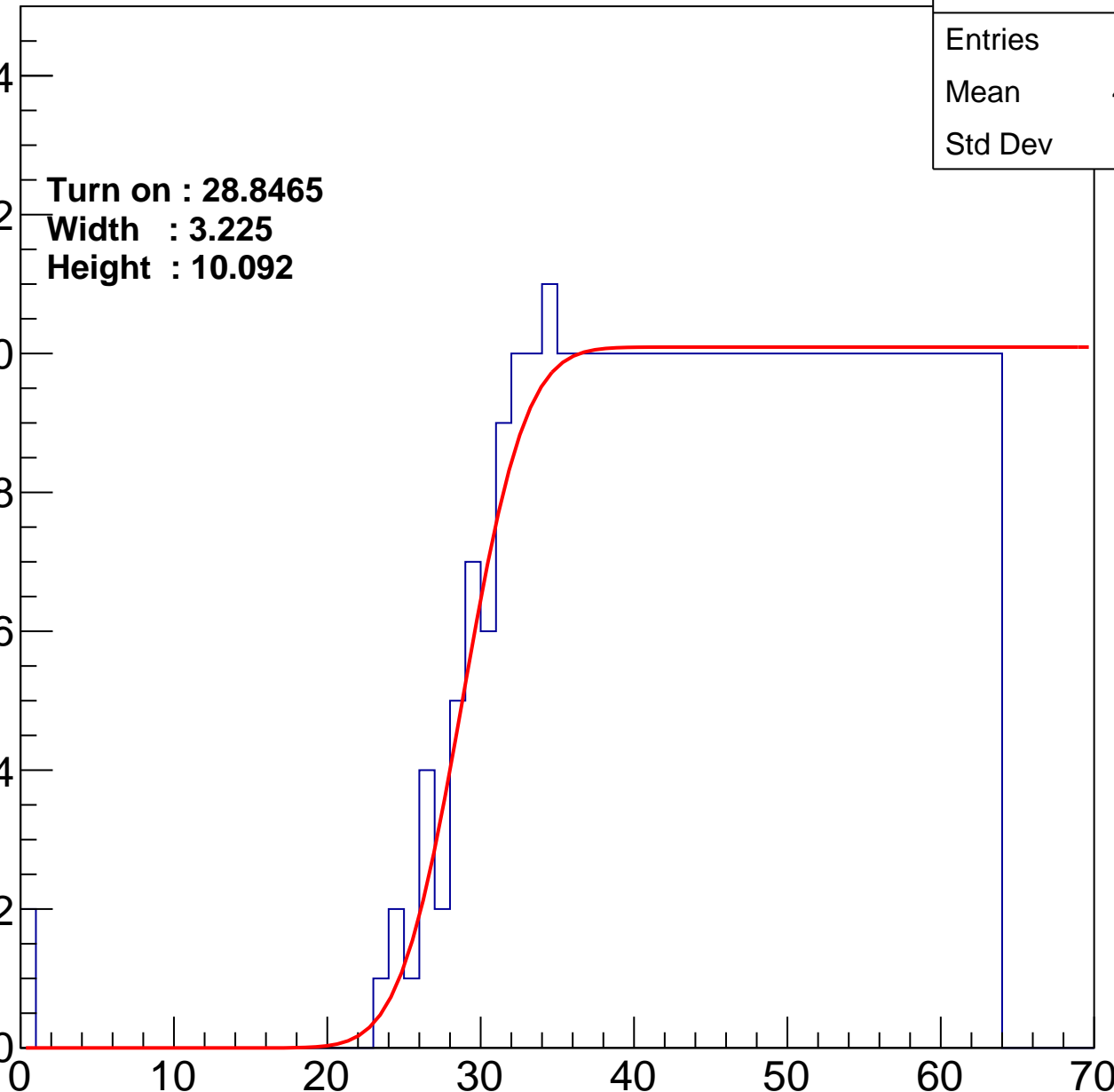
Width : 3.225

Height : 10.092

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch95

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.27 |
| Std Dev | 10.83 |

Turn on : 28.0538

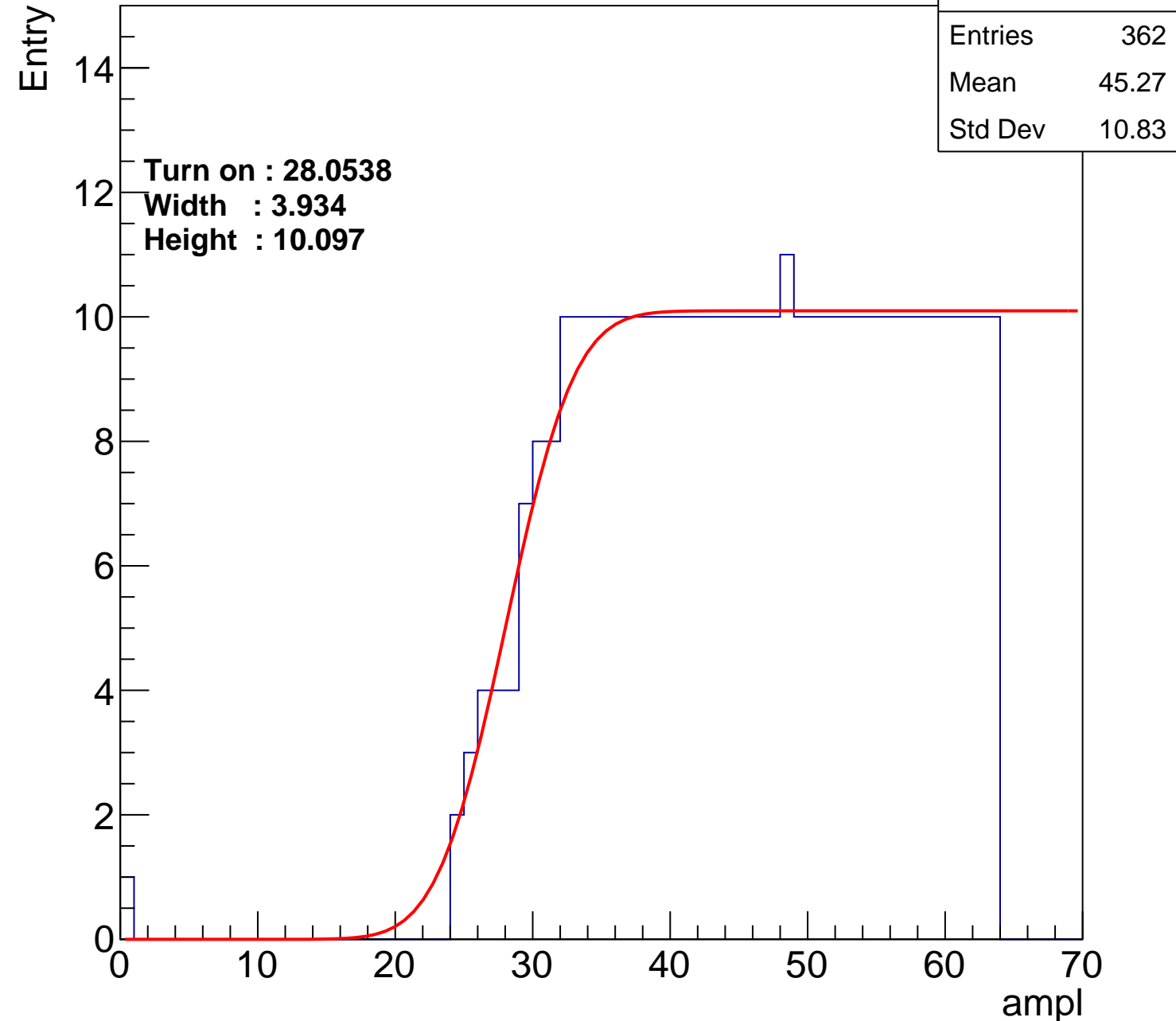
Width : 3.934

Height : 10.097

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch96

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.8  |
| Std Dev | 11.38 |

Turn on : 26.8265

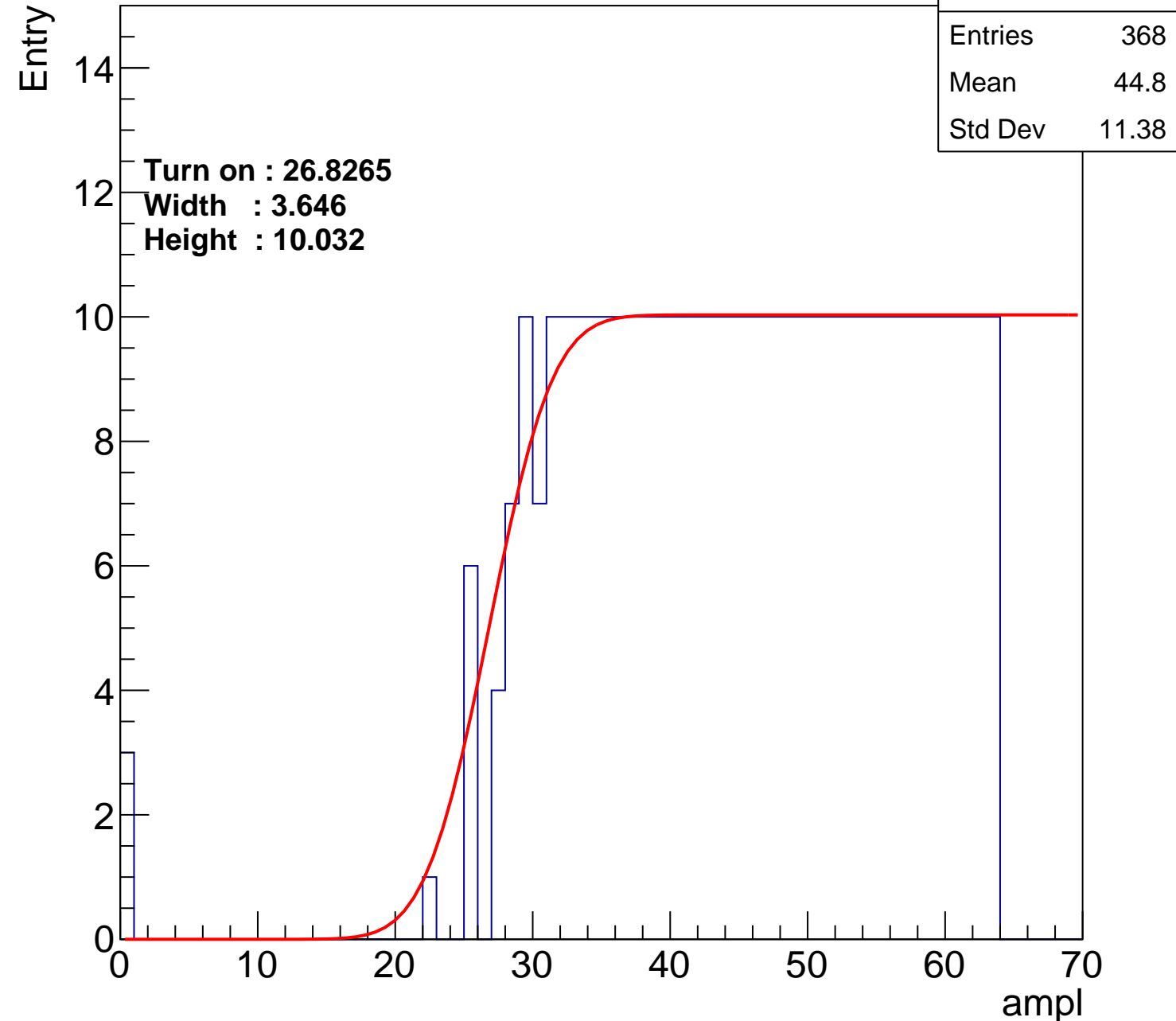
Width : 3.646

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch97

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.55 |
| Std Dev | 11.38 |

Turn on : 27.2240

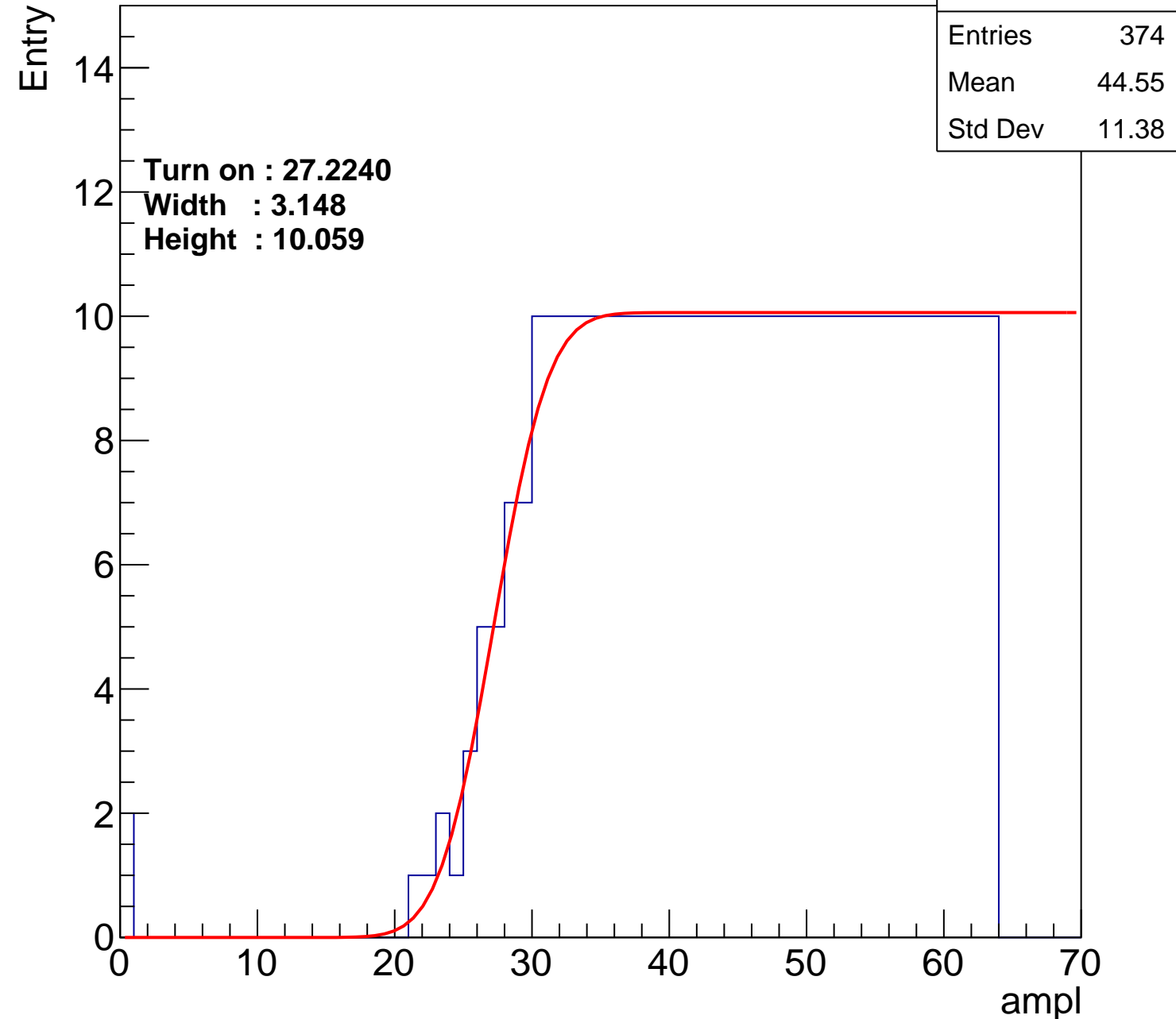
Width : 3.148

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch98

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.36 |
| Std Dev | 11.61 |

**Turn on : 26.9165**

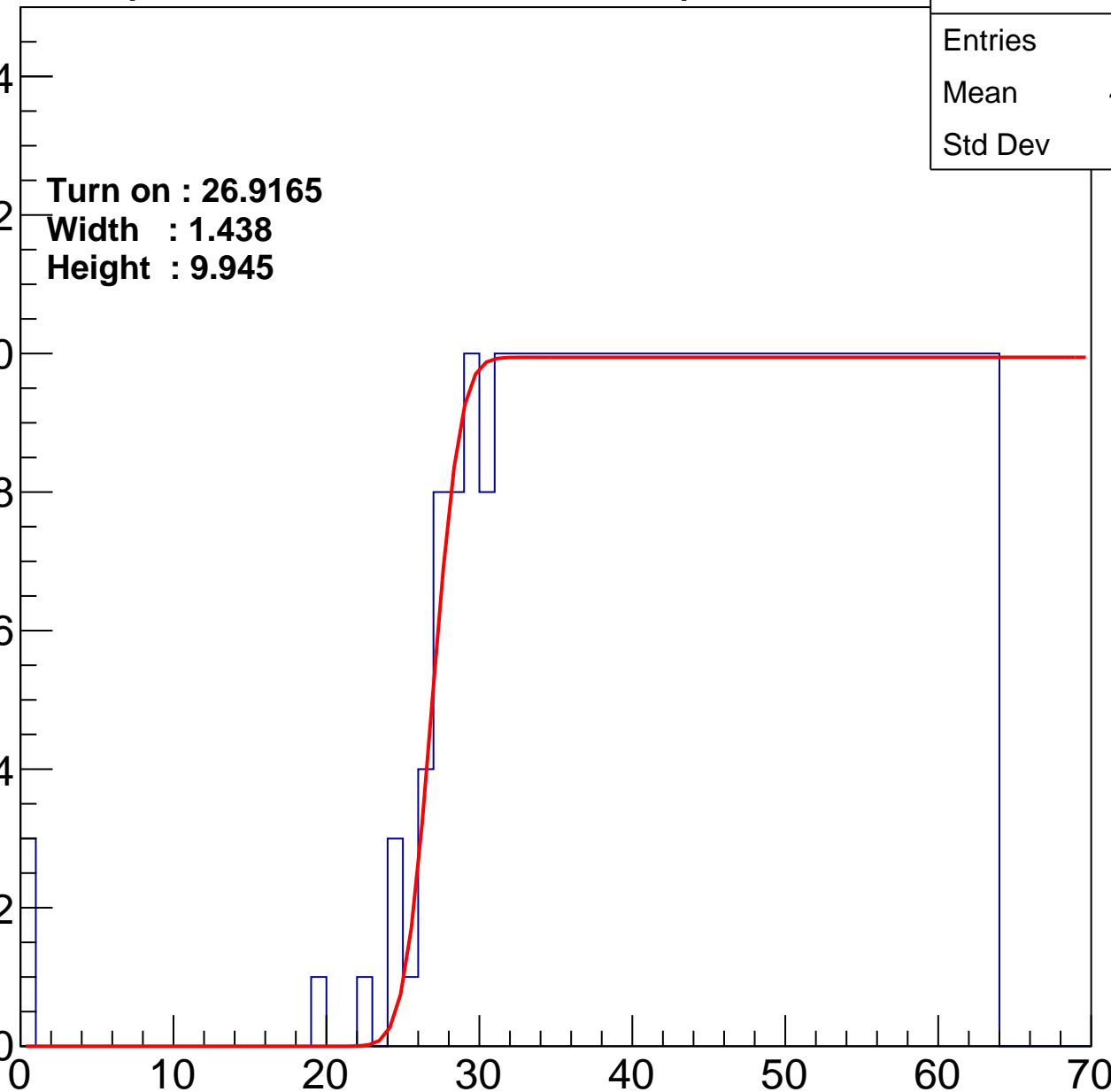
**Width : 1.438**

**Height : 9.945**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch99

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.36 |
| Std Dev | 11.5  |

Turn on : 26.3927

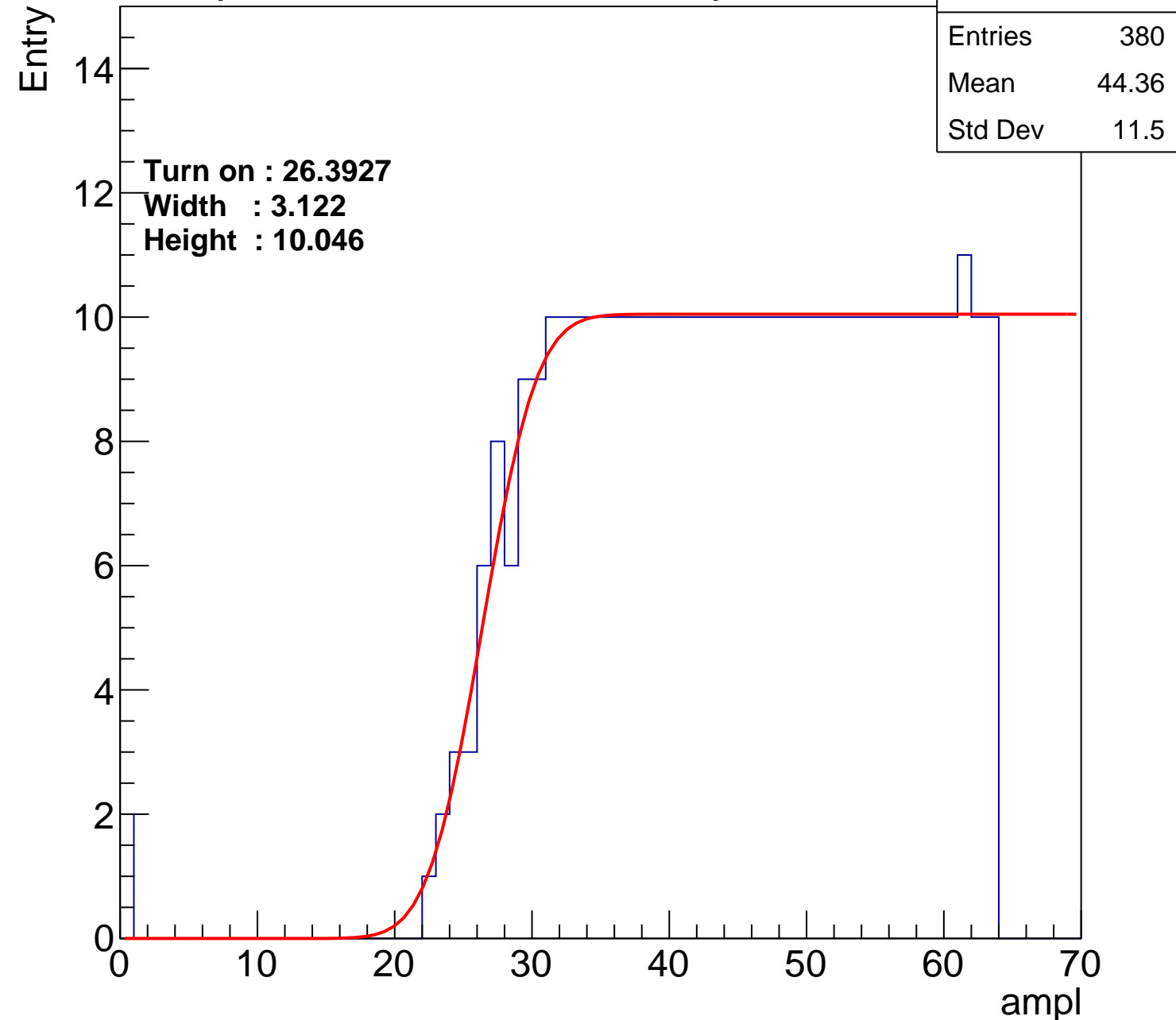
Width : 3.122

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch100

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 357   |
| Mean    | 45.32 |
| Std Dev | 11.15 |

Turn on : 28.6774

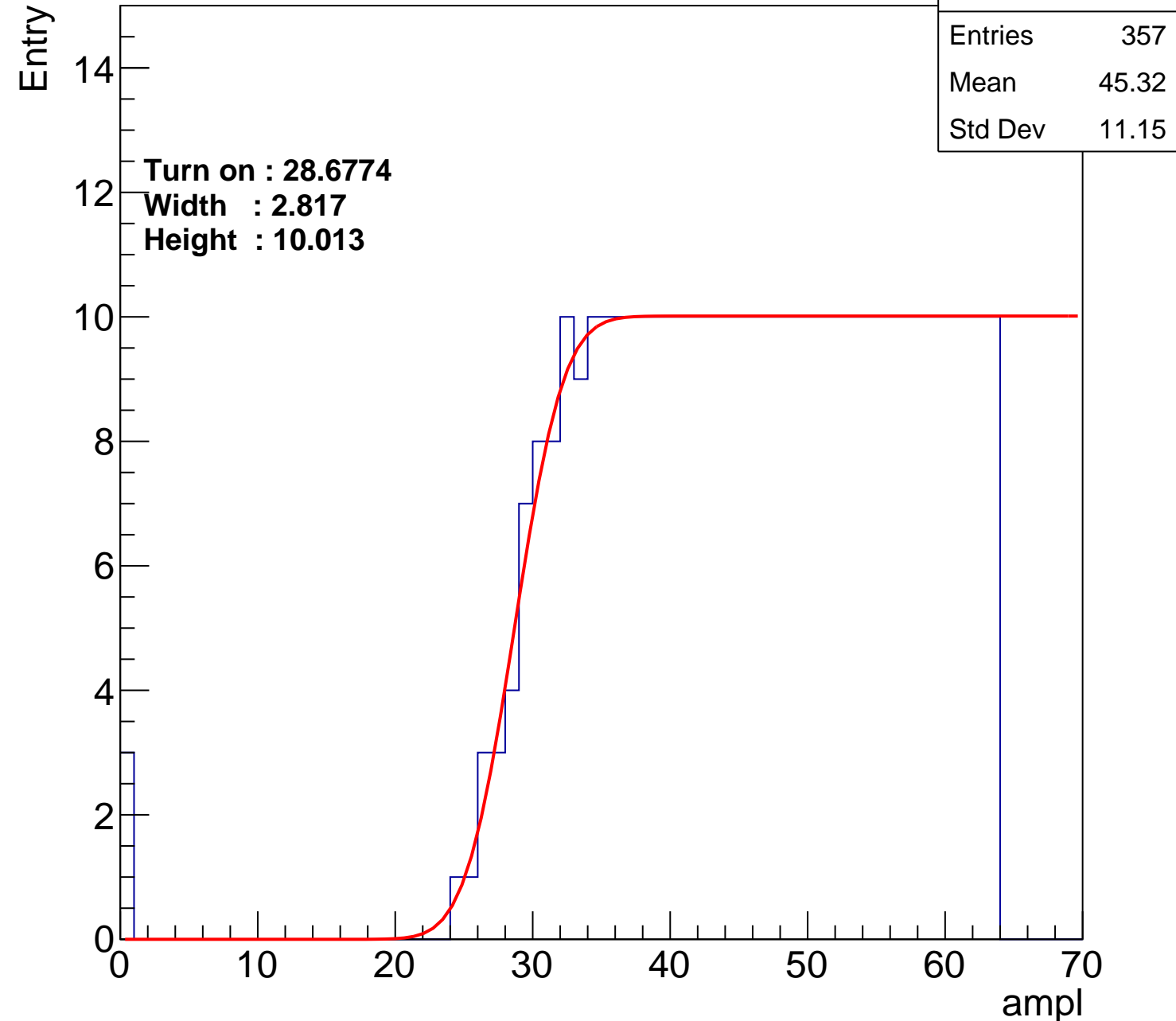
Width : 2.817

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch101

calib\_packv5\_042523\_0143.root, FC#13, port D2

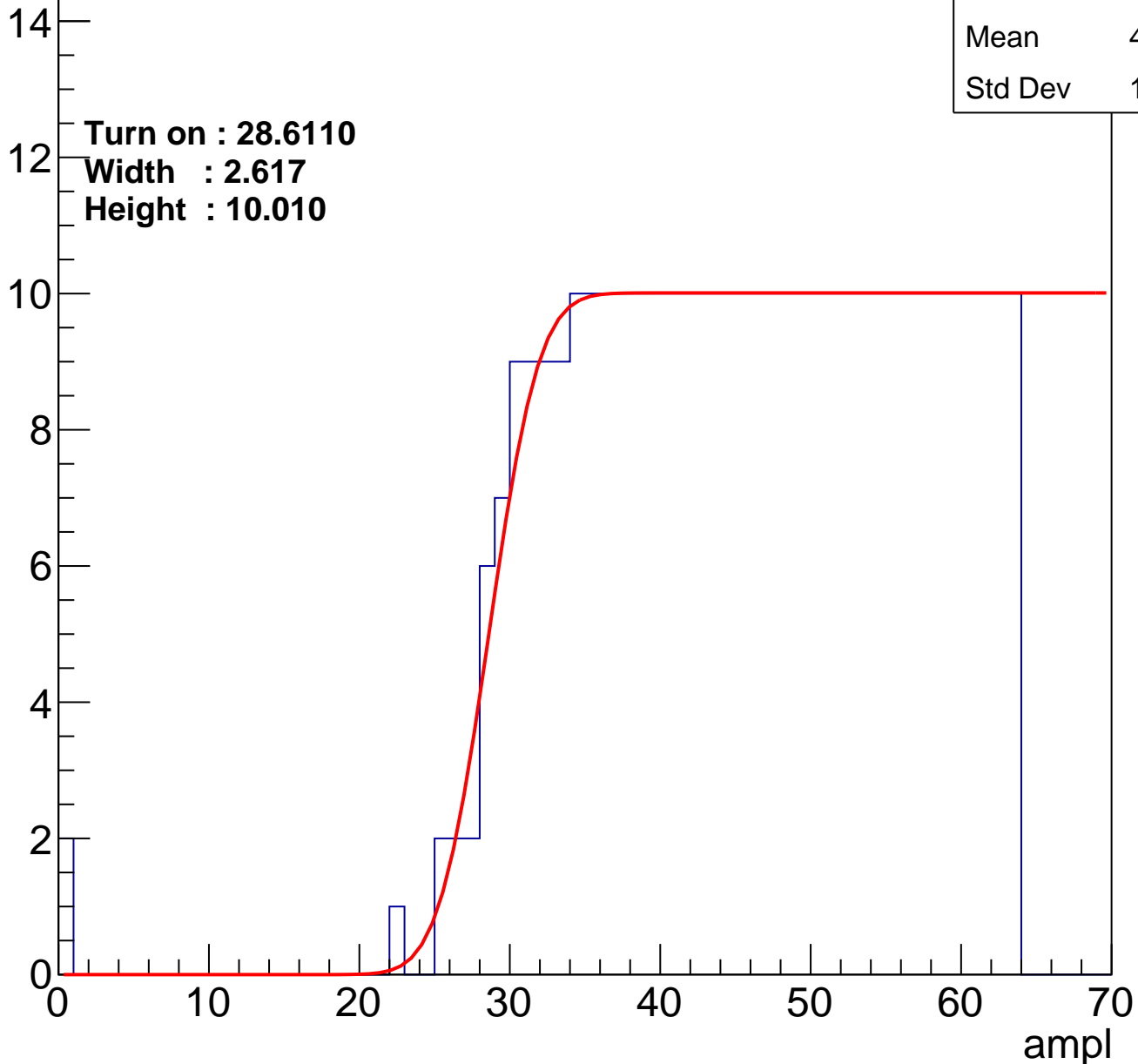
|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.34 |
| Std Dev | 10.96 |

Turn on : 28.6110

Width : 2.617

Height : 10.010

Entry



**B1L003S, U24-ch102**

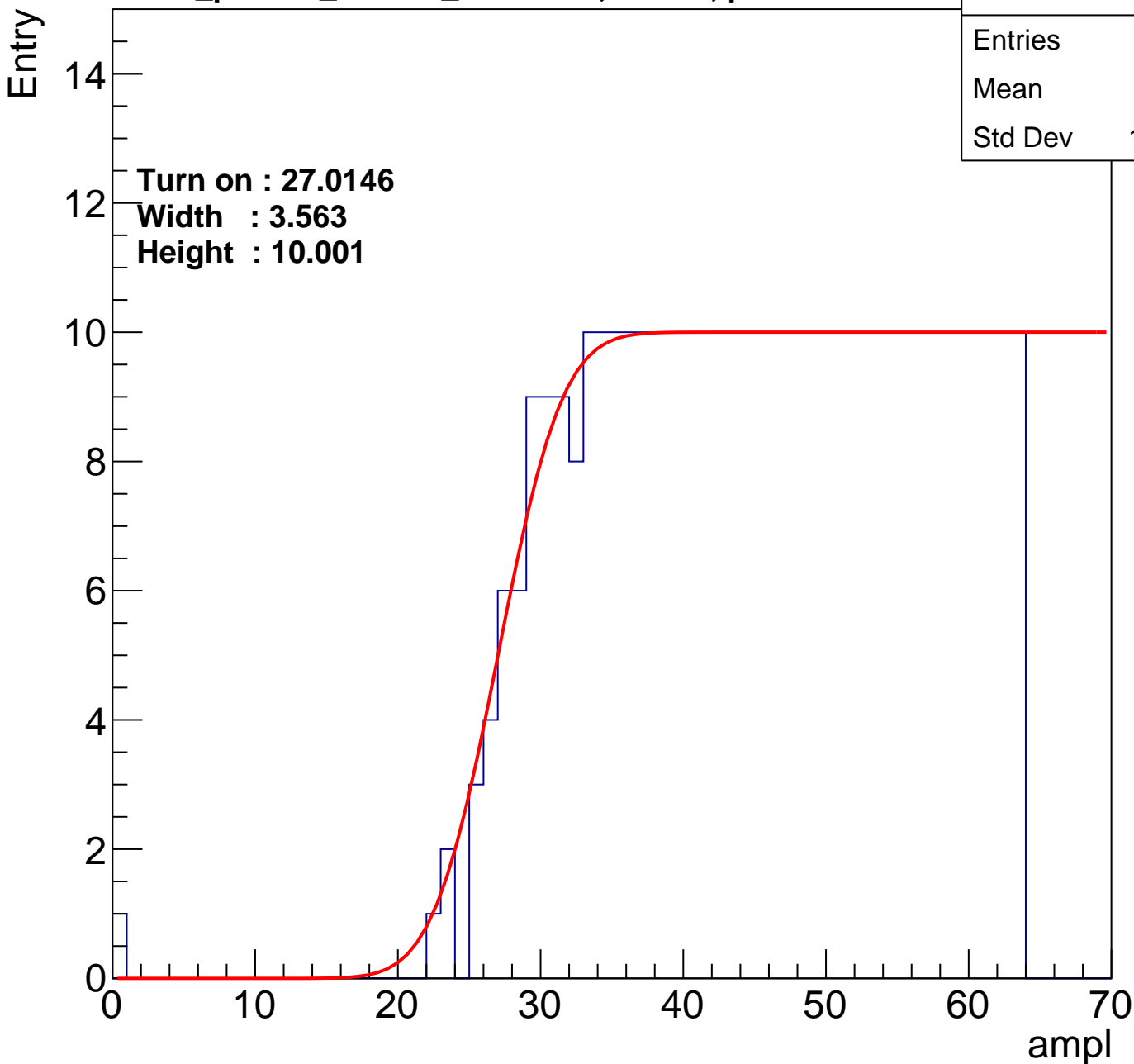
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.9  |
| Std Dev | 11.04 |

**Turn on : 27.0146**

**Width : 3.563**

**Height : 10.001**





# B1L003S, U24-ch103

calib\_packv5\_042523\_0143.root, FC#13, port D2

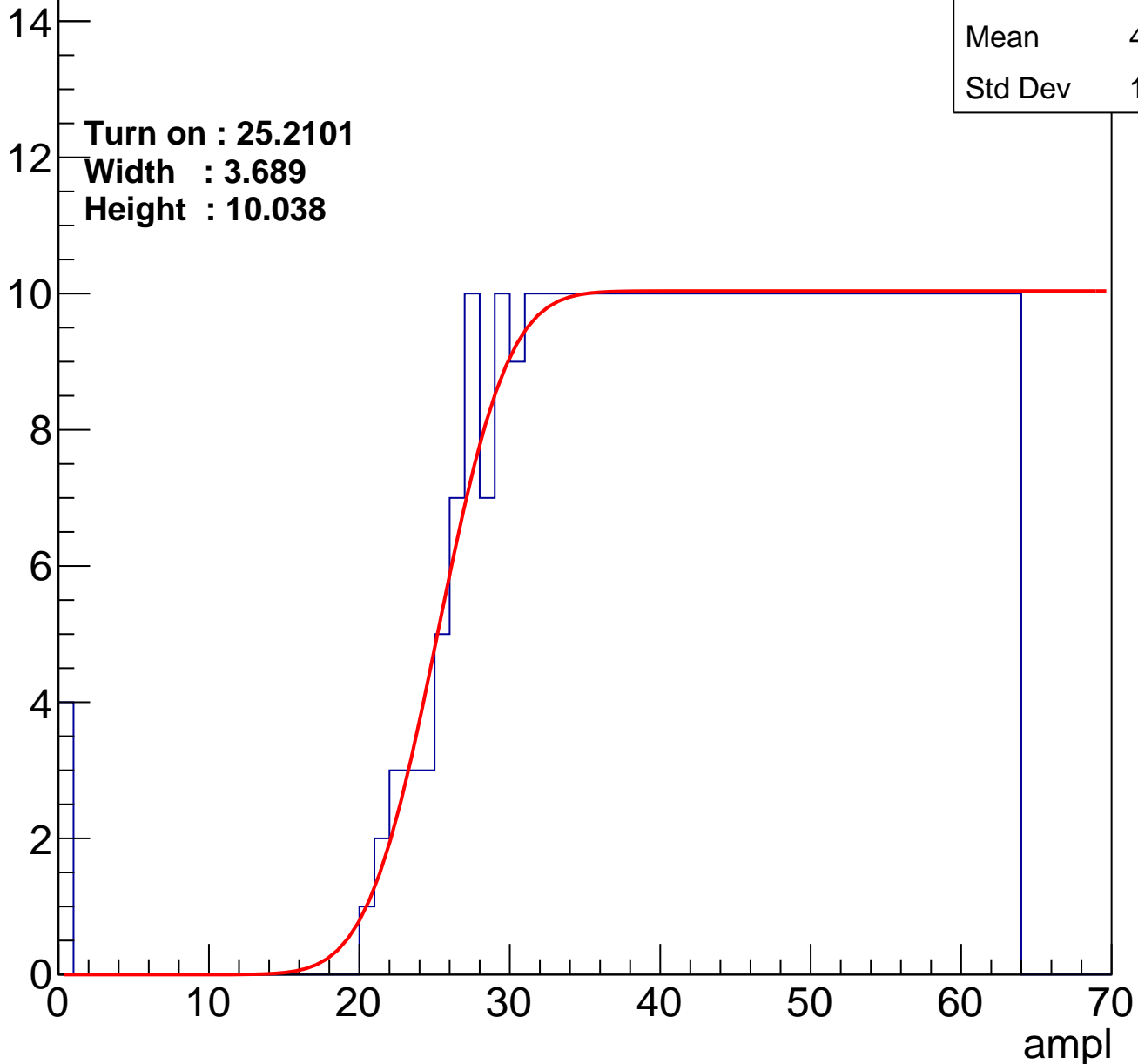
|         |       |
|---------|-------|
| Entries | 394   |
| Mean    | 43.43 |
| Std Dev | 12.23 |

Turn on : 25.2101

Width : 3.689

Height : 10.038

Entry



# B1L003S, U24-ch104

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.39 |
| Std Dev | 10.73 |

Turn on : 27.9032

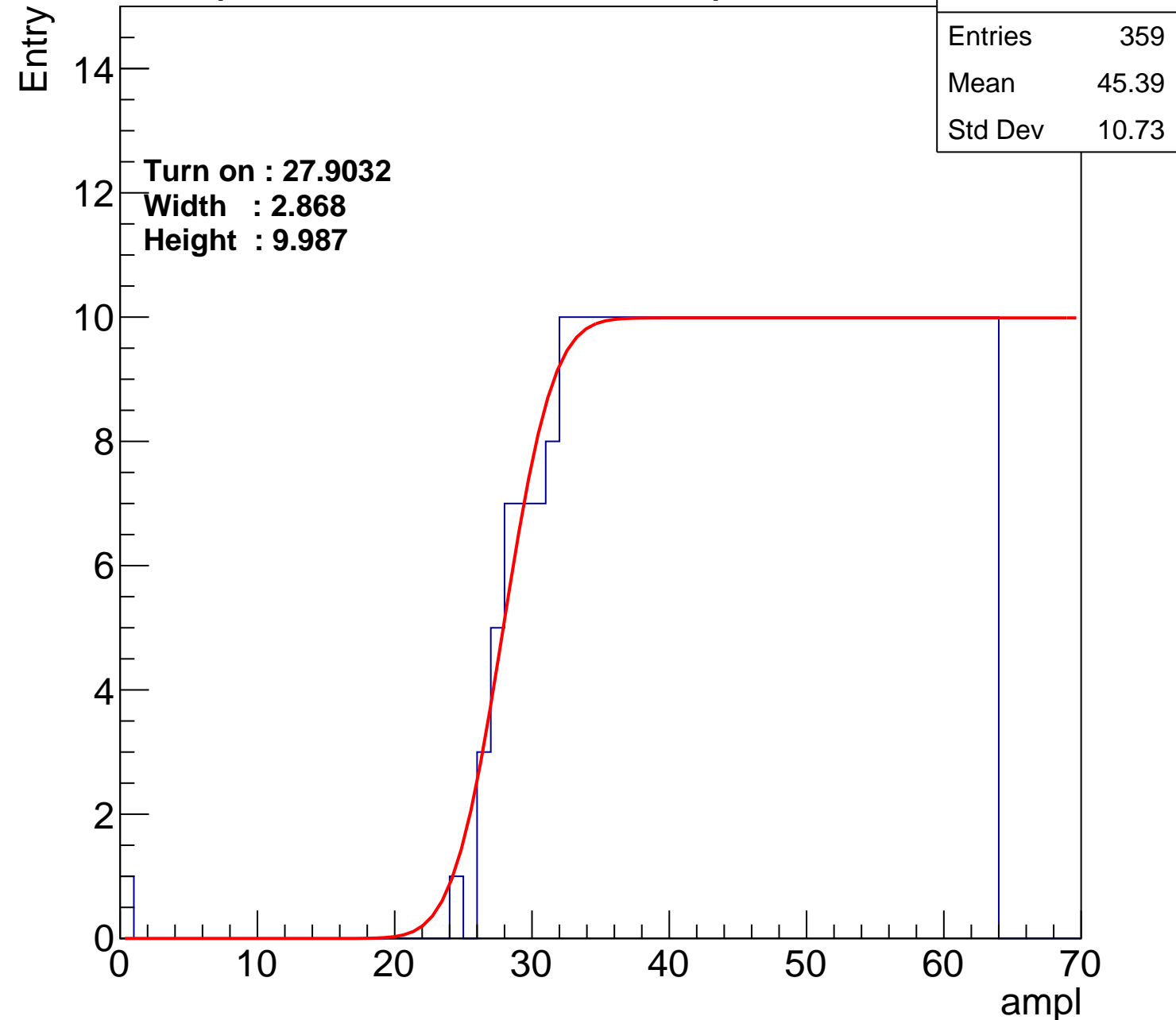
Width : 2.868

Height : 9.987

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch105

calib\_packv5\_042523\_0143.root, FC#13, port D2

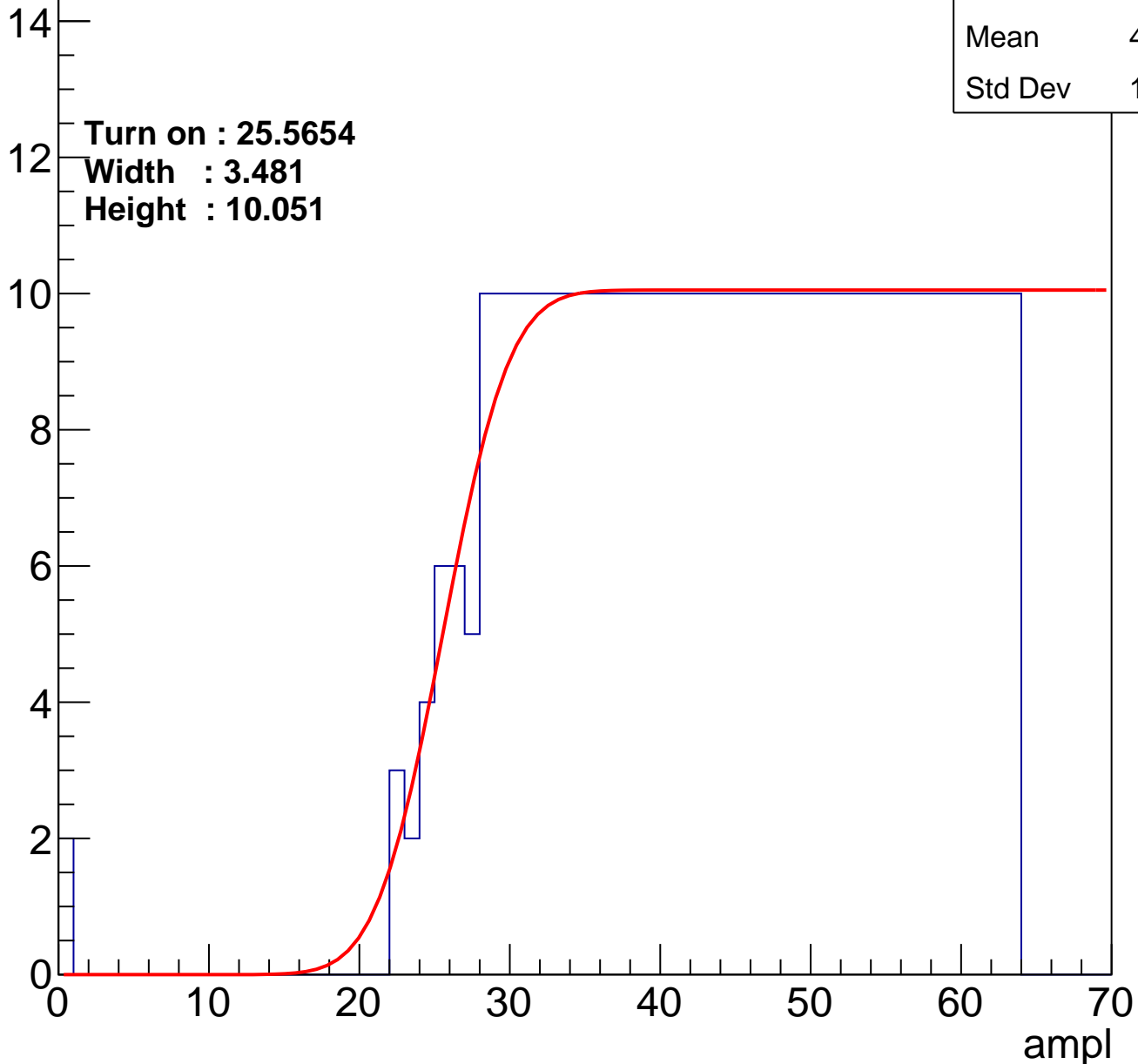
|         |       |
|---------|-------|
| Entries | 388   |
| Mean    | 43.89 |
| Std Dev | 11.69 |

Turn on : 25.5654

Width : 3.481

Height : 10.051

Entry



# B1L003S, U24-ch106

calib\_packv5\_042523\_0143.root, FC#13, port D2

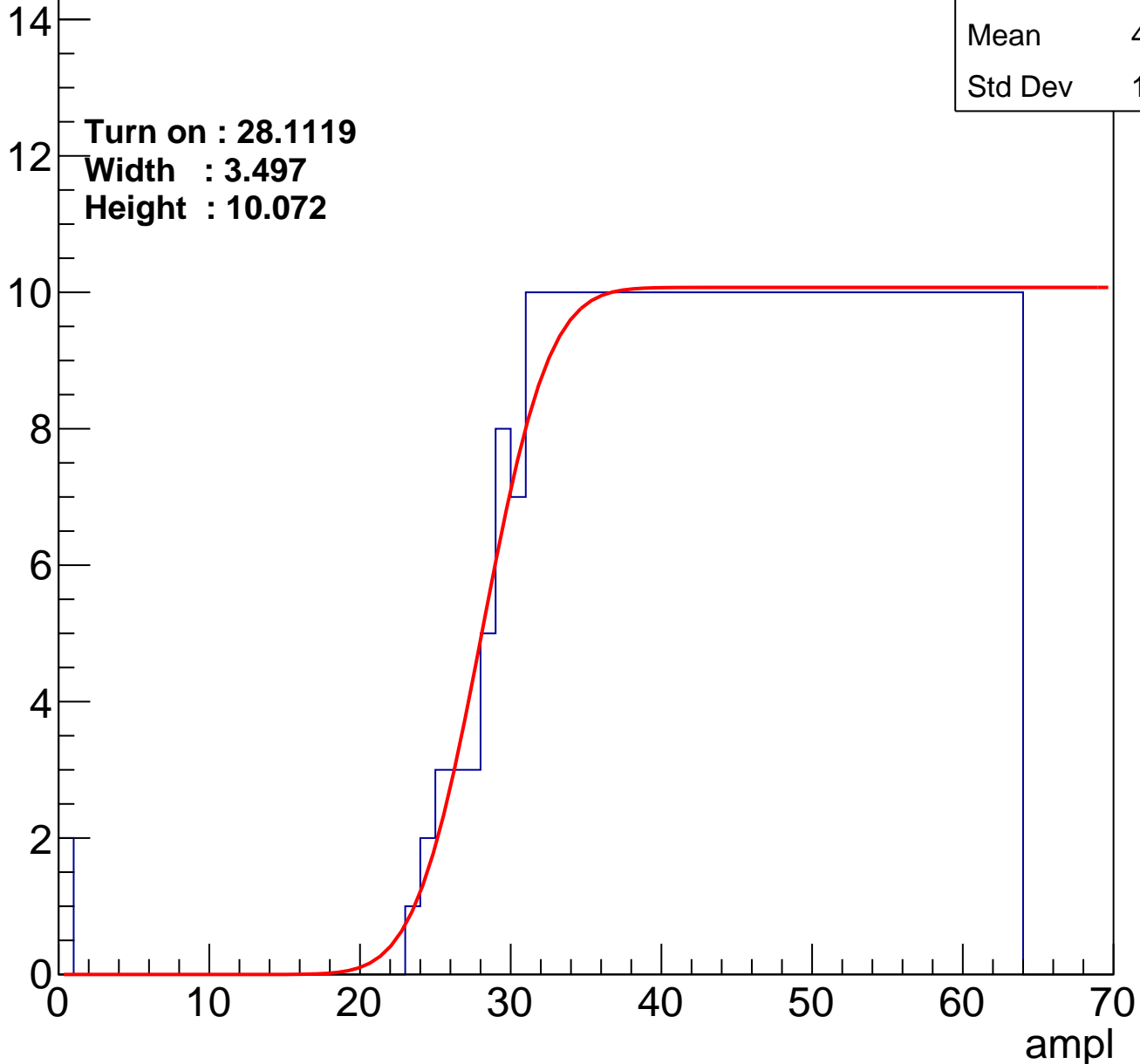
|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.05 |
| Std Dev | 11.12 |

Turn on : 28.1119

Width : 3.497

Height : 10.072

Entry



# B1L003S, U24-ch107

calib\_packv5\_042523\_0143.root, FC#13, port D2

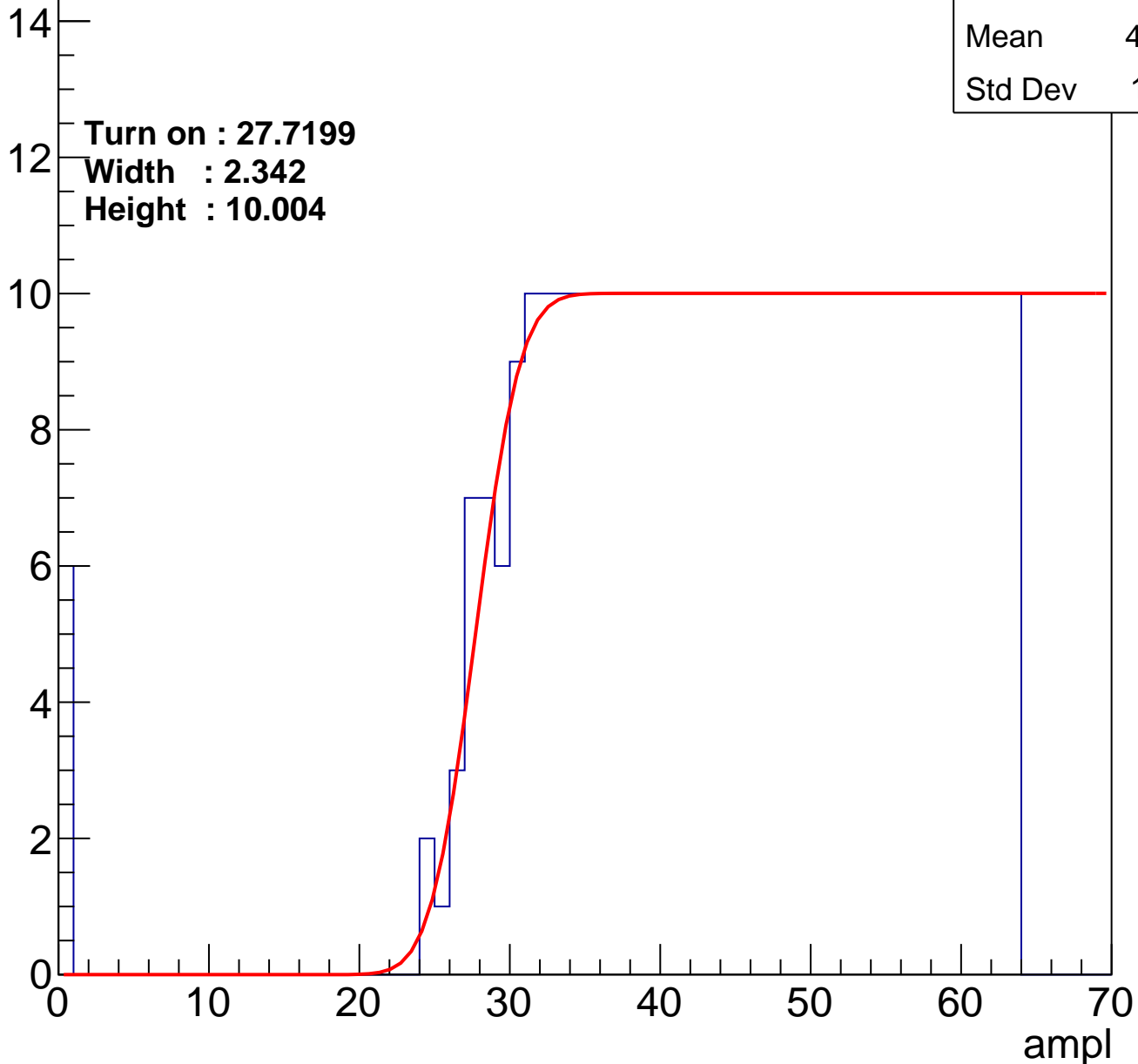
|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.45 |
| Std Dev | 12.01 |

Turn on : 27.7199

Width : 2.342

Height : 10.004

Entry



# B1L003S, U24-ch108

calib\_packv5\_042523\_0143.root, FC#13, port D2

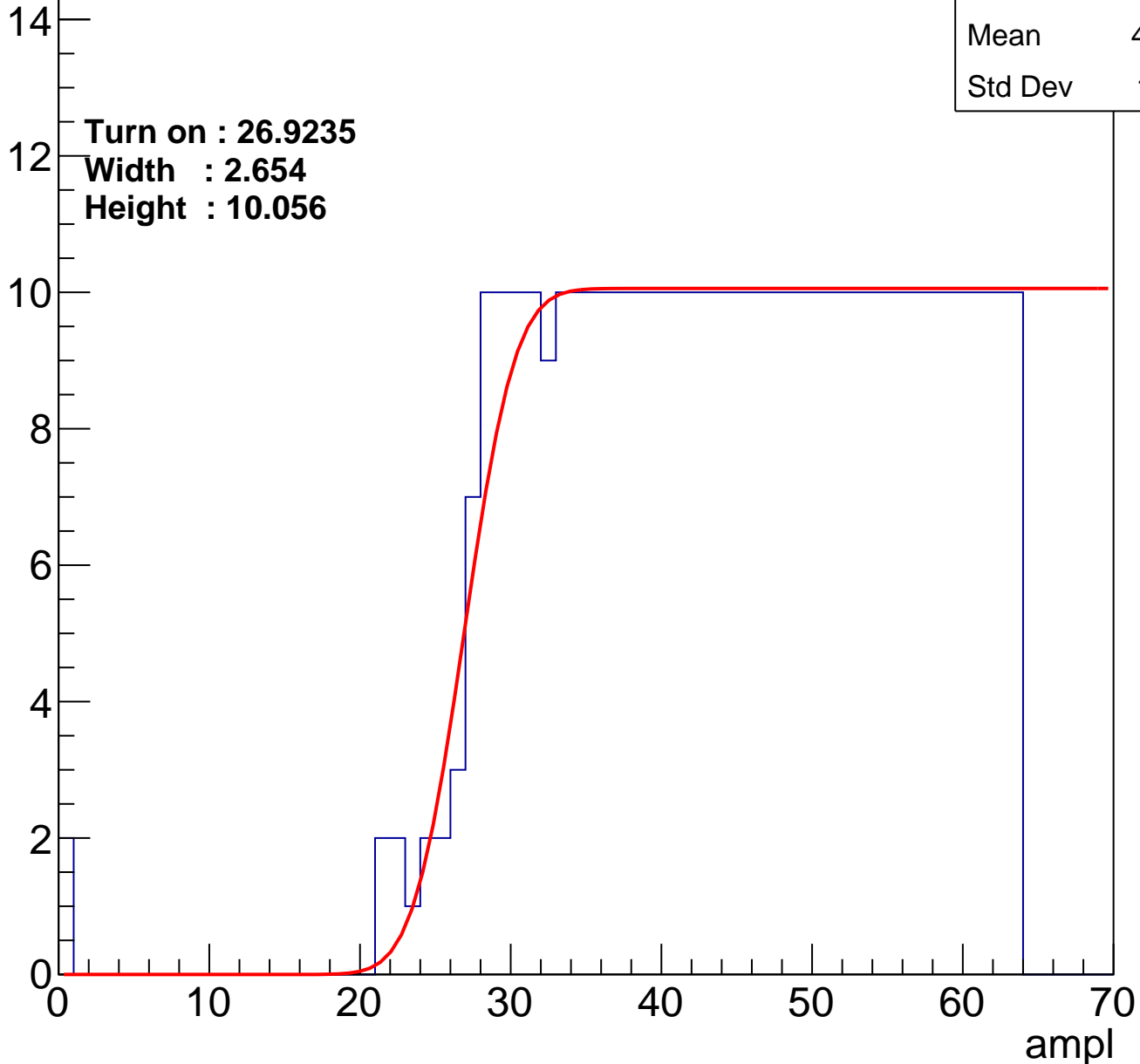
|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.27 |
| Std Dev | 11.51 |

Turn on : 26.9235

Width : 2.654

Height : 10.056

Entry



# B1L003S, U24-ch109

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.76 |
| Std Dev | 11.43 |

**Turn on : 27.5486**

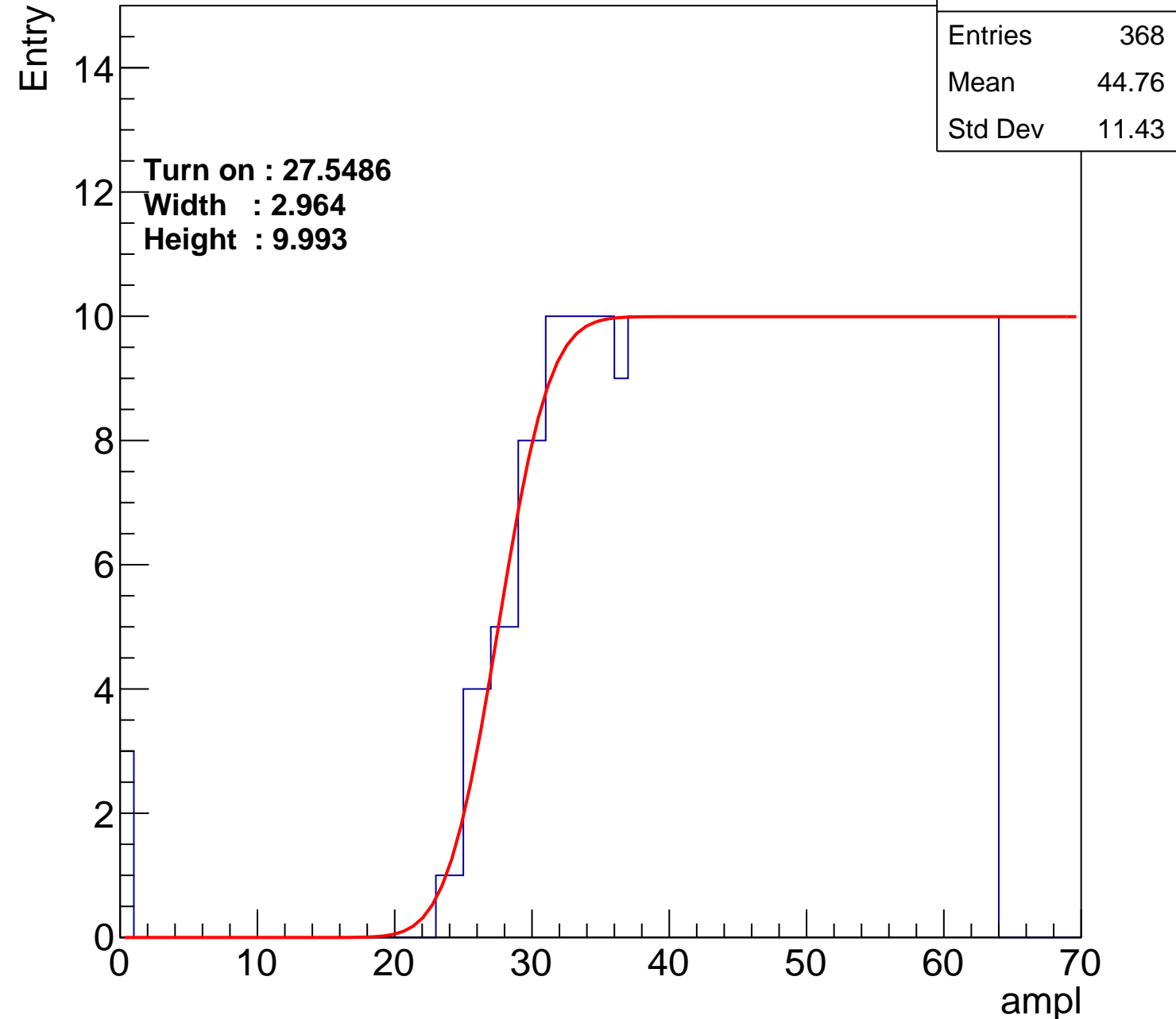
**Width : 2.964**

**Height : 9.993**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch110

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 349   |
| Mean    | 45.6  |
| Std Dev | 11.65 |

Turn on : 30.1798

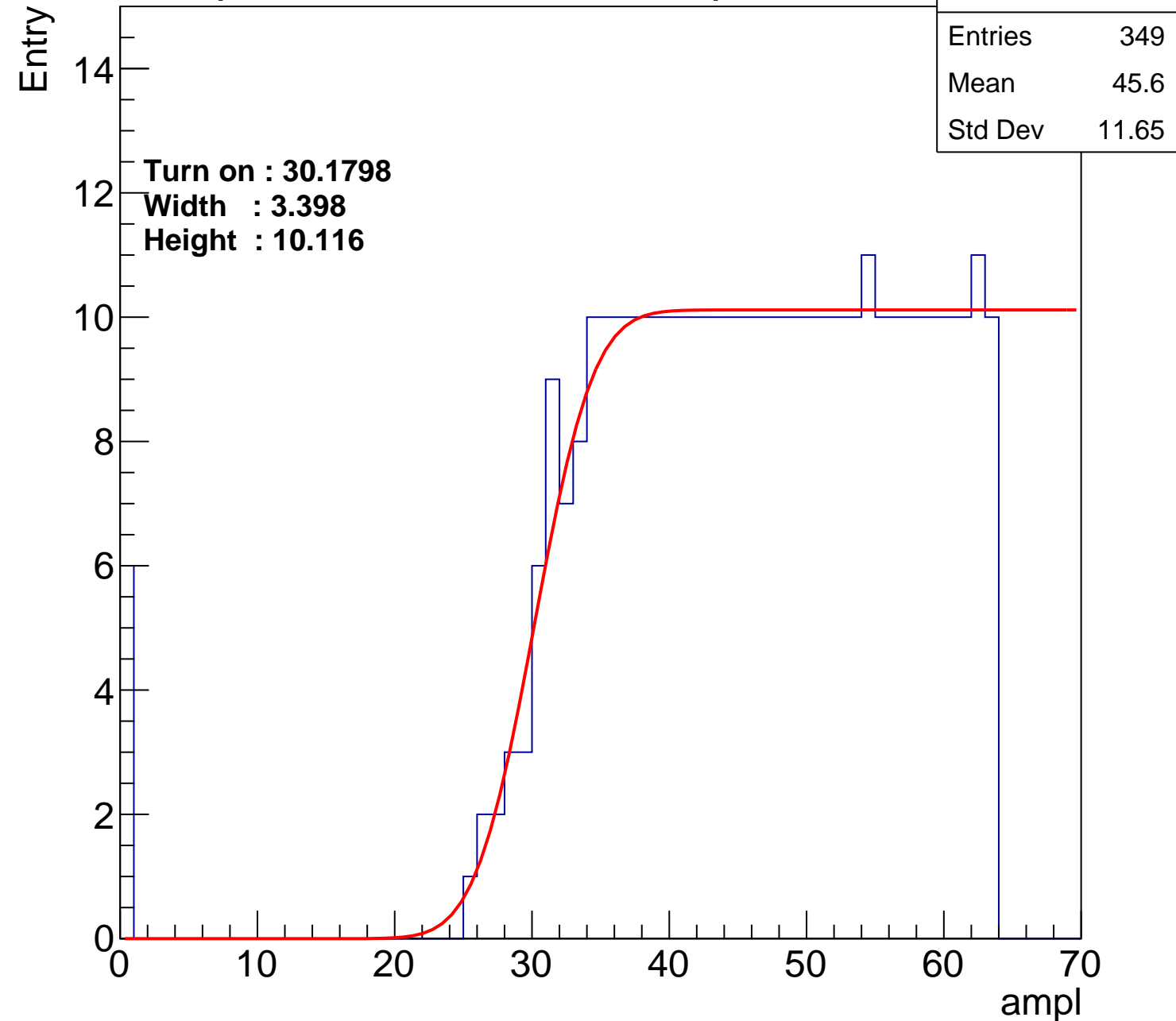
Width : 3.398

Height : 10.116

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U24-ch111

calib\_packv5\_042523\_0143.root, FC#13, port D2

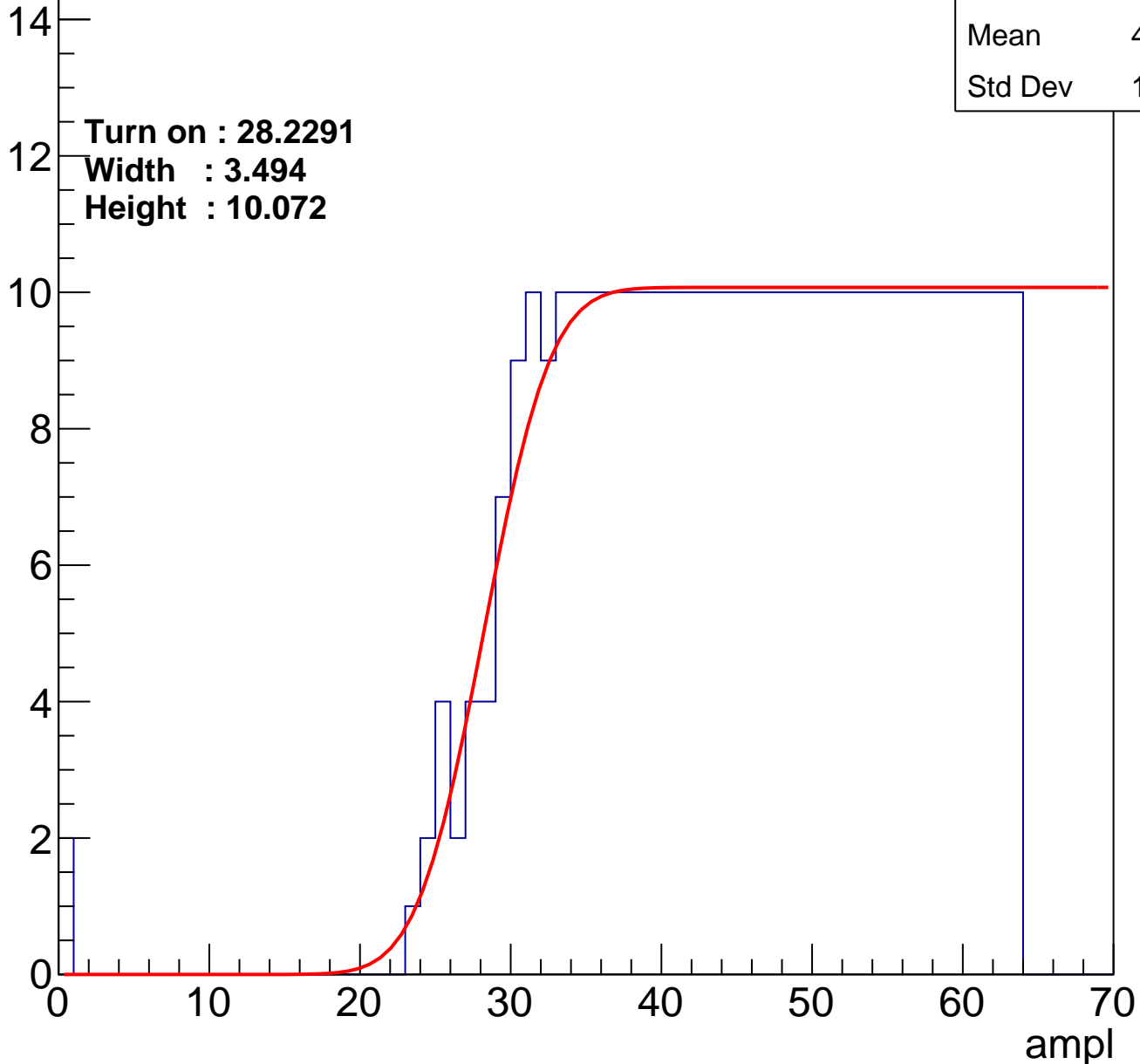
|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.04 |
| Std Dev | 11.13 |

Turn on : 28.2291

Width : 3.494

Height : 10.072

Entry



# B1L003S, U24-ch112

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 43.73 |
| Std Dev | 12.25 |

Turn on : 25.9724

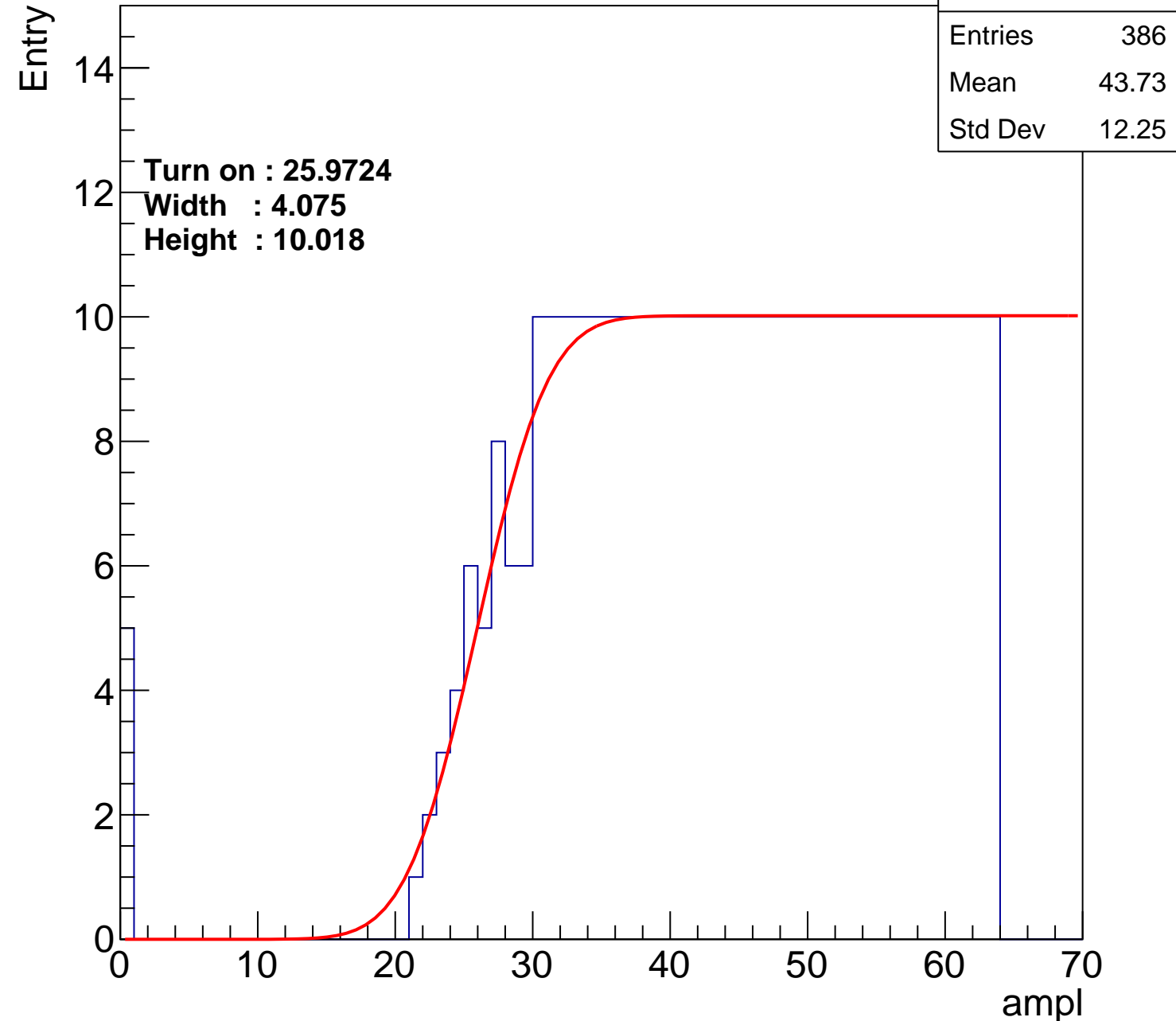
Width : 4.075

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch113

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.96 |
| Std Dev | 11.14 |

**Turn on : 28.1704**

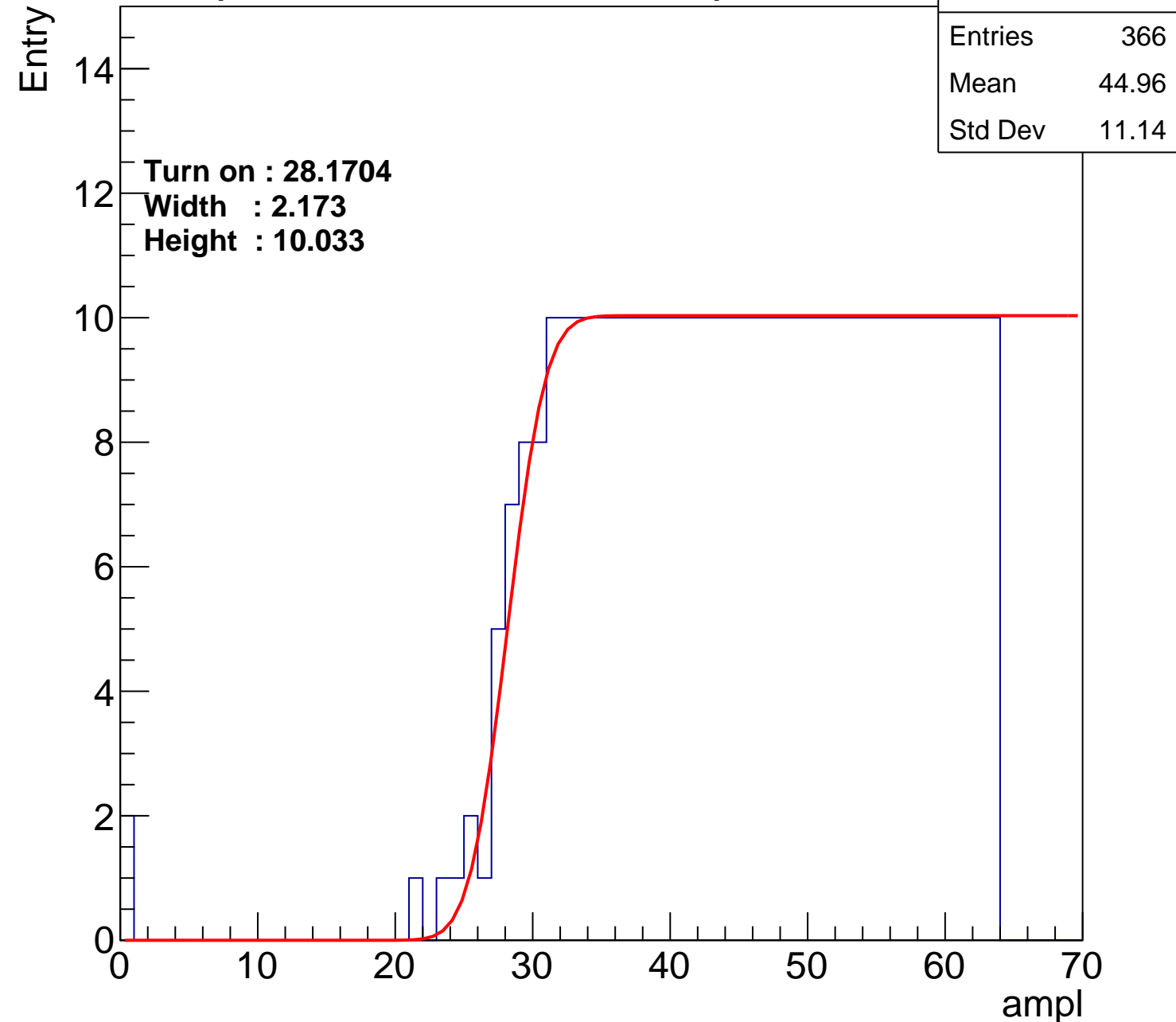
**Width : 2.173**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch114

calib\_packv5\_042523\_0143.root, FC#13, port D2

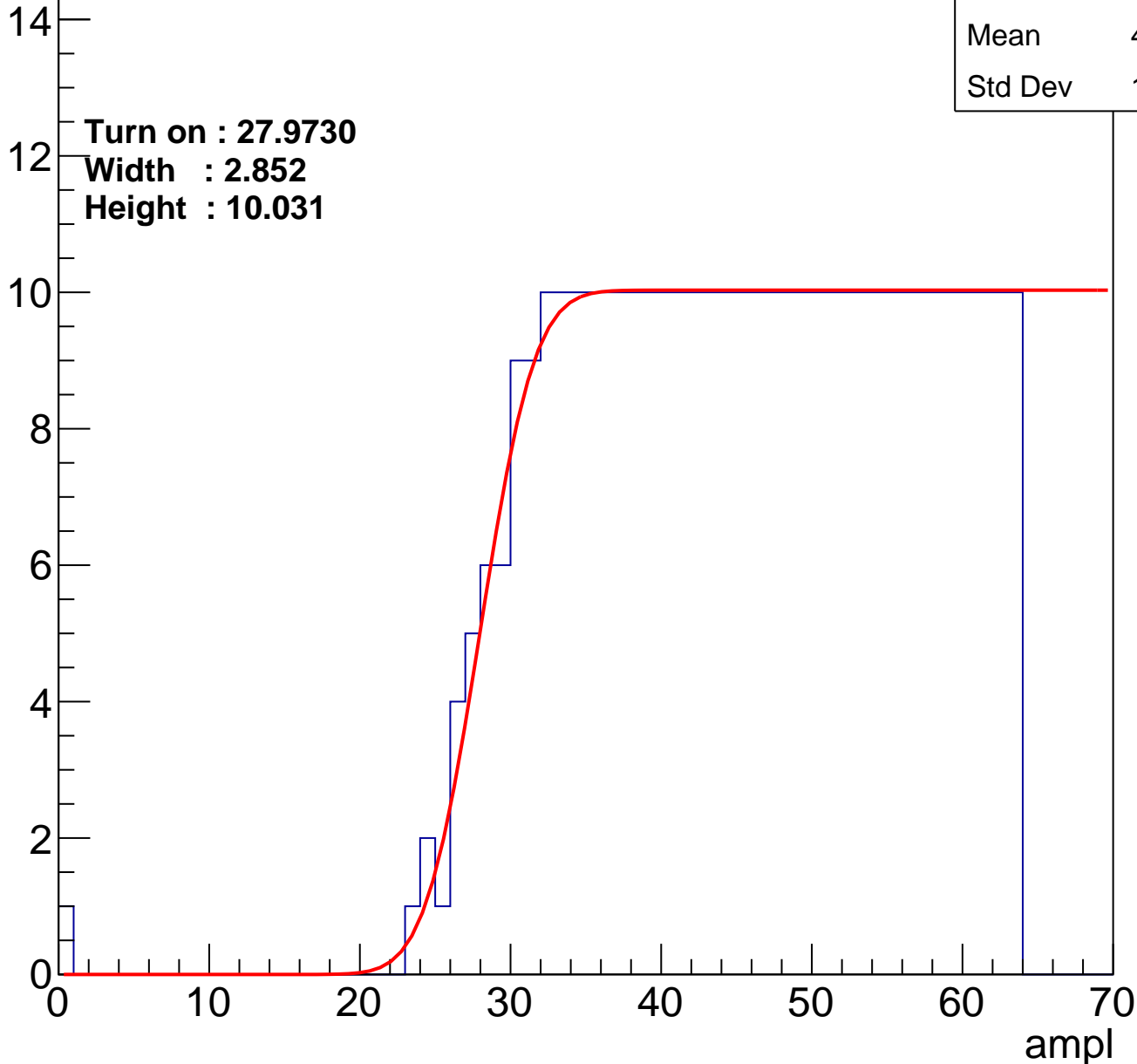
|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.13 |
| Std Dev | 10.89 |

Turn on : 27.9730

Width : 2.852

Height : 10.031

Entry



# B1L003S, U24-ch115

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 385   |
| Mean    | 43.98 |
| Std Dev | 11.78 |

Turn on : 26.3232

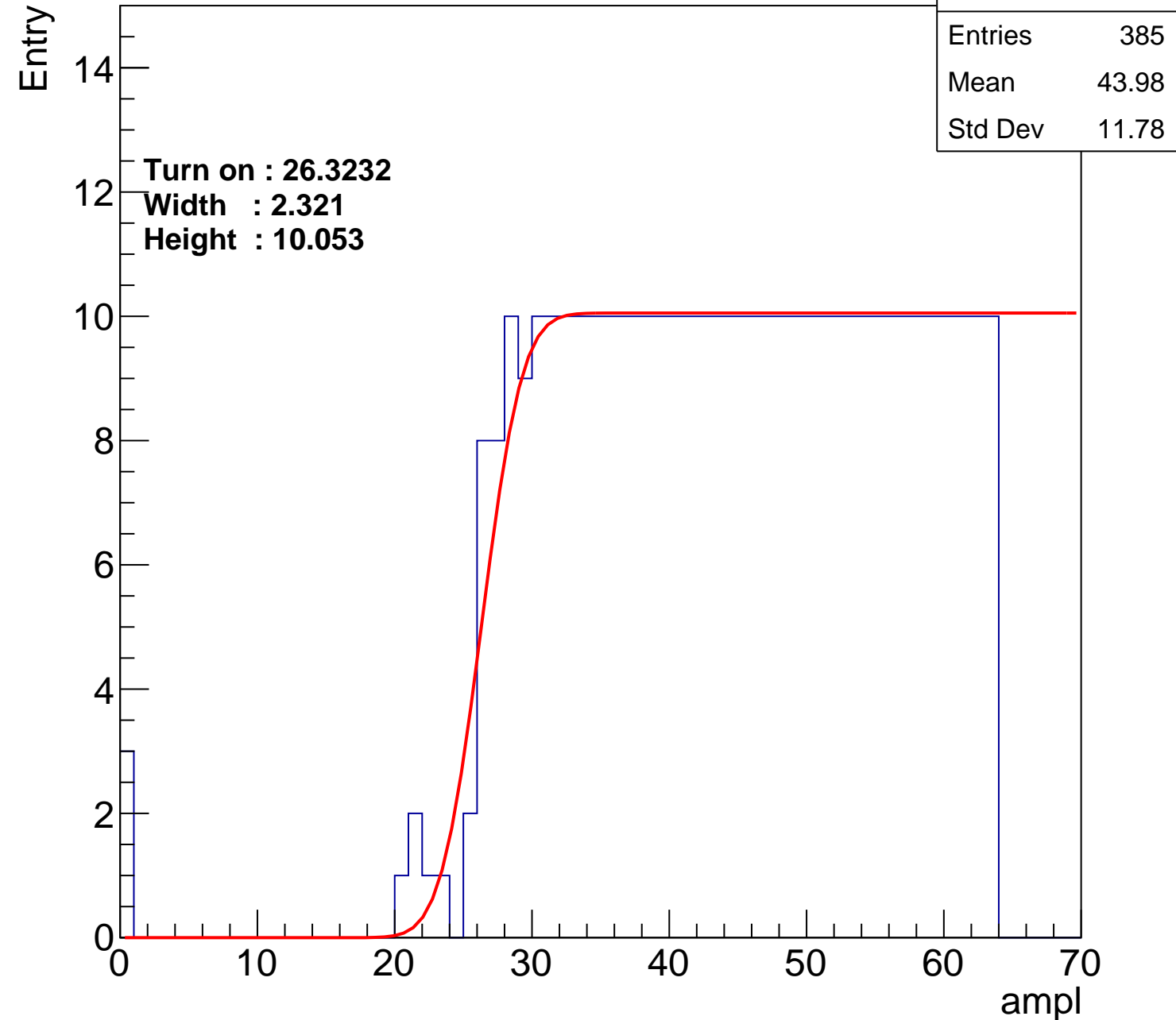
Width : 2.321

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch116

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 367   |
| Mean    | 44.64 |
| Std Dev | 11.93 |

**Turn on : 27.9867**

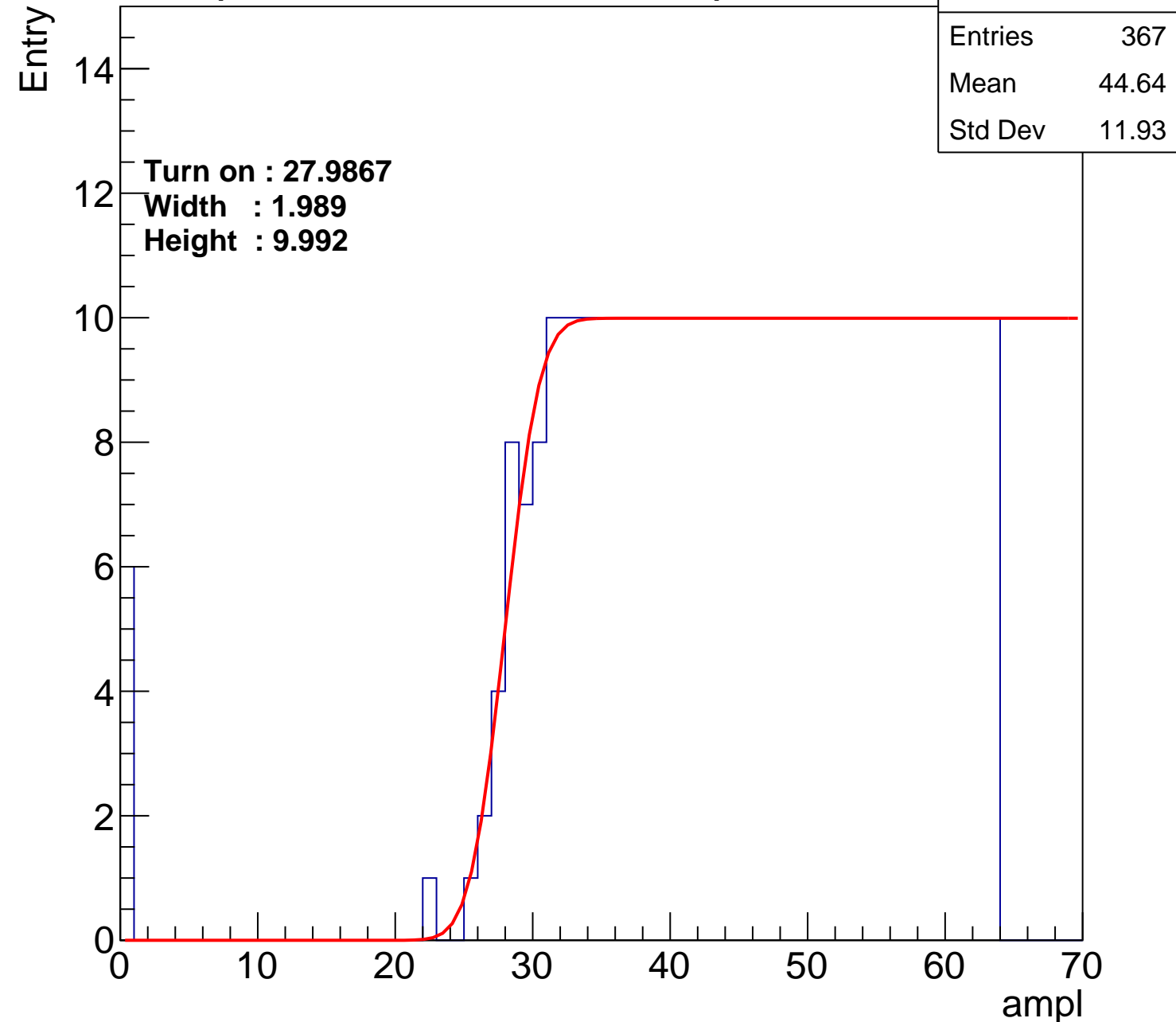
**Width : 1.989**

**Height : 9.992**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch117

calib\_packv5\_042523\_0143.root, FC#13, port D2

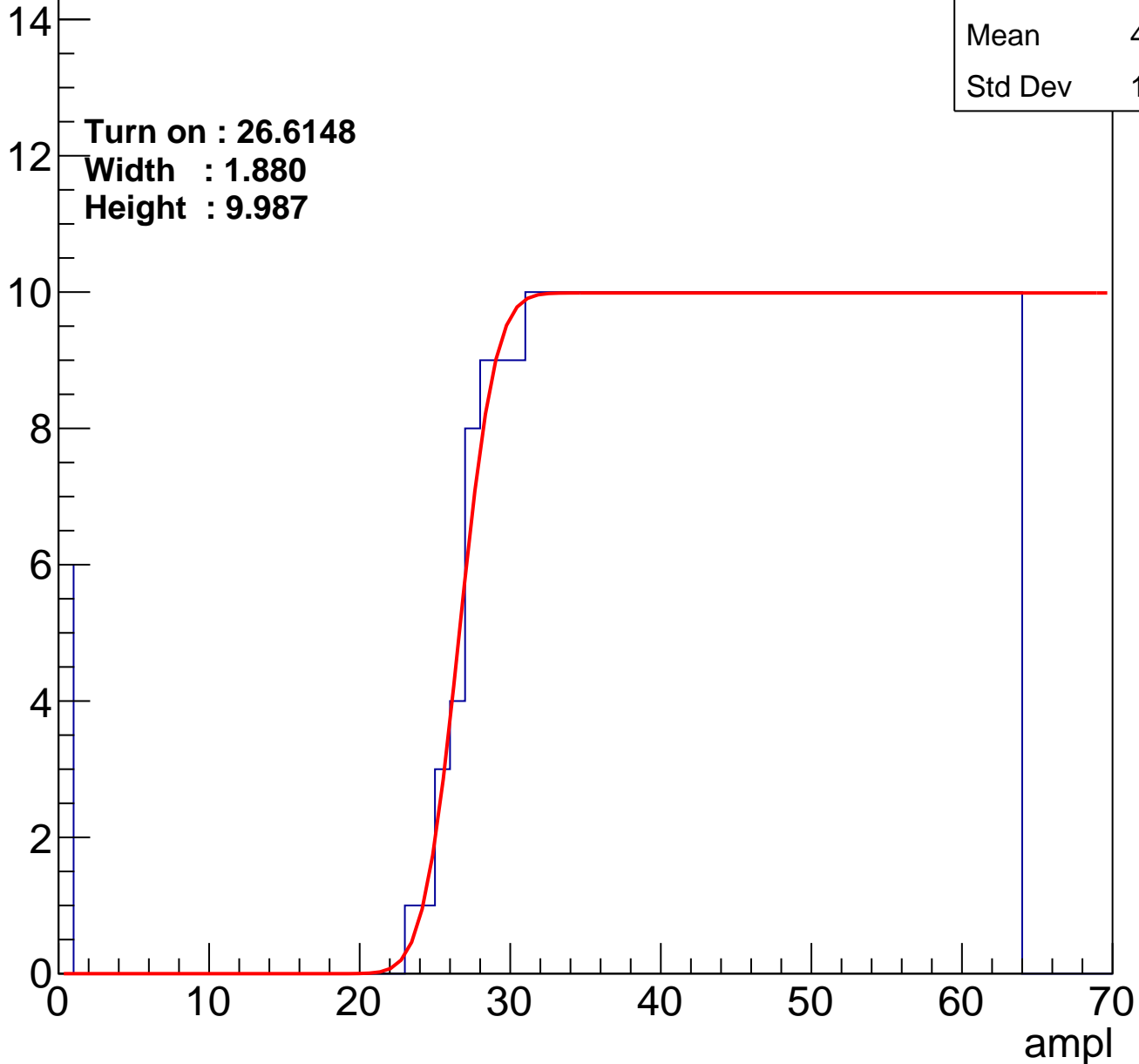
|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.04 |
| Std Dev | 12.15 |

Turn on : 26.6148

Width : 1.880

Height : 9.987

Entry



# B1L003S, U24-ch118

calib\_packv5\_042523\_0143.root, FC#13, port D2

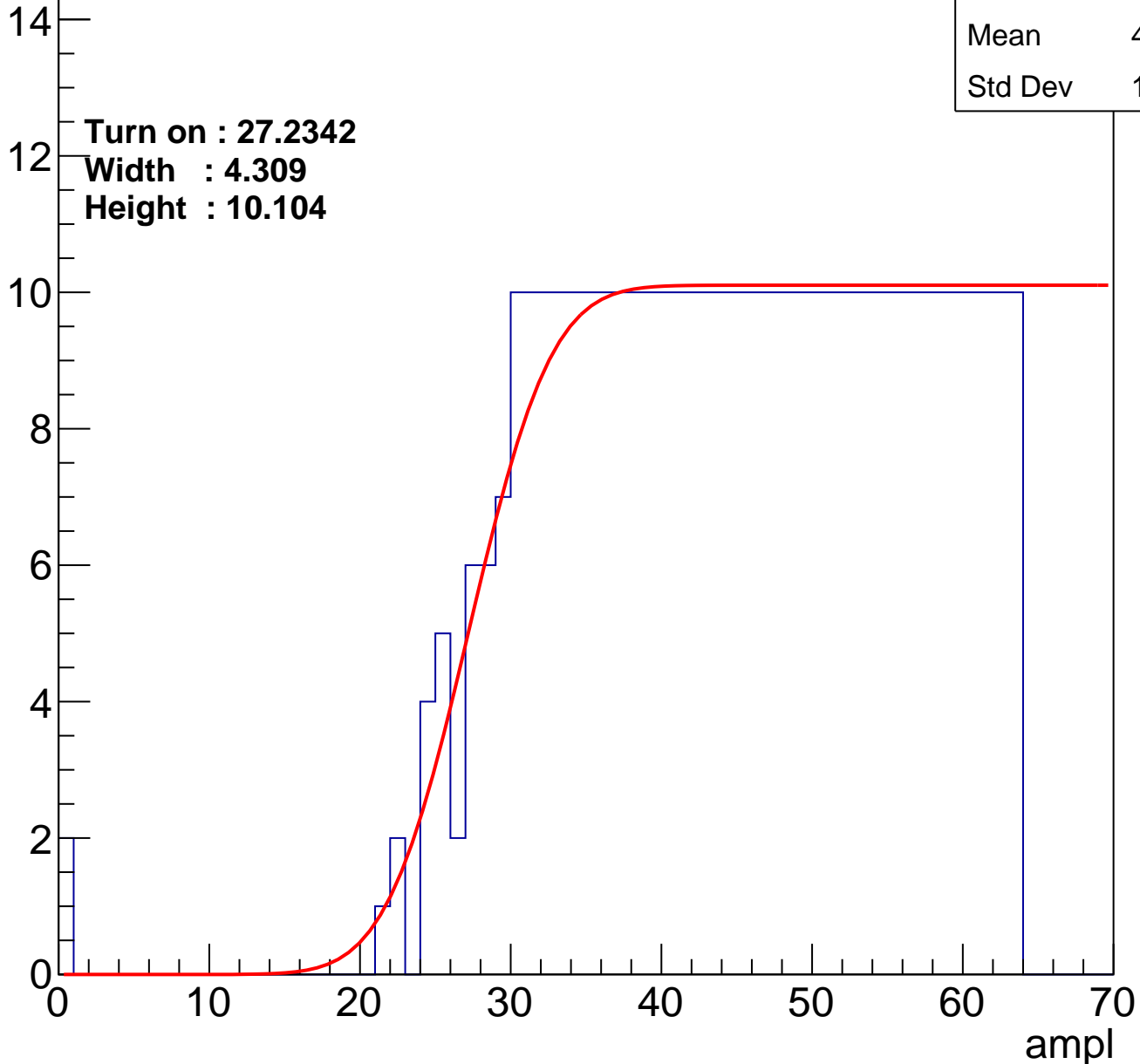
|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.48 |
| Std Dev | 11.43 |

Turn on : 27.2342

Width : 4.309

Height : 10.104

Entry





# B1L003S, U24-ch119

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.78 |
| Std Dev | 10.69 |

Turn on : 29.1518

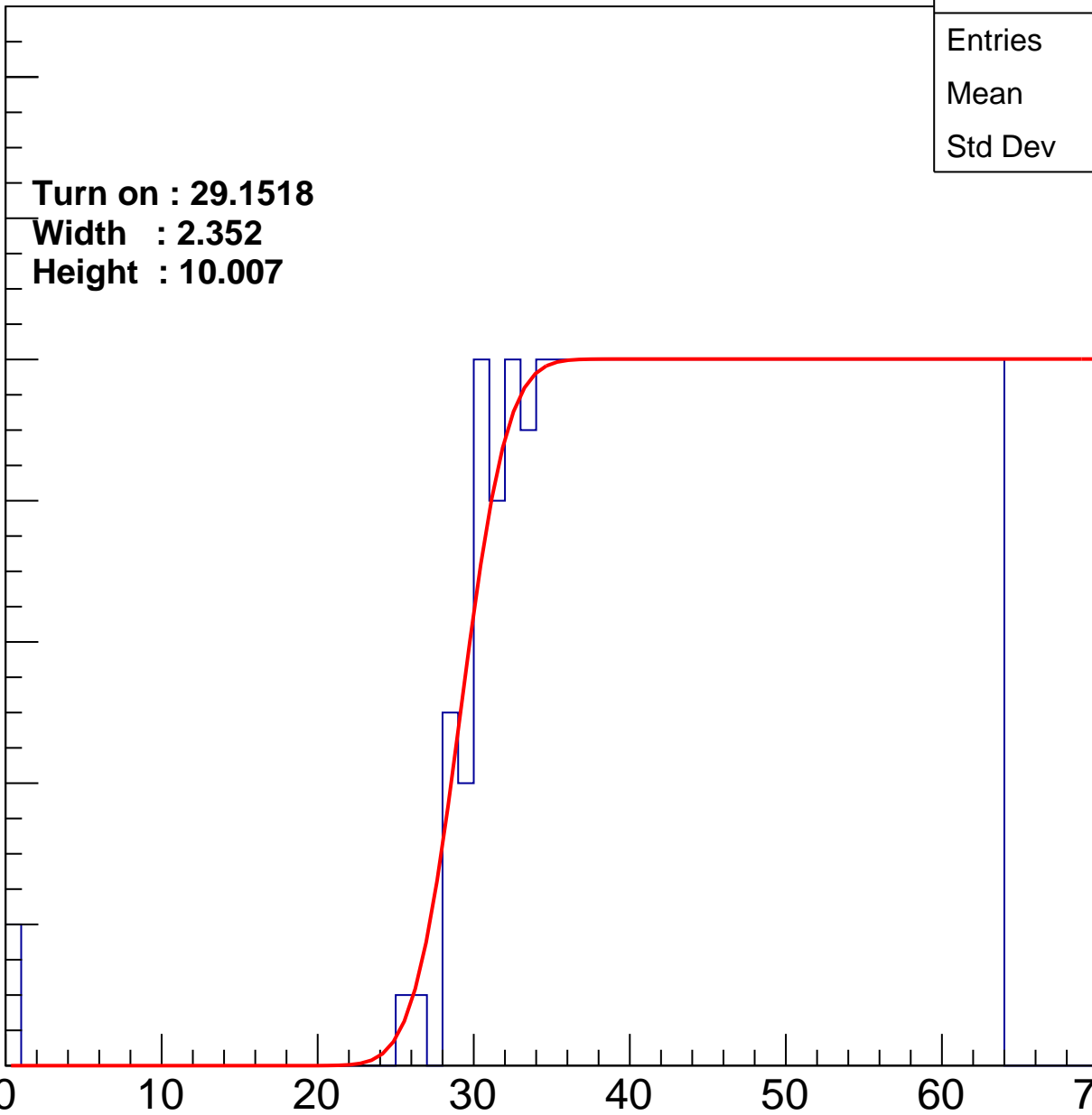
Width : 2.352

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch120

calib\_packv5\_042523\_0143.root, FC#13, port D2

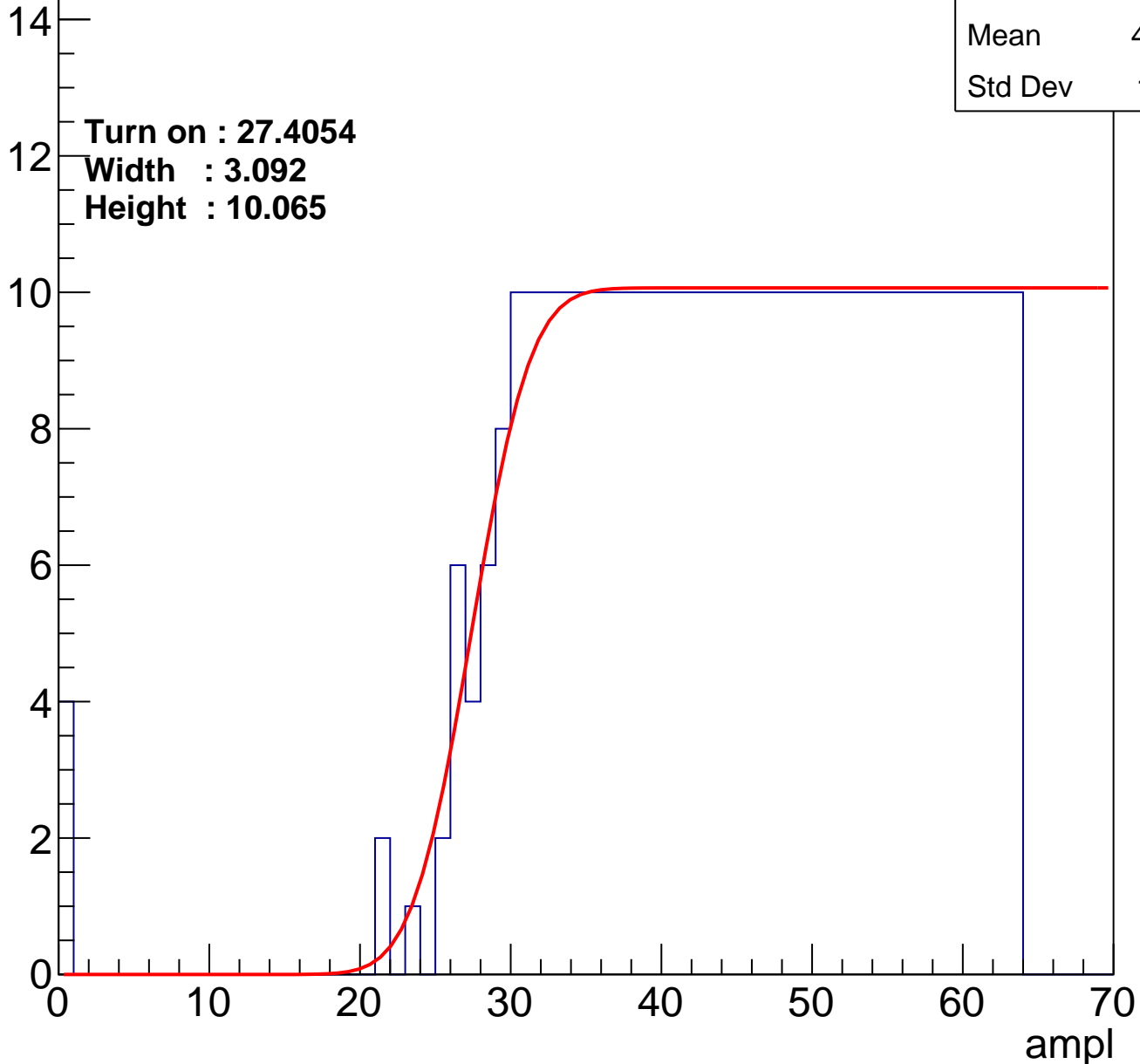
|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.47 |
| Std Dev | 11.71 |

Turn on : 27.4054

Width : 3.092

Height : 10.065

Entry



# B1L003S, U24-ch121

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 43.79 |
| Std Dev | 12.15 |

Turn on : 25.9311

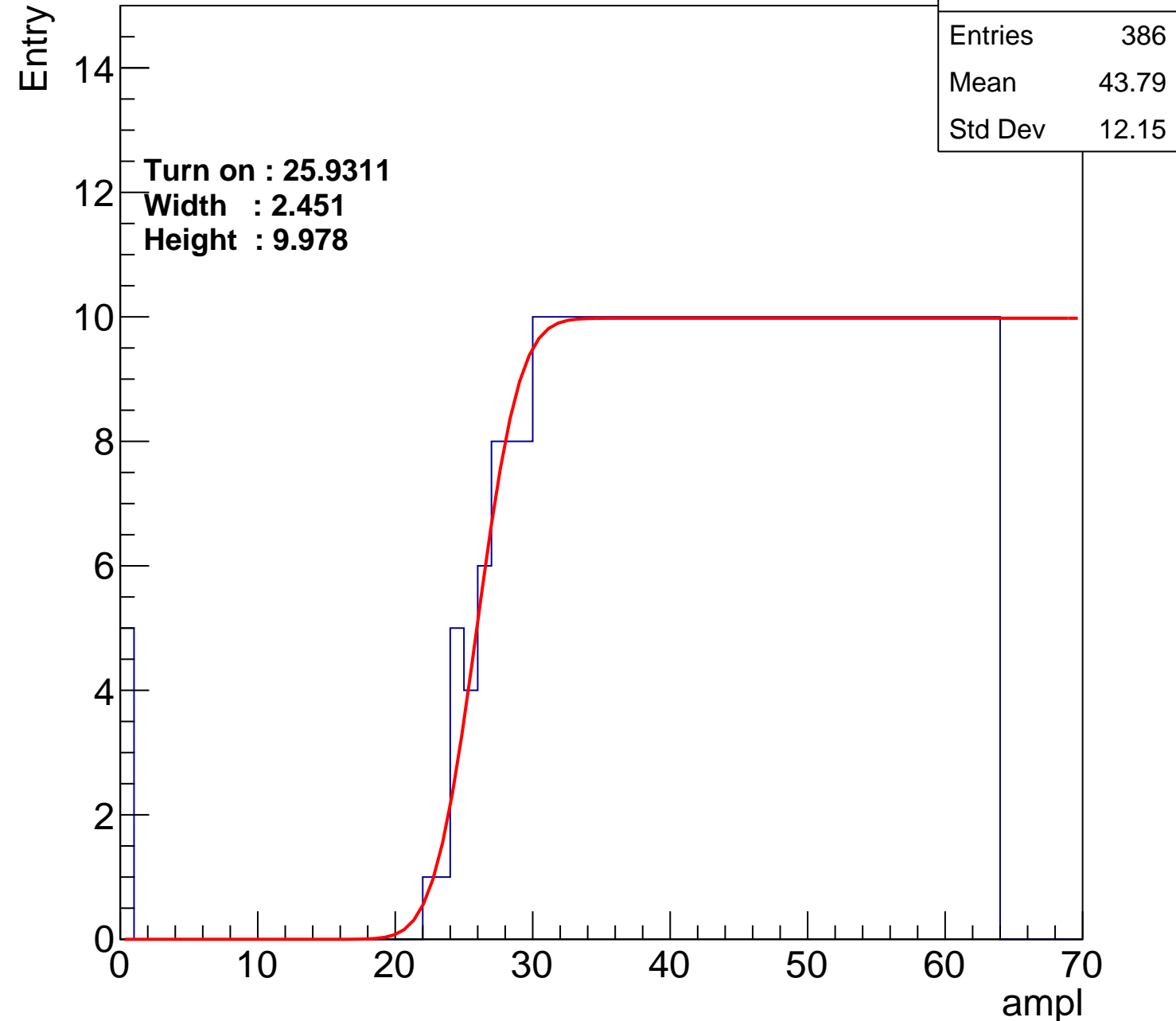
Width : 2.451

Height : 9.978

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch122

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 355   |
| Mean    | 45.34 |
| Std Dev | 11.33 |

Turn on : 29.4571

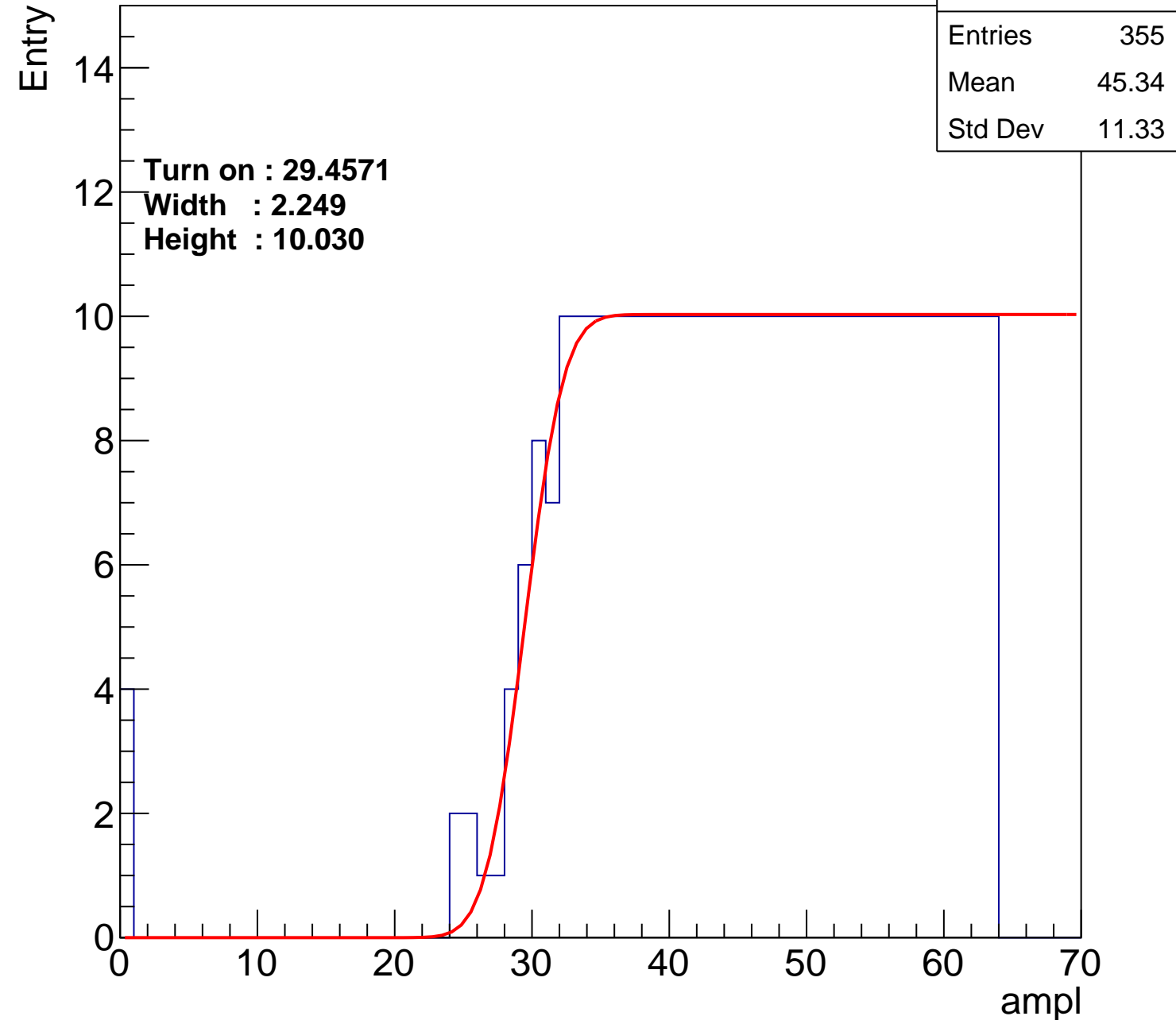
Width : 2.249

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch123

calib\_packv5\_042523\_0143.root, FC#13, port D2

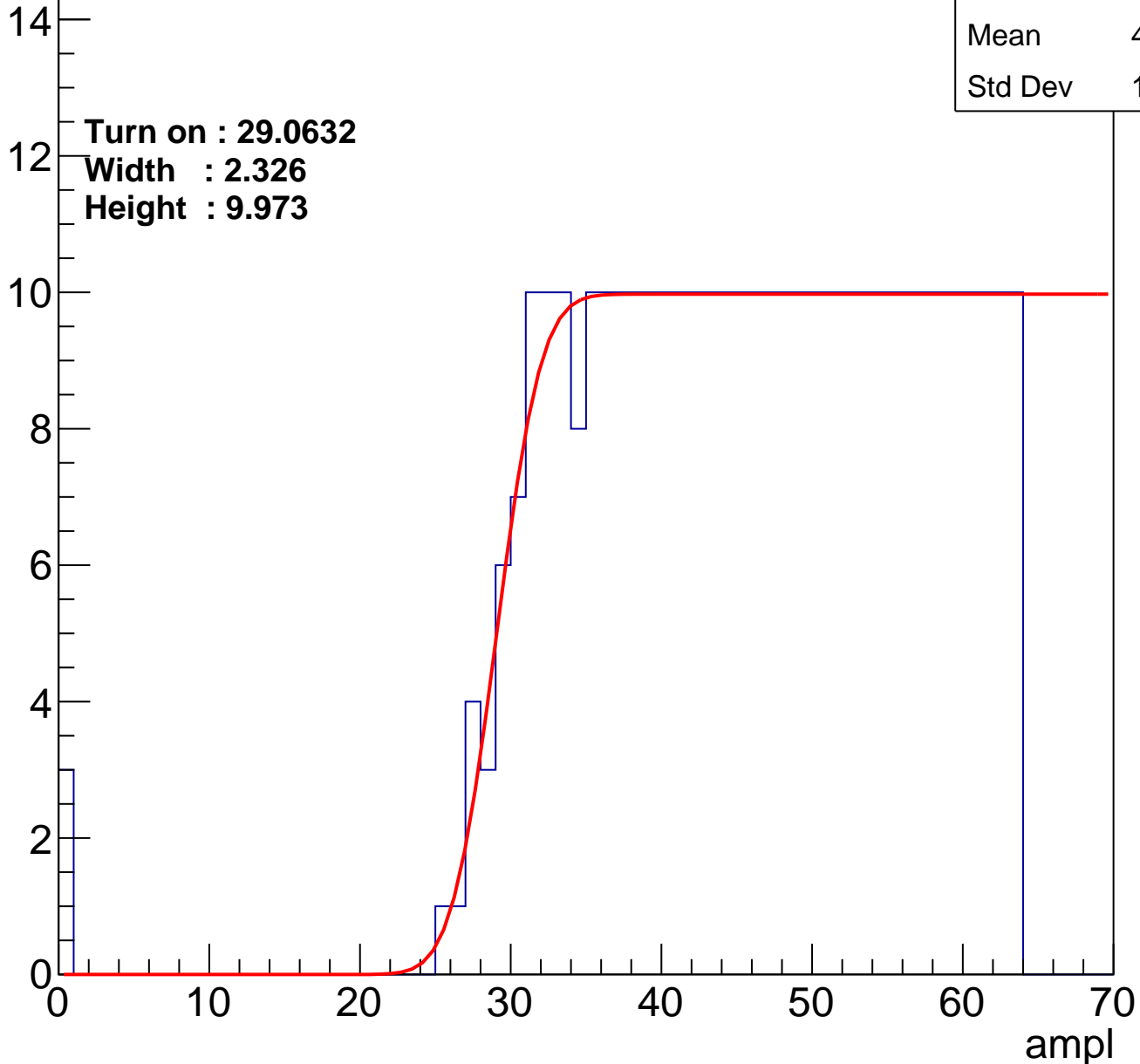
|         |       |
|---------|-------|
| Entries | 353   |
| Mean    | 45.52 |
| Std Dev | 11.04 |

Turn on : 29.0632

Width : 2.326

Height : 9.973

Entry



# B1L003S, U24-ch124

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.74 |
| Std Dev | 11.28 |

Turn on : 27.0342

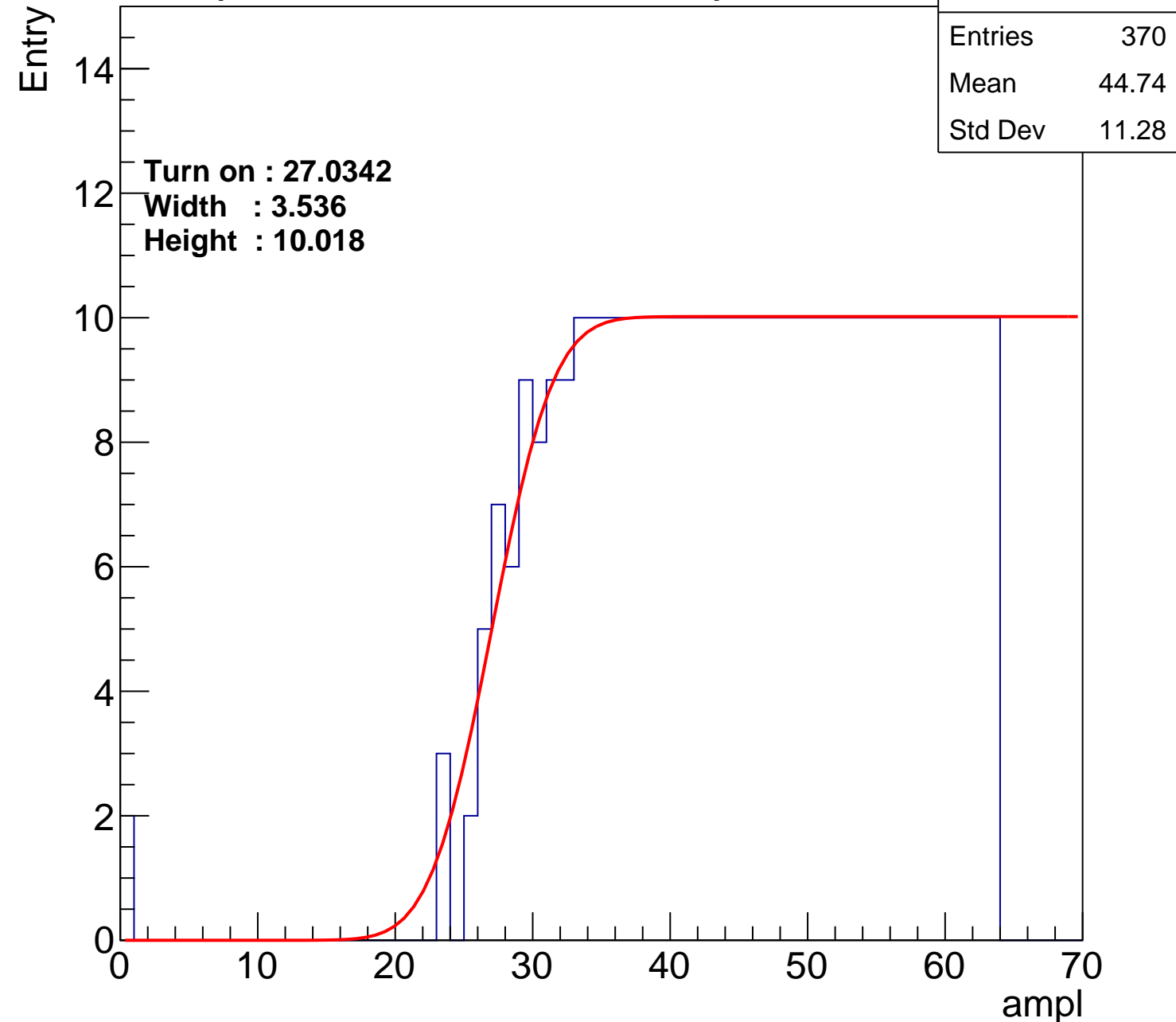
Width : 3.536

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch125

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.21 |
| Std Dev | 11.02 |

Turn on : 28.2054

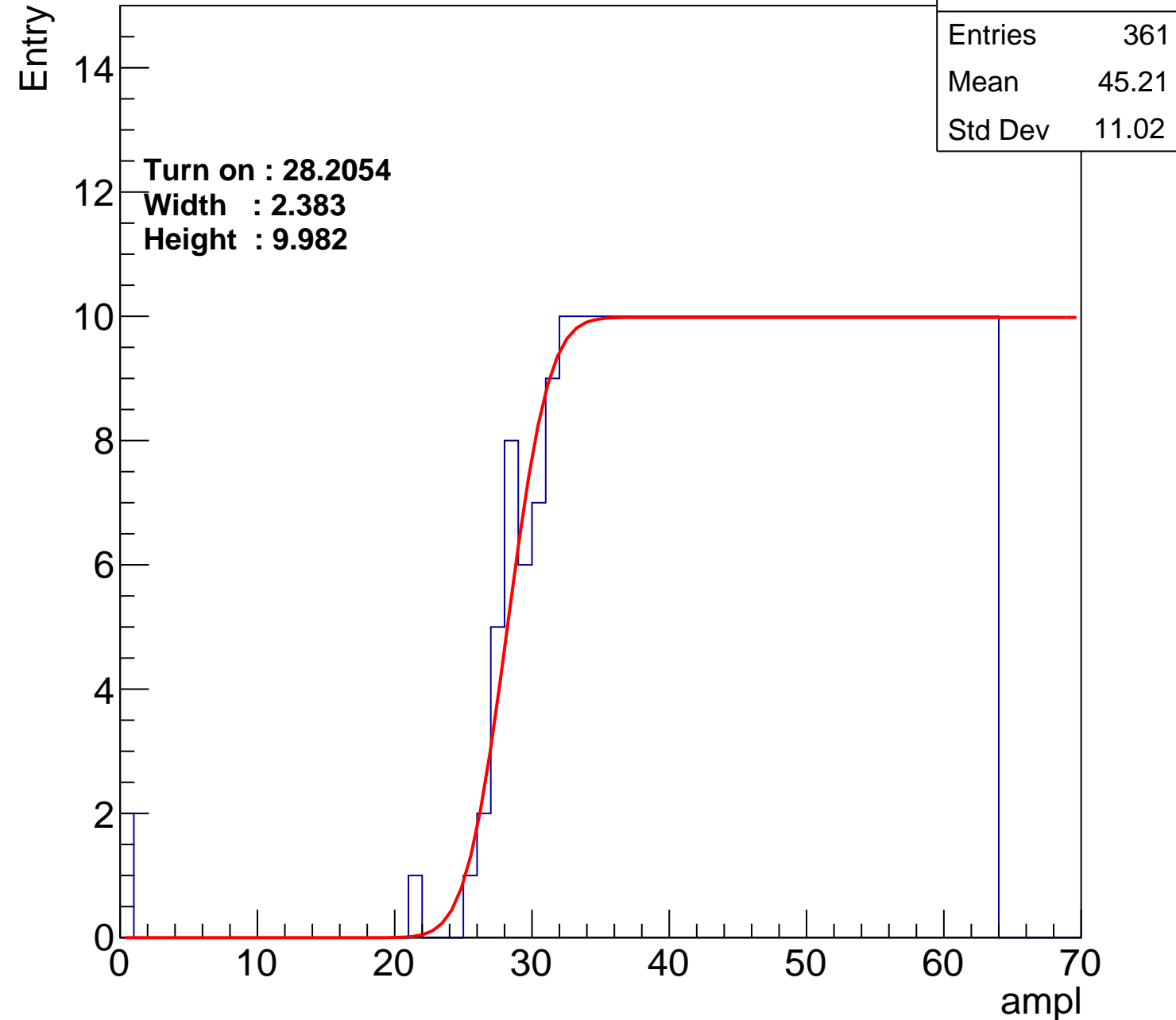
Width : 2.383

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U24-ch126

calib\_packv5\_042523\_0143.root, FC#13, port D2

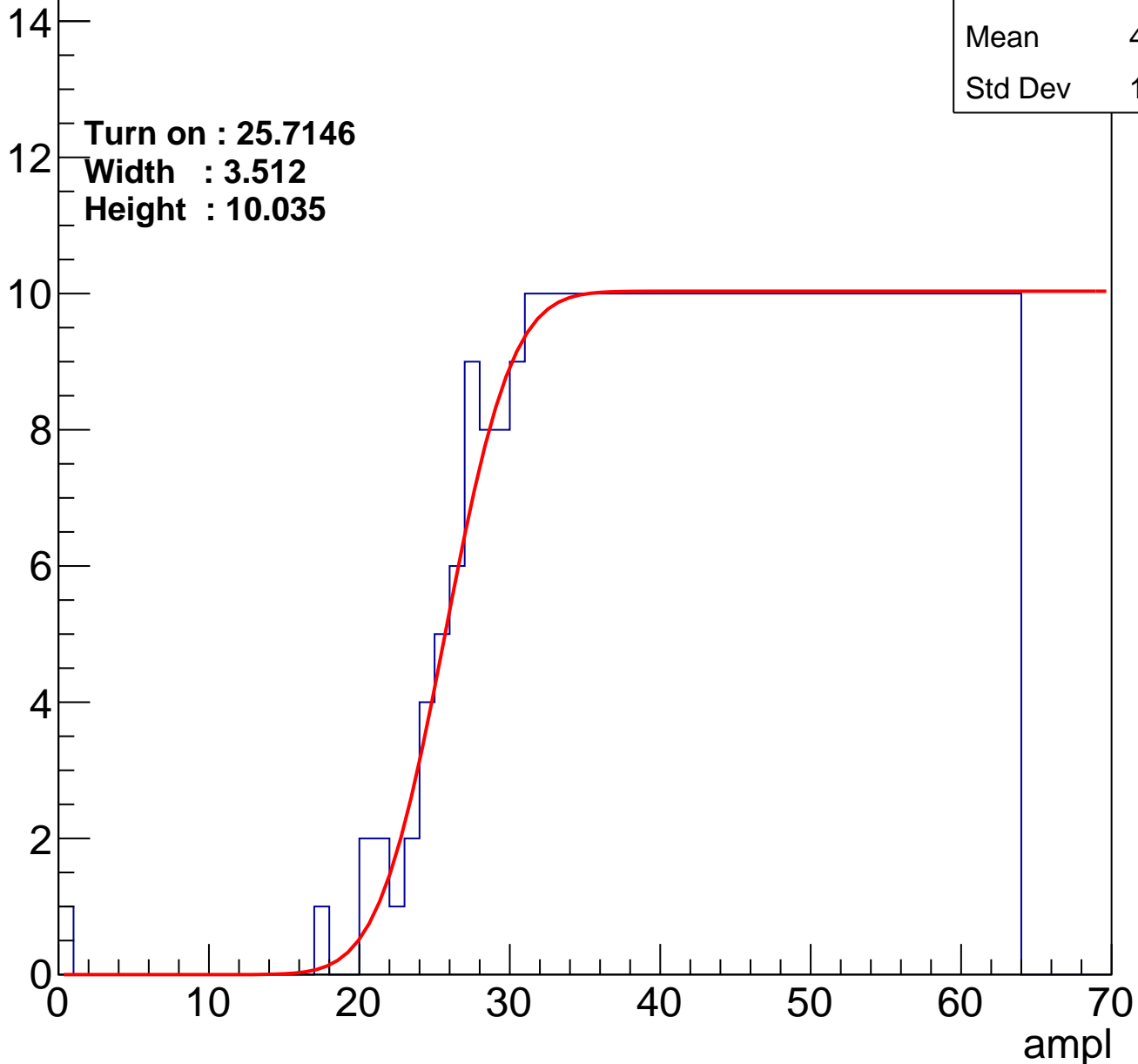
|         |       |
|---------|-------|
| Entries | 388   |
| Mean    | 43.87 |
| Std Dev | 11.65 |

Turn on : 25.7146

Width : 3.512

Height : 10.035

Entry





# B1L003S, U24-ch127

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.15 |
| Std Dev | 11.05 |

Turn on : 27.9264

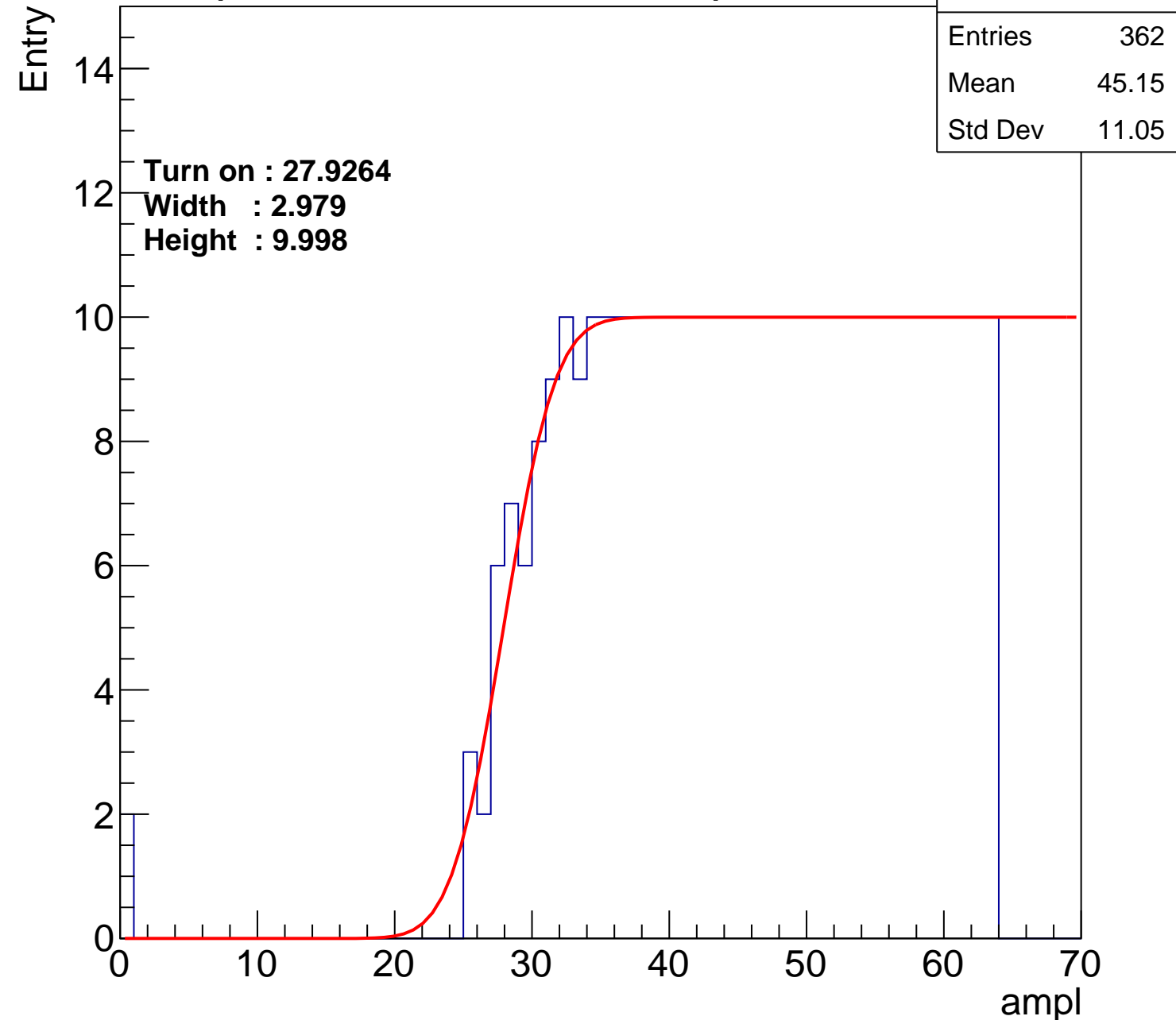
Width : 2.979

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



B1L003S, U24-ch127

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.15 |
| Std Dev | 11.05 |

**Turn on : 27.9264**

**Width : 2.979**

**Height : 9.998**

