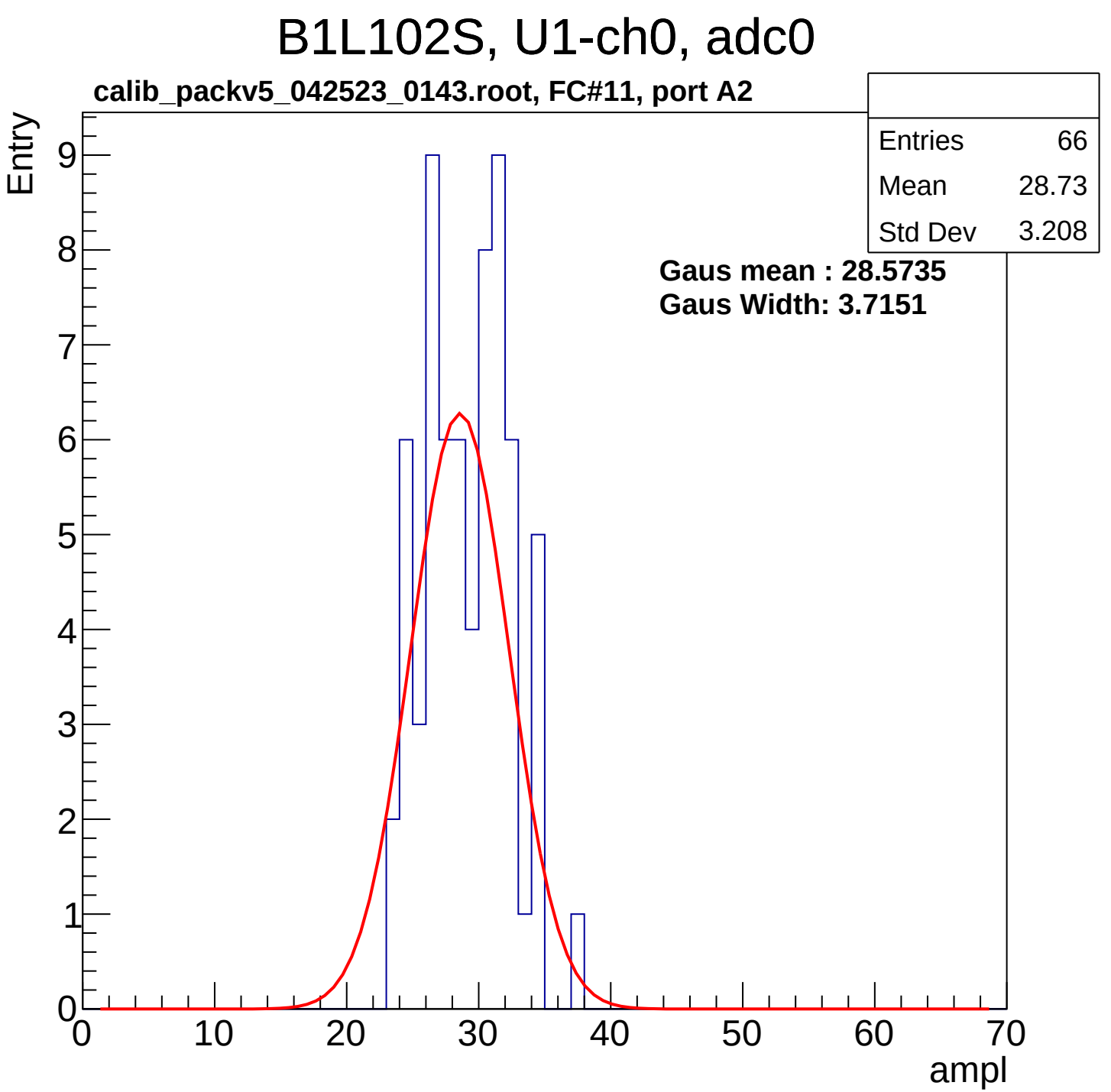
B1L102S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 28.73
Std Dev 3.208
Gaus mean : 28.5735
Gaus Width: 3.7151
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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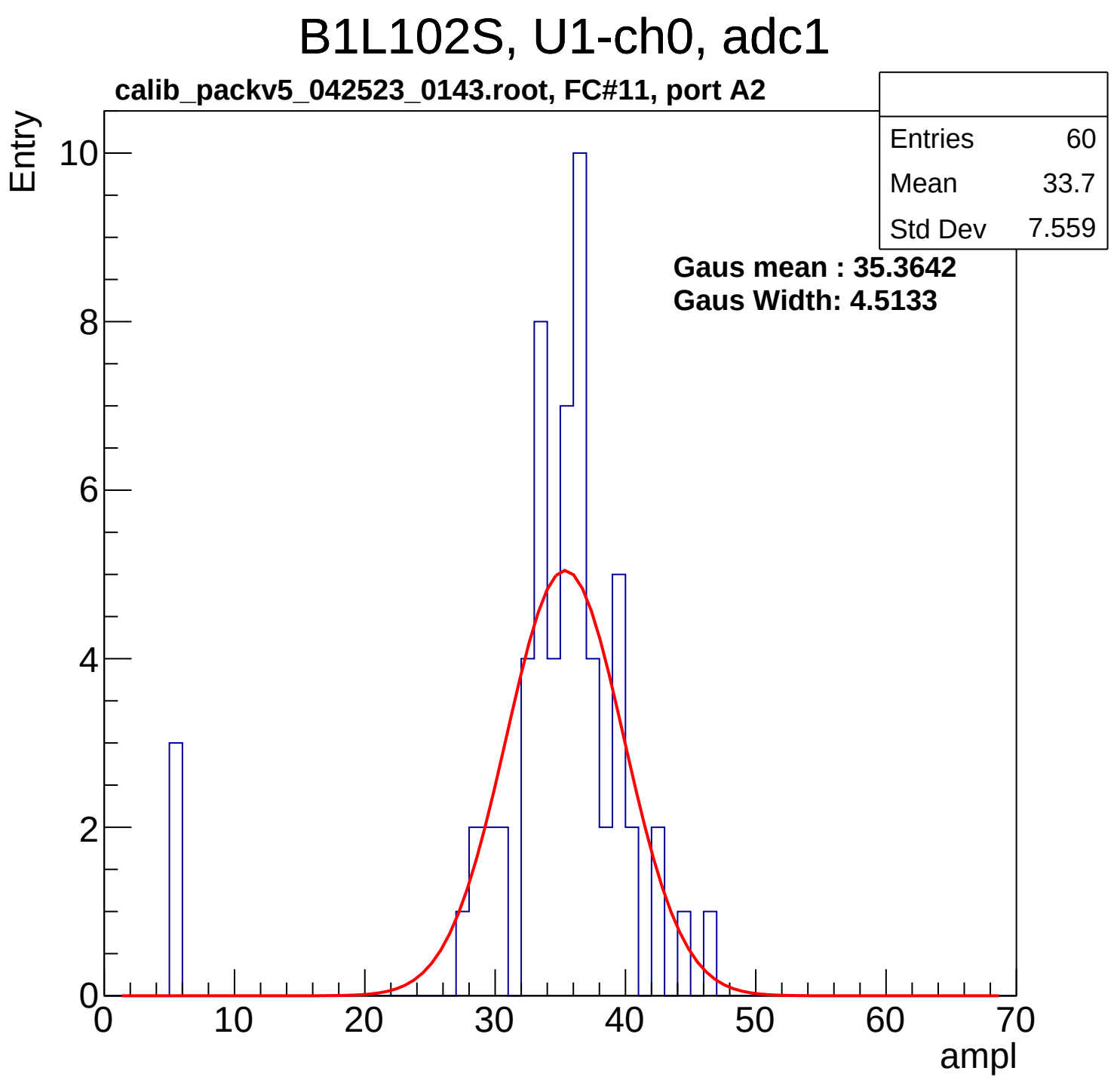
B1L102S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 33.7
Std Dev 7.559
Gaus mean : 35.3642
Gaus Width: 4.5133
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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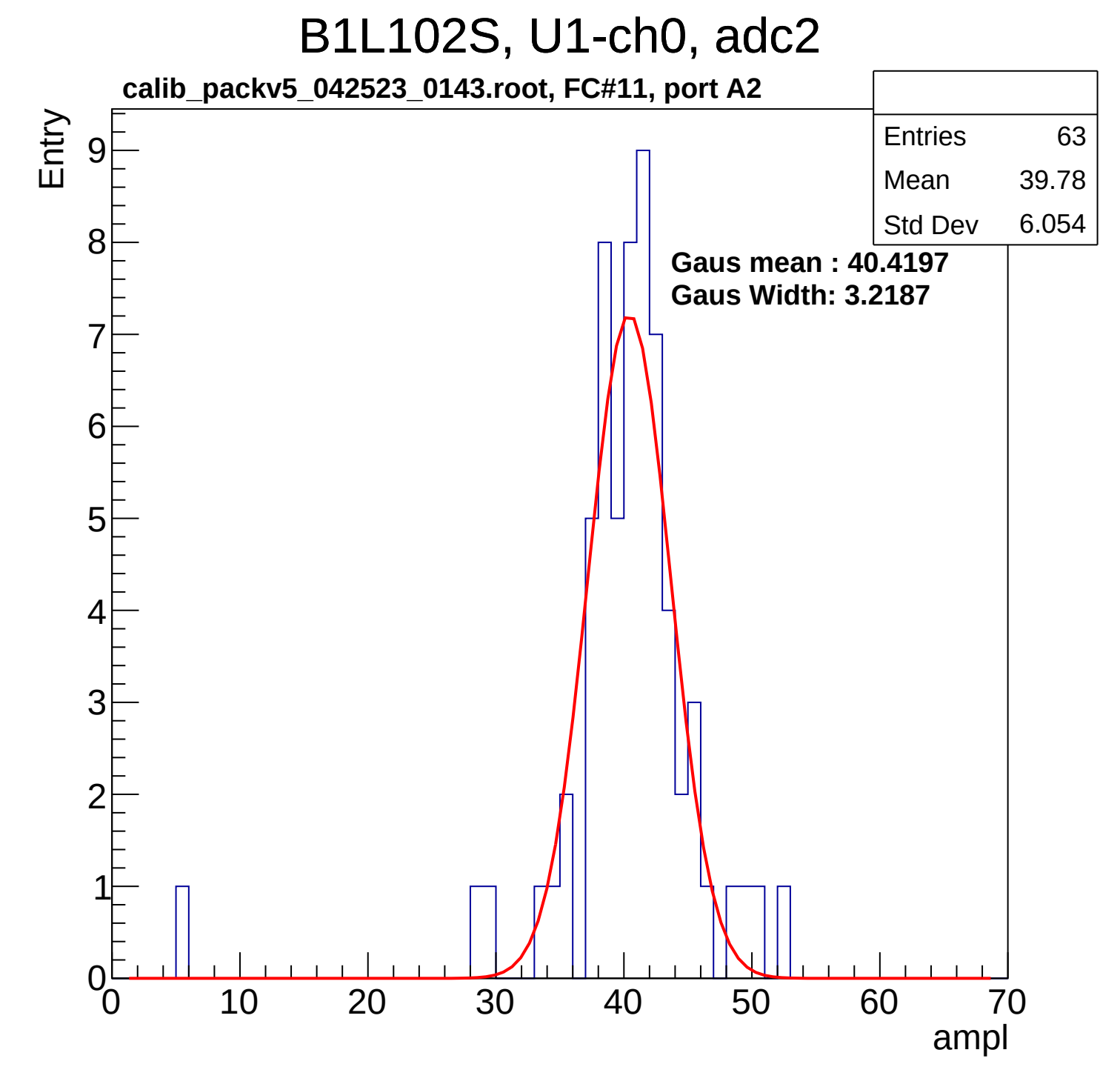
B1L102S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 39.78
Std Dev 6.054
Gaus mean : 40.4197
Gaus Width: 3.2187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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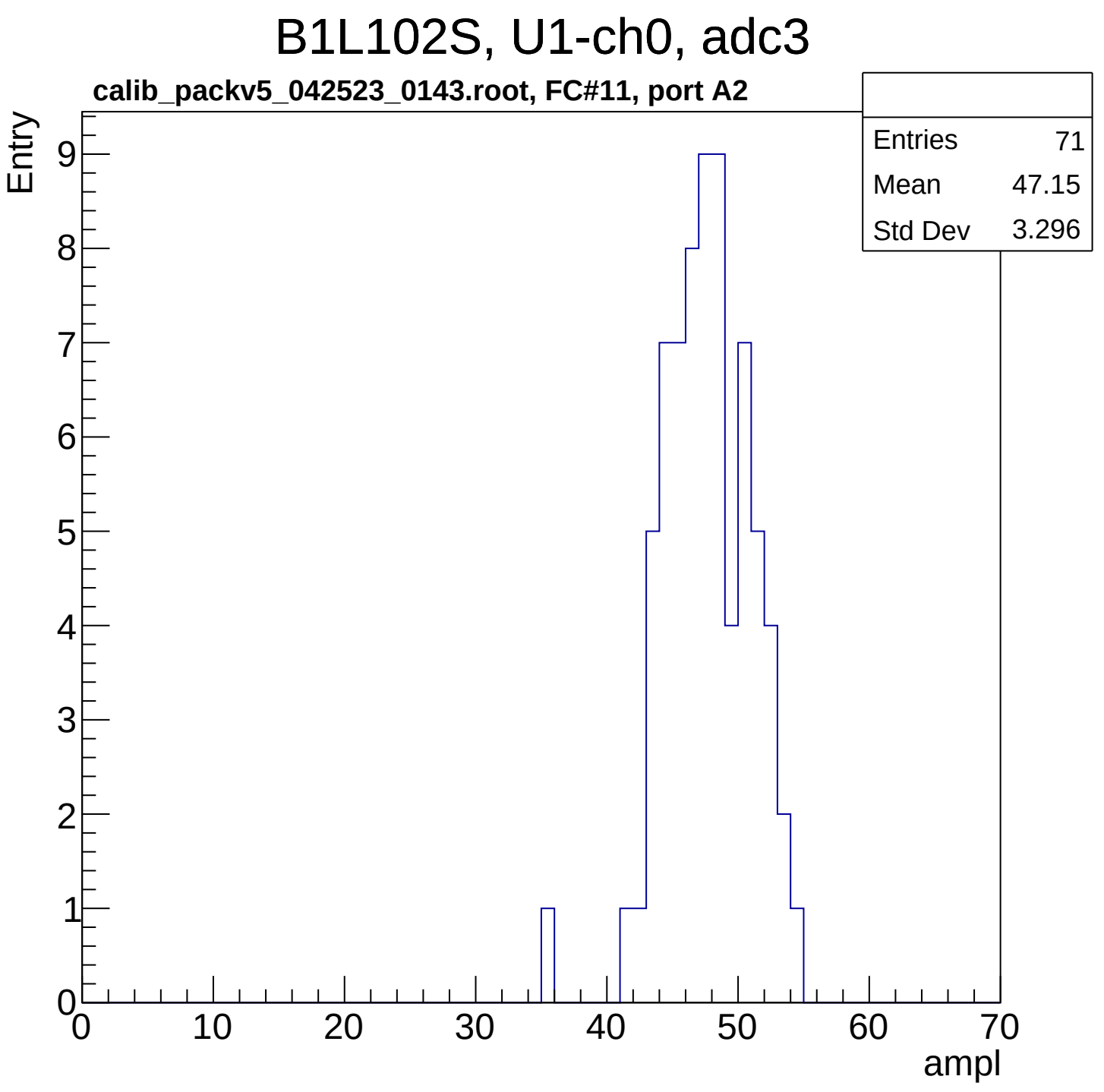
B1L102S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 47.15
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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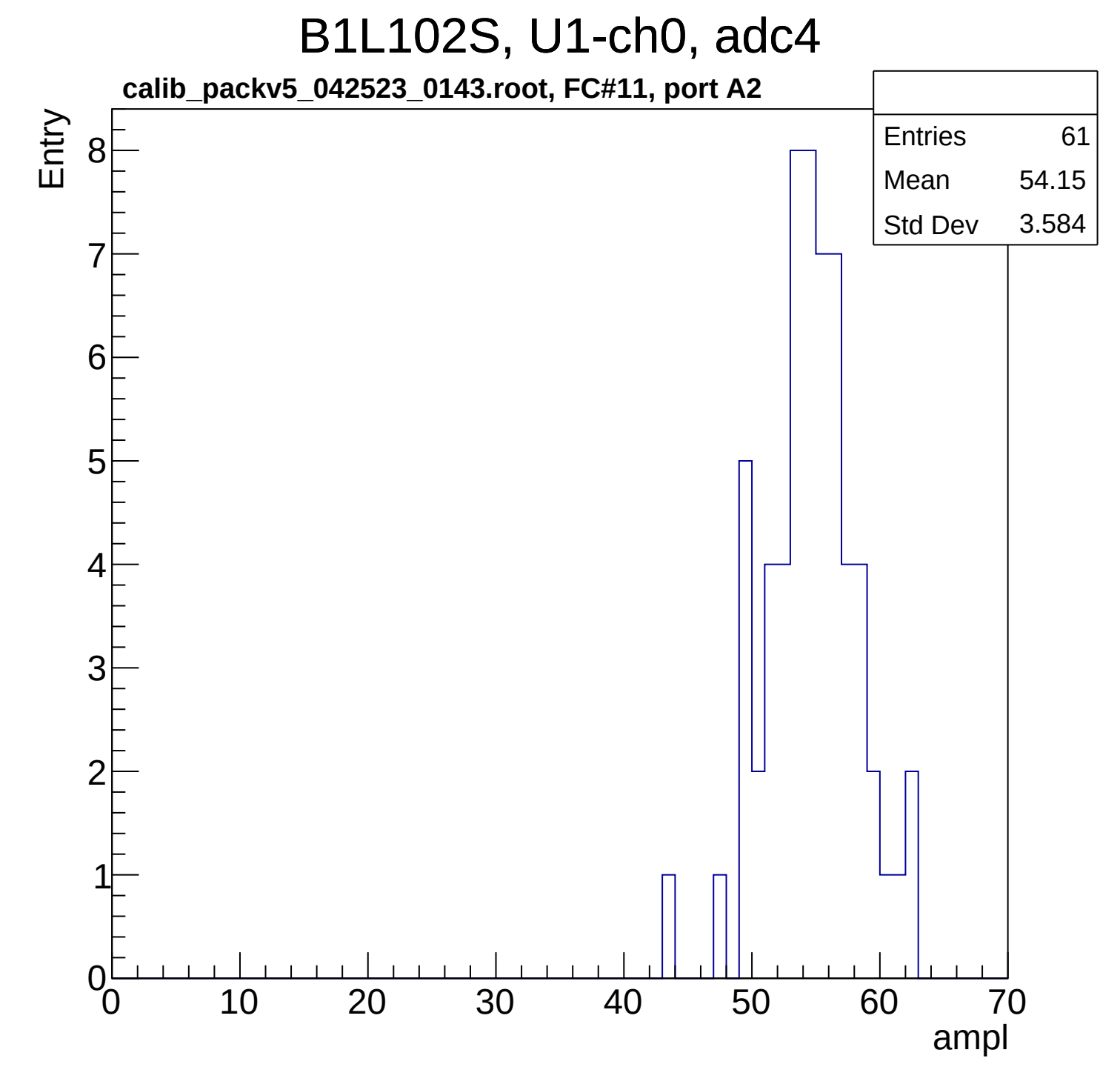
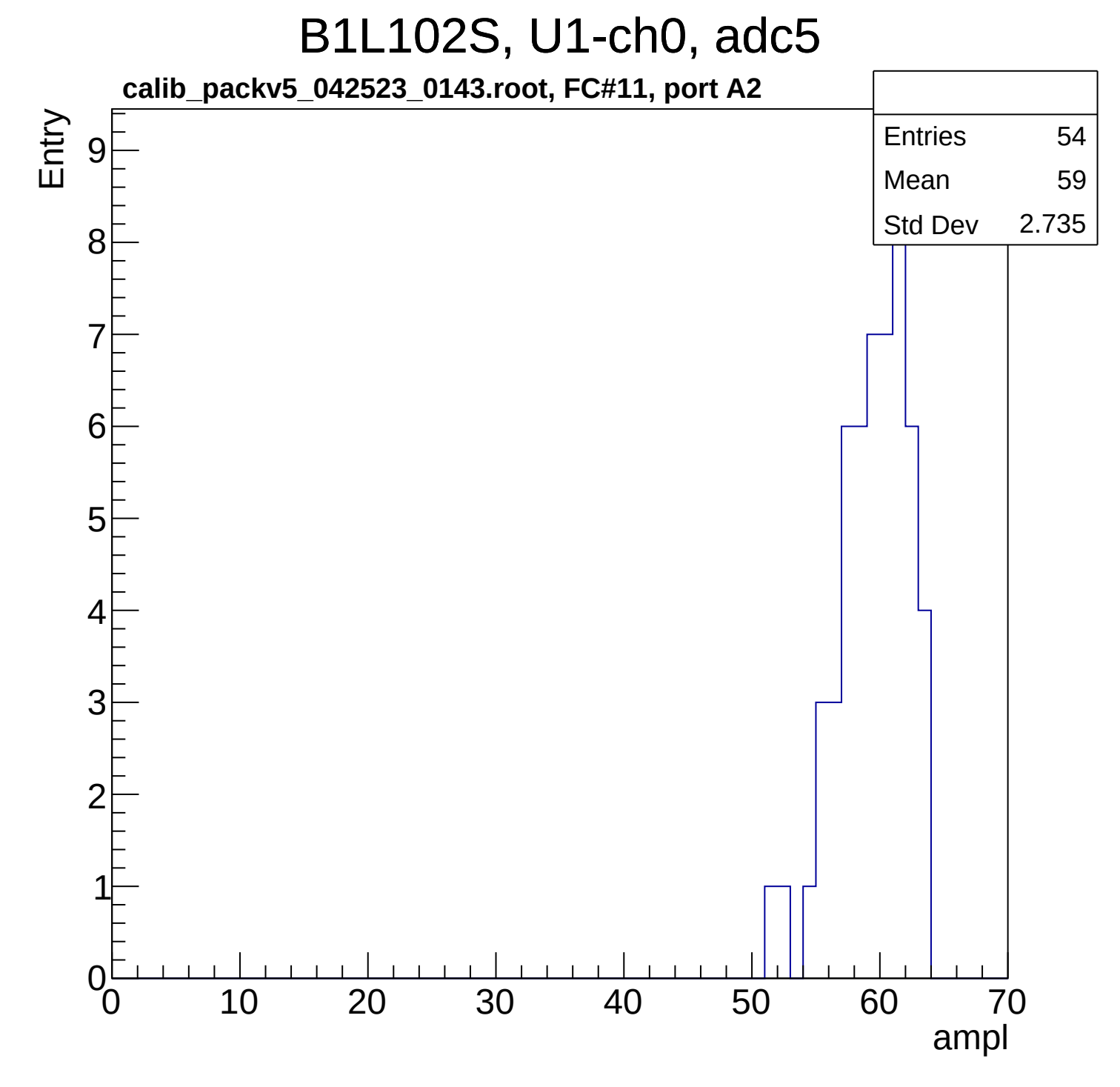

B1L102S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 54.15
Std Dev 3.584
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 59
Std Dev 2.735
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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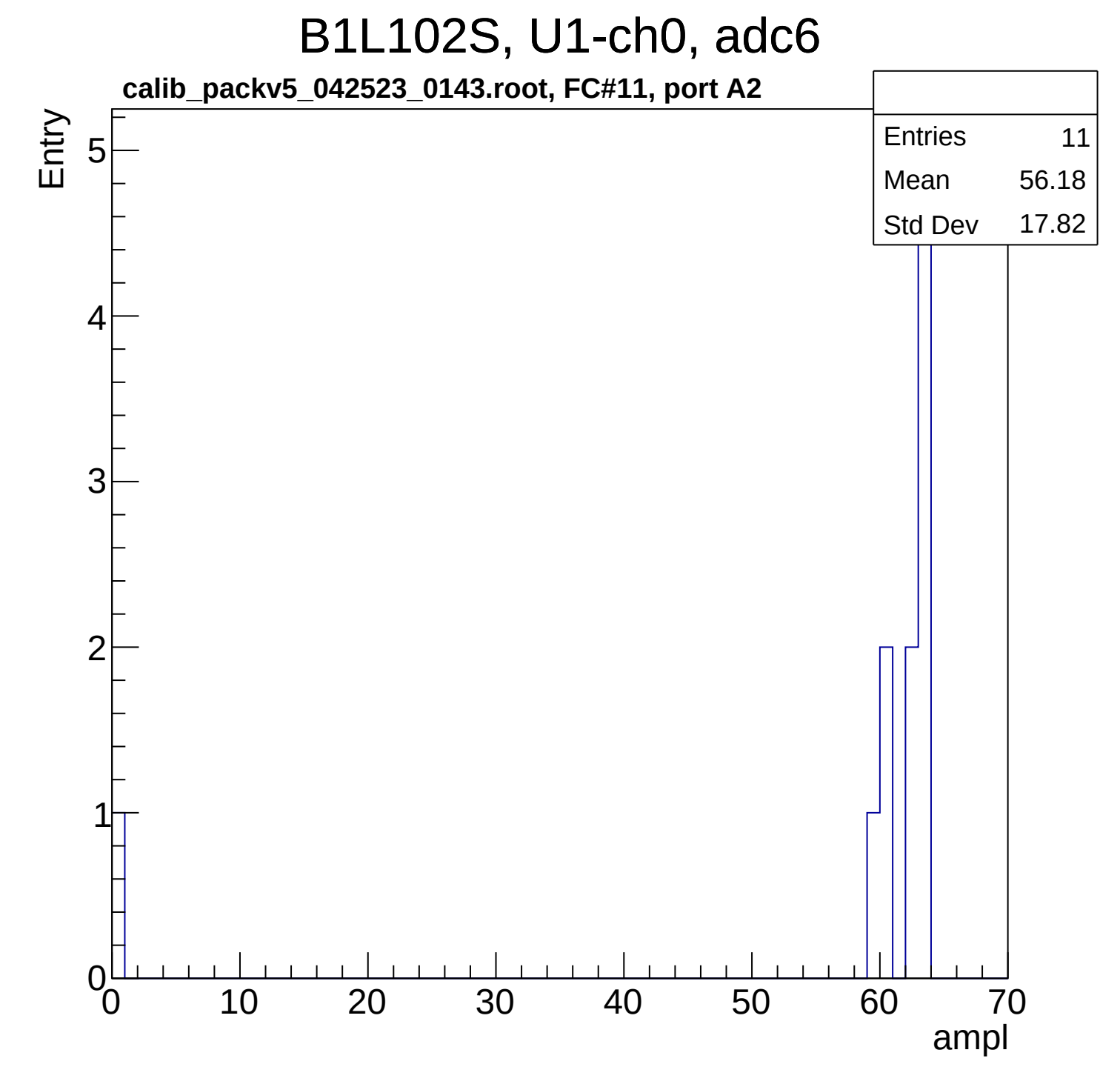
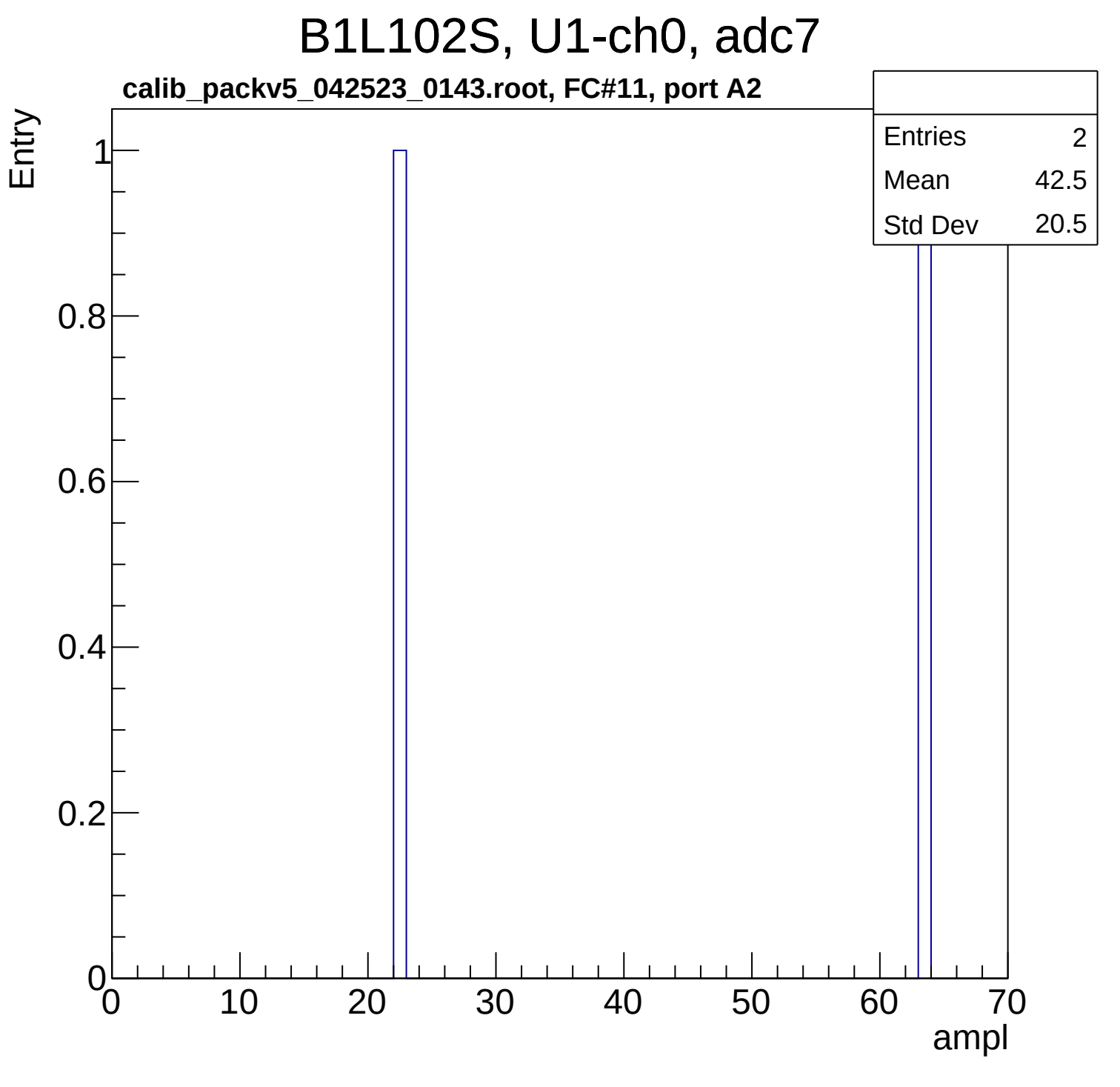
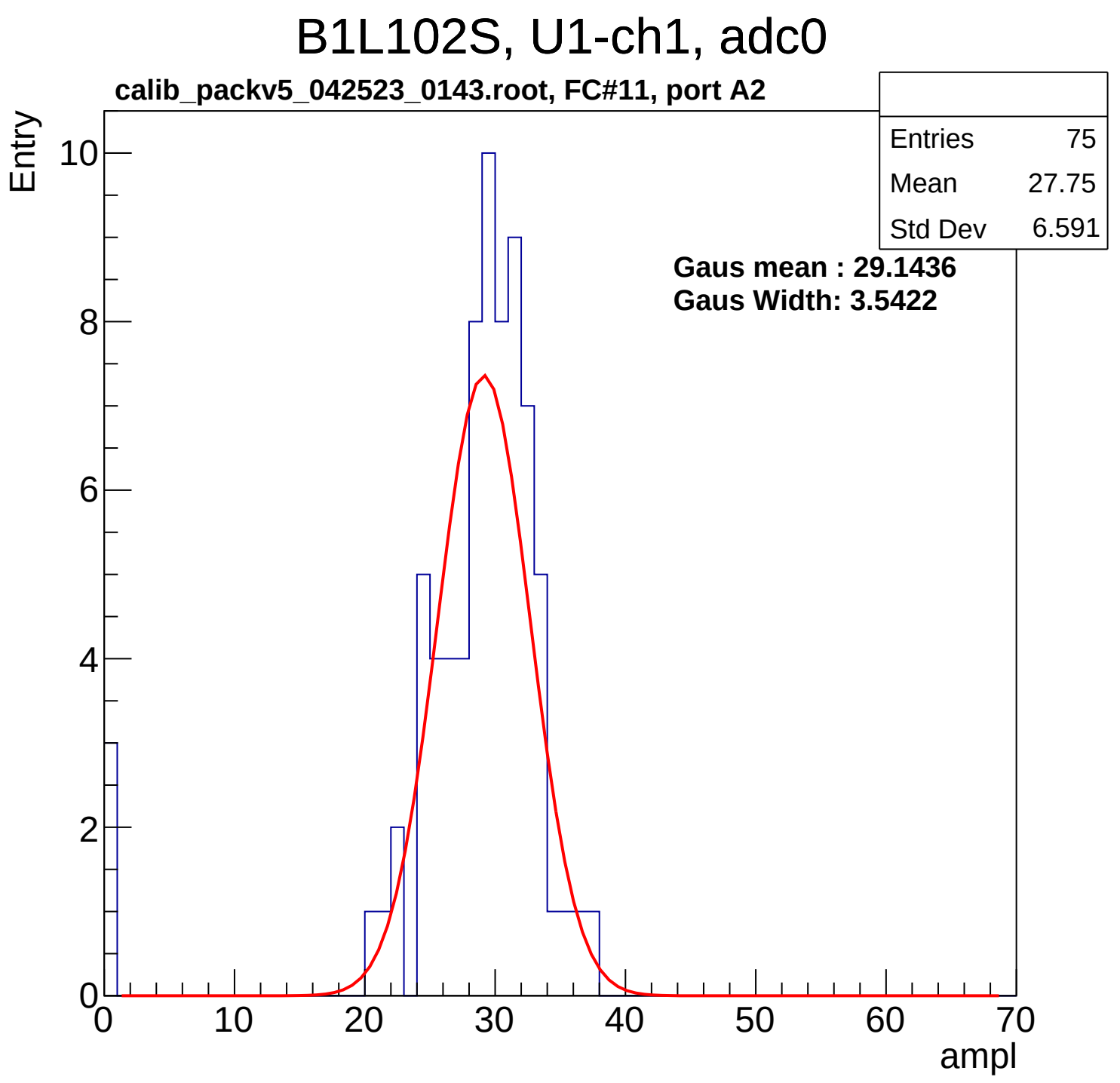
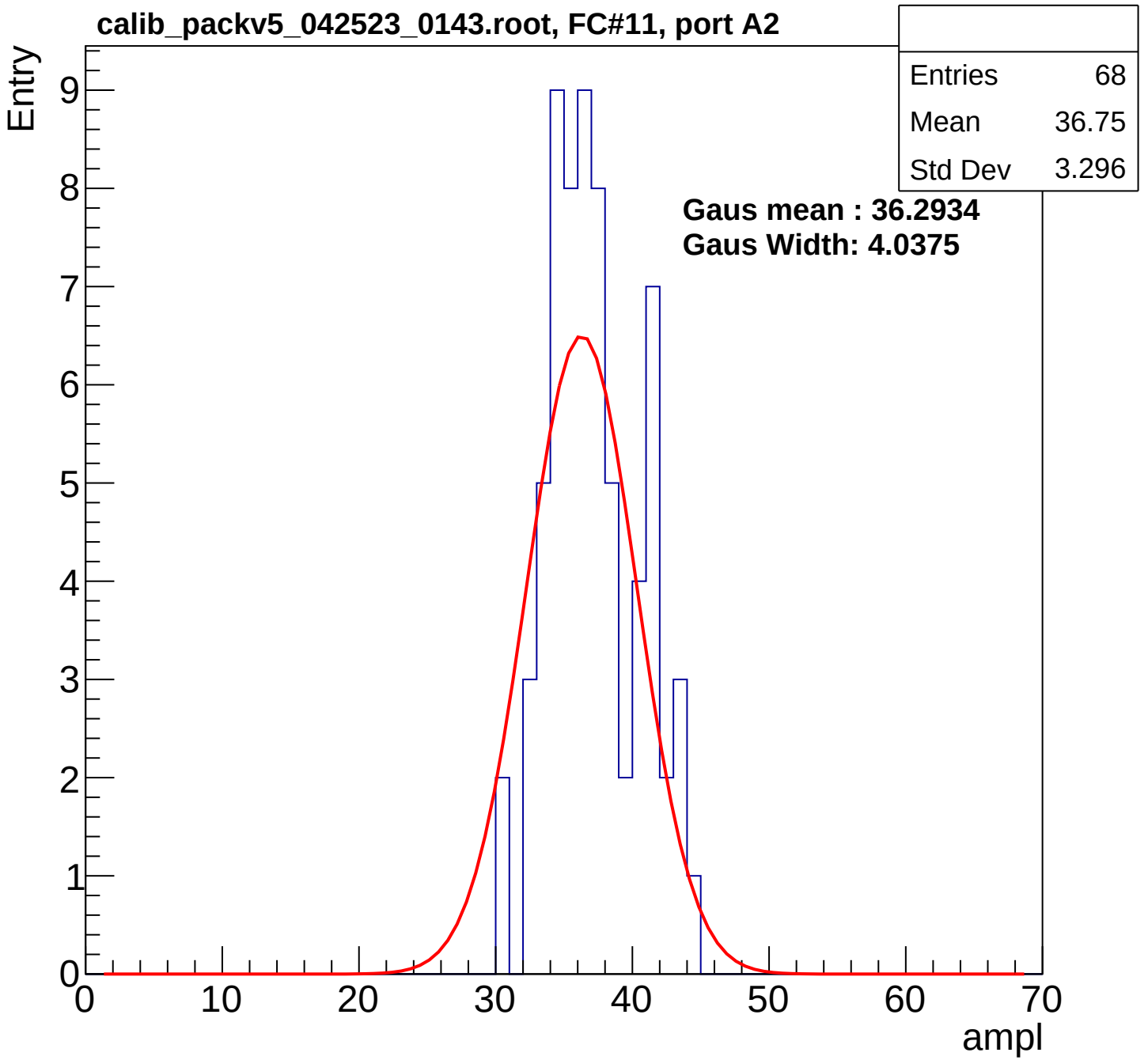

B1L102S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 56.18
Std Dev 17.82
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L102S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 27.75
Std Dev 6.591
Gaus mean : 29.1436
Gaus Width: 3.5422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 36.75
Std Dev 3.296
Gaus mean : 36.2934
Gaus Width: 4.0375
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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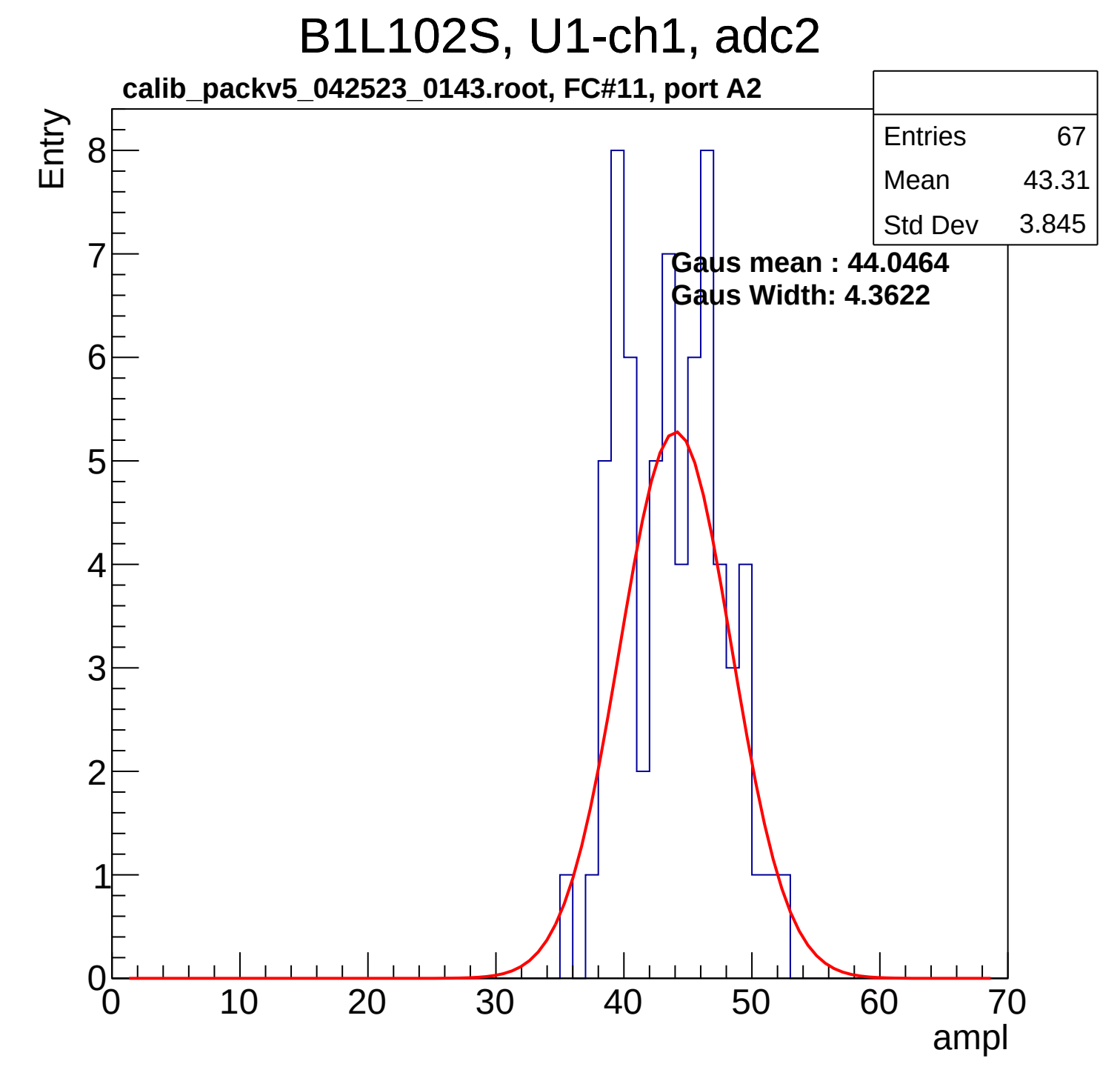
B1L102S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 43.31
Std Dev 3.845
Gaus mean : 44.0464
Gaus Width: 4.3622
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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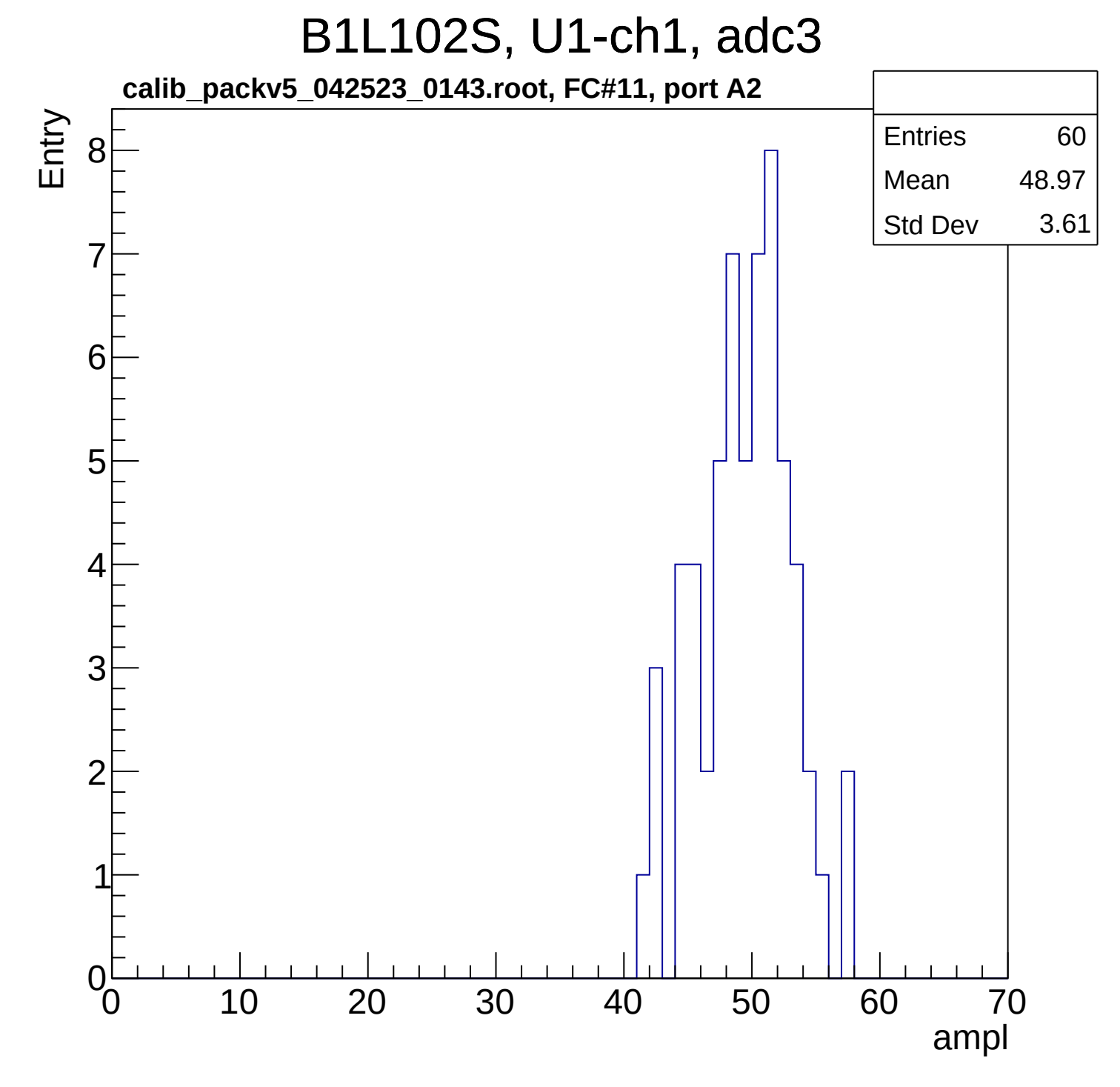
B1L102S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 48.97
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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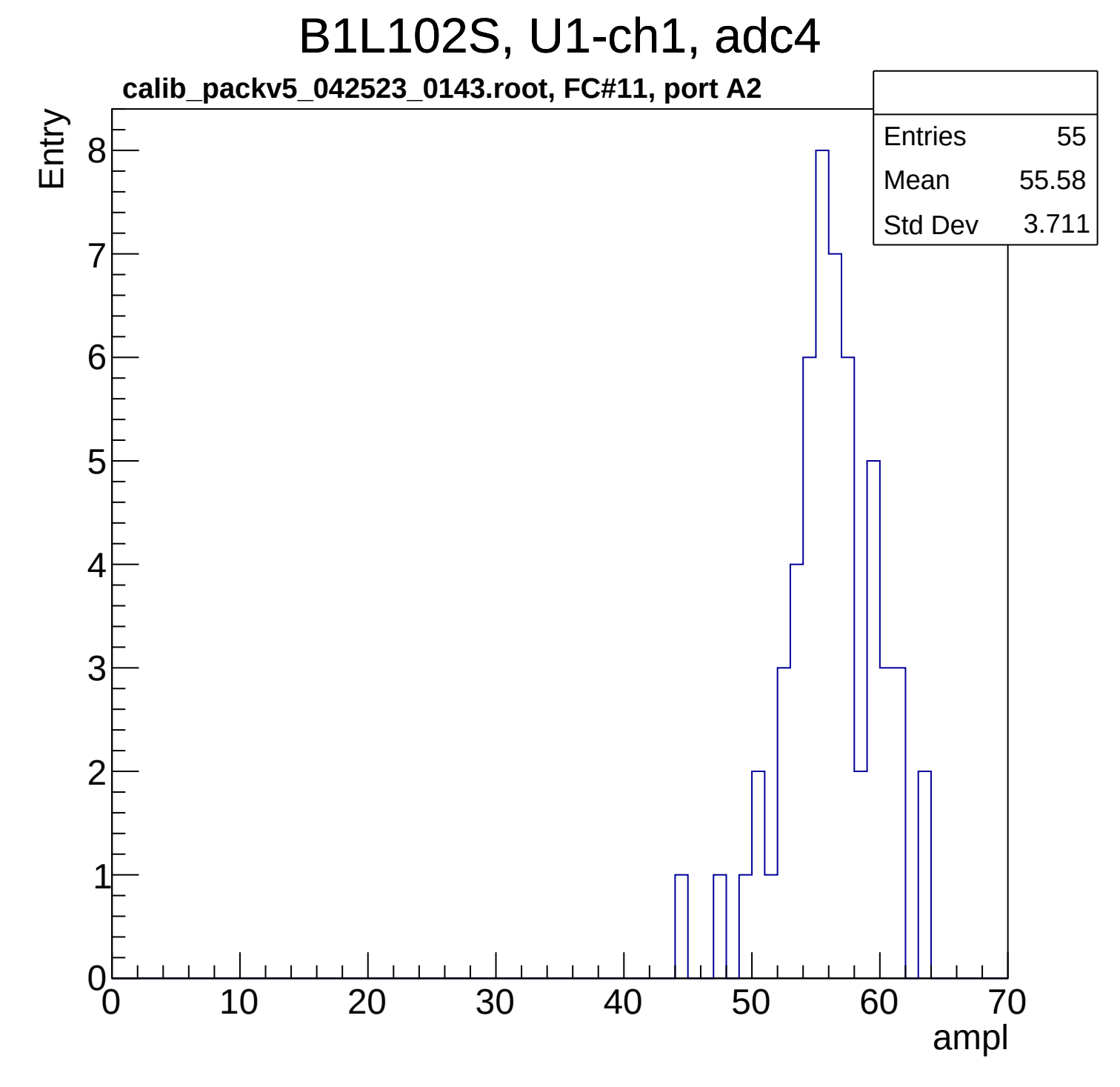
B1L102S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 55.58
Std Dev 3.711
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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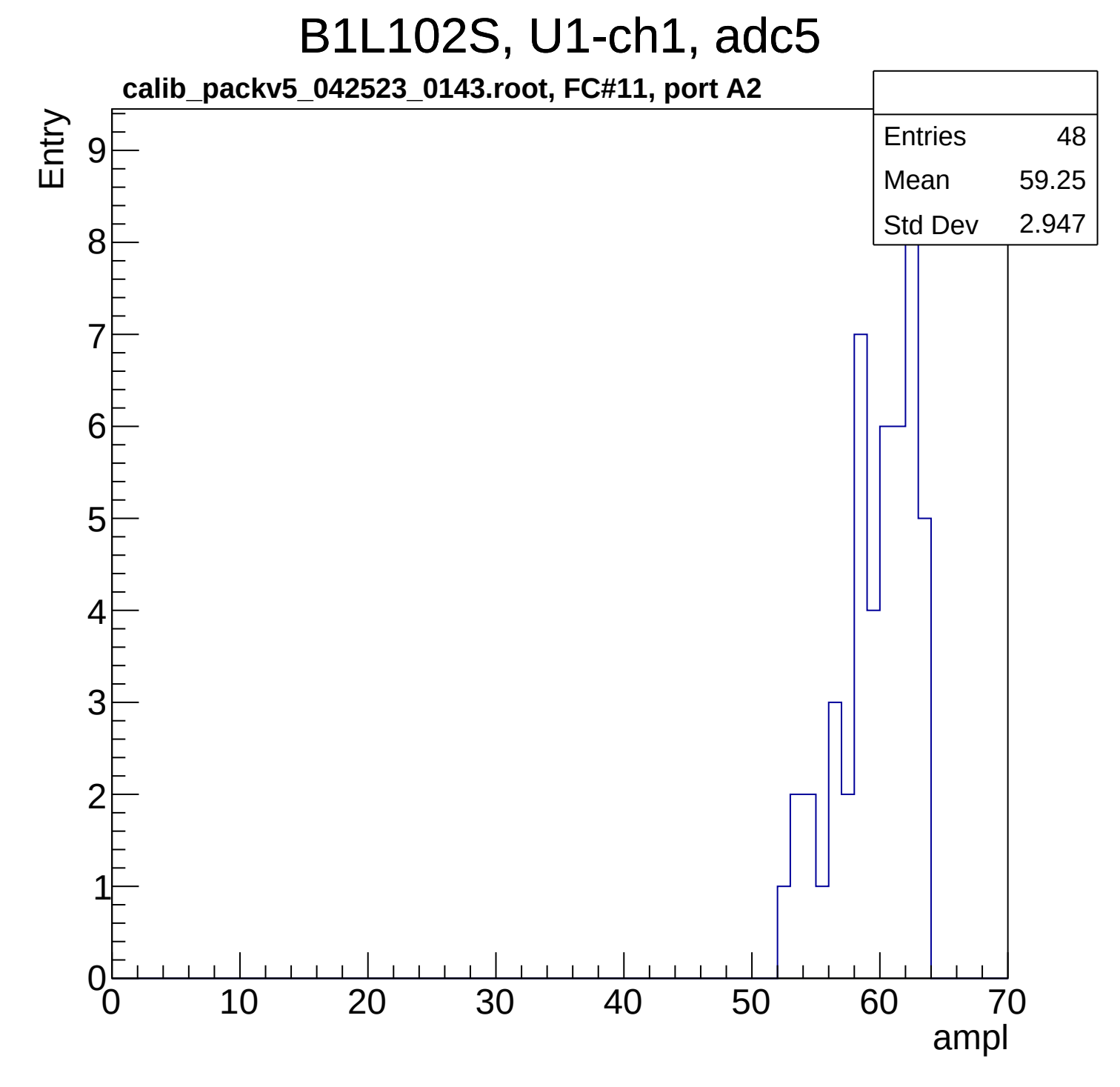
B1L102S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 59.25
Std Dev 2.947
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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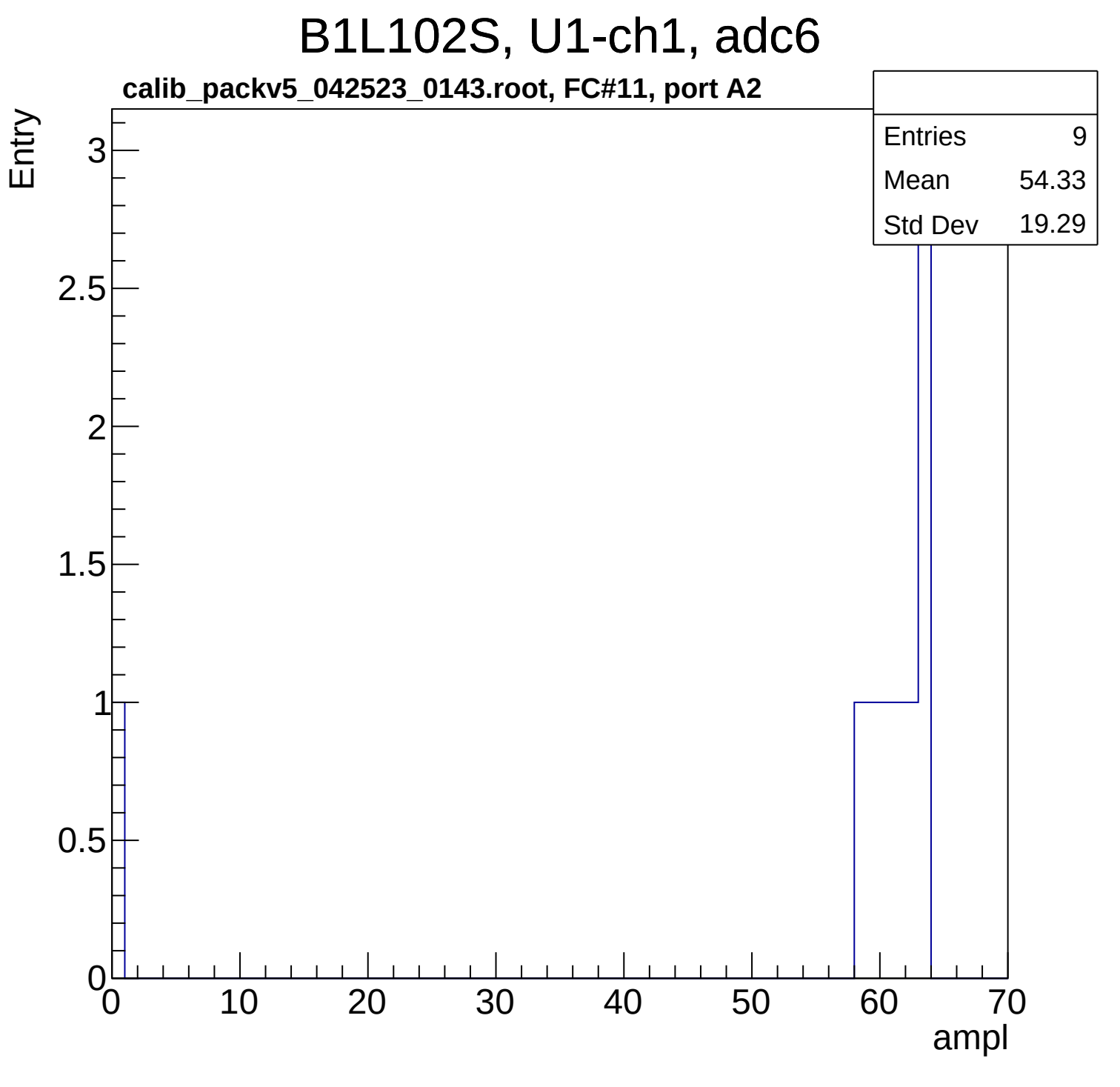
B1L102S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 54.33
Std Dev 19.29
Entry
ampl
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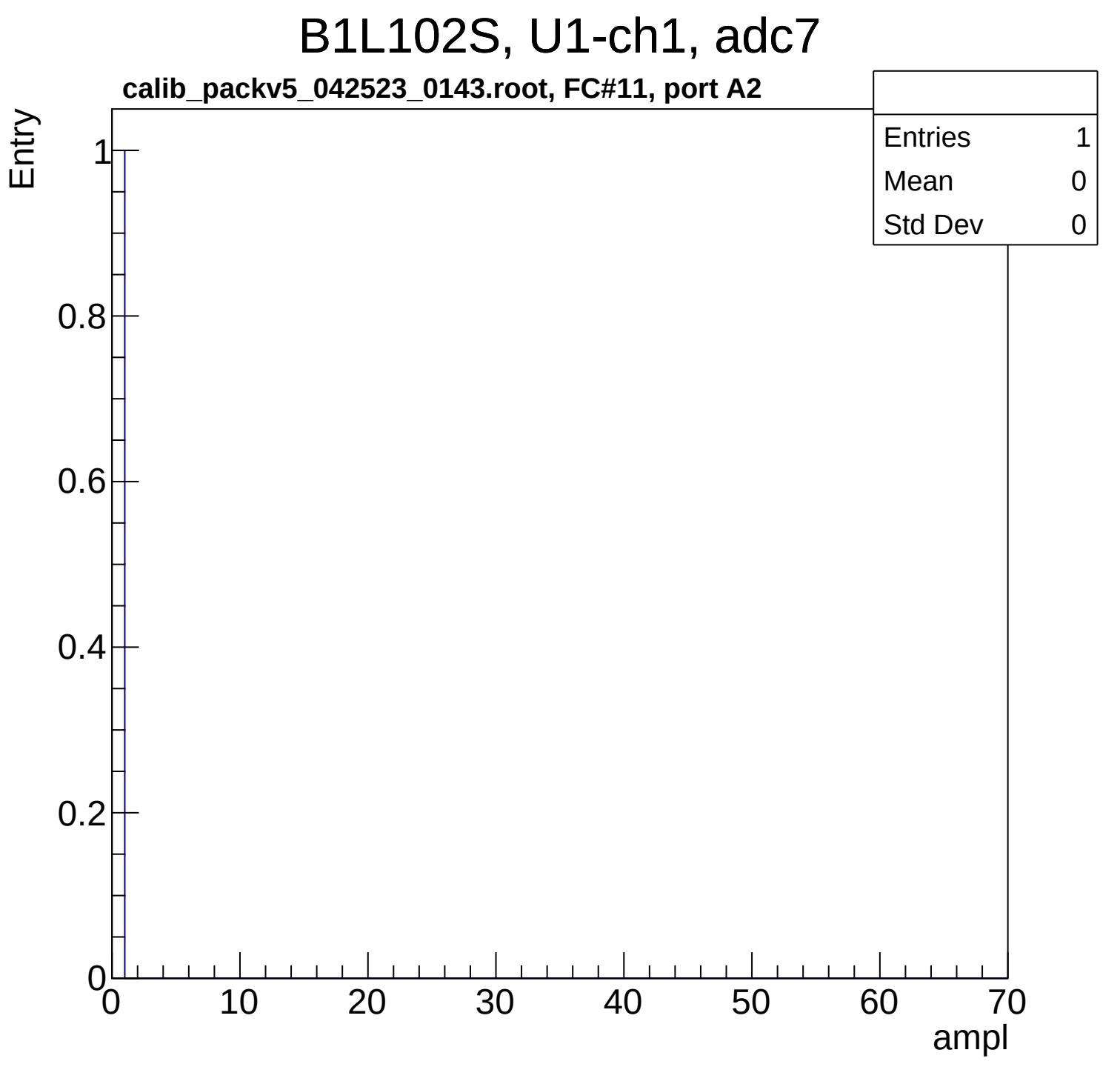
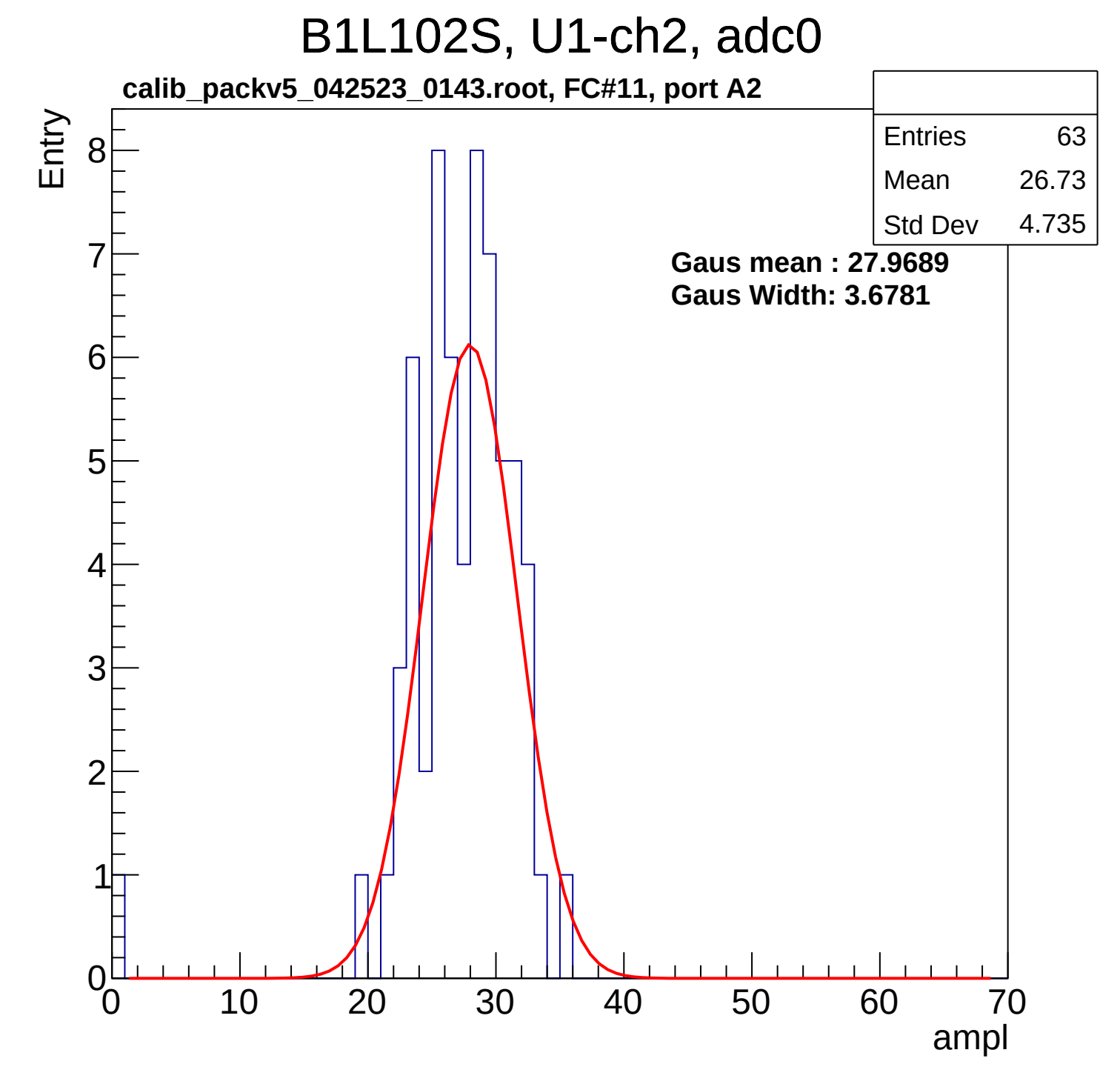
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 26.73
Std Dev 4.735
Gaus mean : 27.9689
Gaus Width: 3.6781
Entry
ampl
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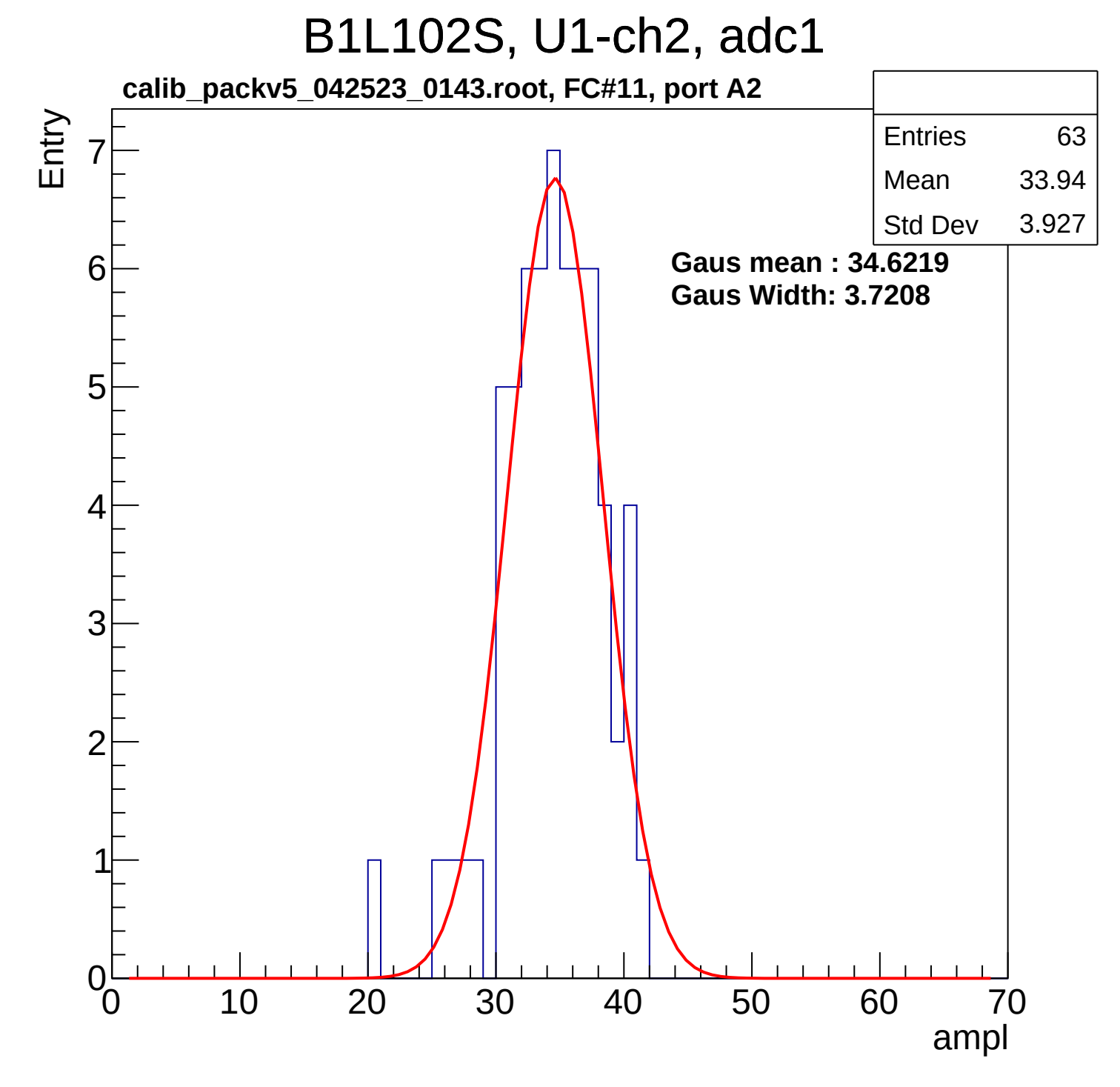
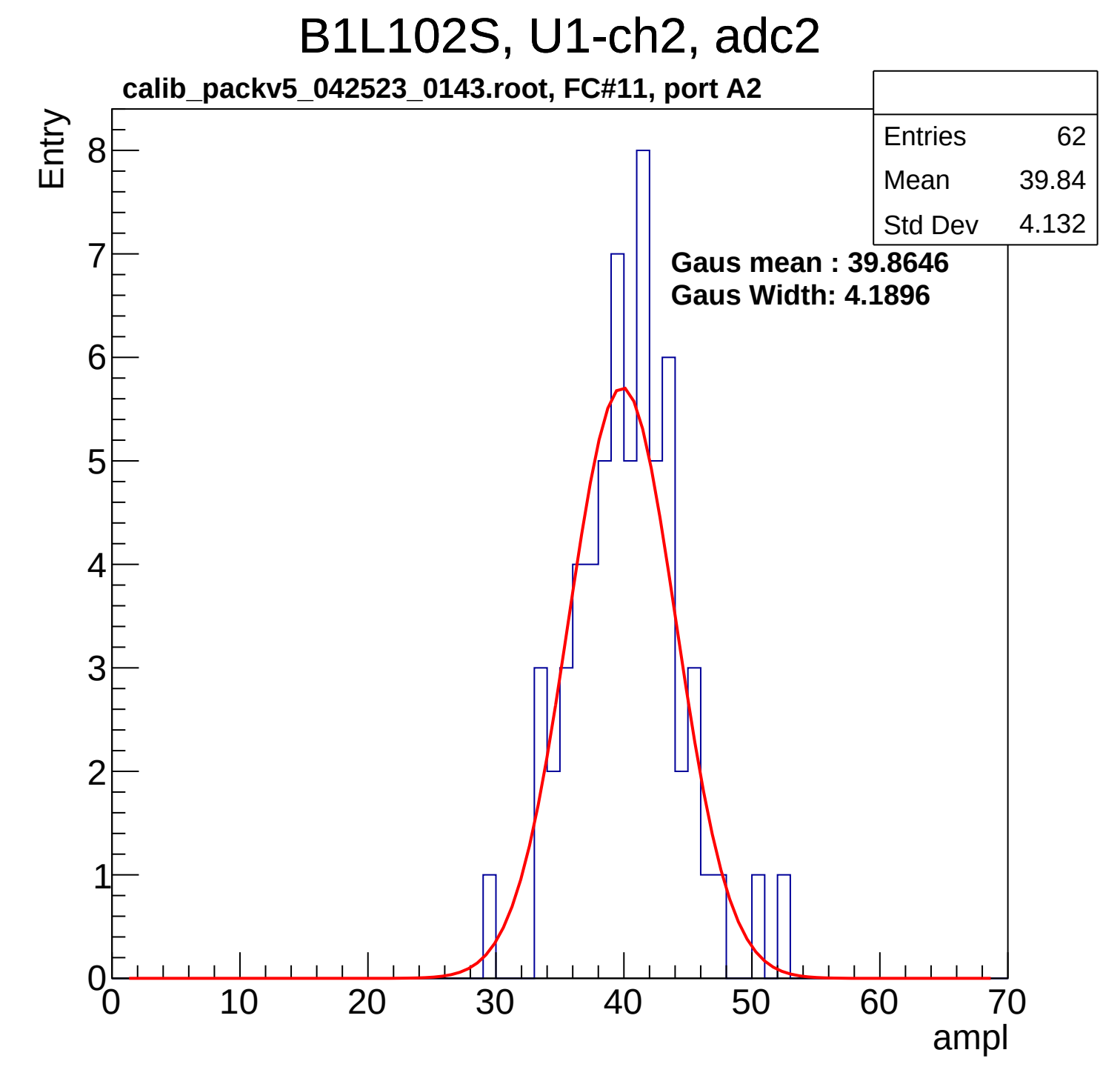
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 33.94
Std Dev 3.927
Gaus mean : 34.6219
Gaus Width: 3.7208
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 39.84
Std Dev 4.132
Gaus mean : 39.8646
Gaus Width: 4.1896
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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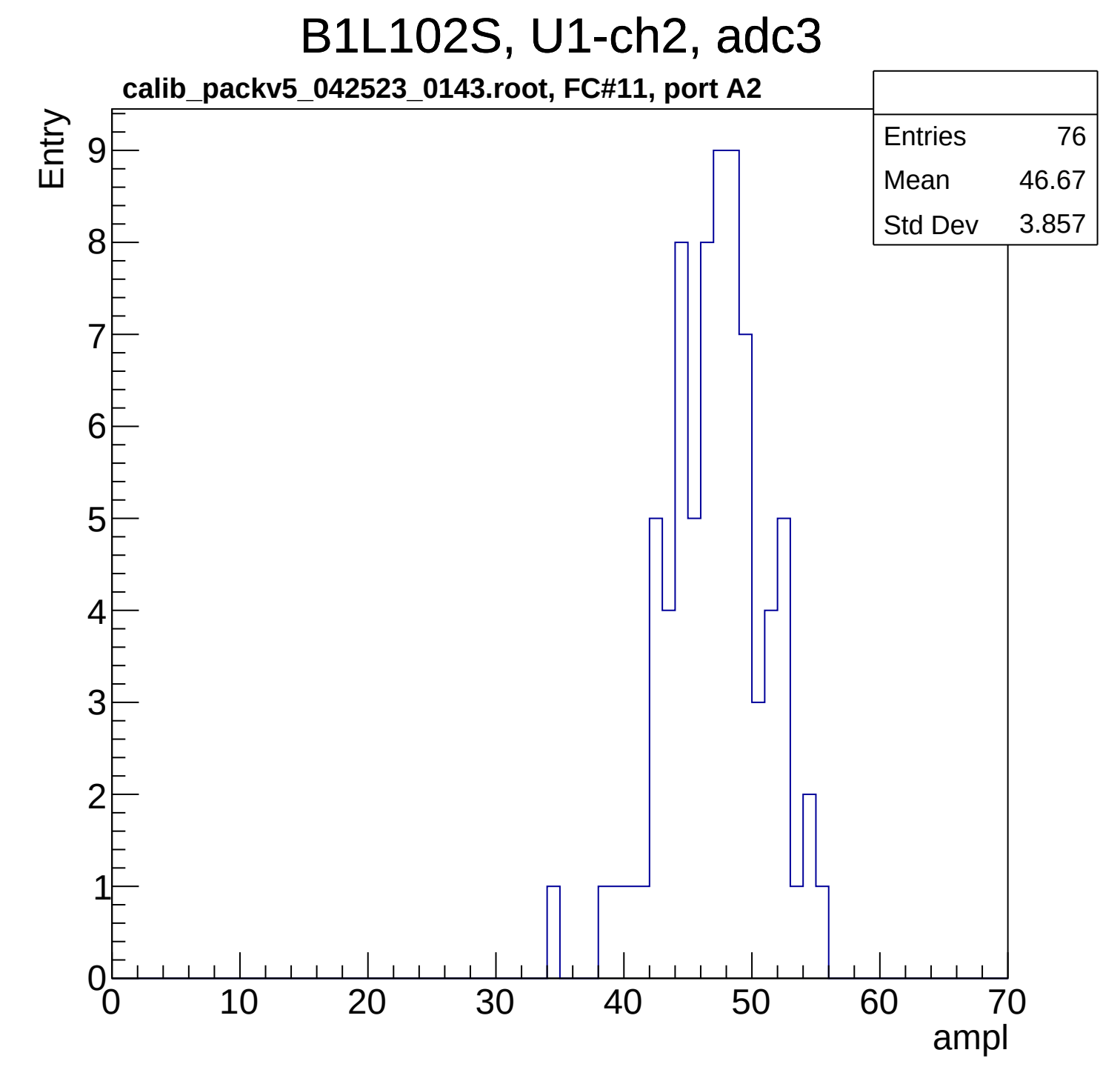
B1L102S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 46.67
Std Dev 3.857
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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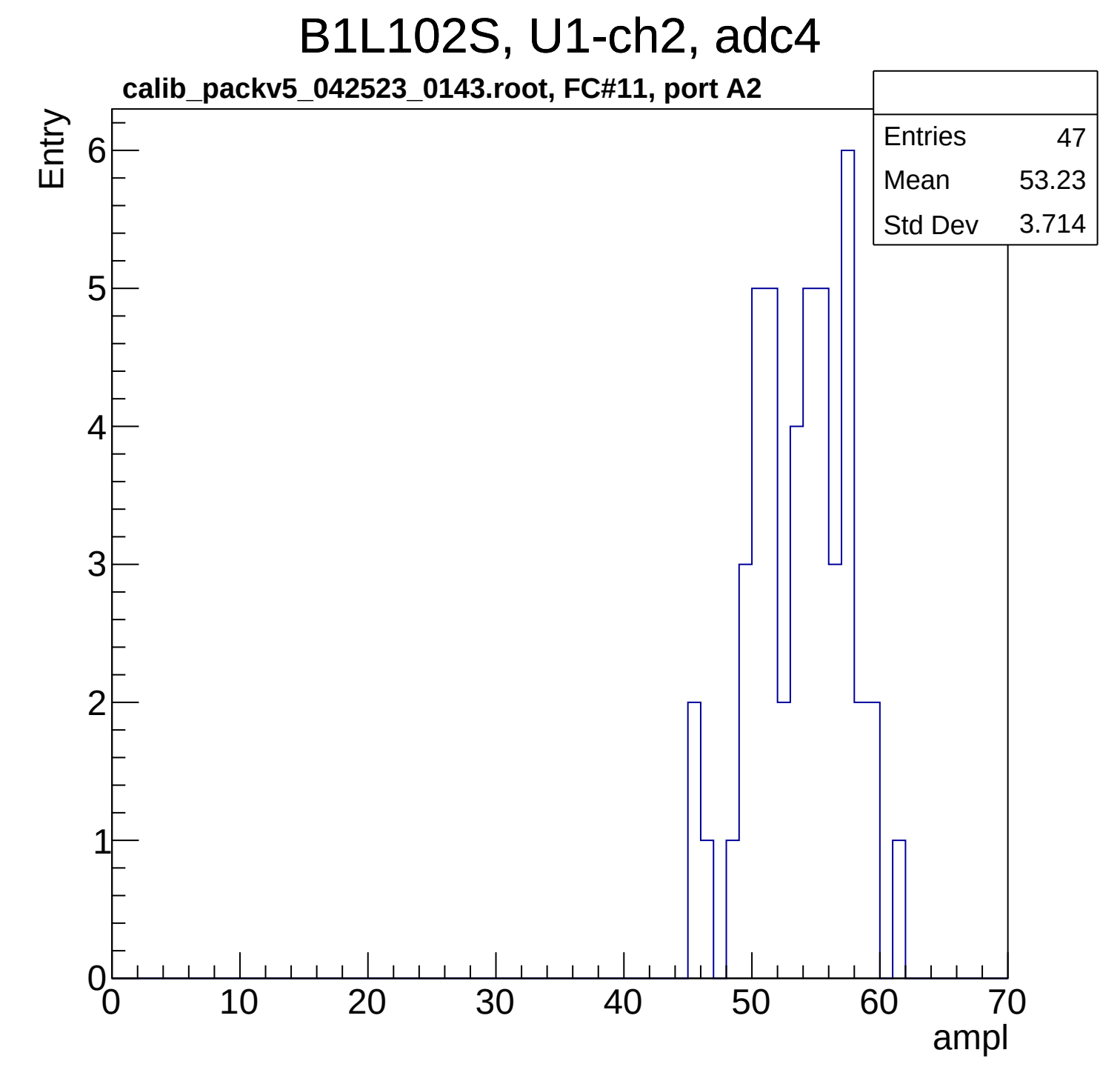
B1L102S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 53.23
Std Dev 3.714
Entry
ampl
0
1
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3
4
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0
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40
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60
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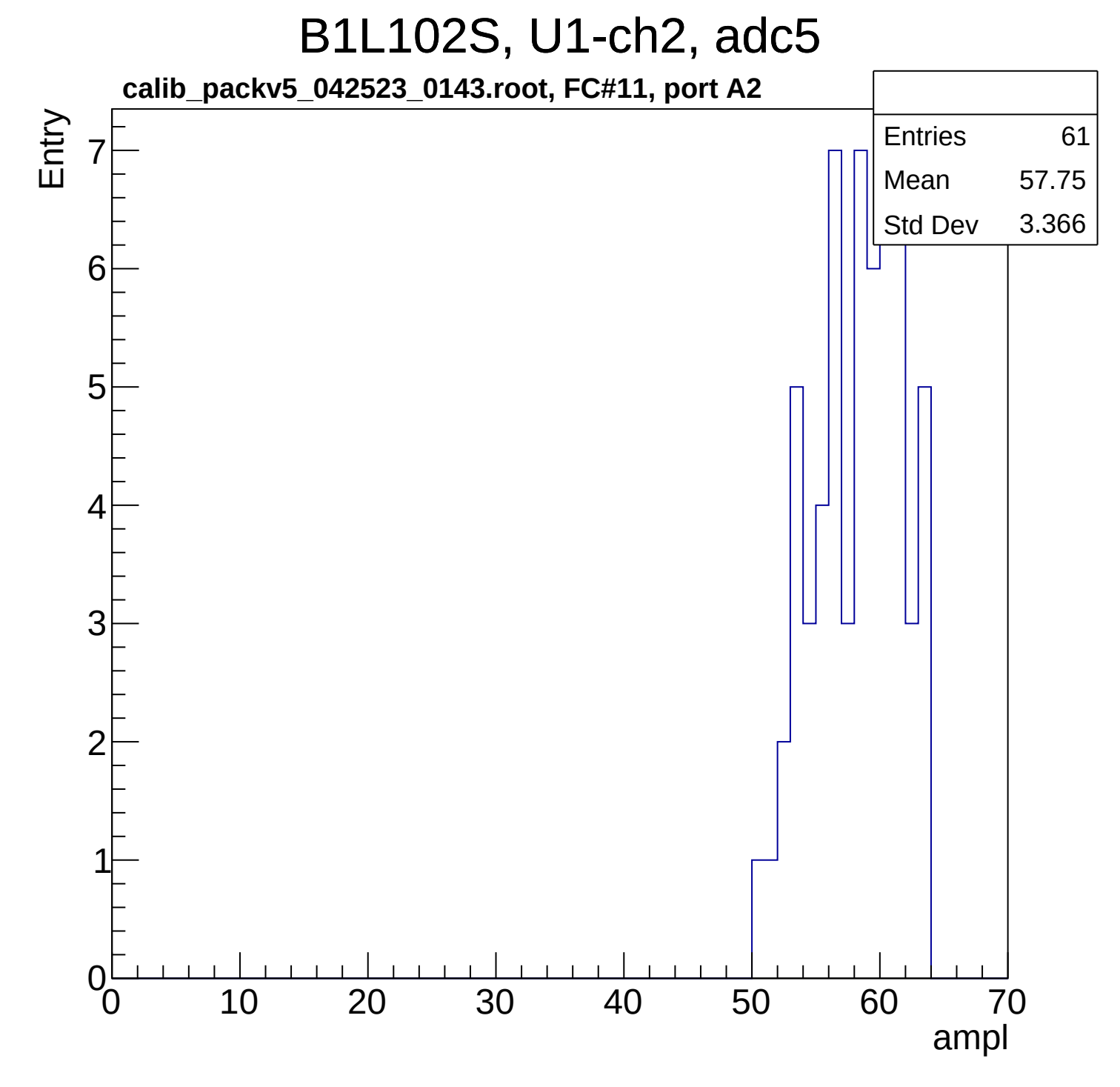
B1L102S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 57.75
Std Dev 3.366
Entry
ampl
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4
5
6
7
0
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20
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40
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60
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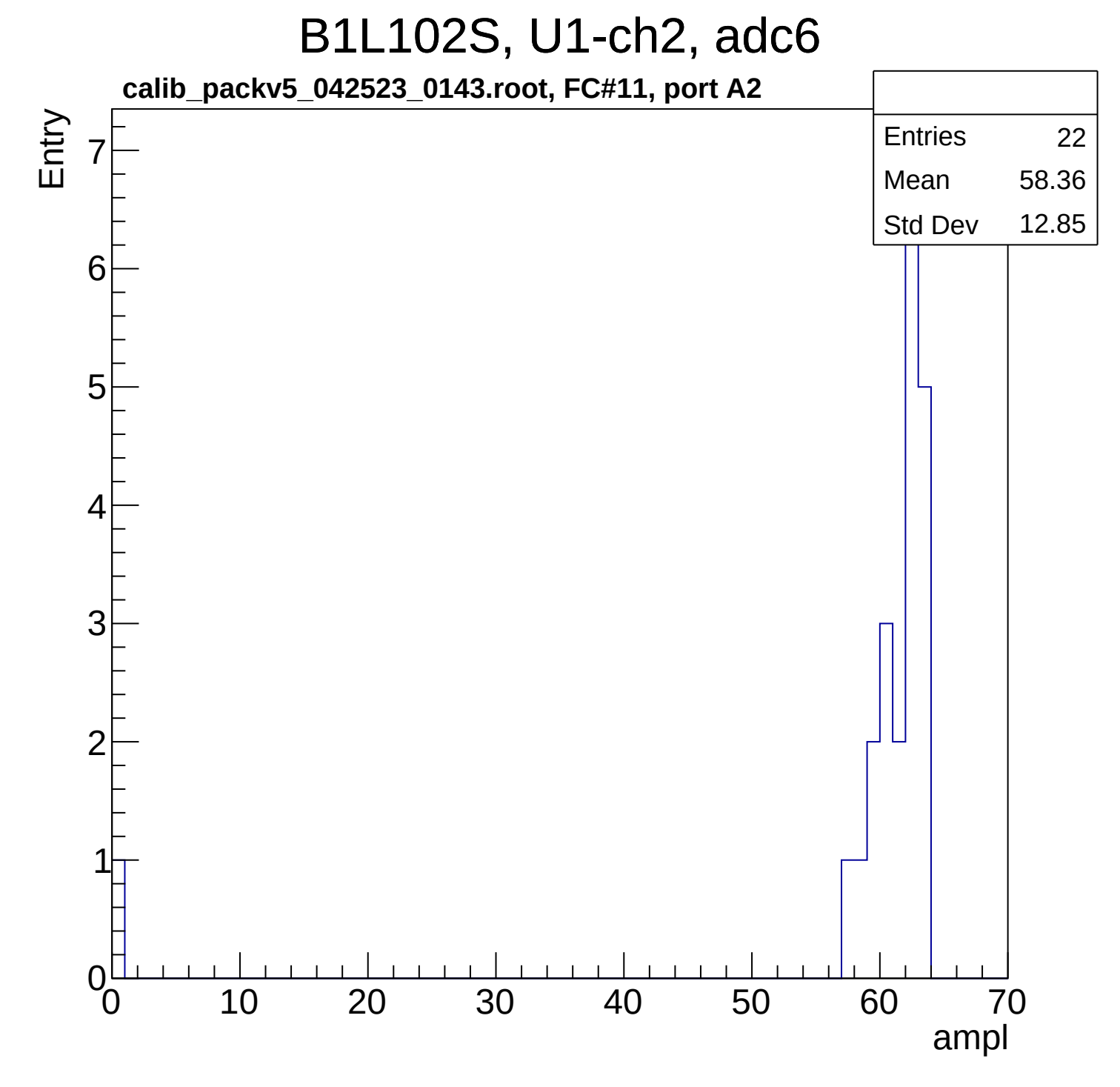
B1L102S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 22
Mean 58.36
Std Dev 12.85
Entry
ampl
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3
4
5
6
7
0
10
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40
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60
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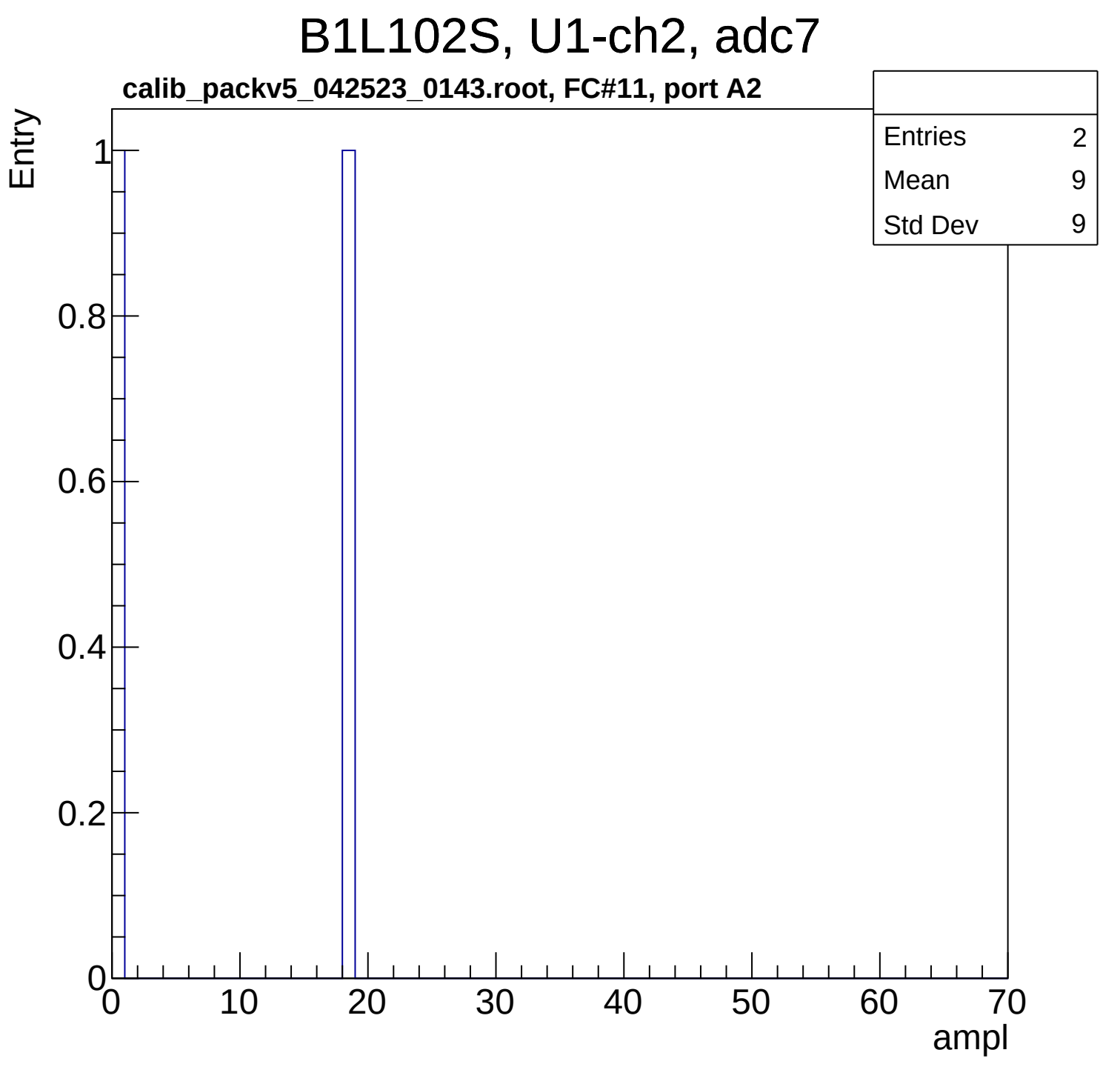
B1L102S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 9
Std Dev 9
Entry
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0.6
0.4
0.2
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10
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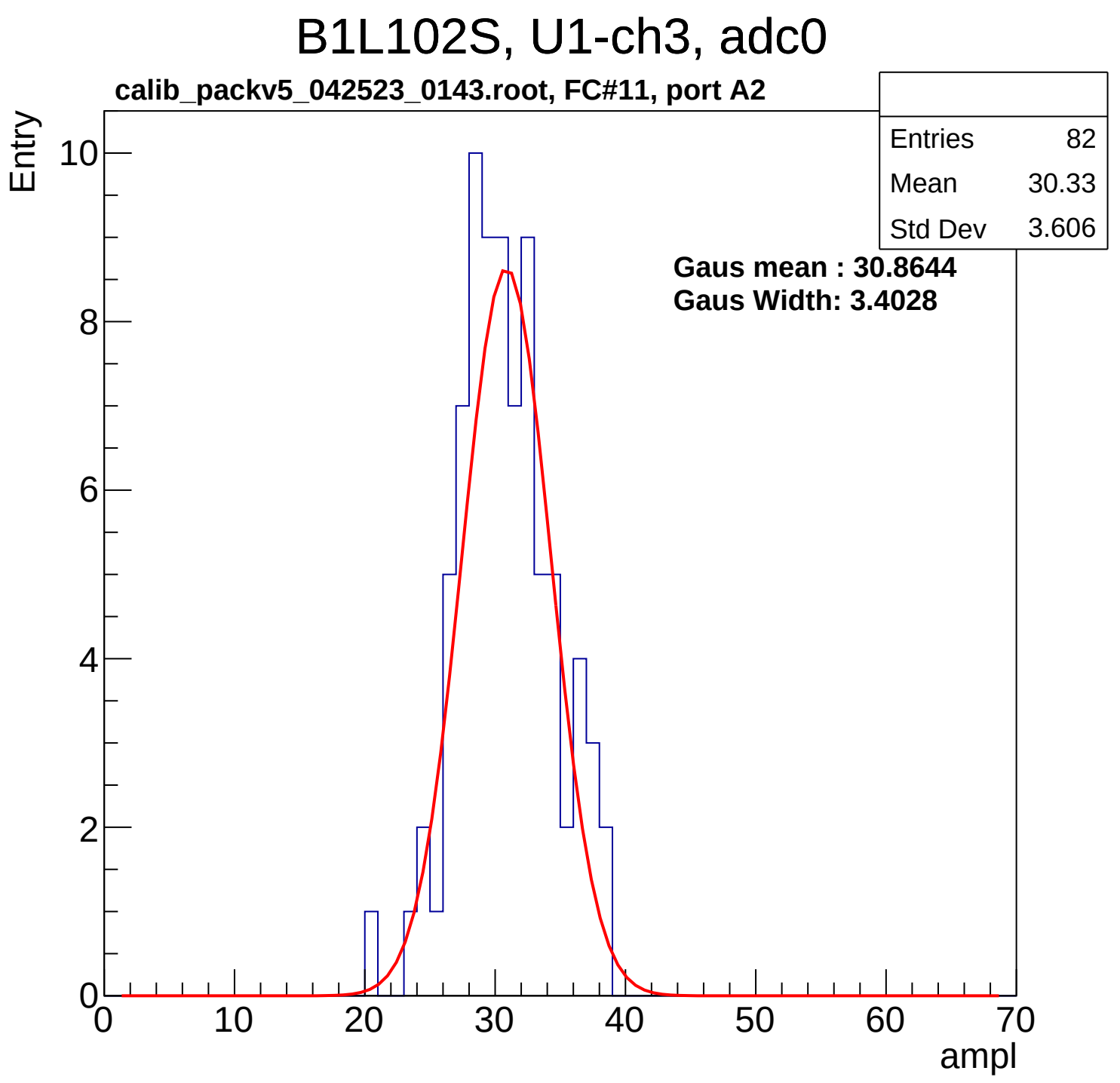
40
50
60
70
ampl


B1L102S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 30.33
Std Dev 3.606
Gaus mean : 30.8644
Gaus Width: 3.4028
Entry
ampl
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2
4
6
8
10
0
10
20
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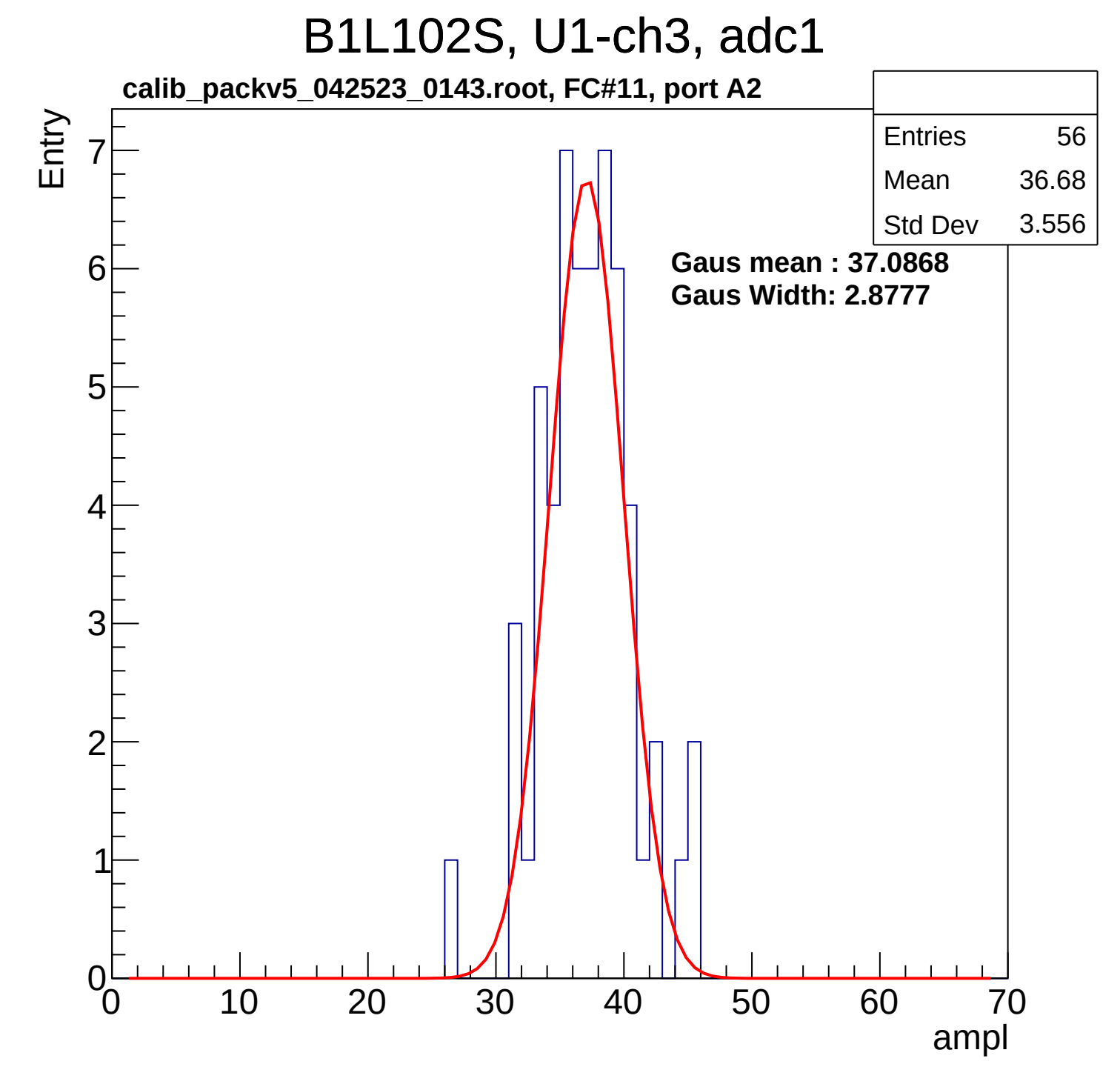
40
50
60
70


B1L102S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 36.68
Std Dev 3.556
Gaus mean : 37.0868
Gaus Width: 2.8777
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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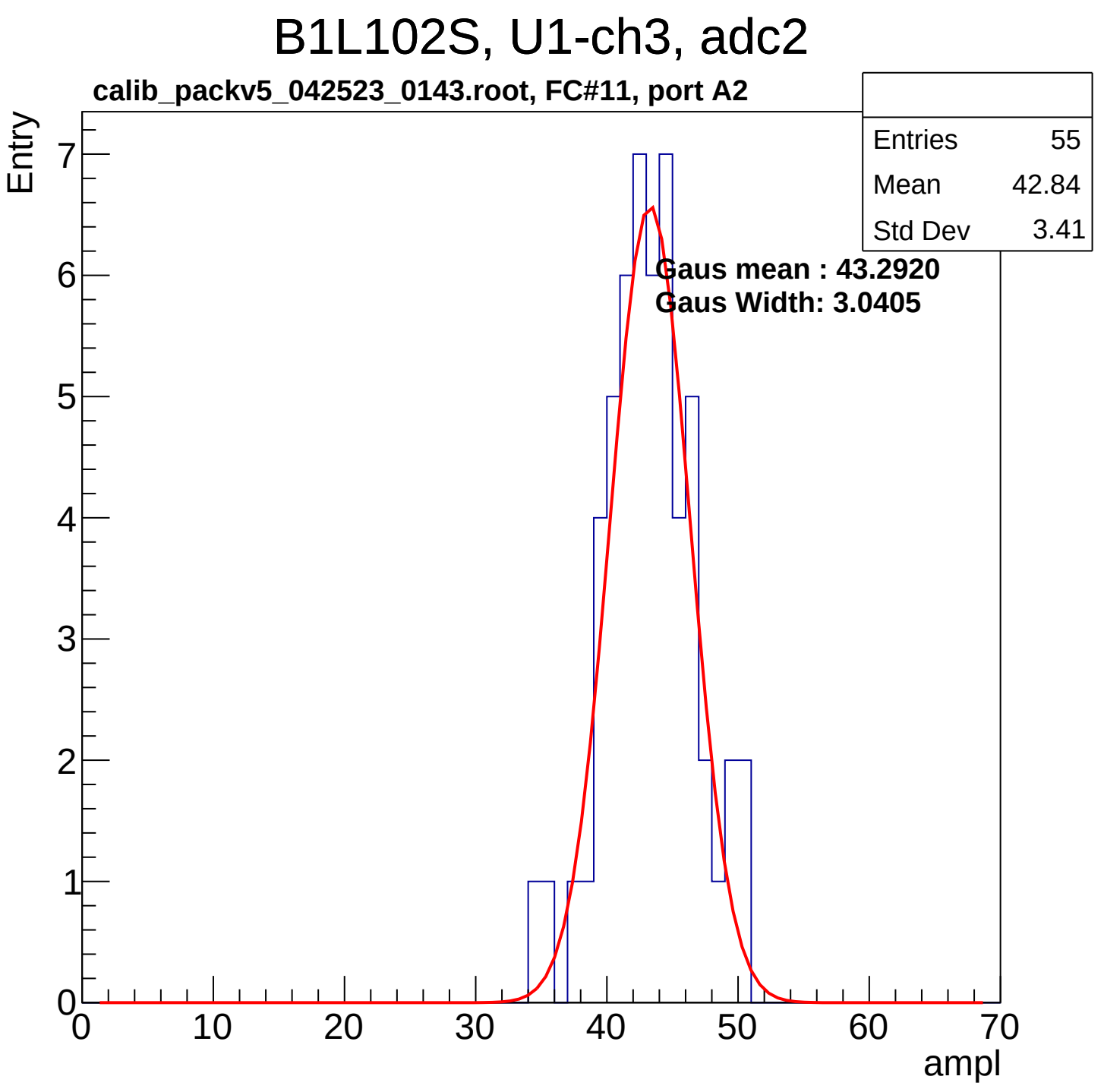
B1L102S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 42.84
Std Dev 3.41
Gaus mean : 43.2920
Gaus Width: 3.0405
Entry
ampl
0
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2
3
4
5
6
7
0
10
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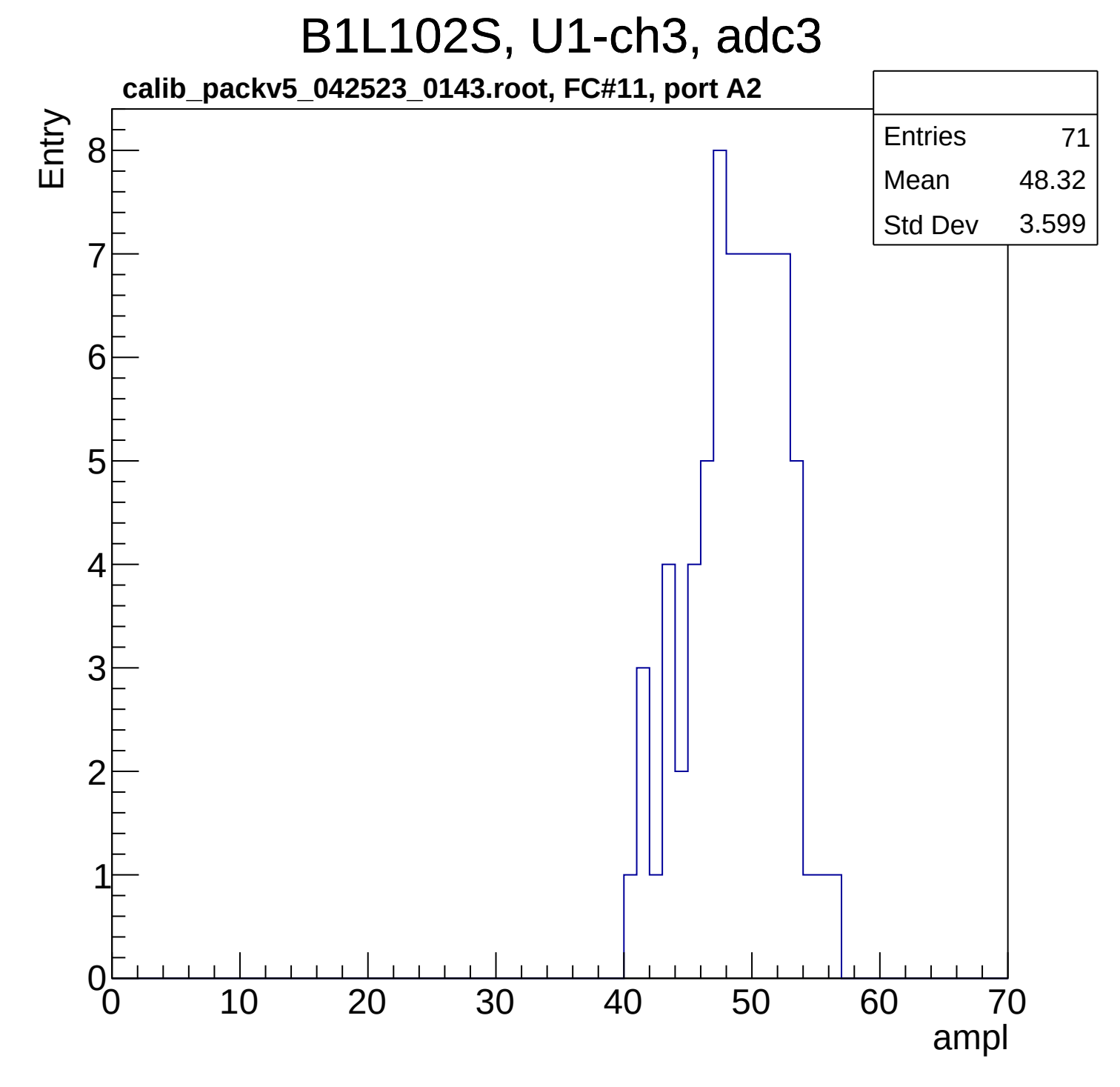
B1L102S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 48.32
Std Dev 3.599
Entry
ampl
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4
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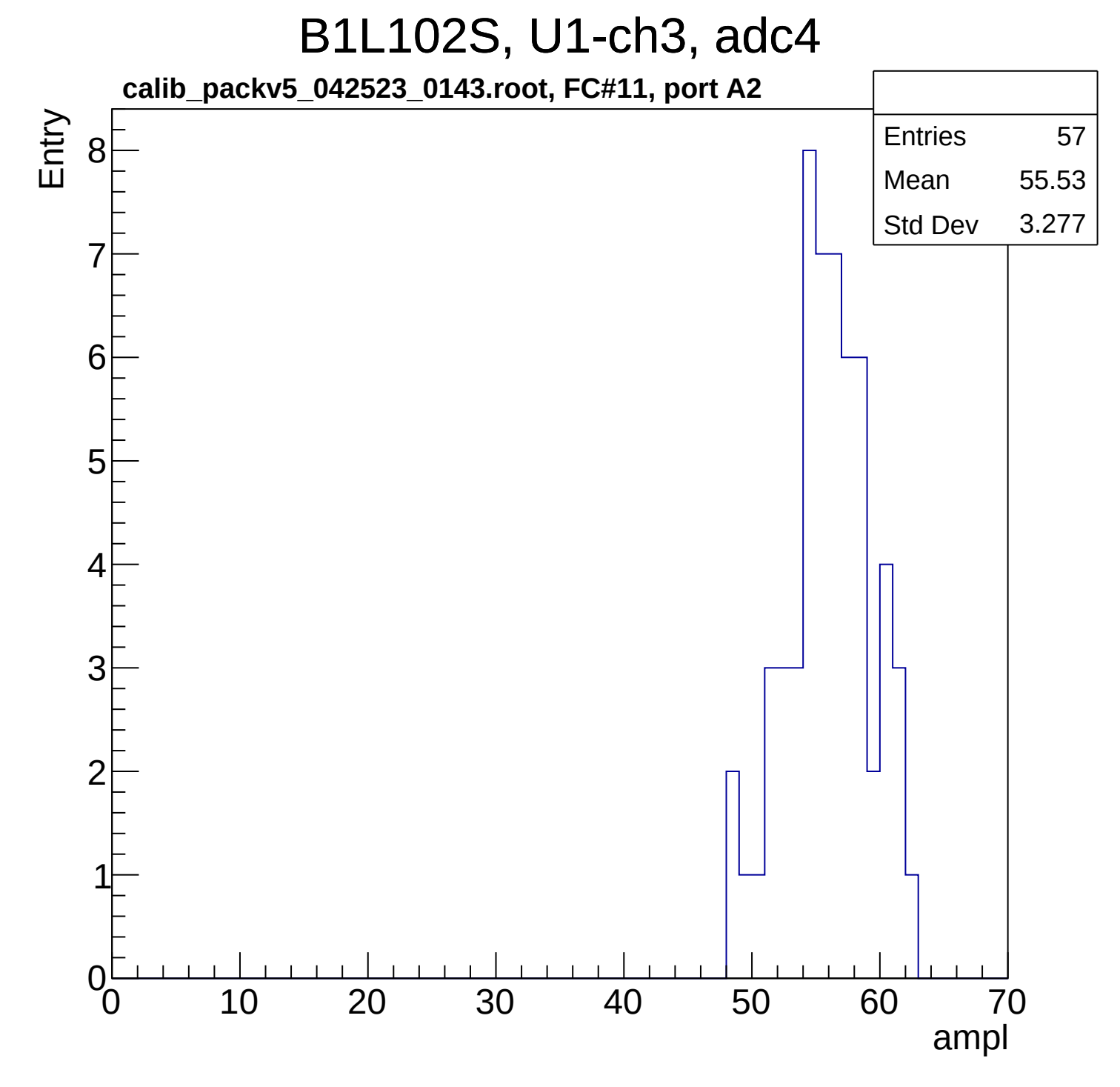
B1L102S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.53
Std Dev 3.277
Entry
ampl
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7
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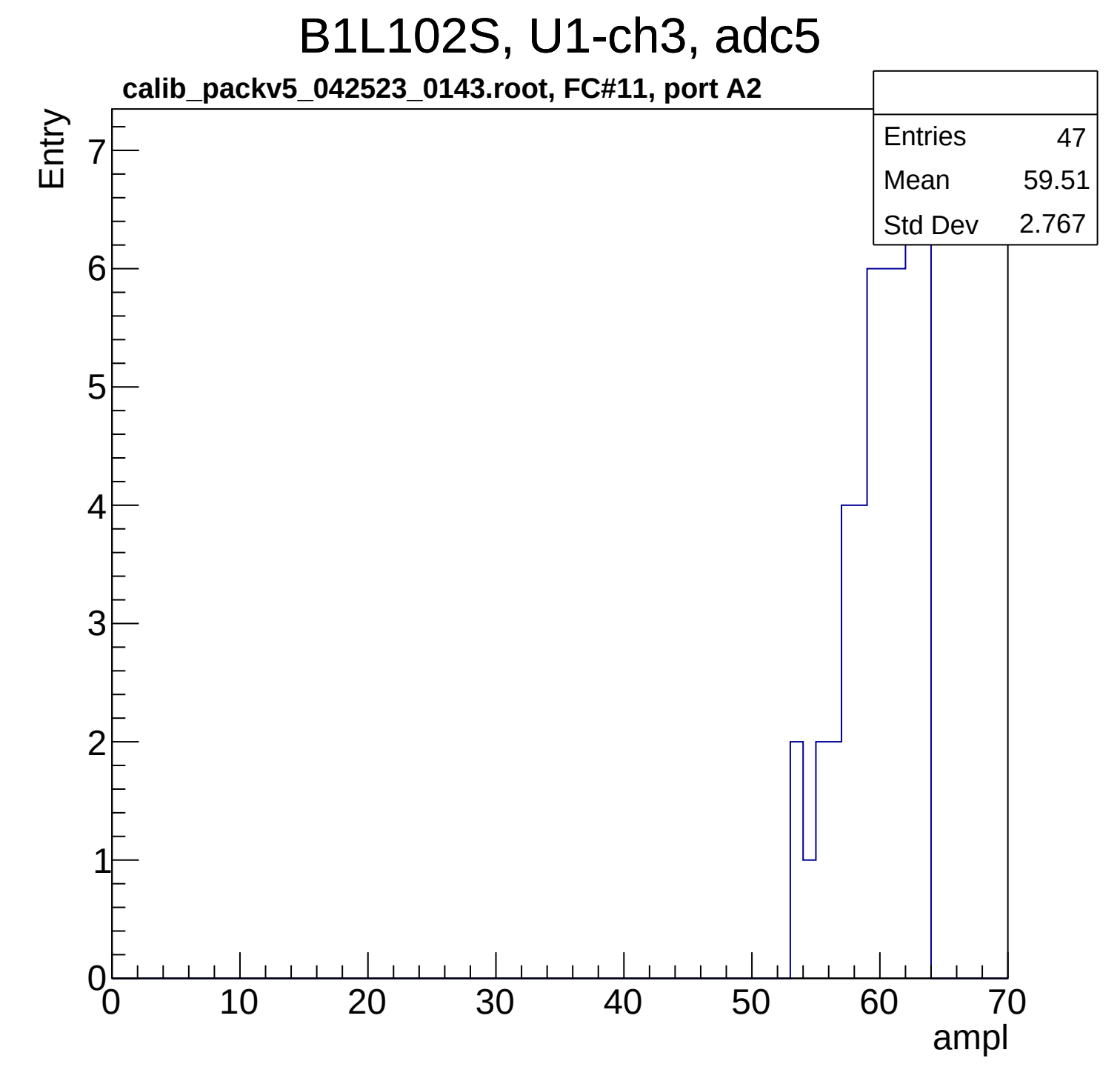
B1L102S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 59.51
Std Dev 2.767
Entry
ampl
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2
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4
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10
20
30
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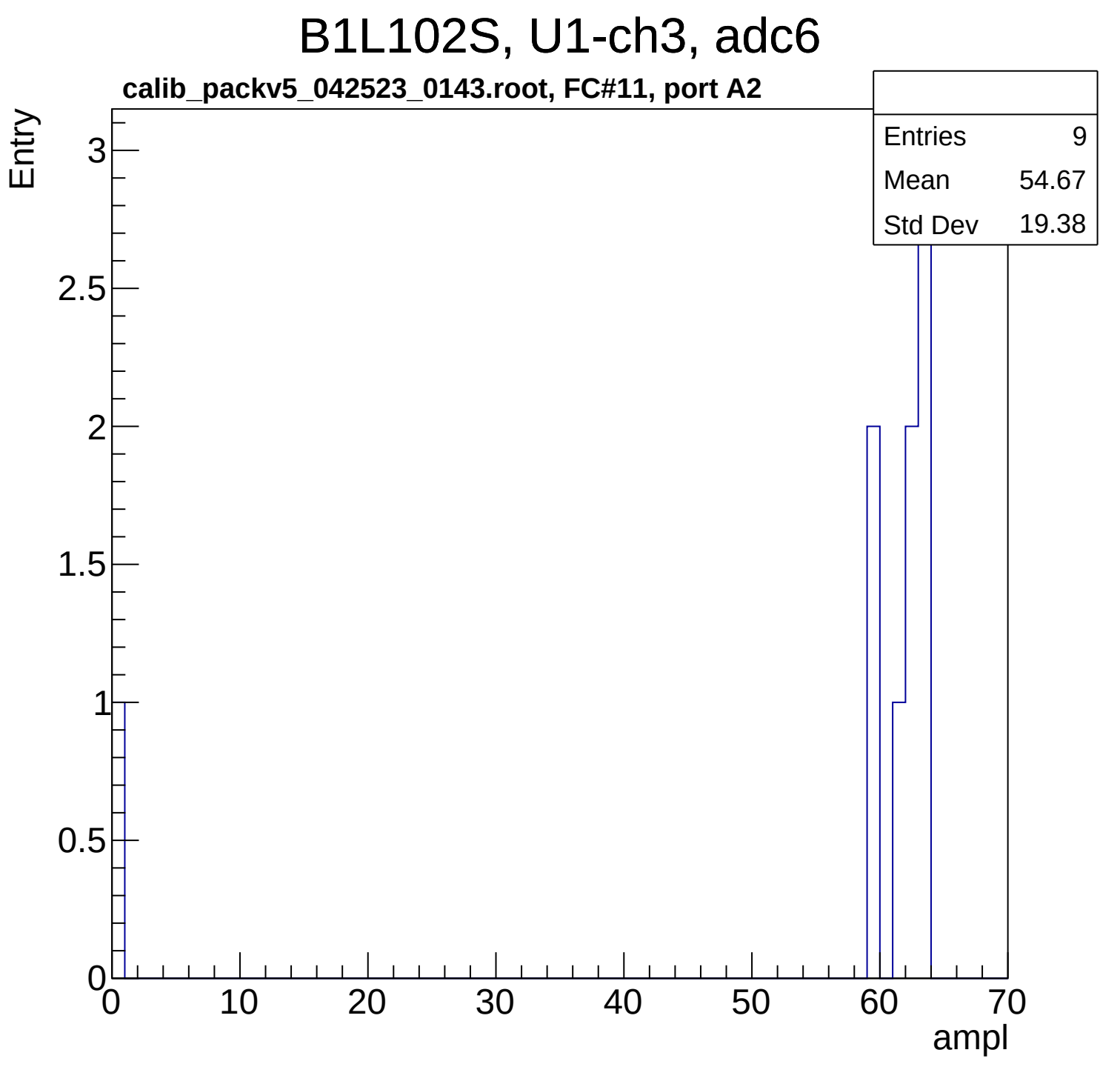
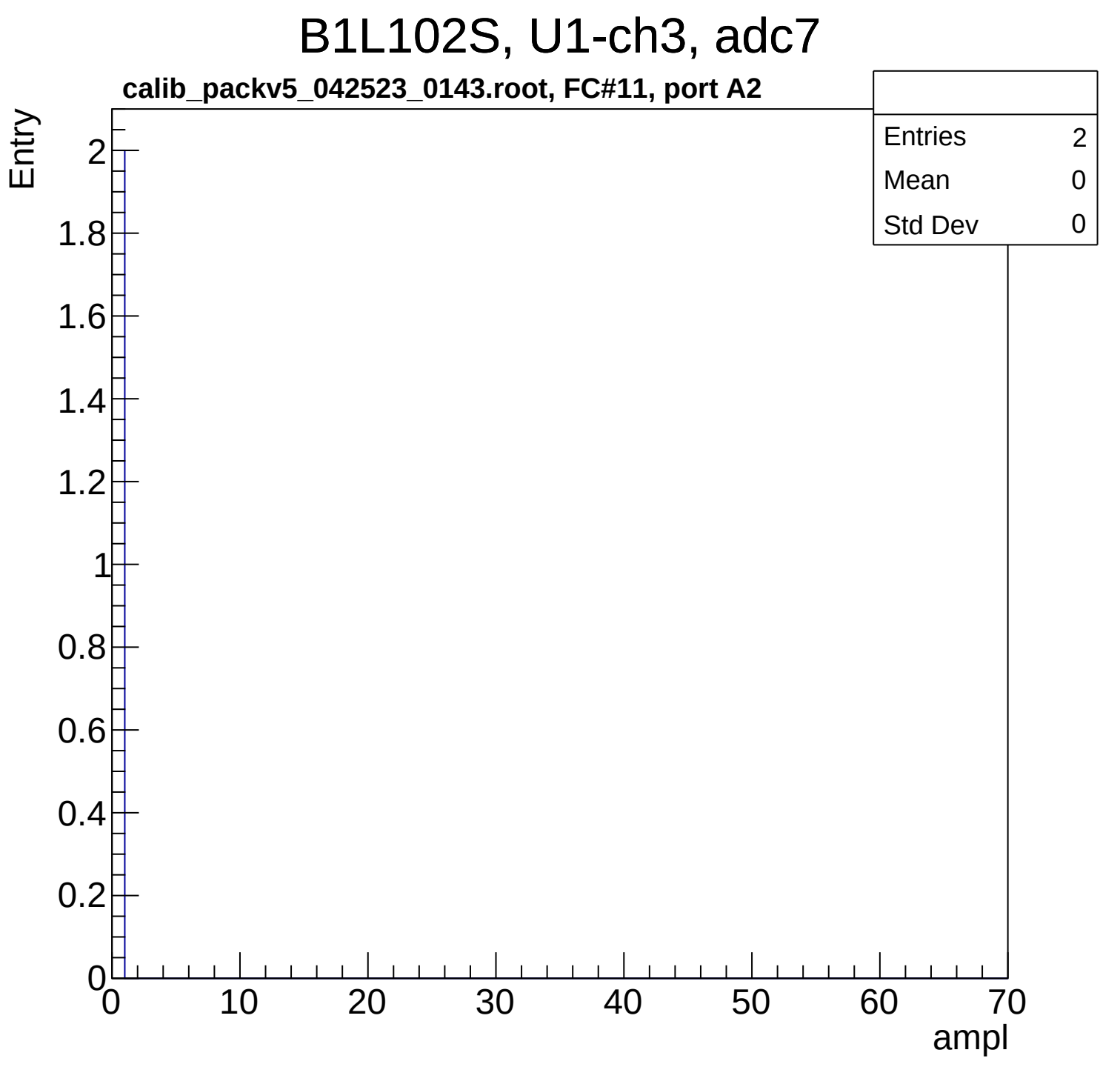
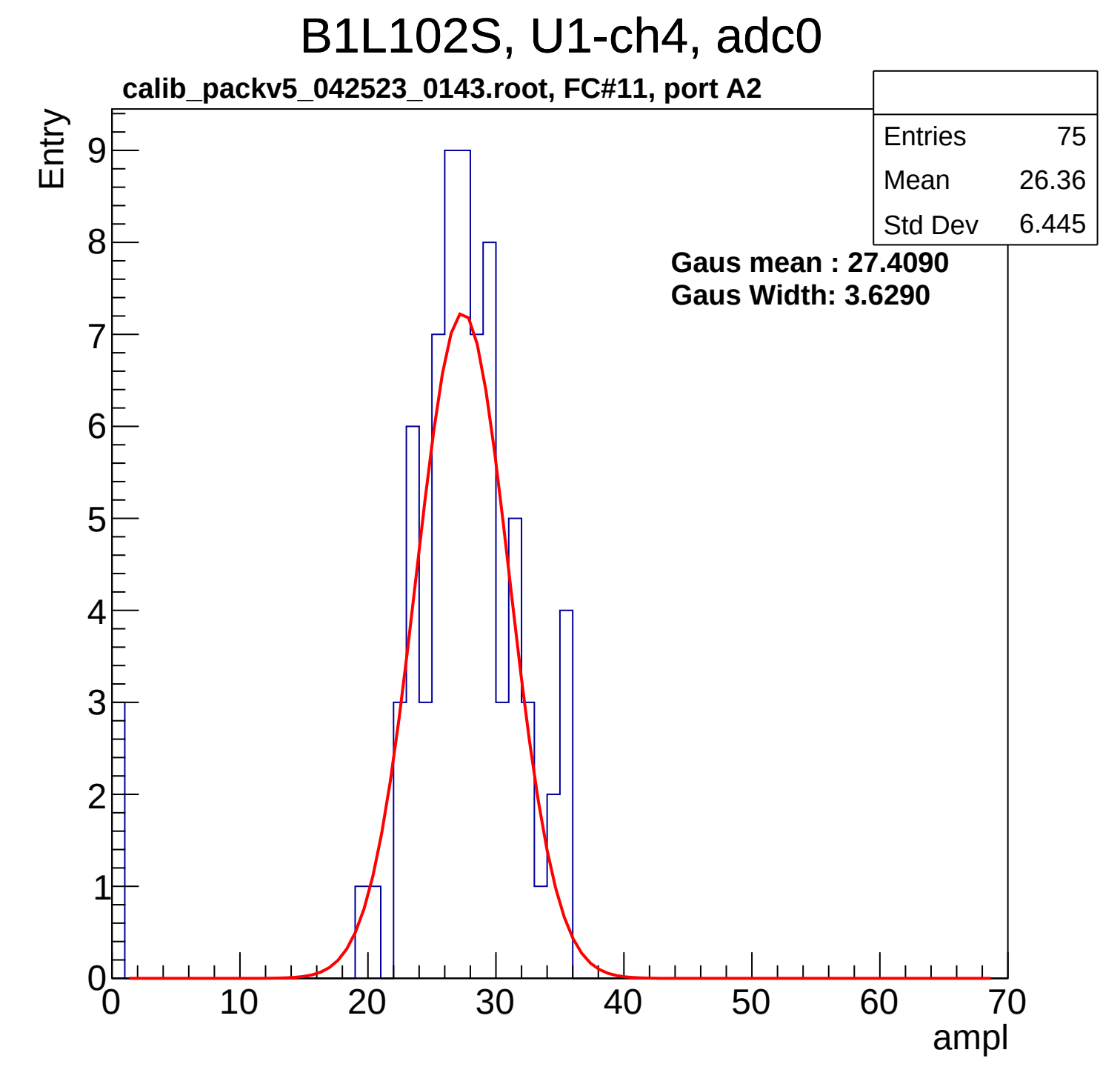
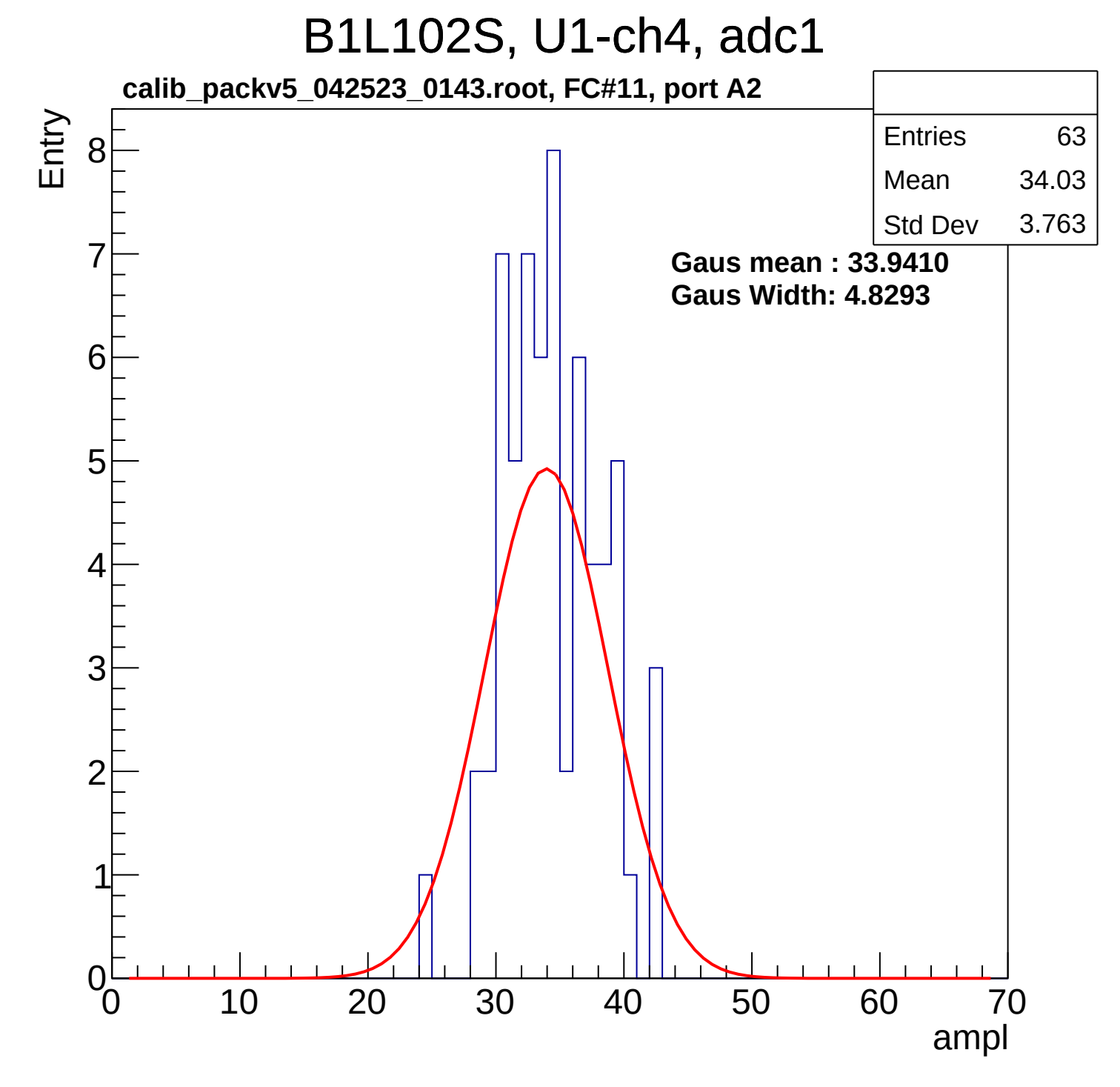
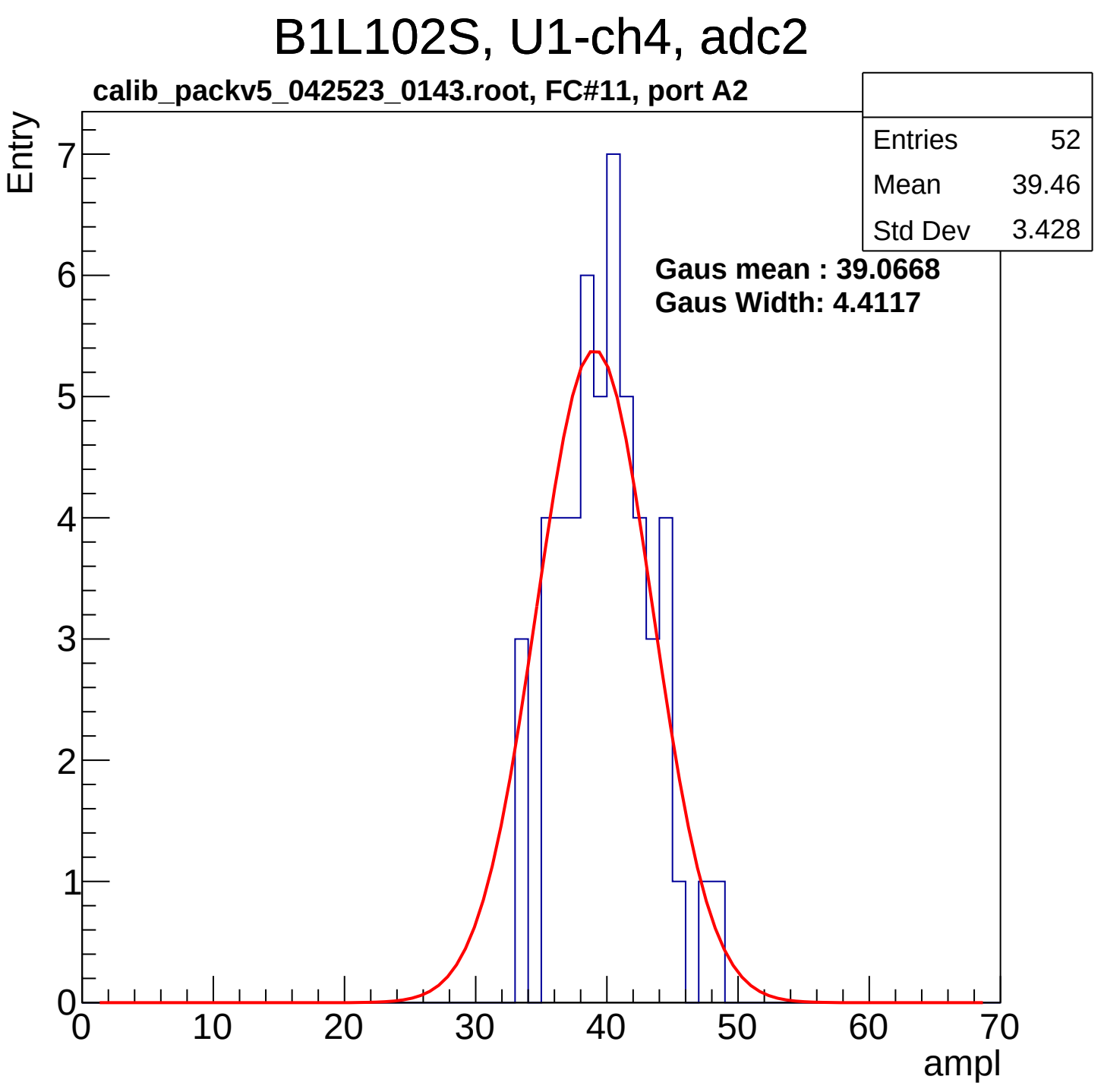
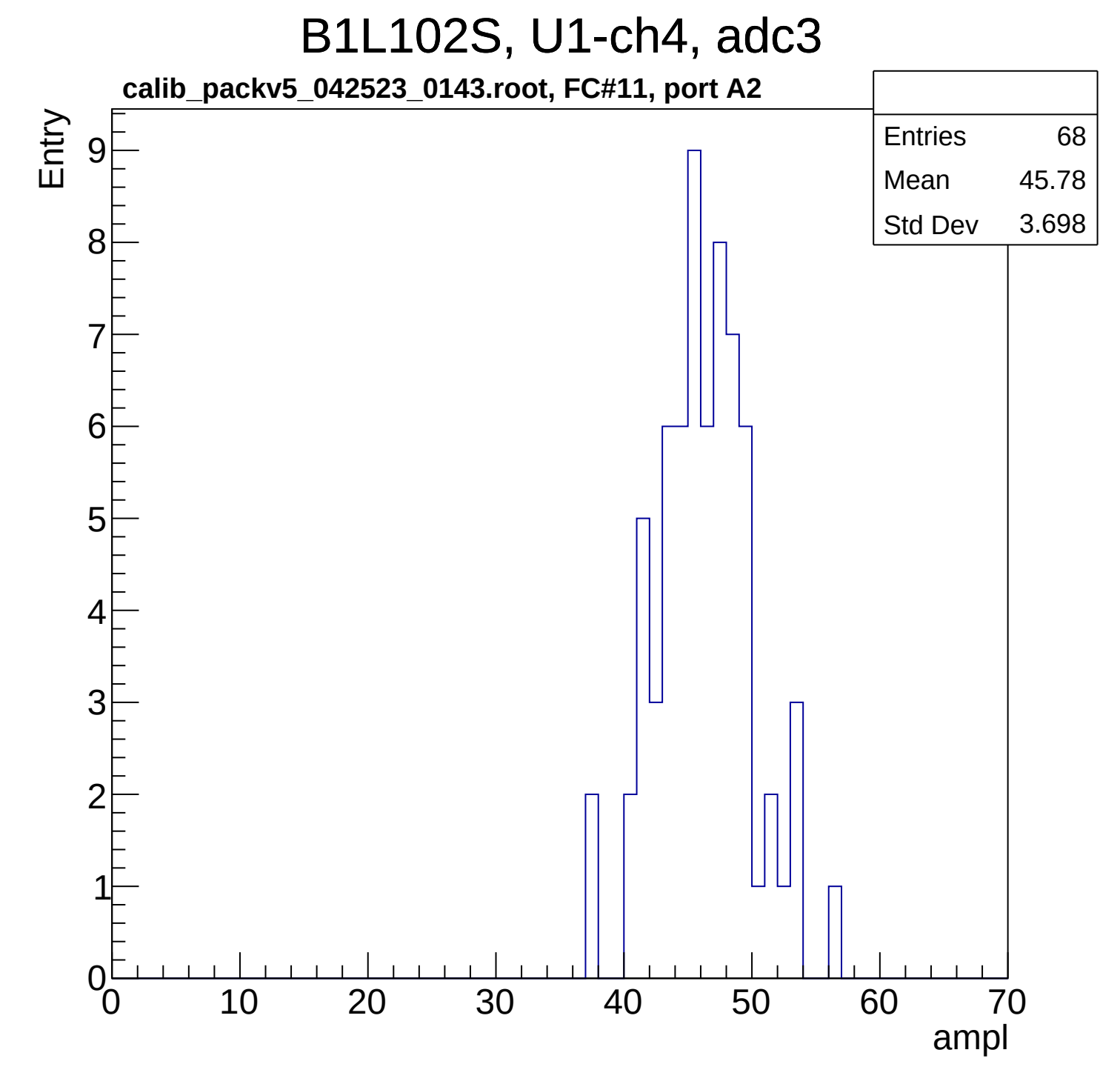

B1L102S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 54.67
Std Dev 19.38
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 26.36
Std Dev 6.445
Gaus mean : 27.4090
Gaus Width: 3.6290
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 34.03
Std Dev 3.763
Gaus mean : 33.9410
Gaus Width: 4.8293
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 39.46
Std Dev 3.428
Gaus mean : 39.0668
Gaus Width: 4.4117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 45.78
Std Dev 3.698
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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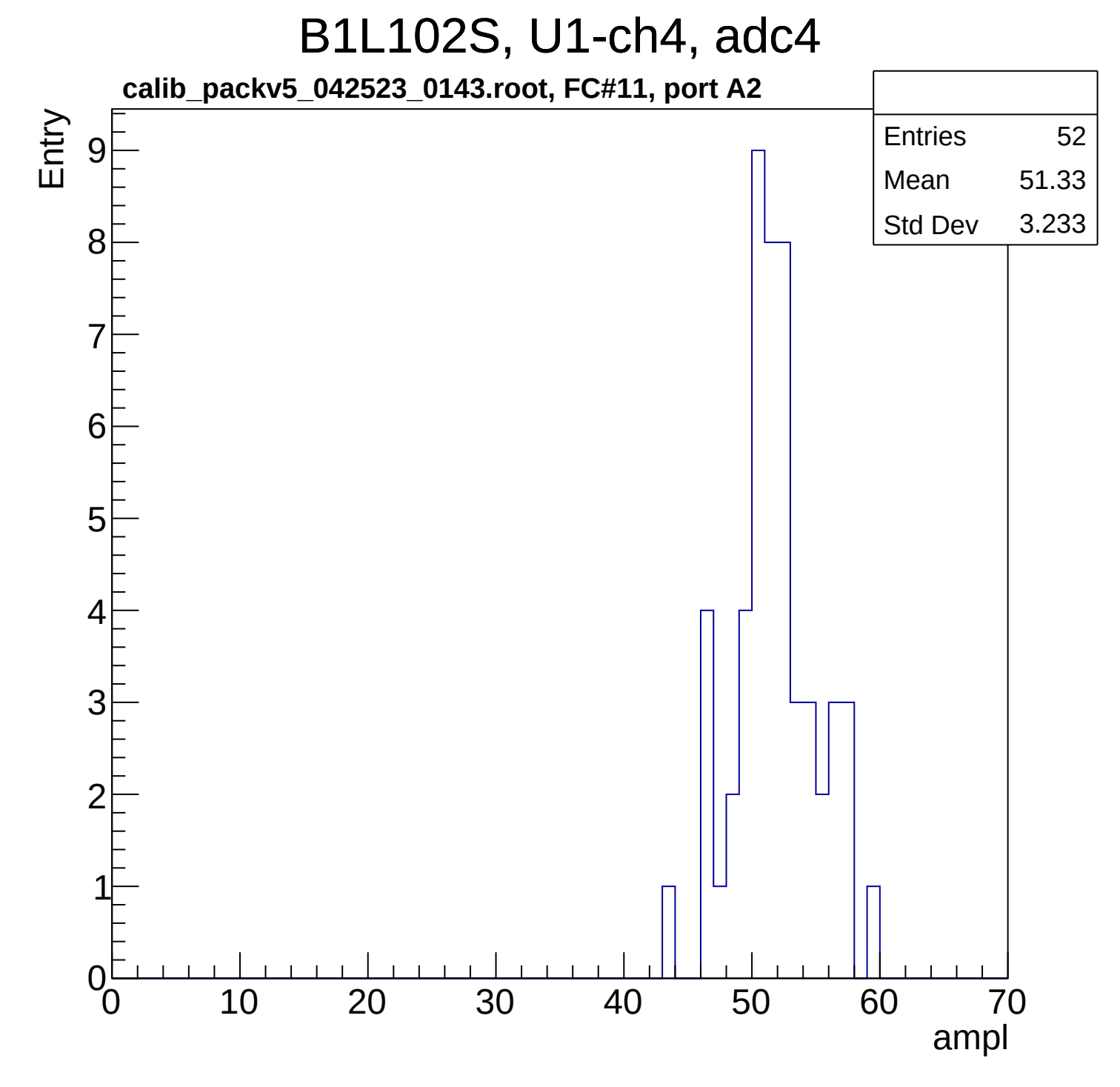
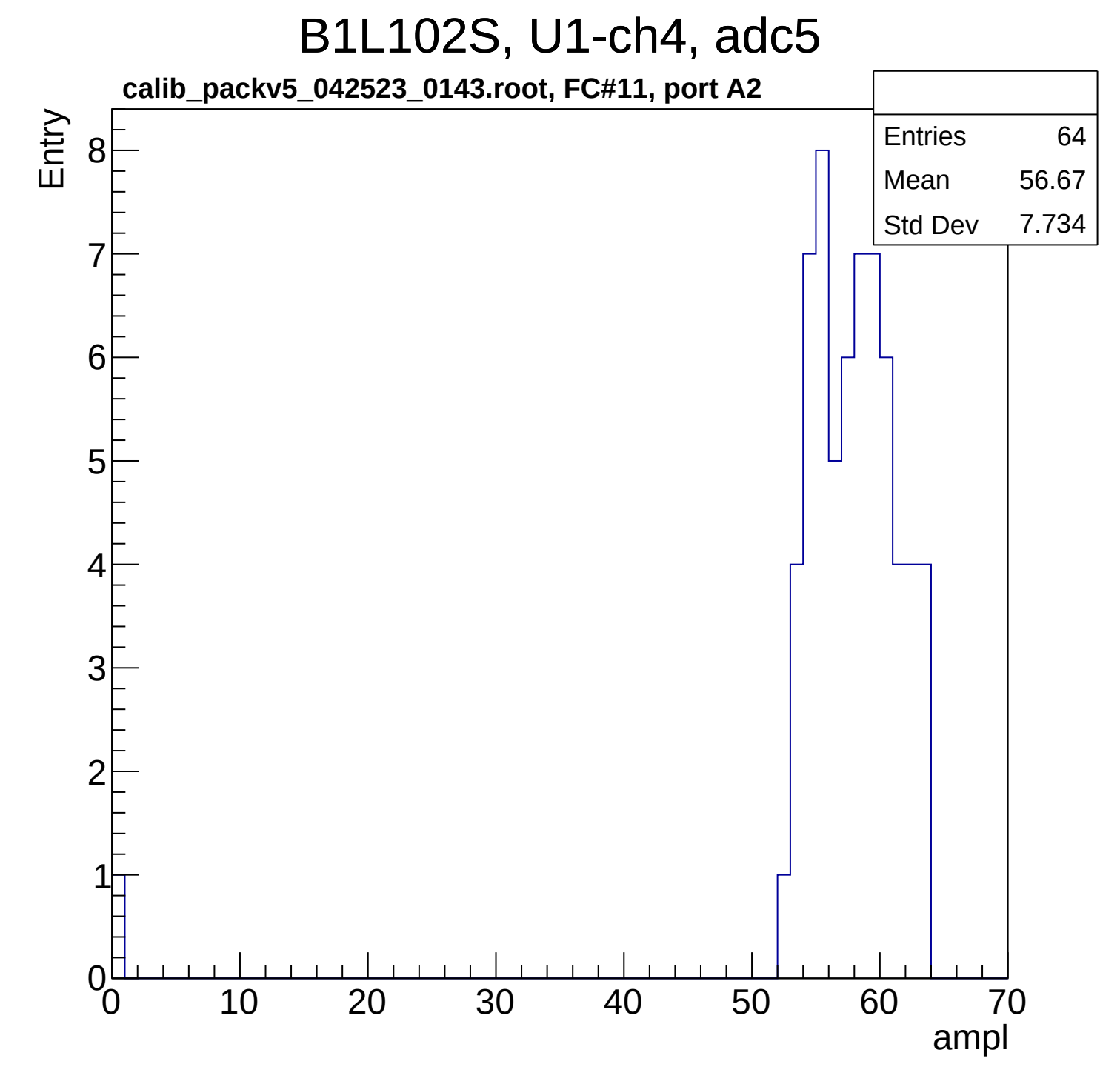
40
50
60
70


B1L102S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 51.33
Std Dev 3.233
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 56.67
Std Dev 7.734
Entry
ampl
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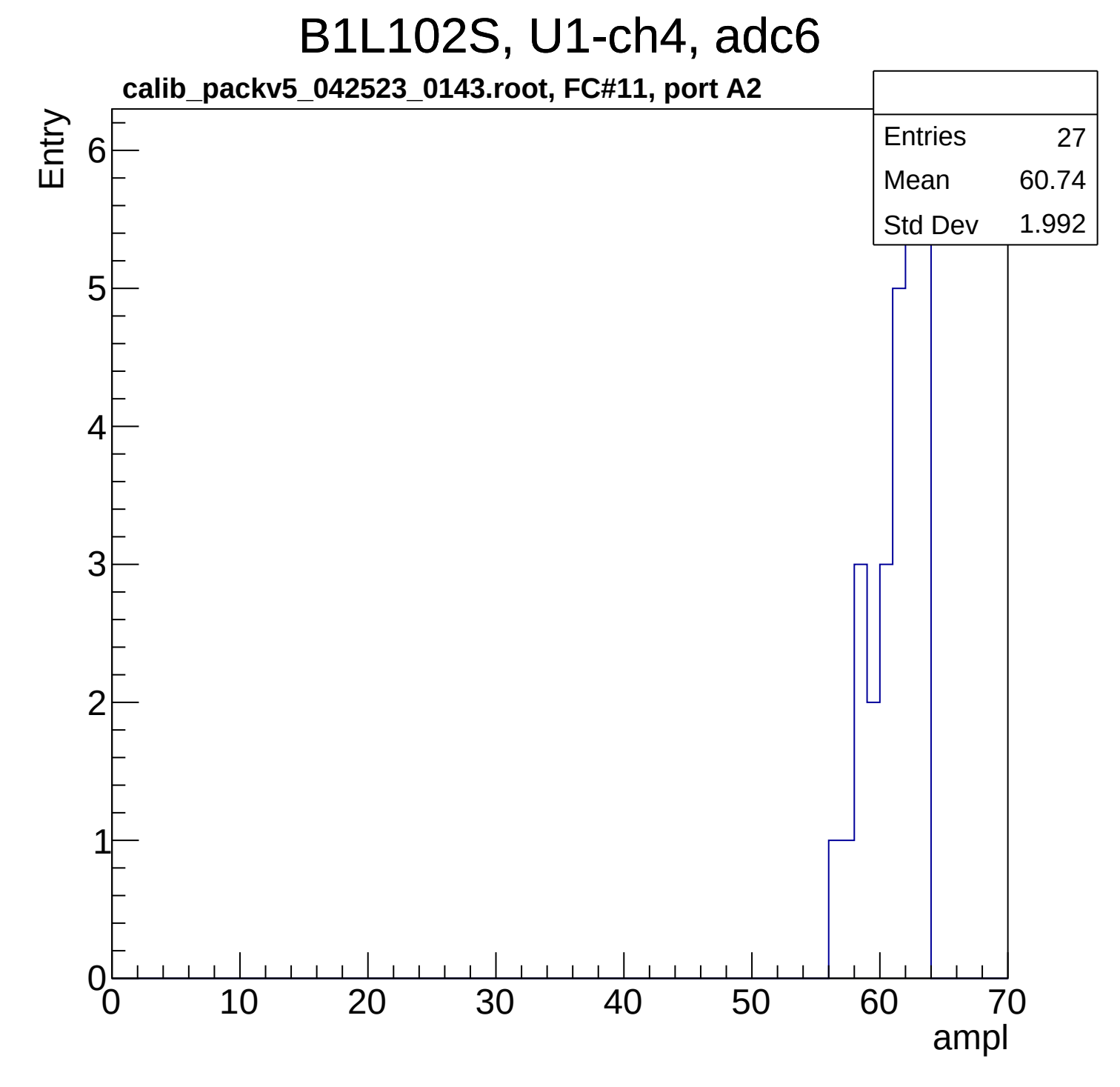
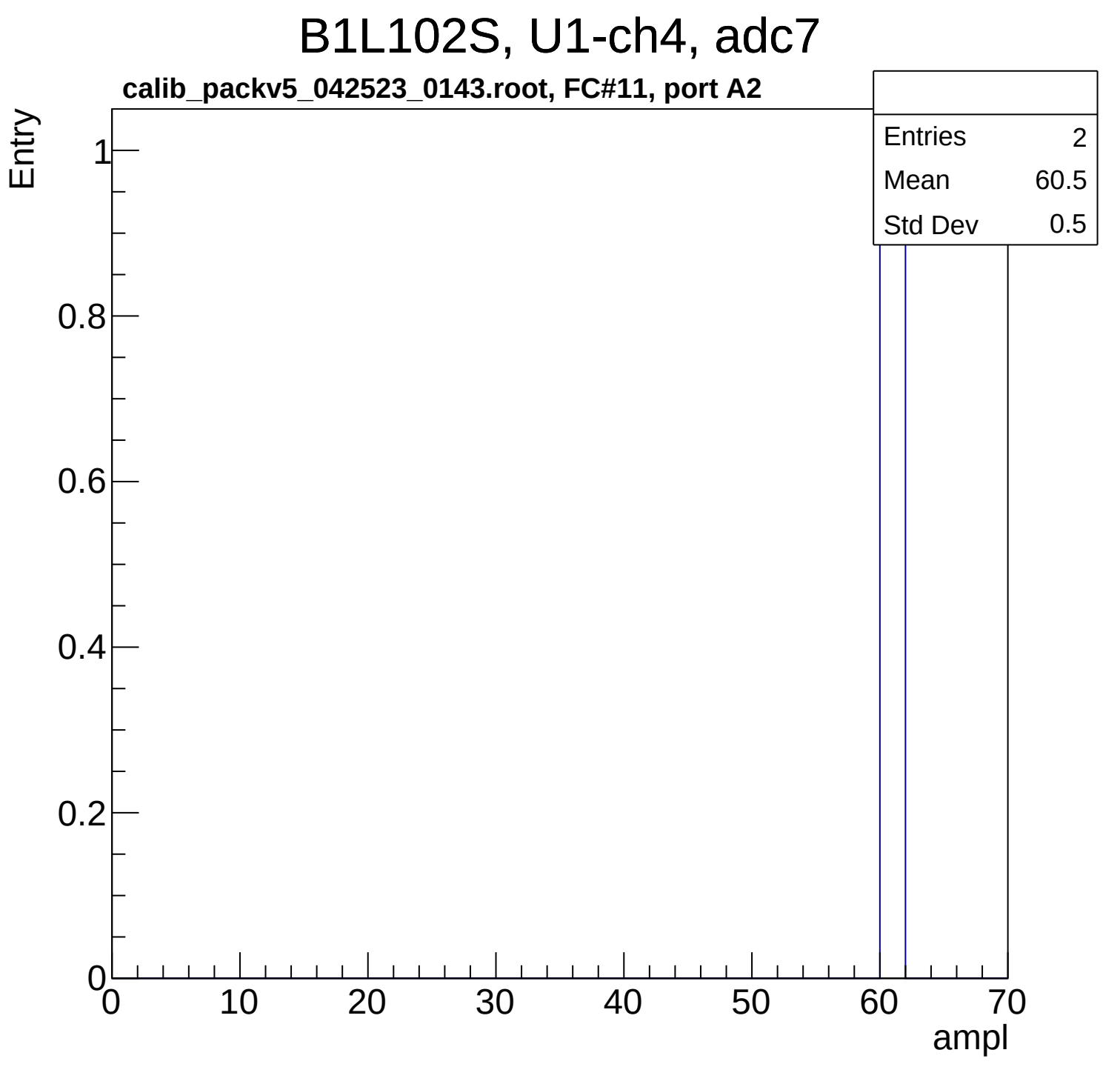
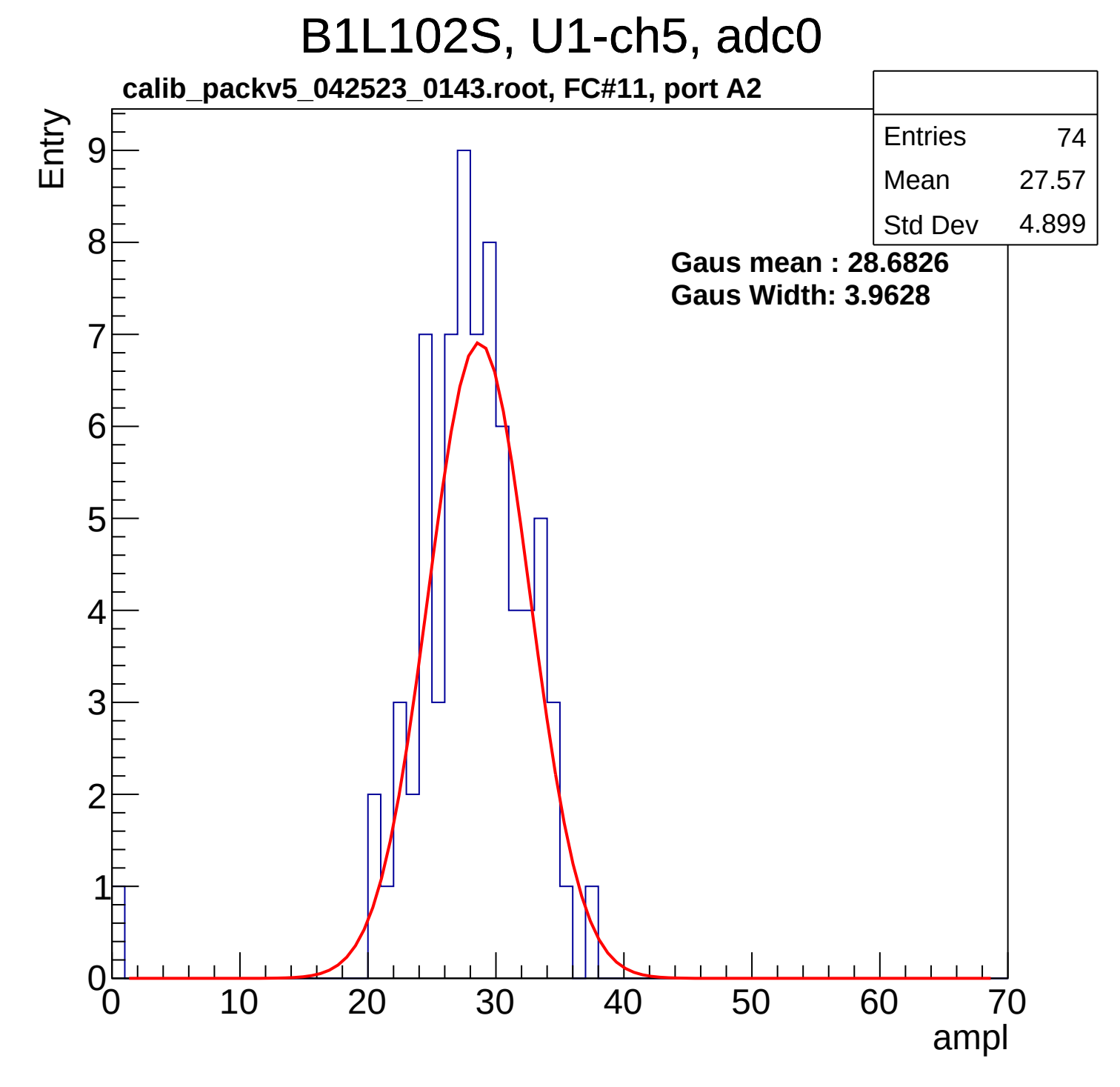
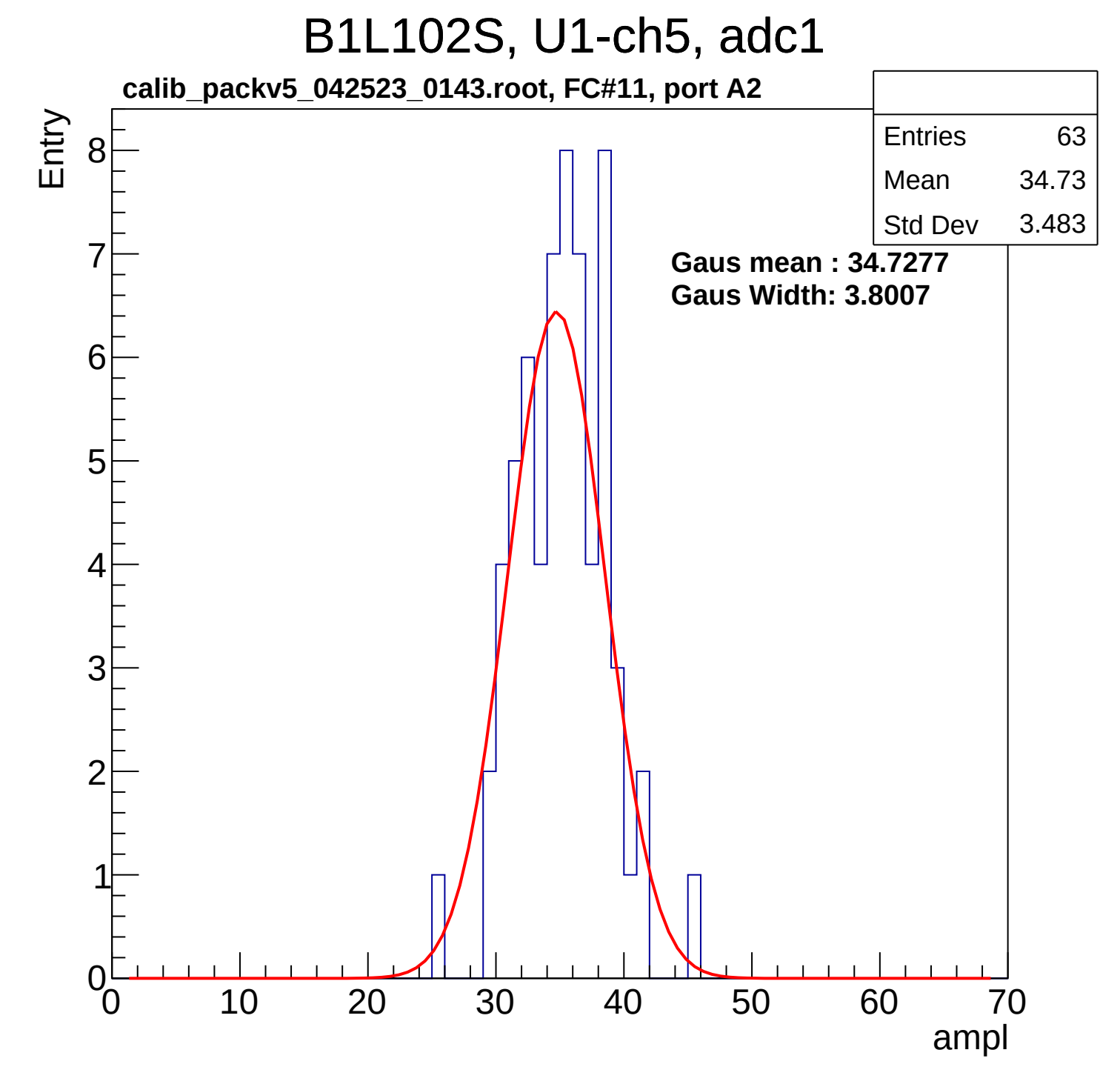
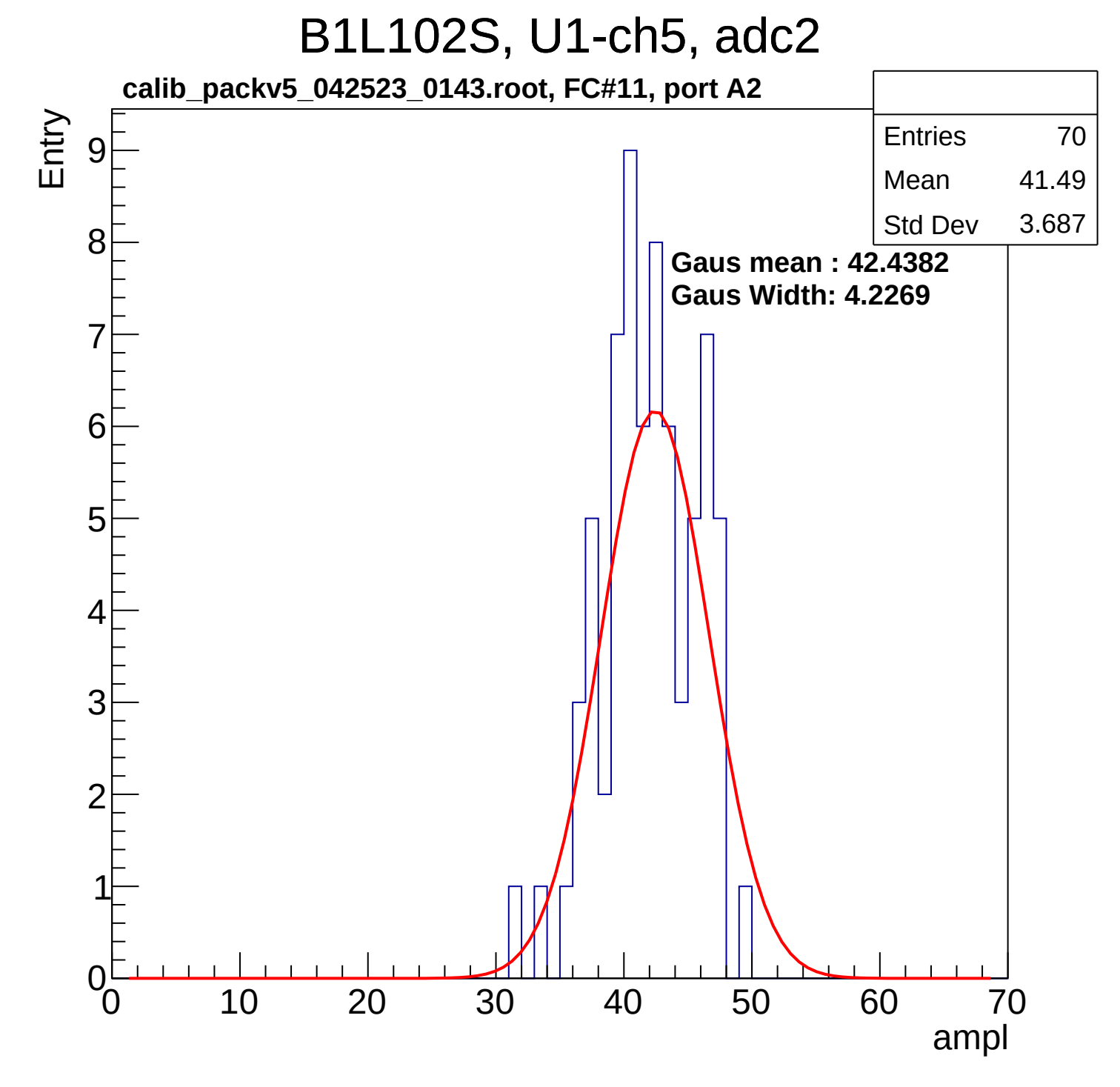
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

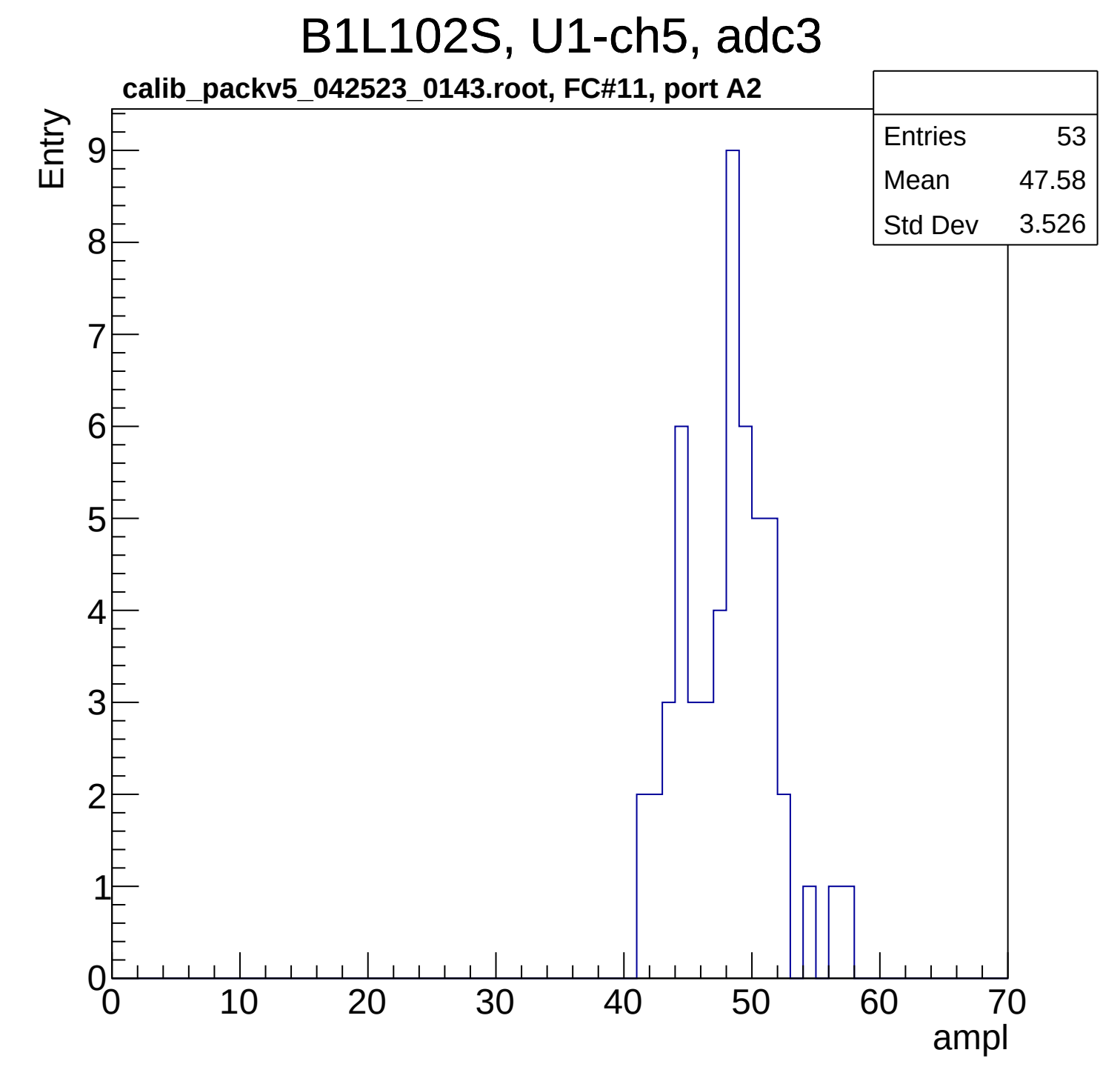

B1L102S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 27
Mean 60.74
Std Dev 1.992
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

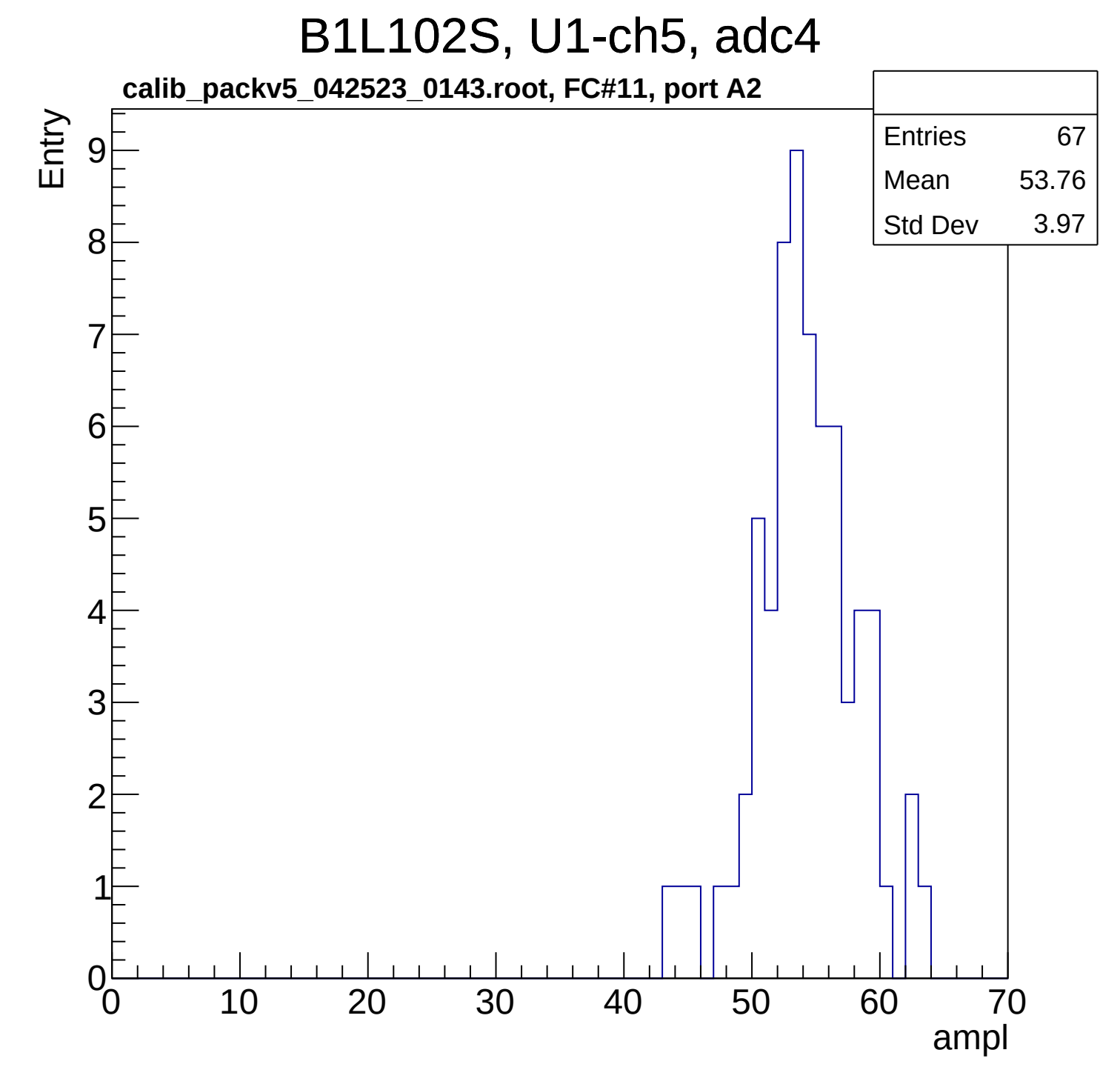

B1L102S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 60.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 27.57
Std Dev 4.899
Gaus mean : 28.6826
Gaus Width: 3.9628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 34.73
Std Dev 3.483
Gaus mean : 34.7277
Gaus Width: 3.8007
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 41.49
Std Dev 3.687
Gaus mean : 42.4382
Gaus Width: 4.2269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 47.58
Std Dev 3.526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 53.76
Std Dev 3.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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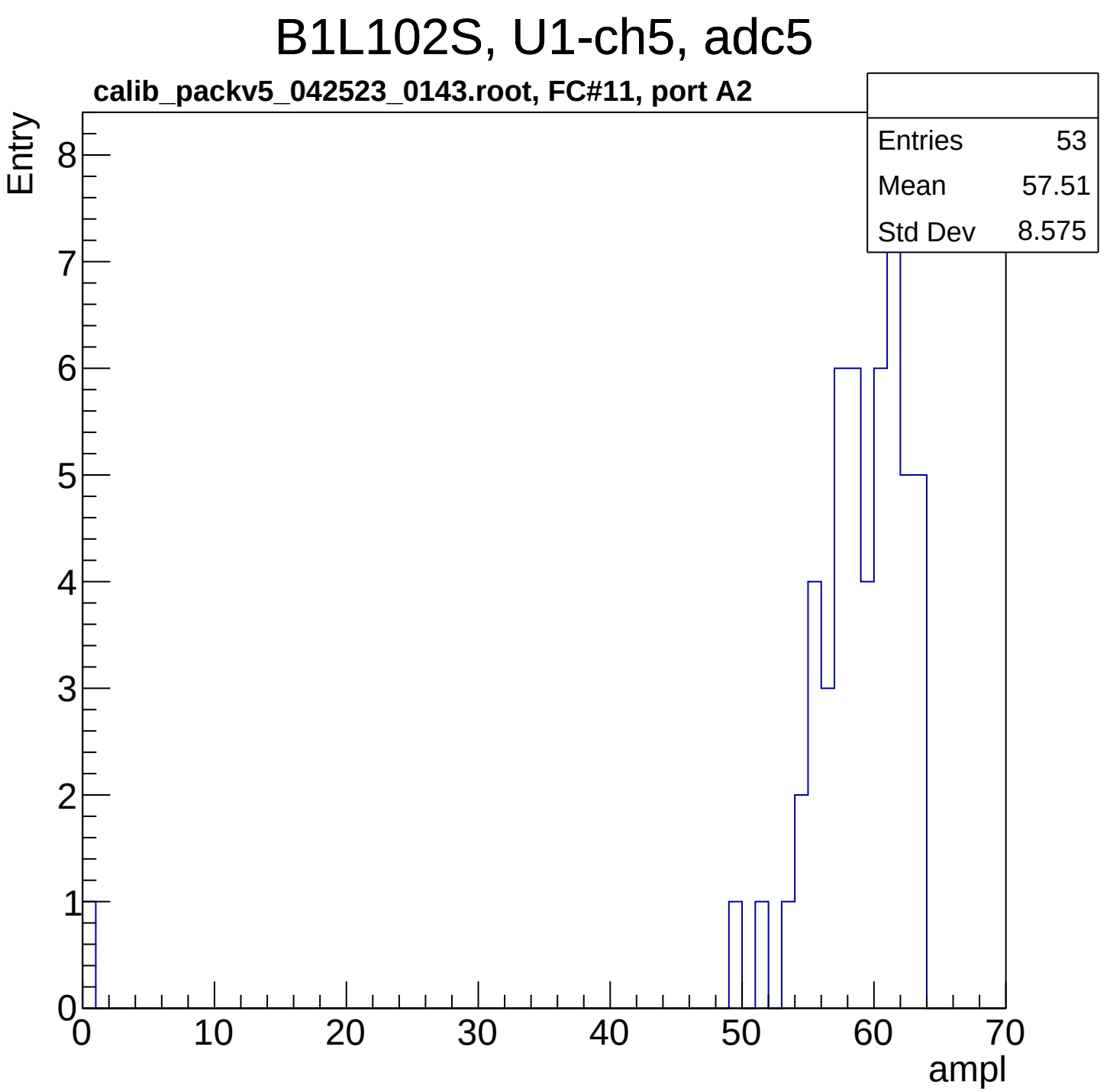
B1L102S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 57.51
Std Dev 8.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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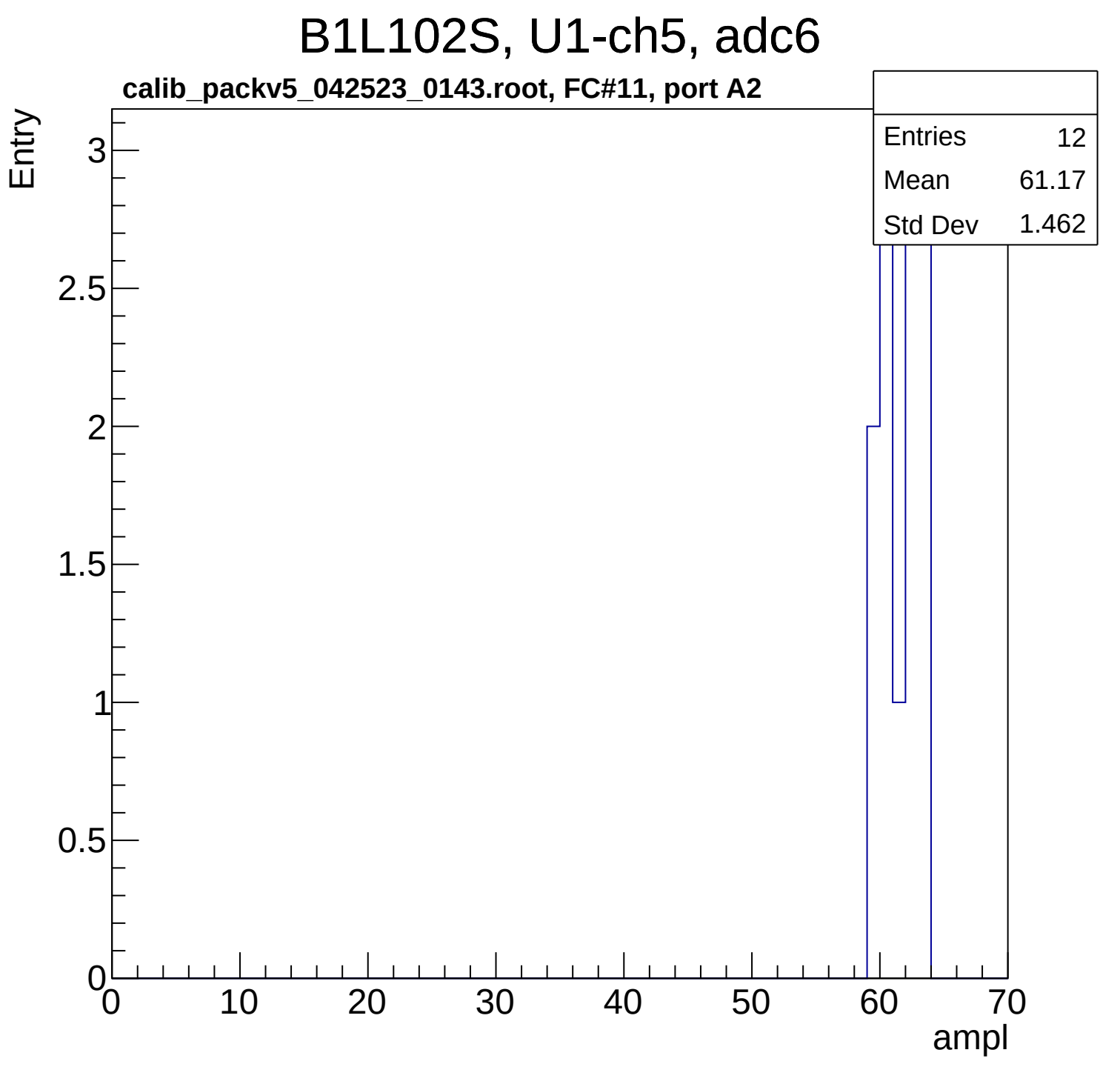
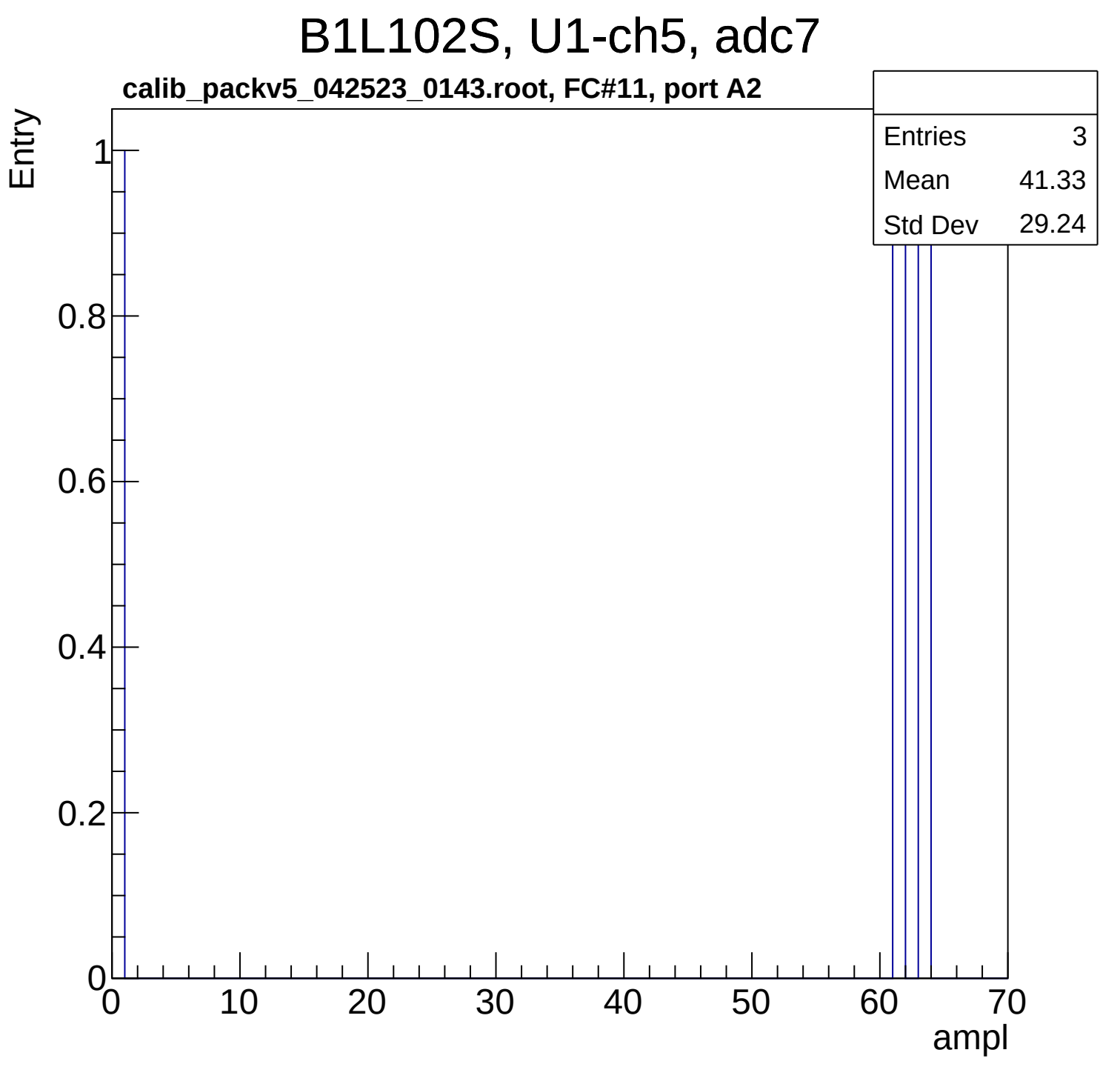
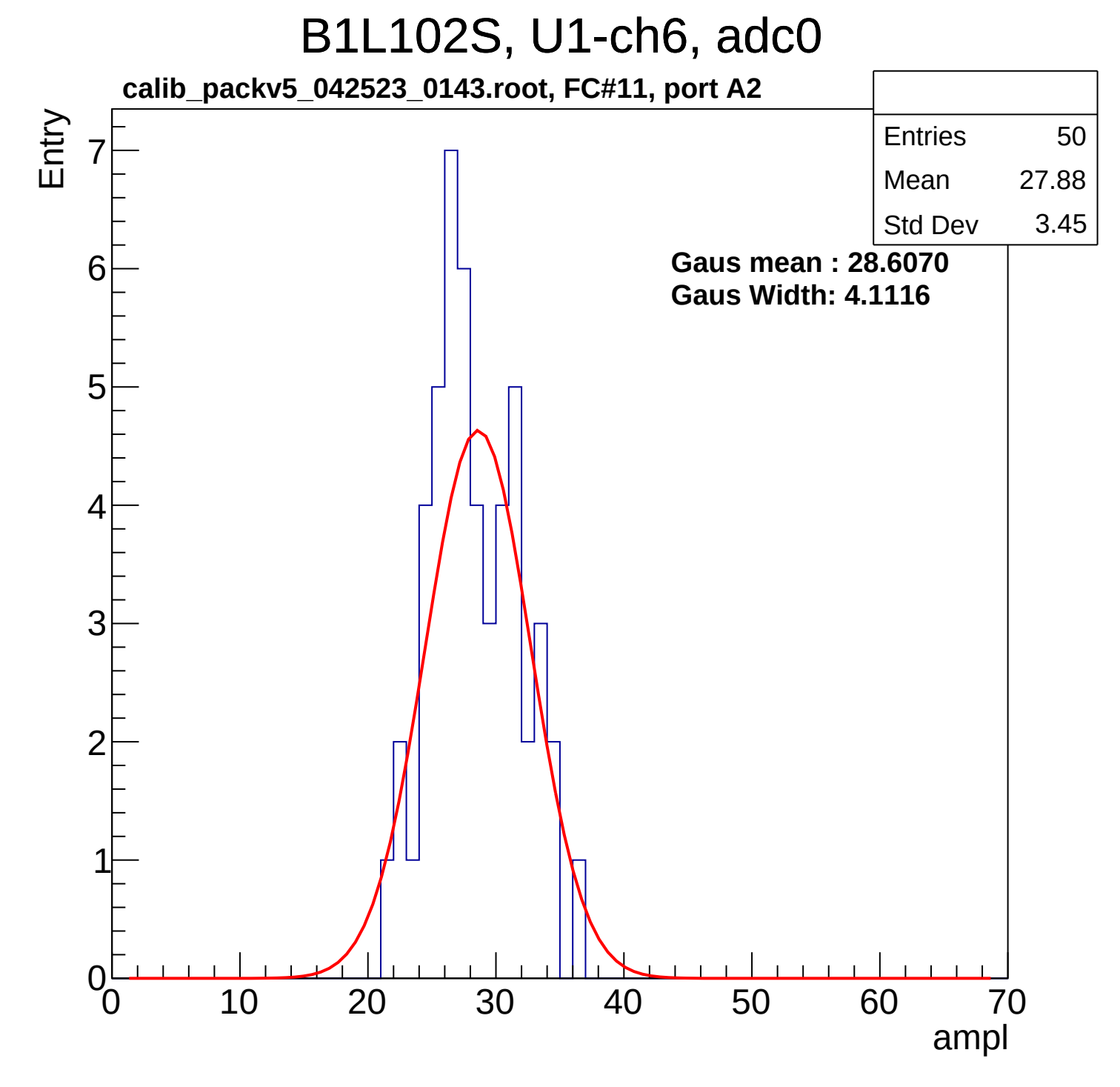
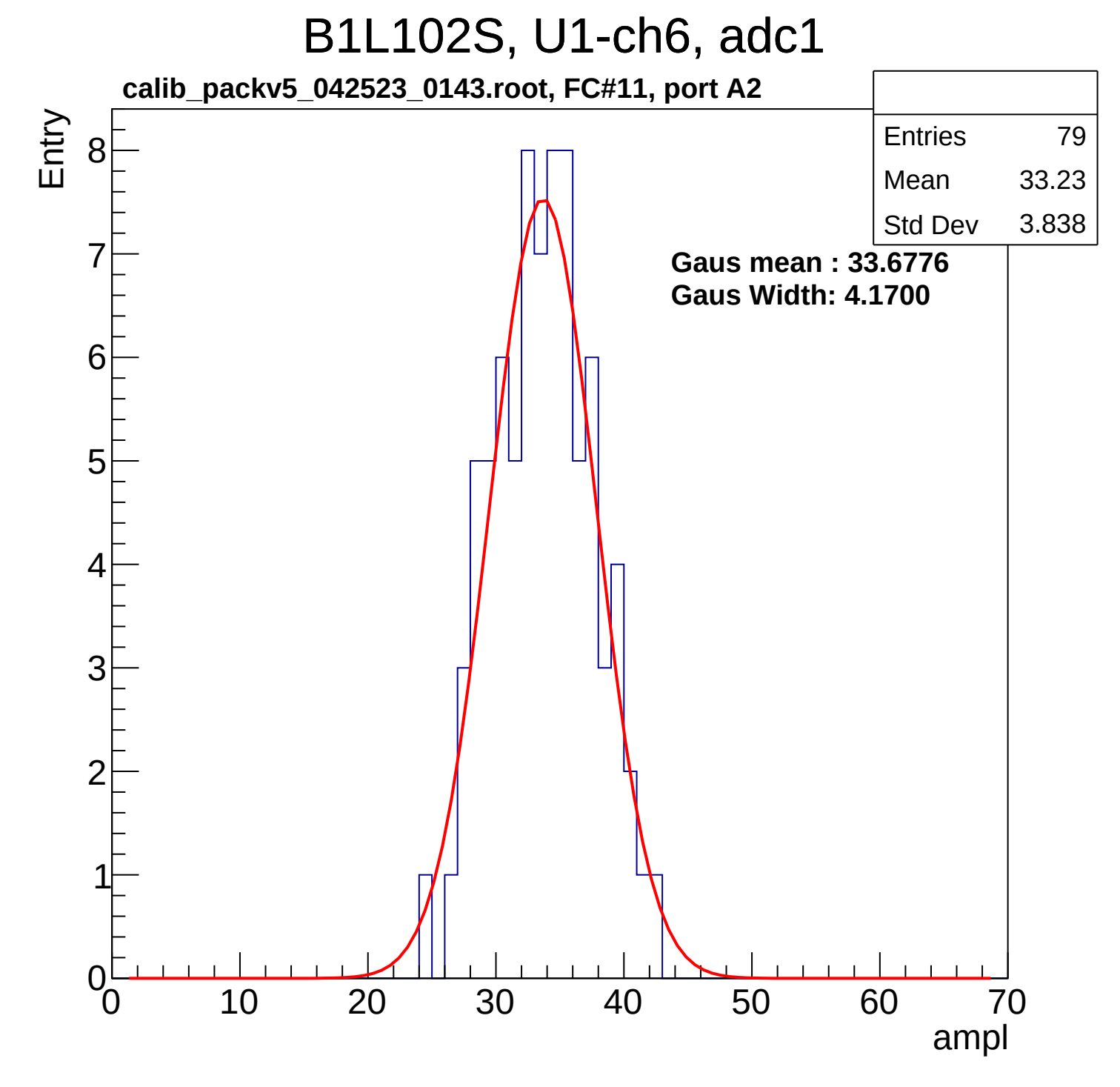
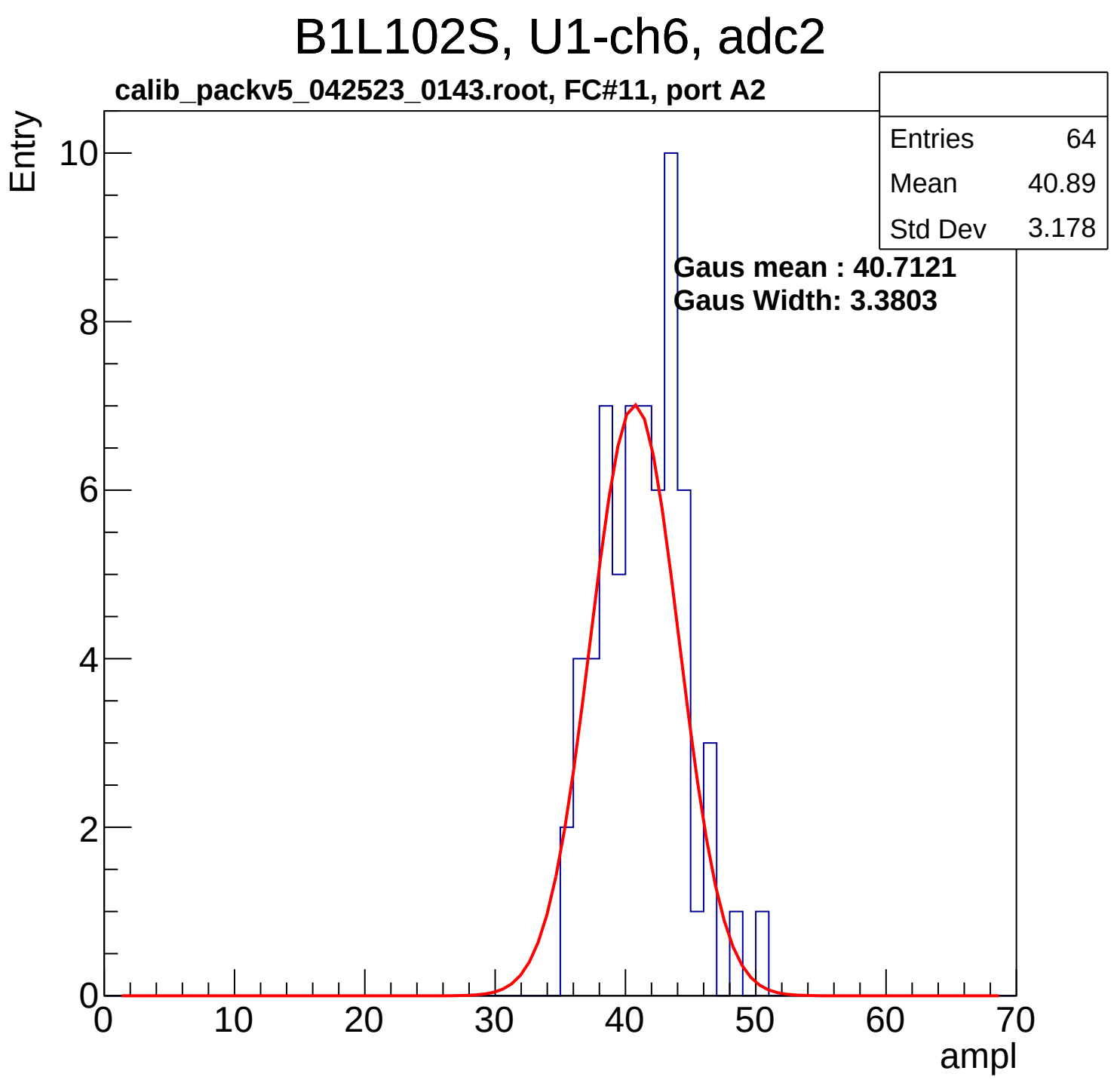
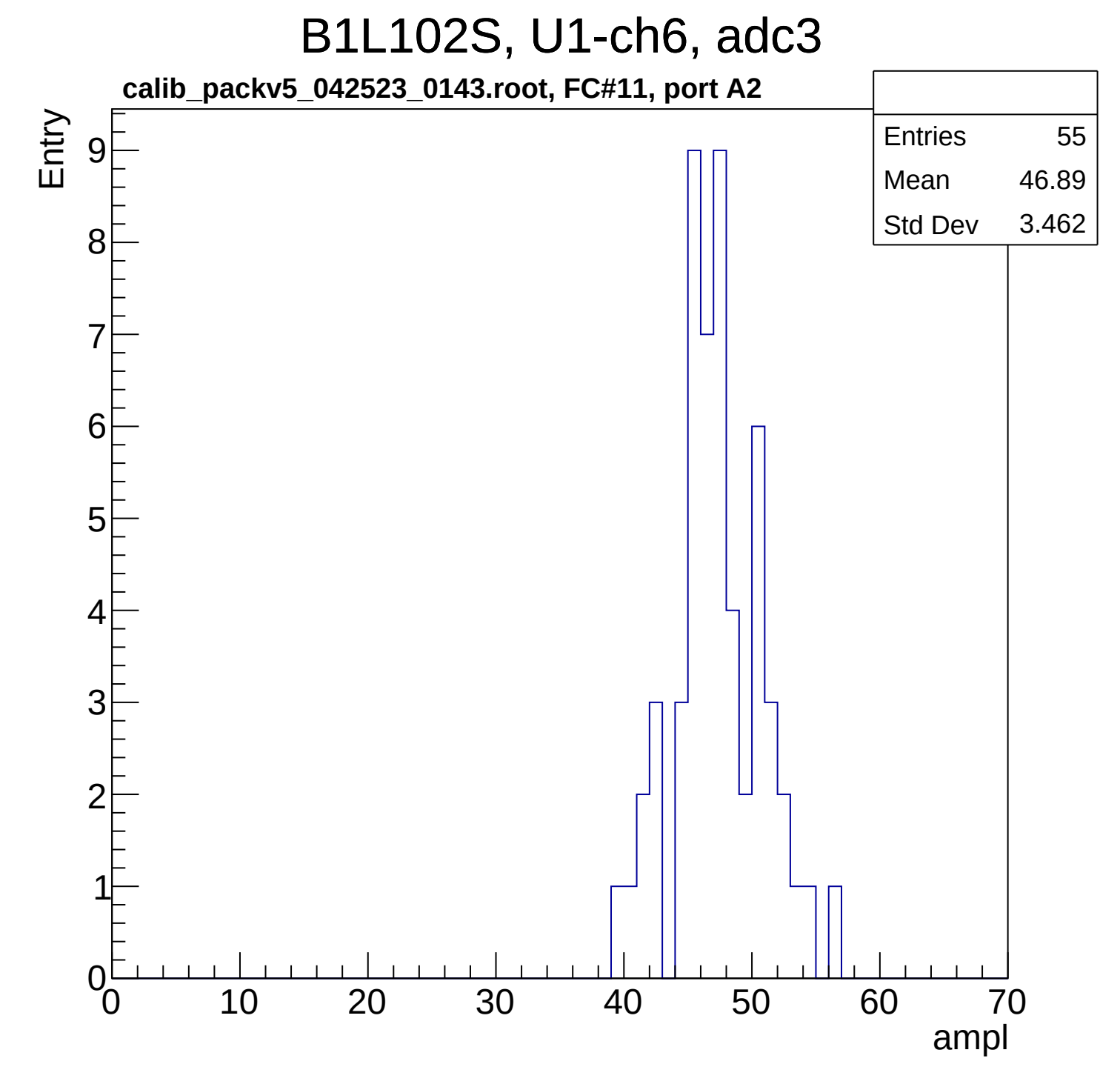

B1L102S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.17
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 27.88
Std Dev 3.45
Gaus mean : 28.6070
Gaus Width: 4.1116
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 33.23
Std Dev 3.838
Gaus mean : 33.6776
Gaus Width: 4.1700
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 40.89
Std Dev 3.178
Gaus mean : 40.7121
Gaus Width: 3.3803
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 46.89
Std Dev 3.462
Entry
ampl
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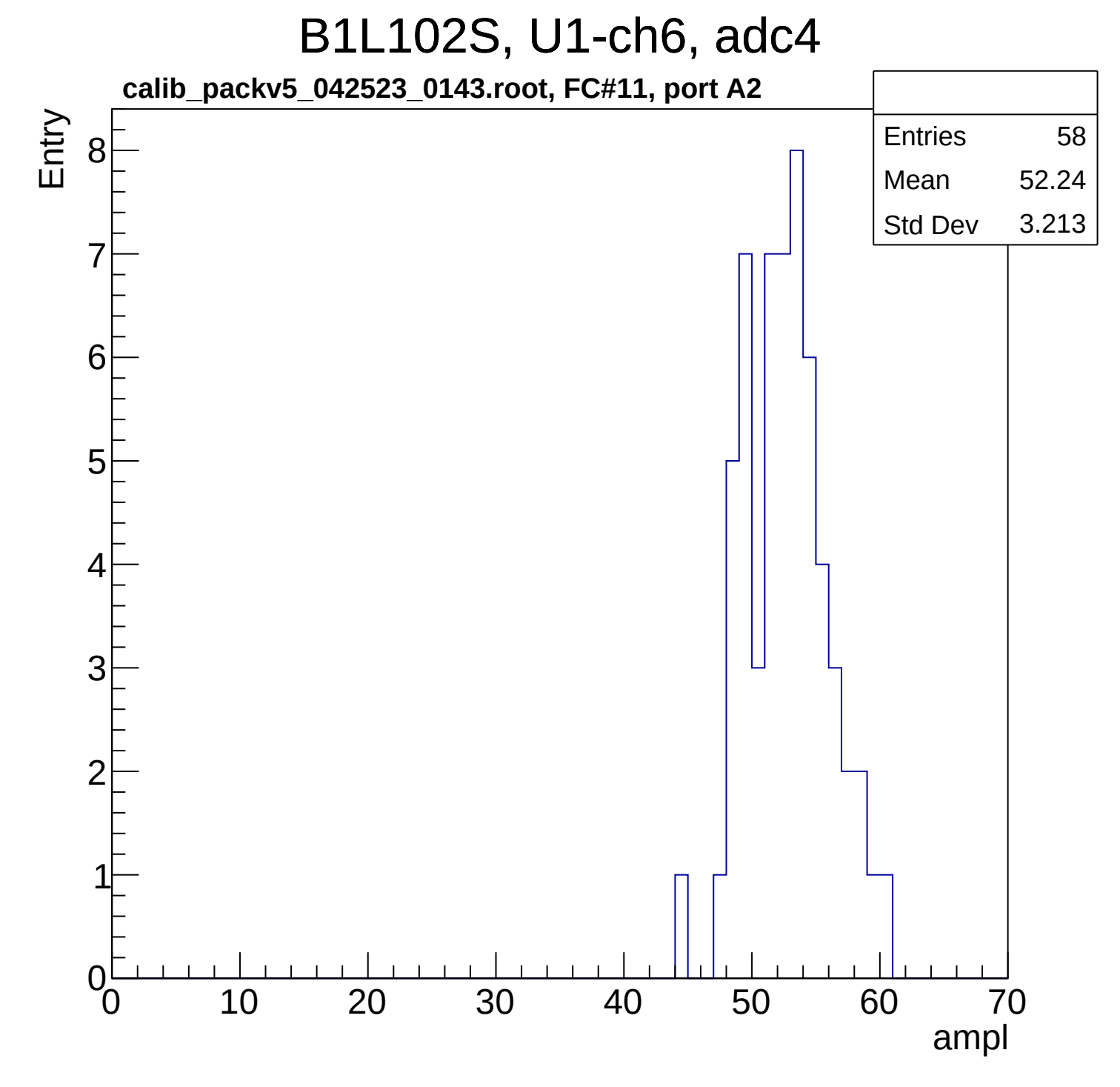
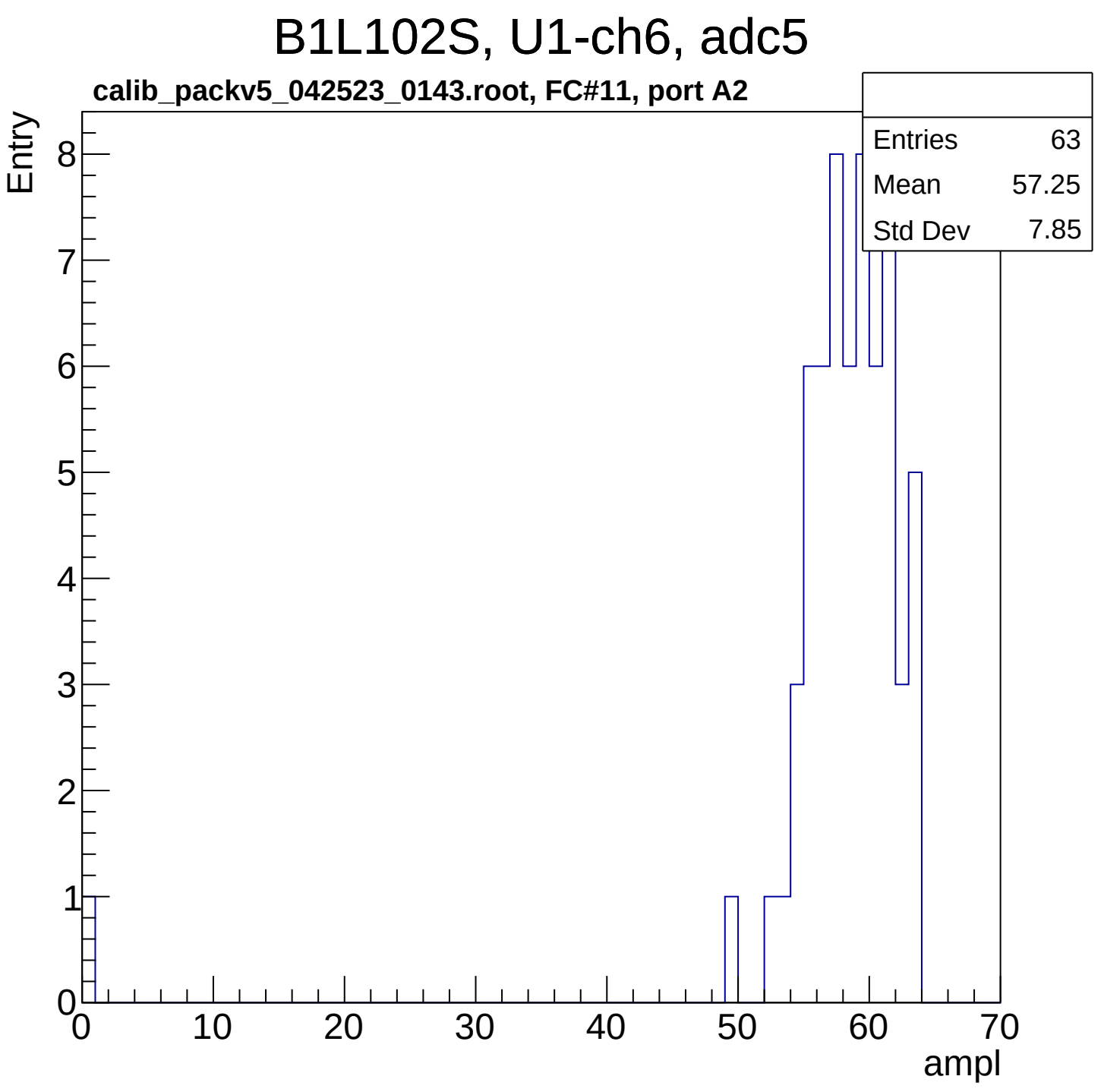
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 52.24
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 57.25
Std Dev 7.85
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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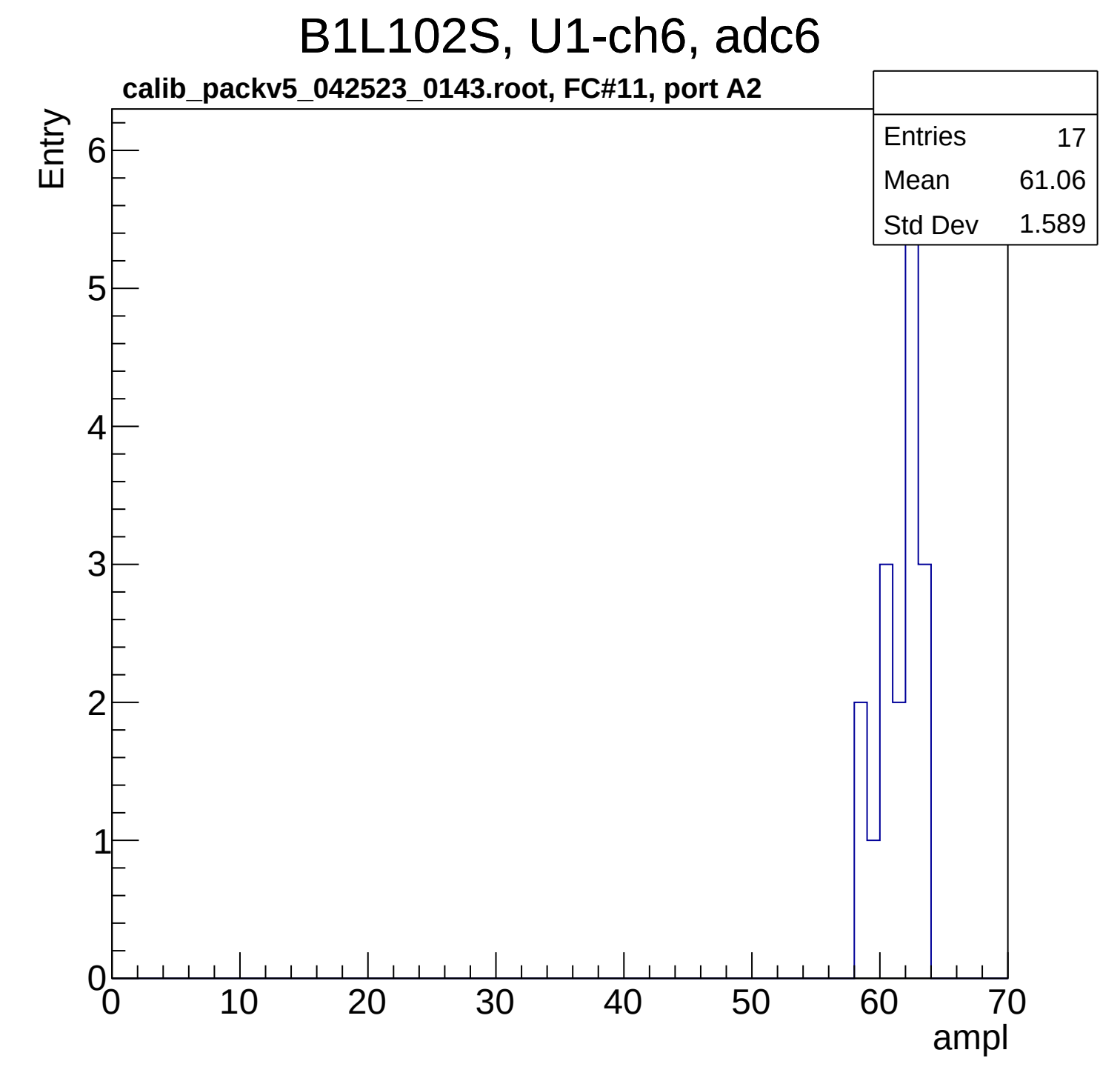
40
50
60
70


B1L102S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 17
Mean 61.06
Std Dev 1.589
Entry
0
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2
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4
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0
10
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30
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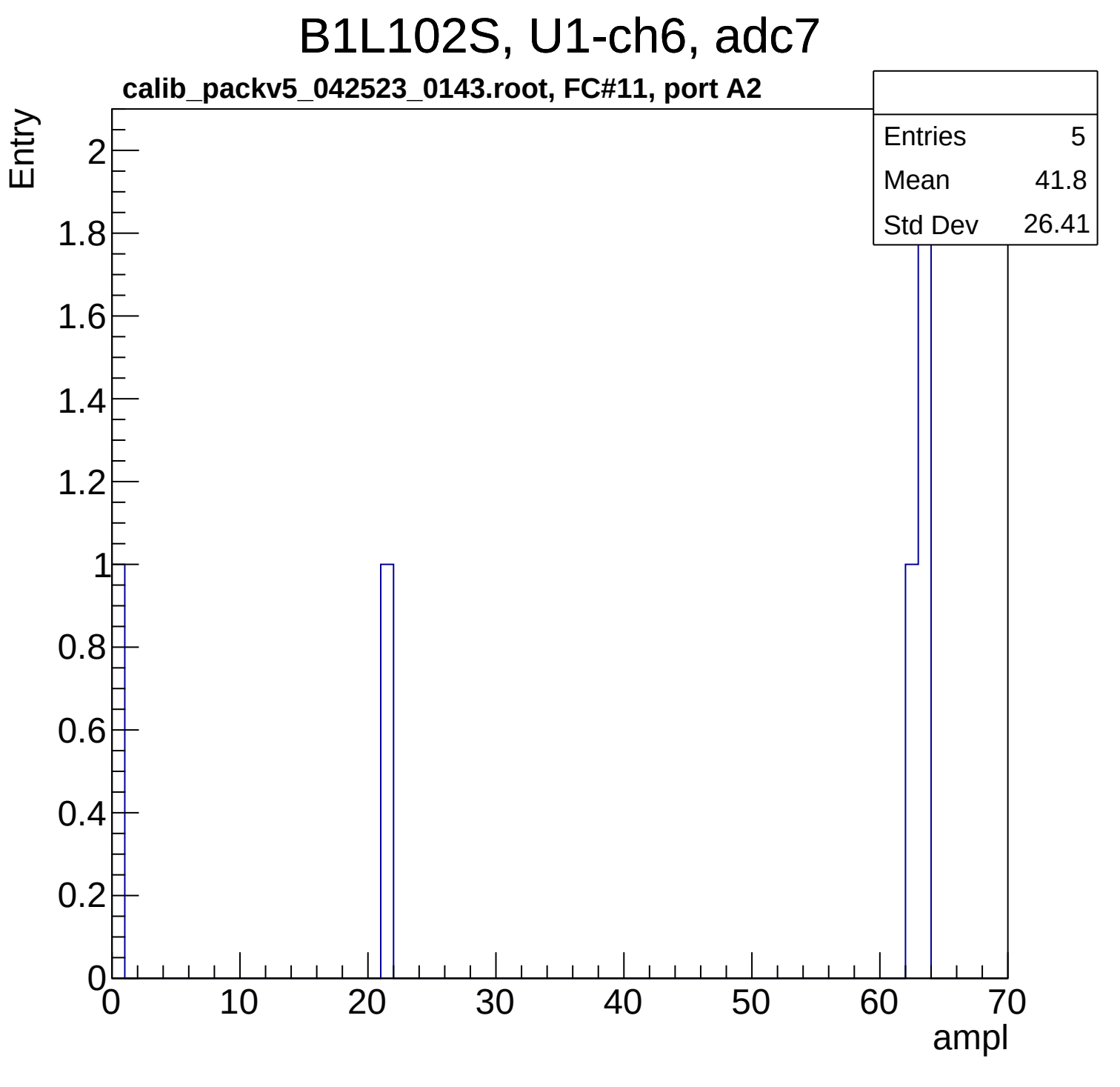
50
60
70
ampl


B1L102S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 41.8
Std Dev 26.41
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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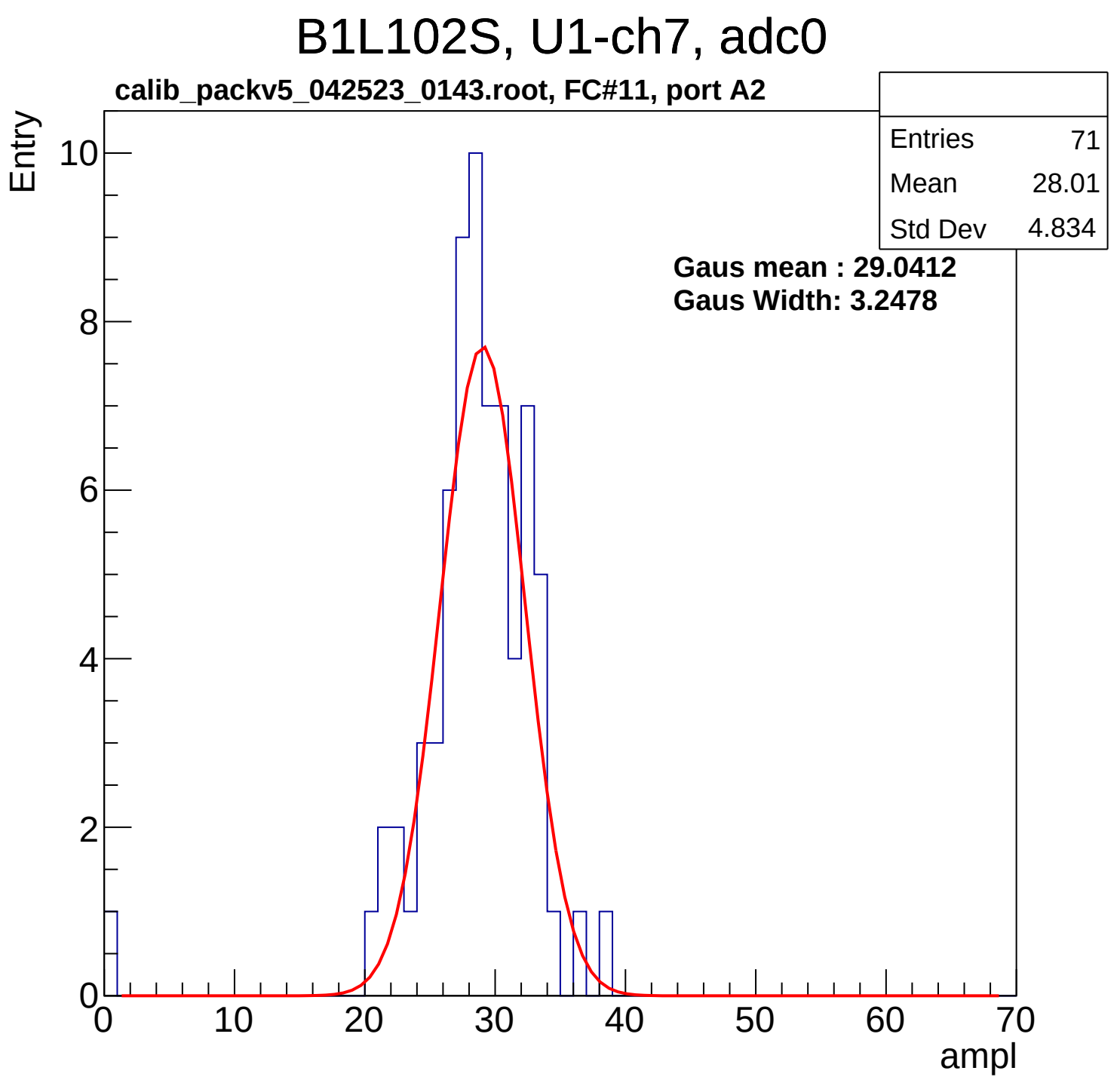
40
50
60
70


B1L102S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.01
Std Dev 4.834
Gaus mean : 29.0412
Gaus Width: 3.2478
Entry
ampl
0
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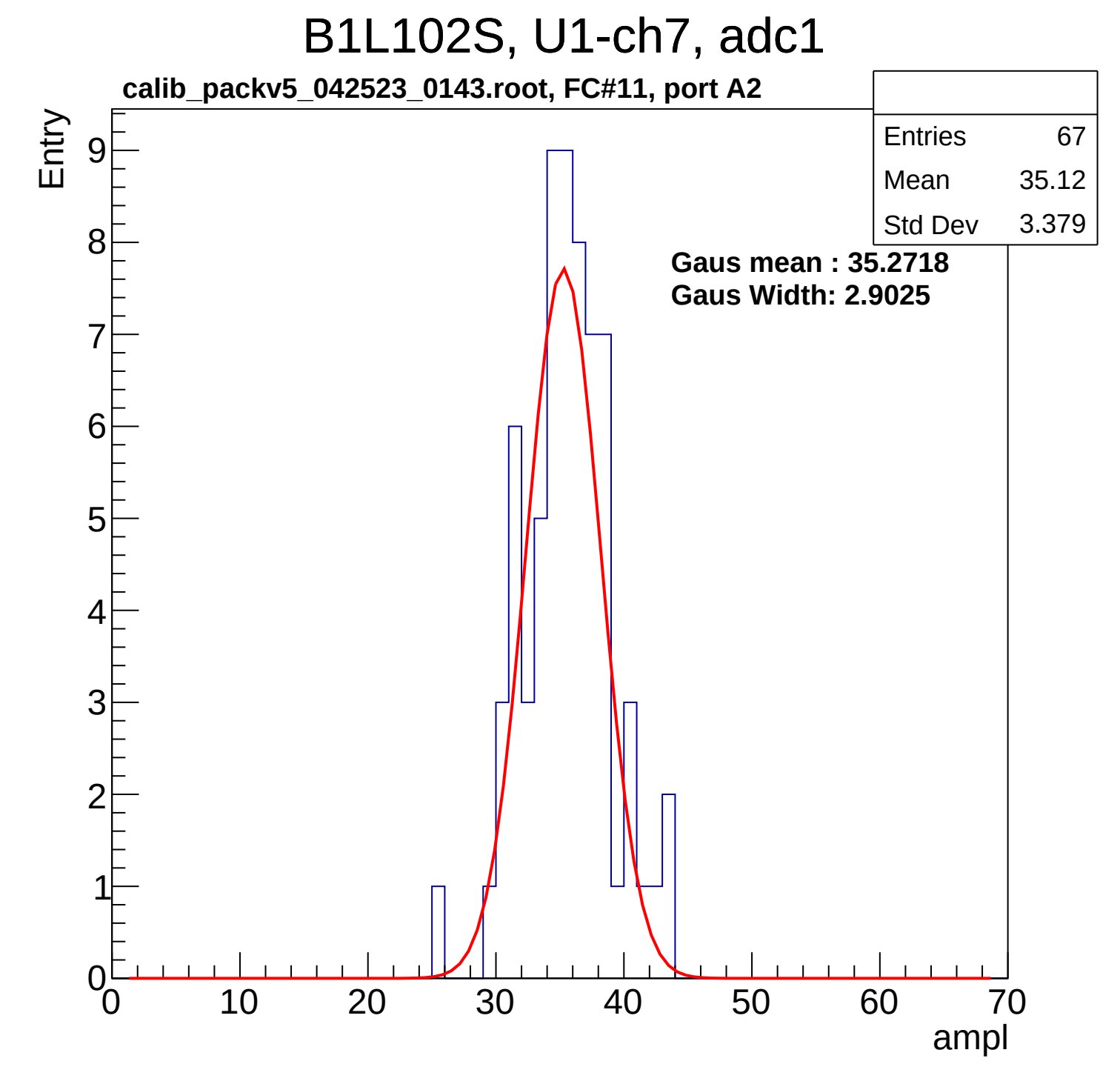
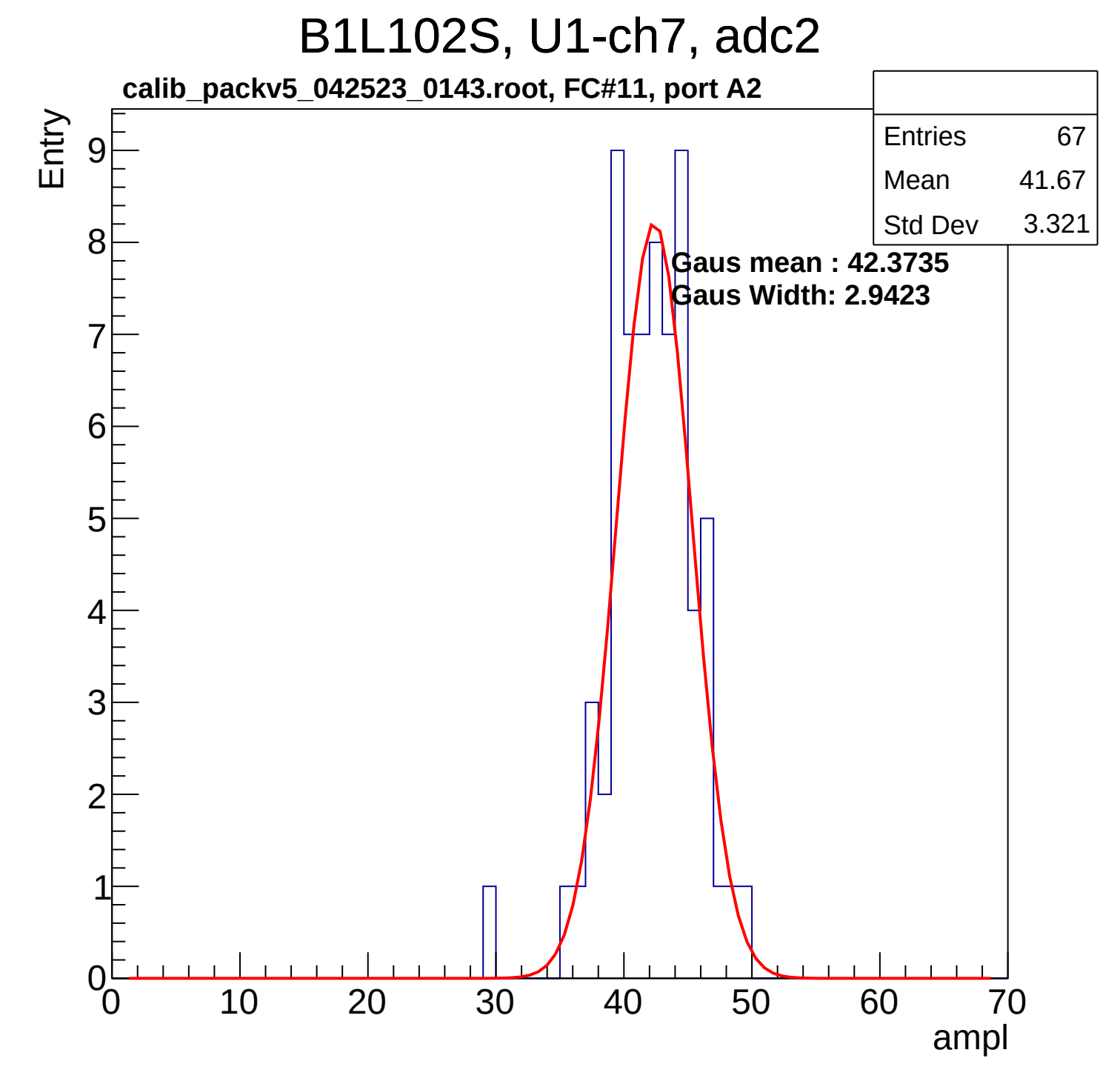
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.12
Std Dev 3.379
Gaus mean : 35.2718
Gaus Width: 2.9025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 41.67
Std Dev 3.321
Gaus mean : 42.3735
Gaus Width: 2.9423
Entry
ampl
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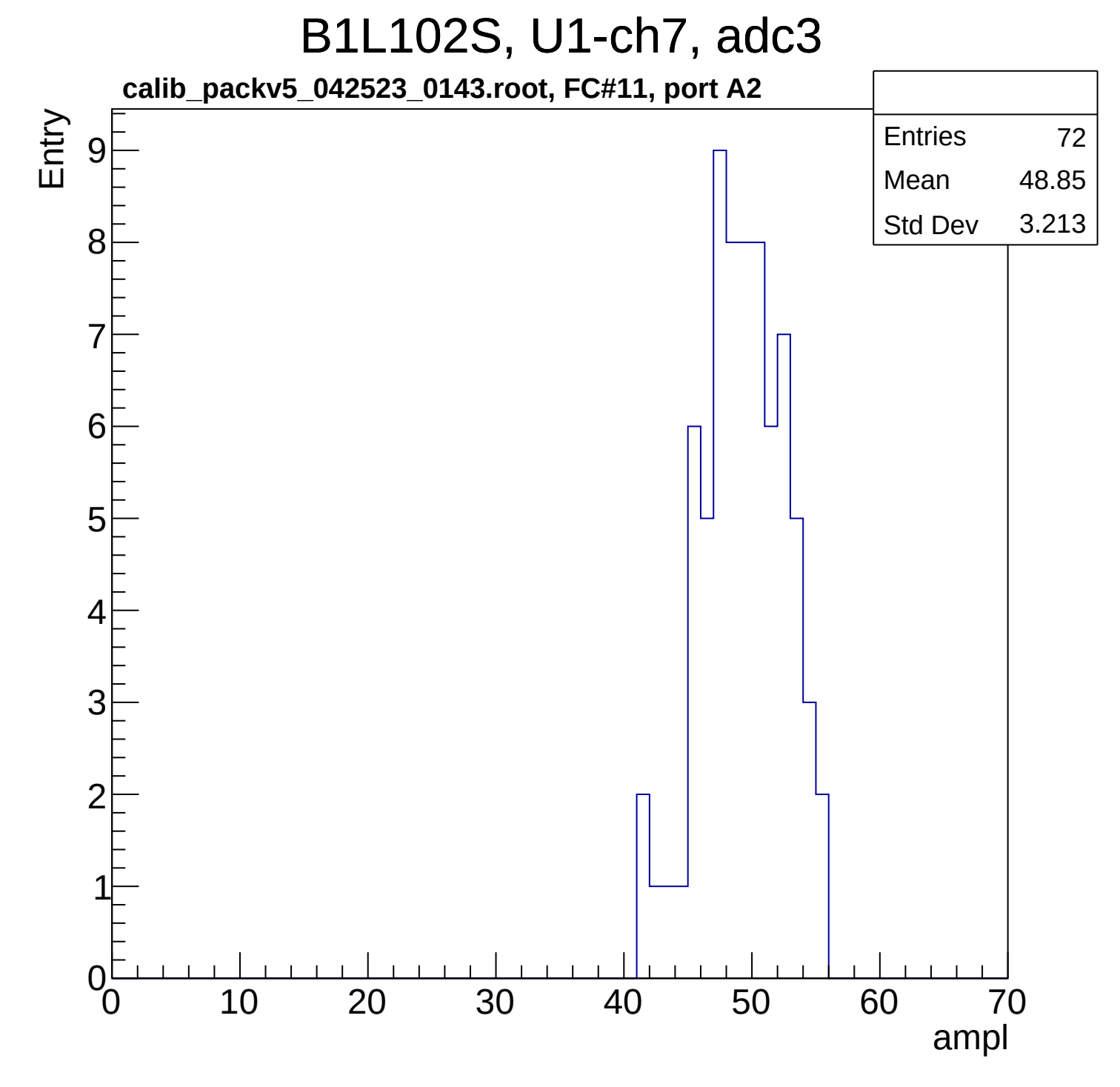
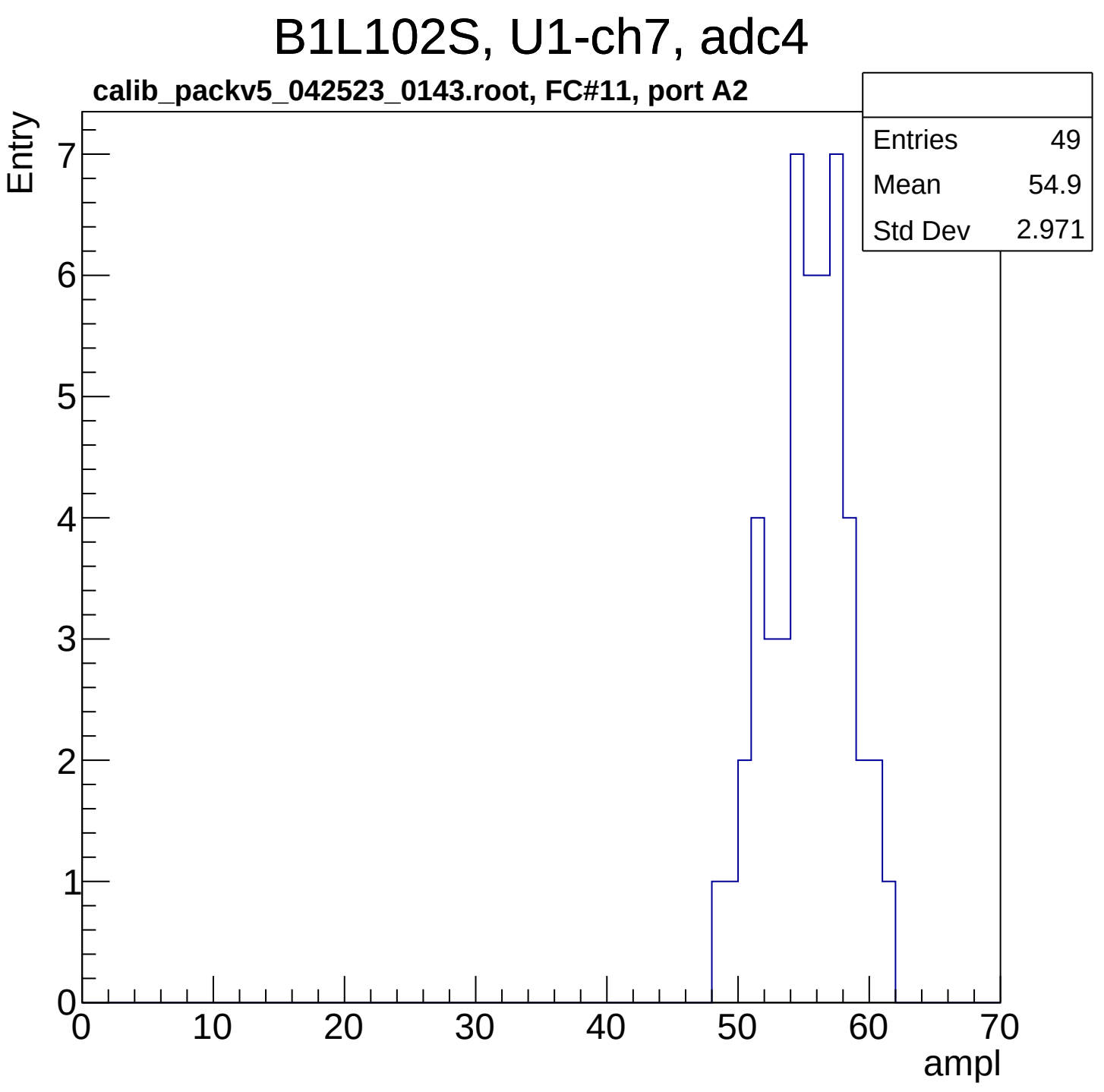
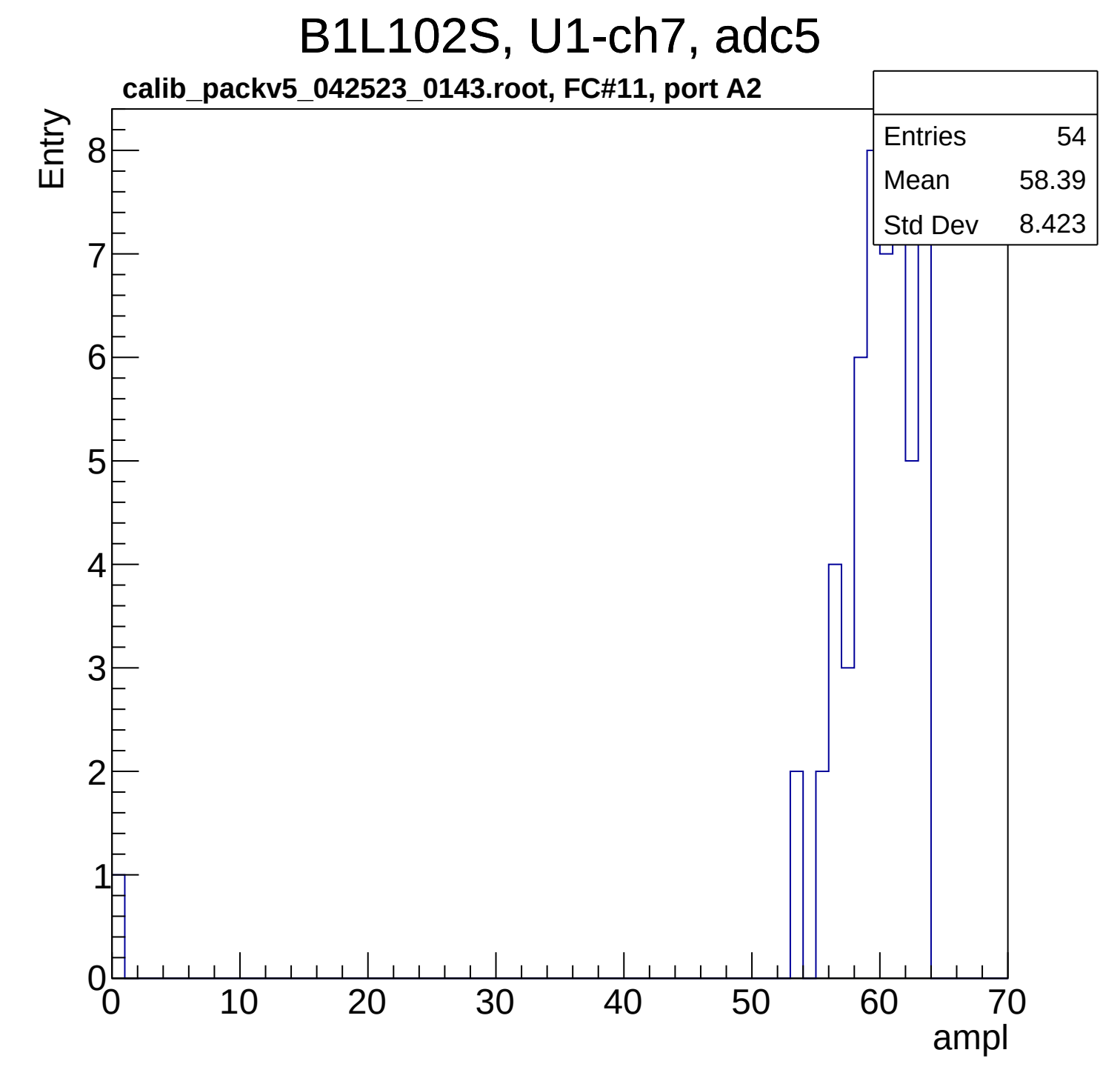
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 48.85
Std Dev 3.213
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 54.9
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 58.39
Std Dev 8.423
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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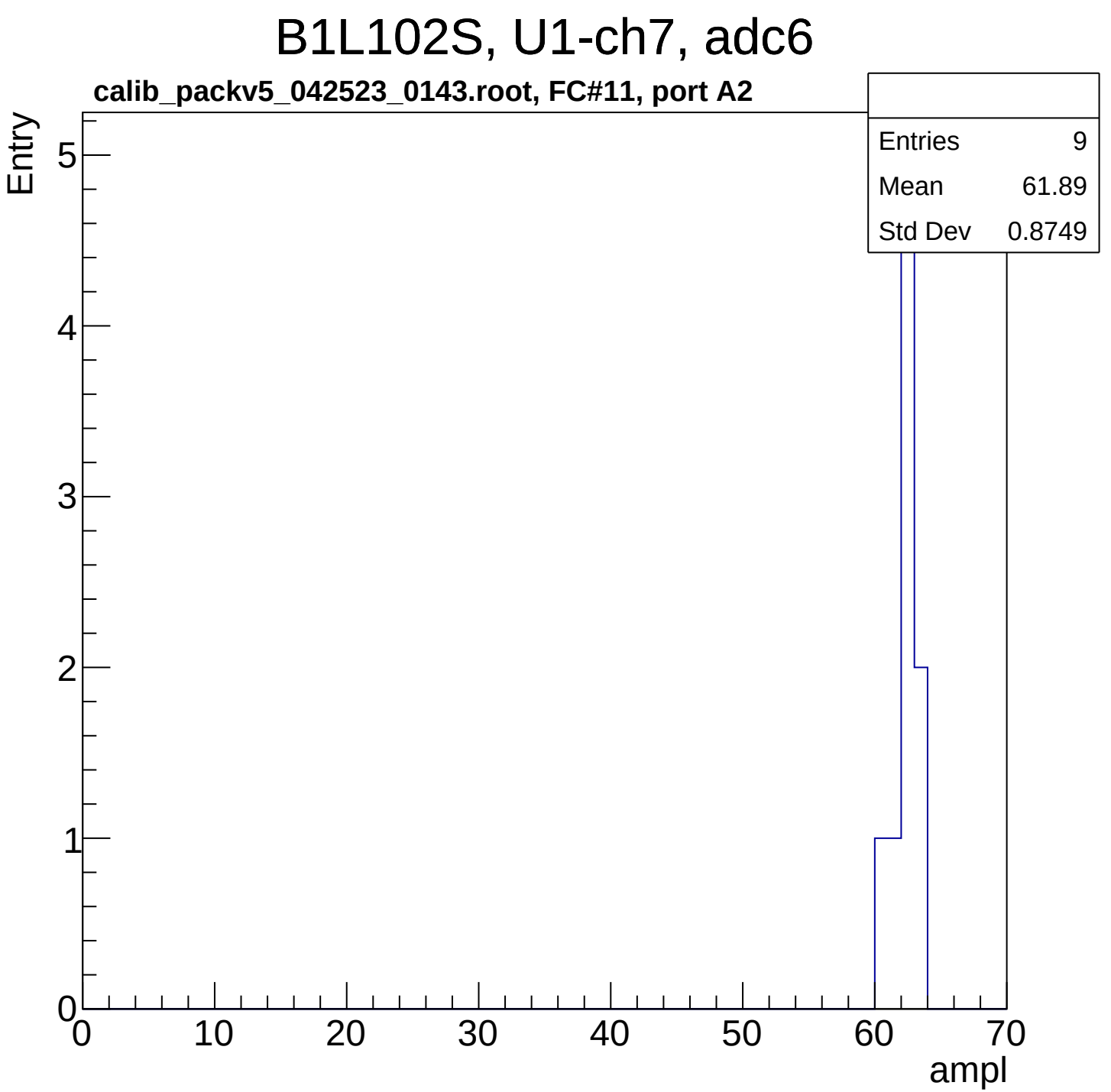
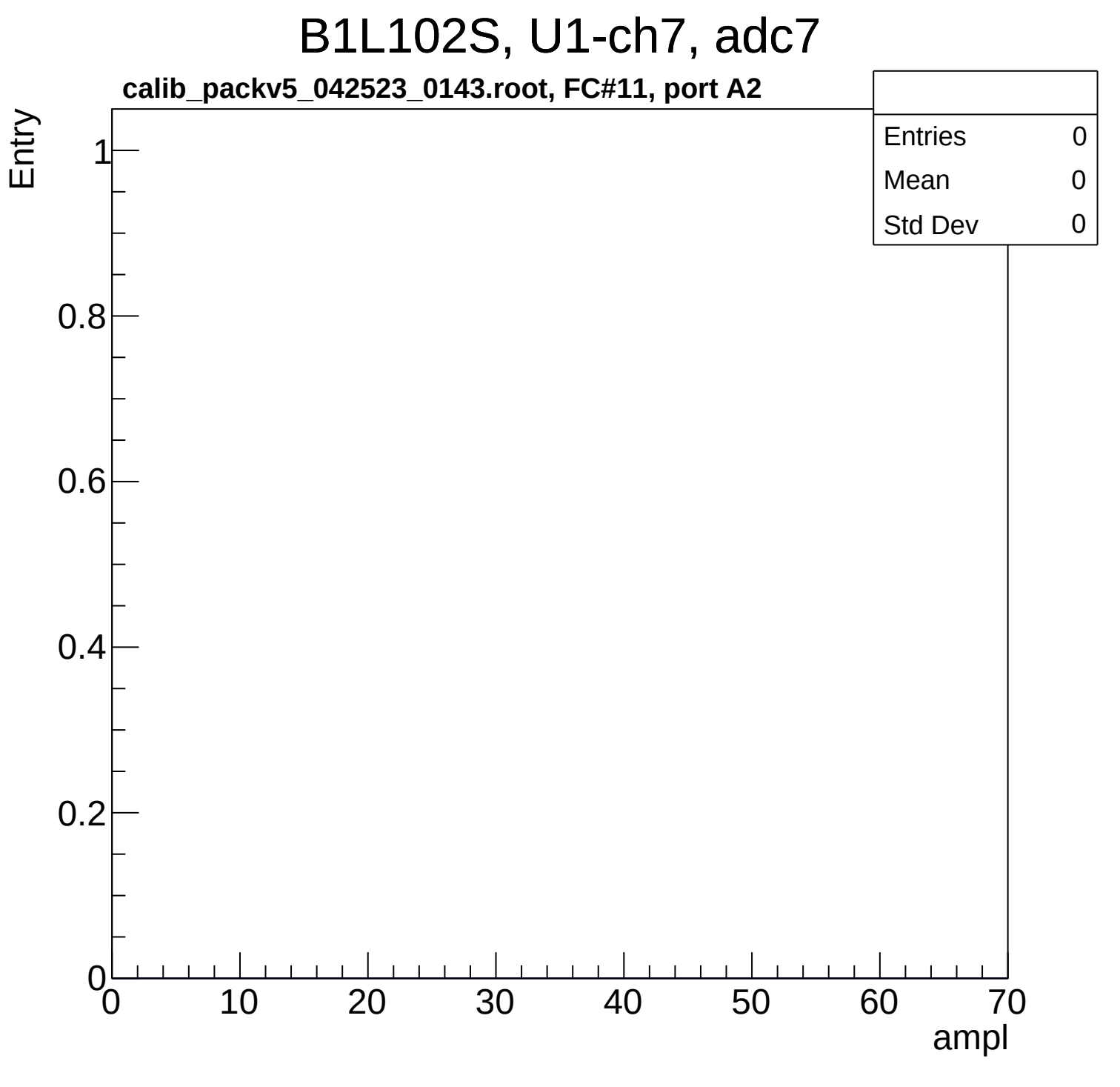

B1L102S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 61.89
Std Dev 0.8749
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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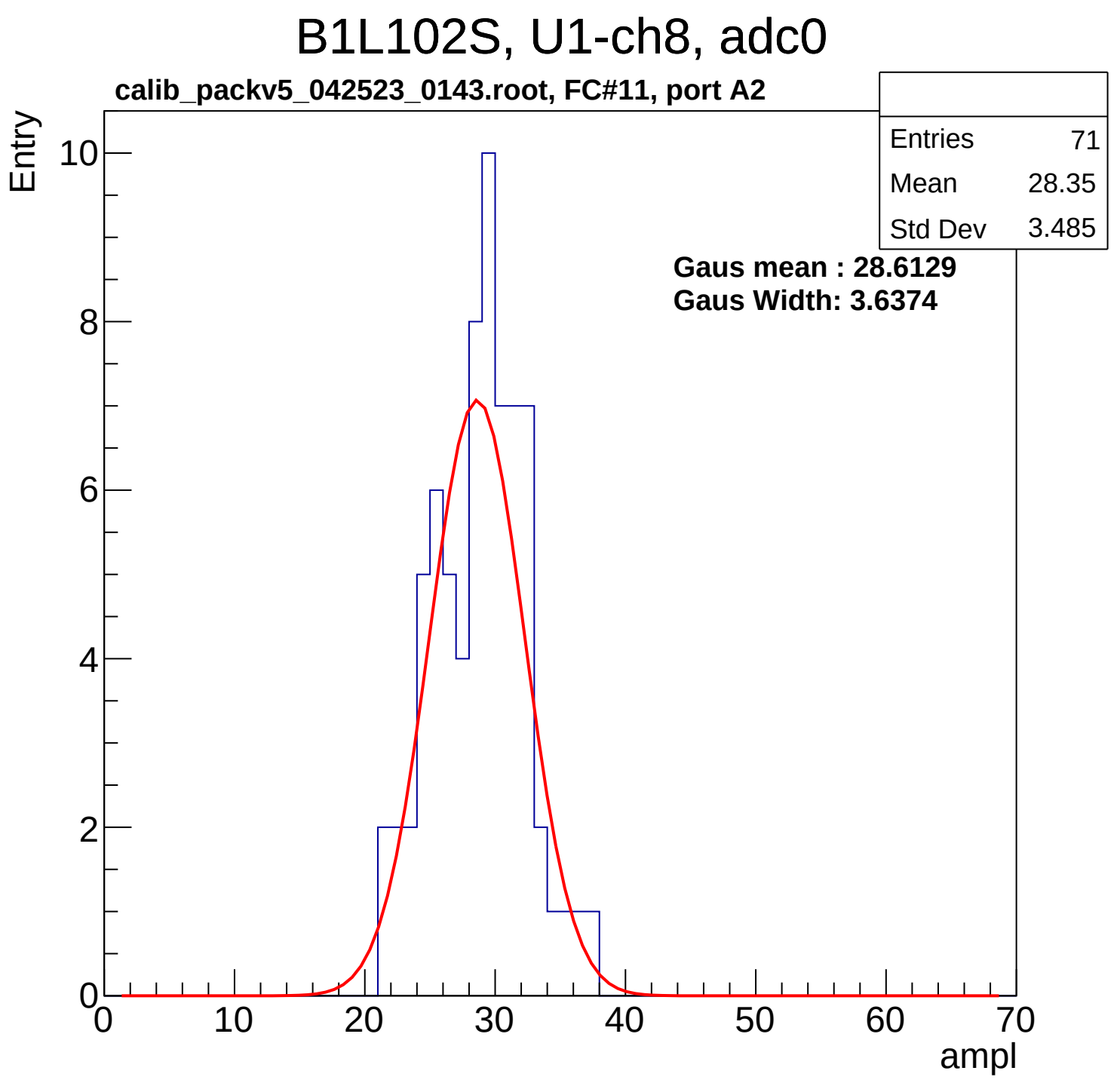
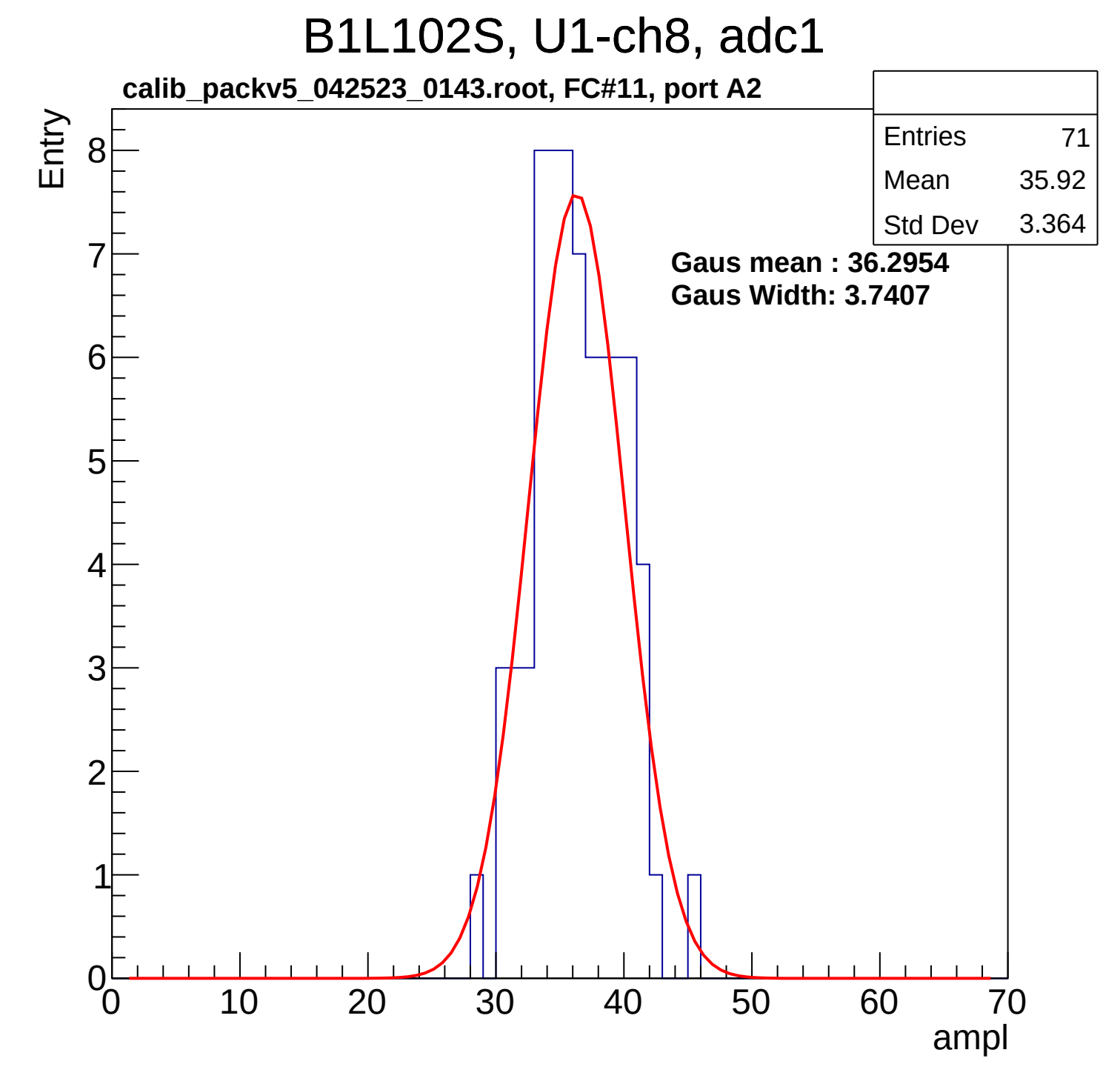
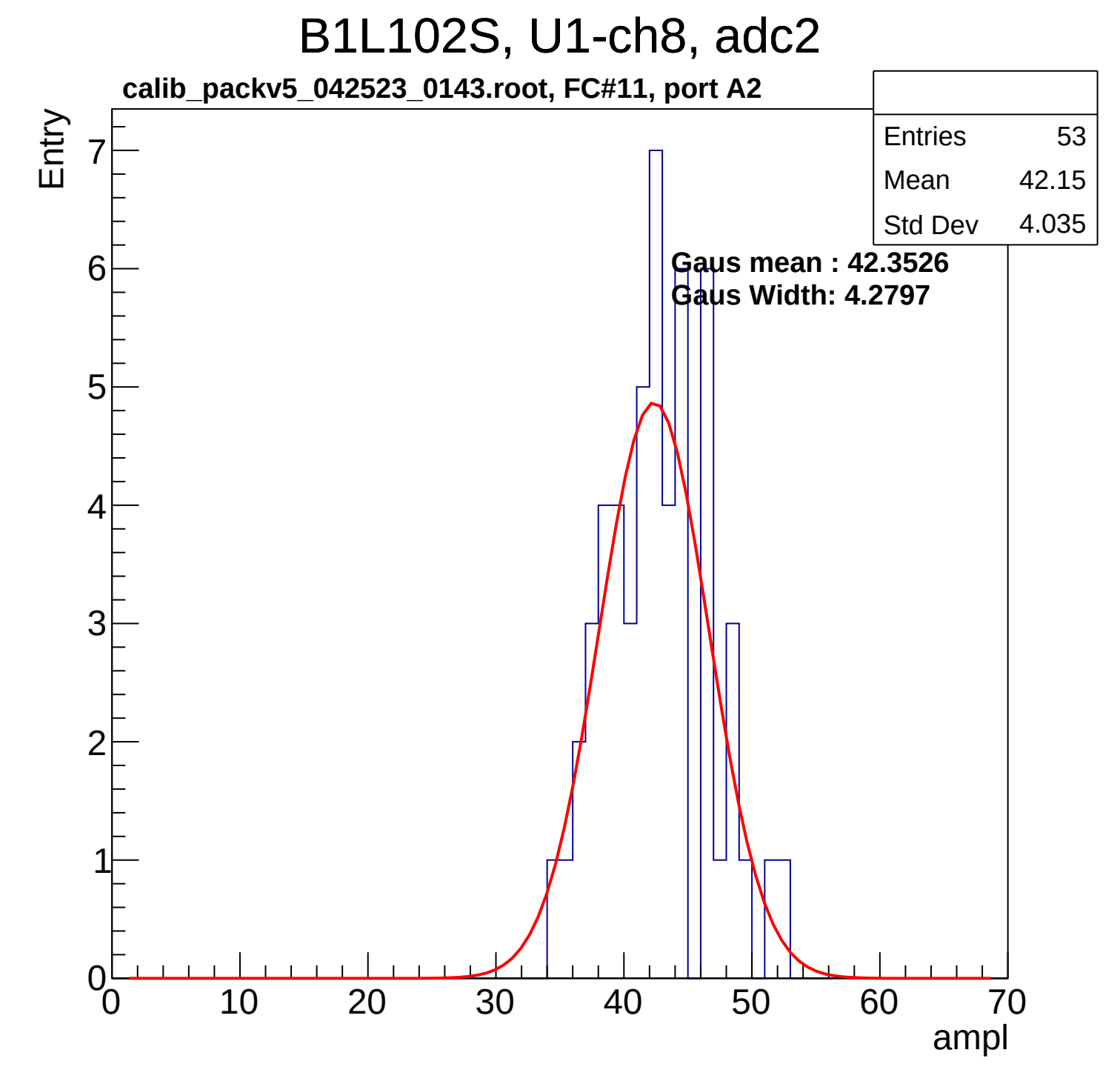
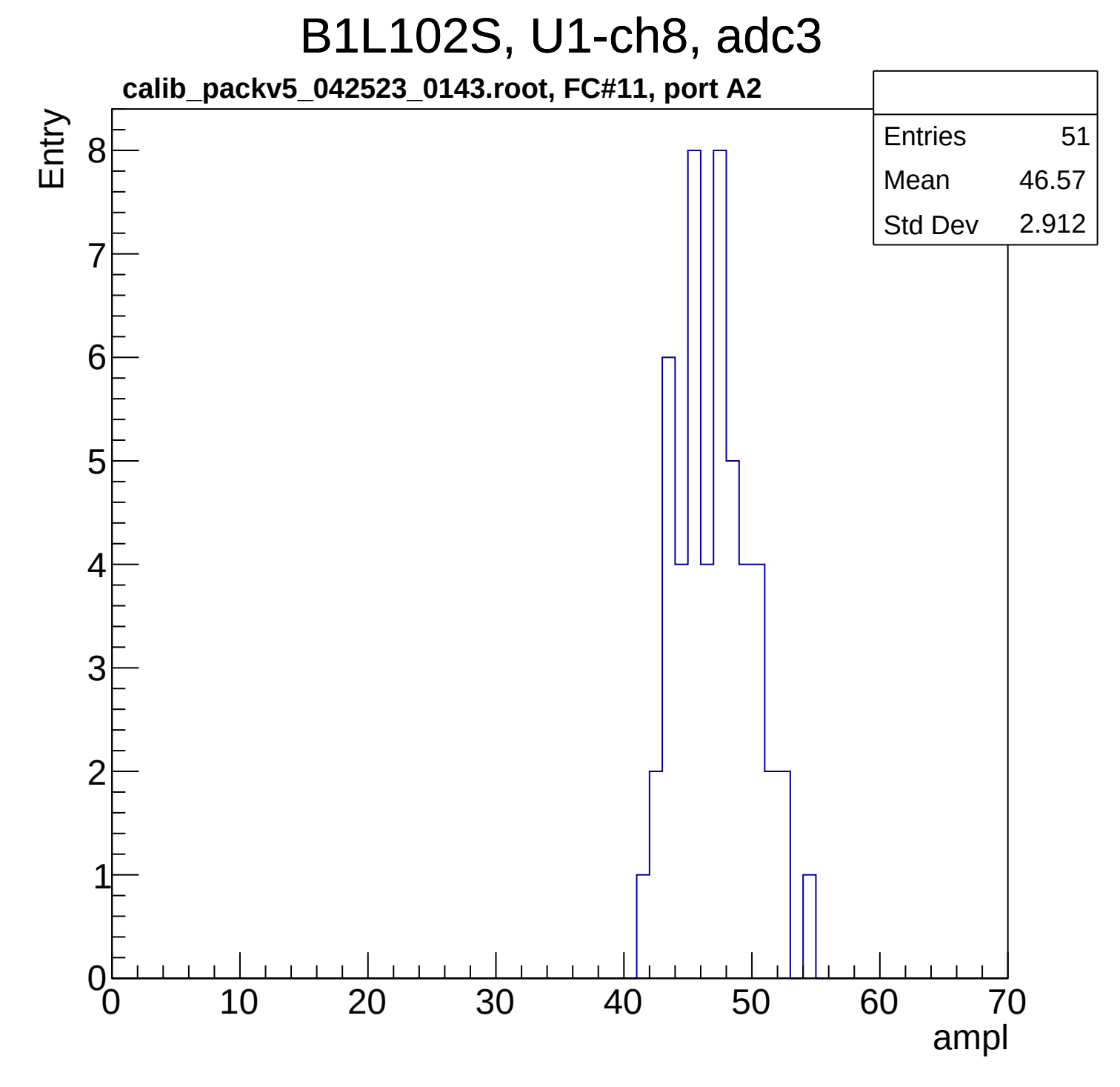
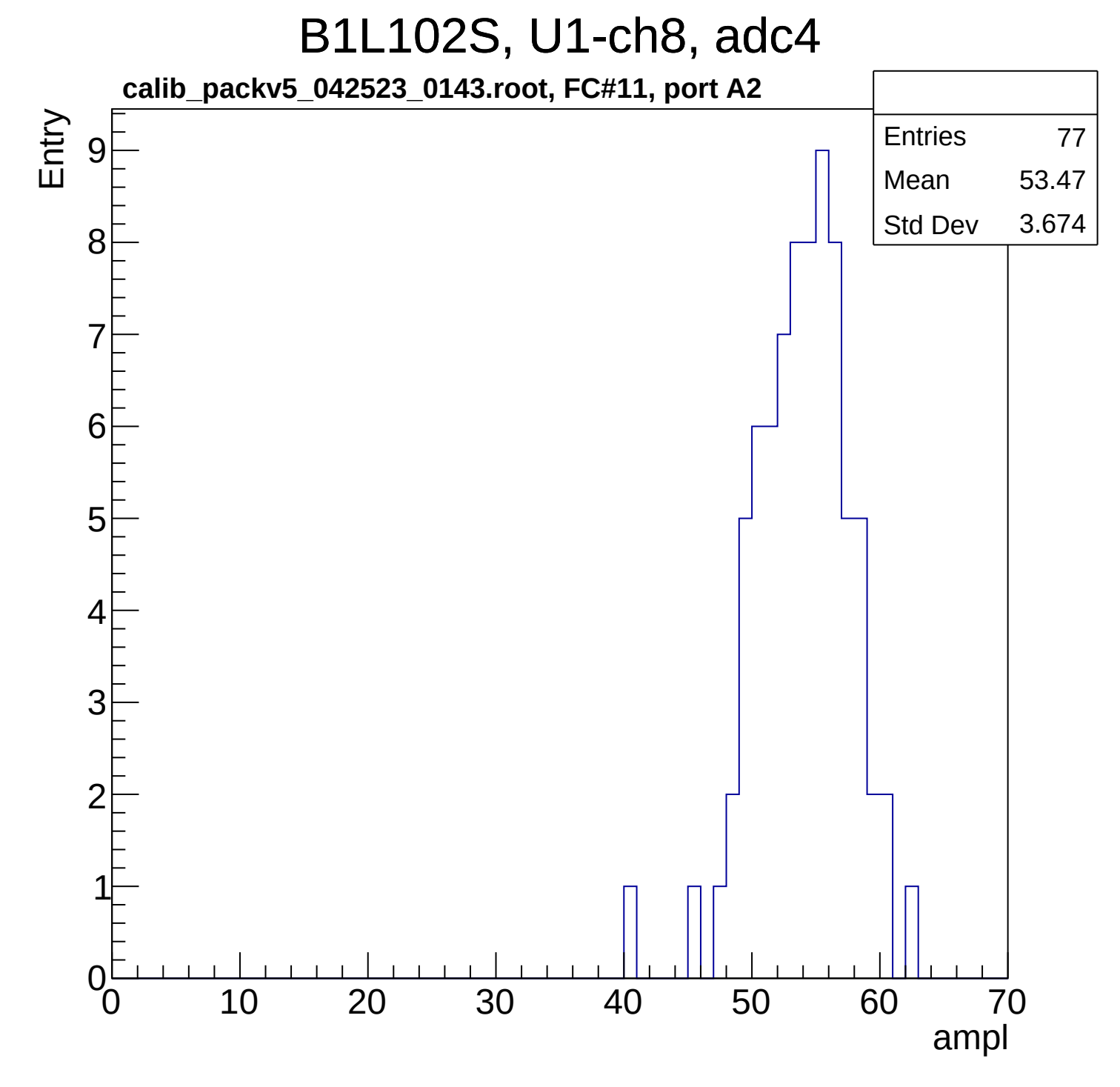
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

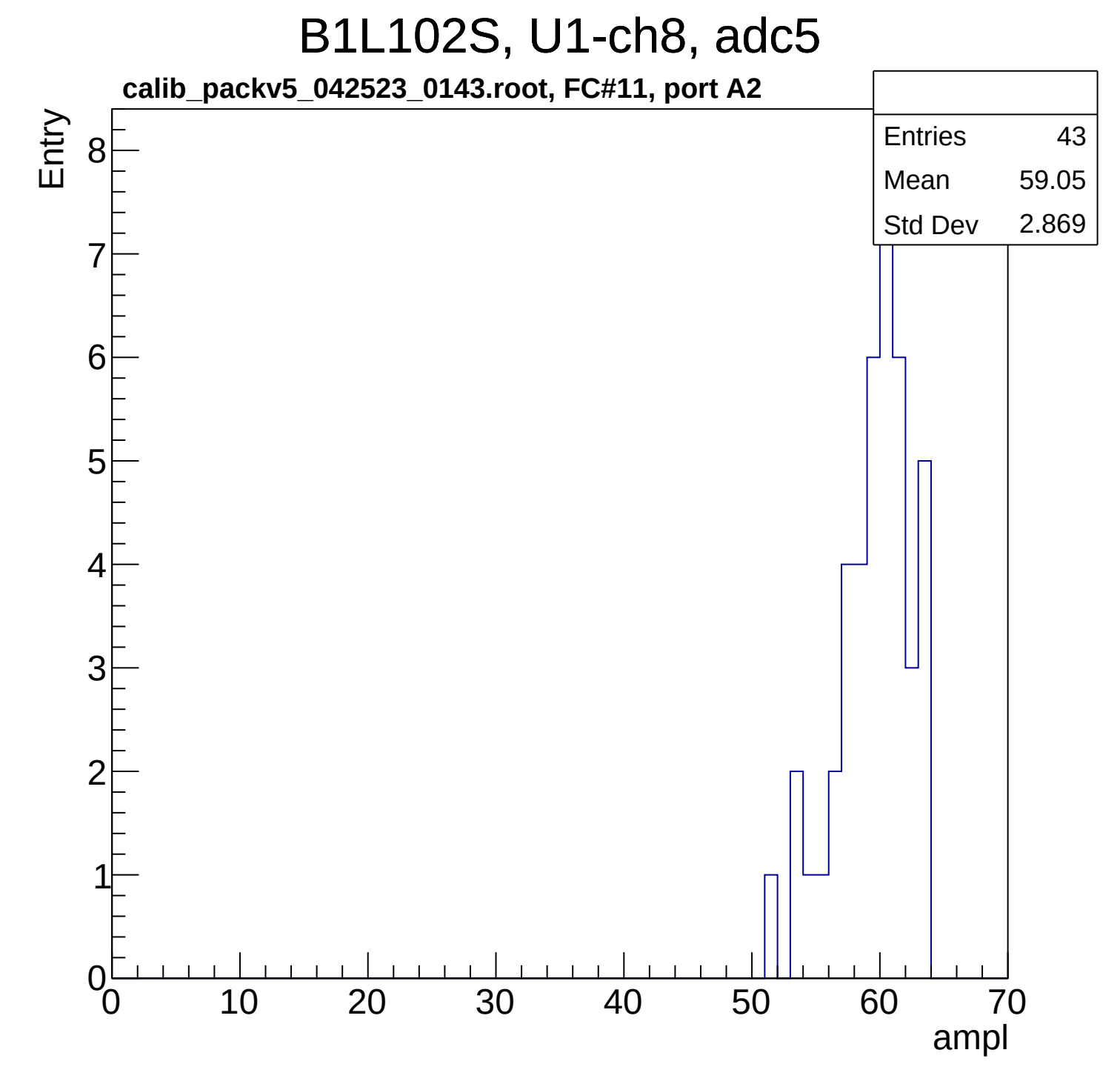

B1L102S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.35
Std Dev 3.485
Gaus mean : 28.6129
Gaus Width: 3.6374
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

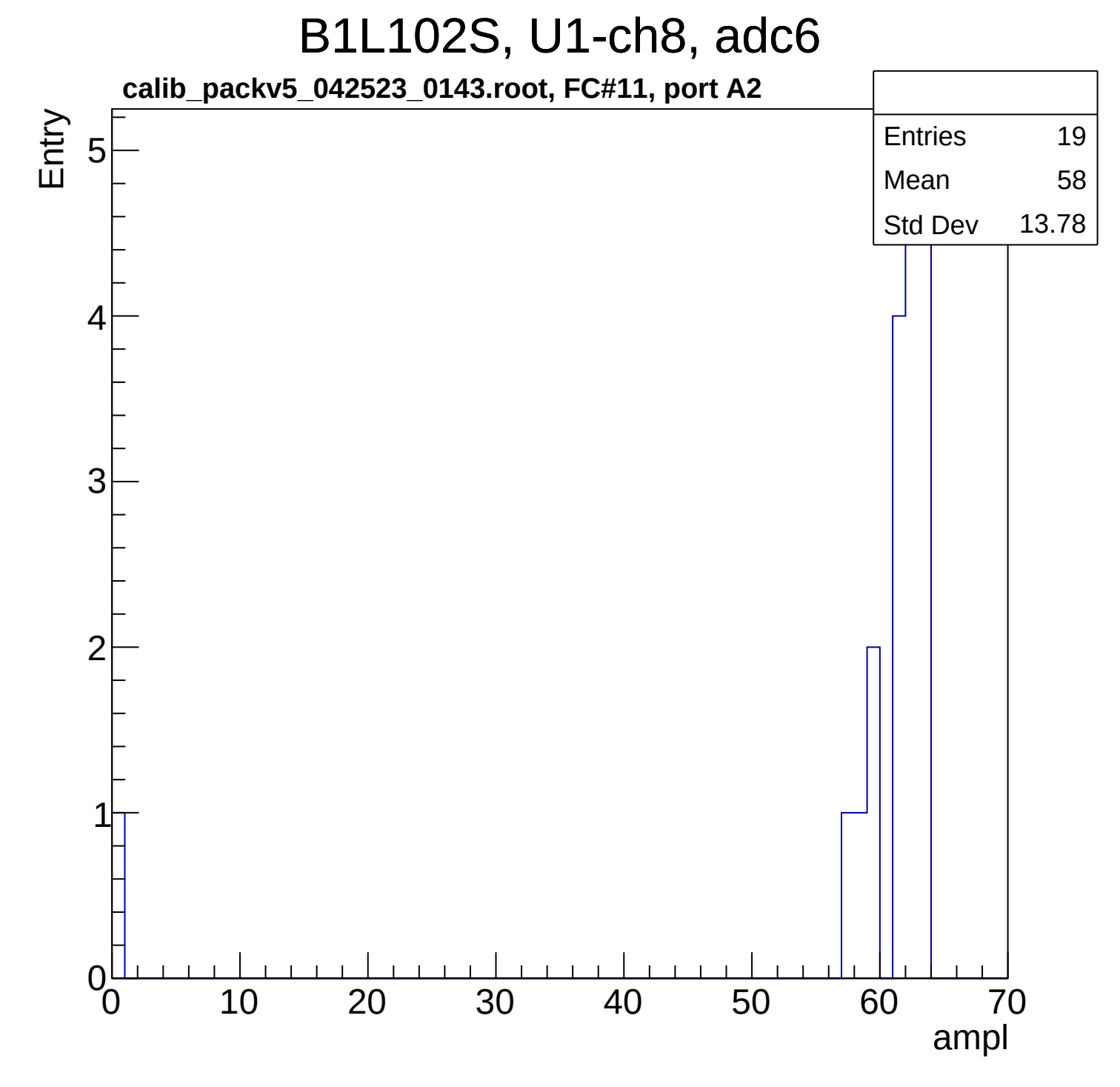

B1L102S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 35.92
Std Dev 3.364
Gaus mean : 36.2954
Gaus Width: 3.7407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

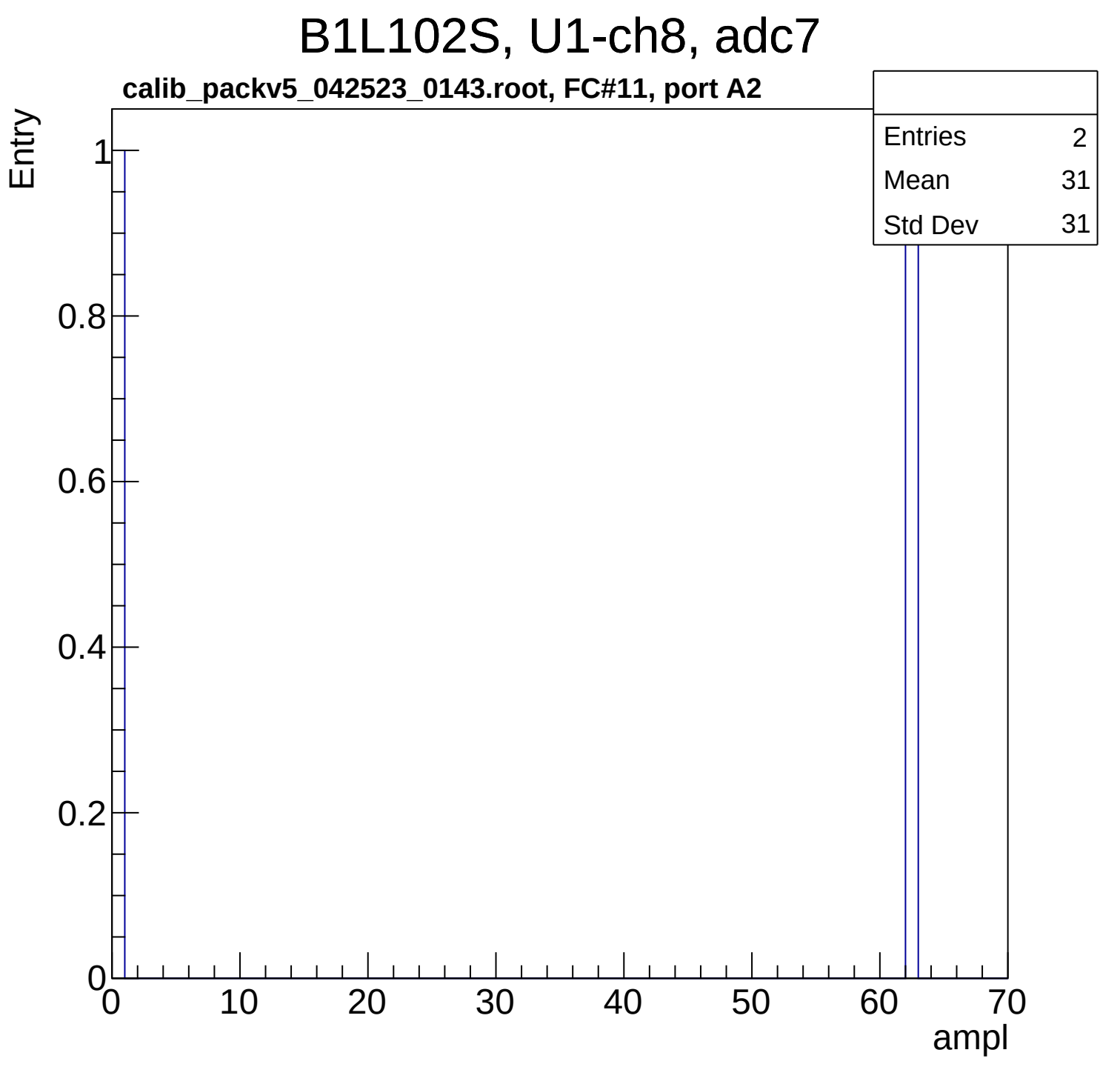

B1L102S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 42.15
Std Dev 4.035
Gaus mean : 42.3526
Gaus Width: 4.2797
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

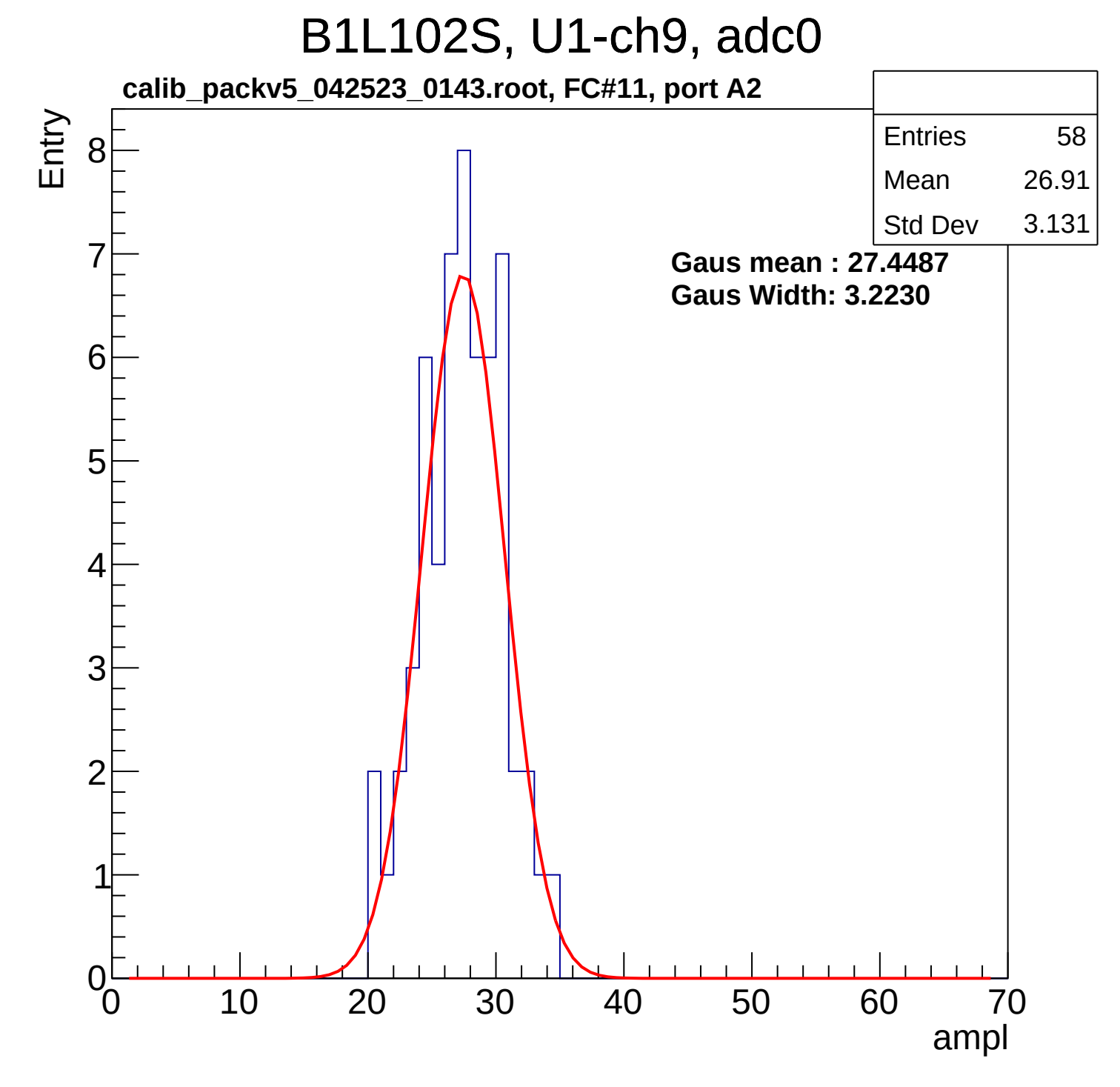

B1L102S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 46.57
Std Dev 2.912
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

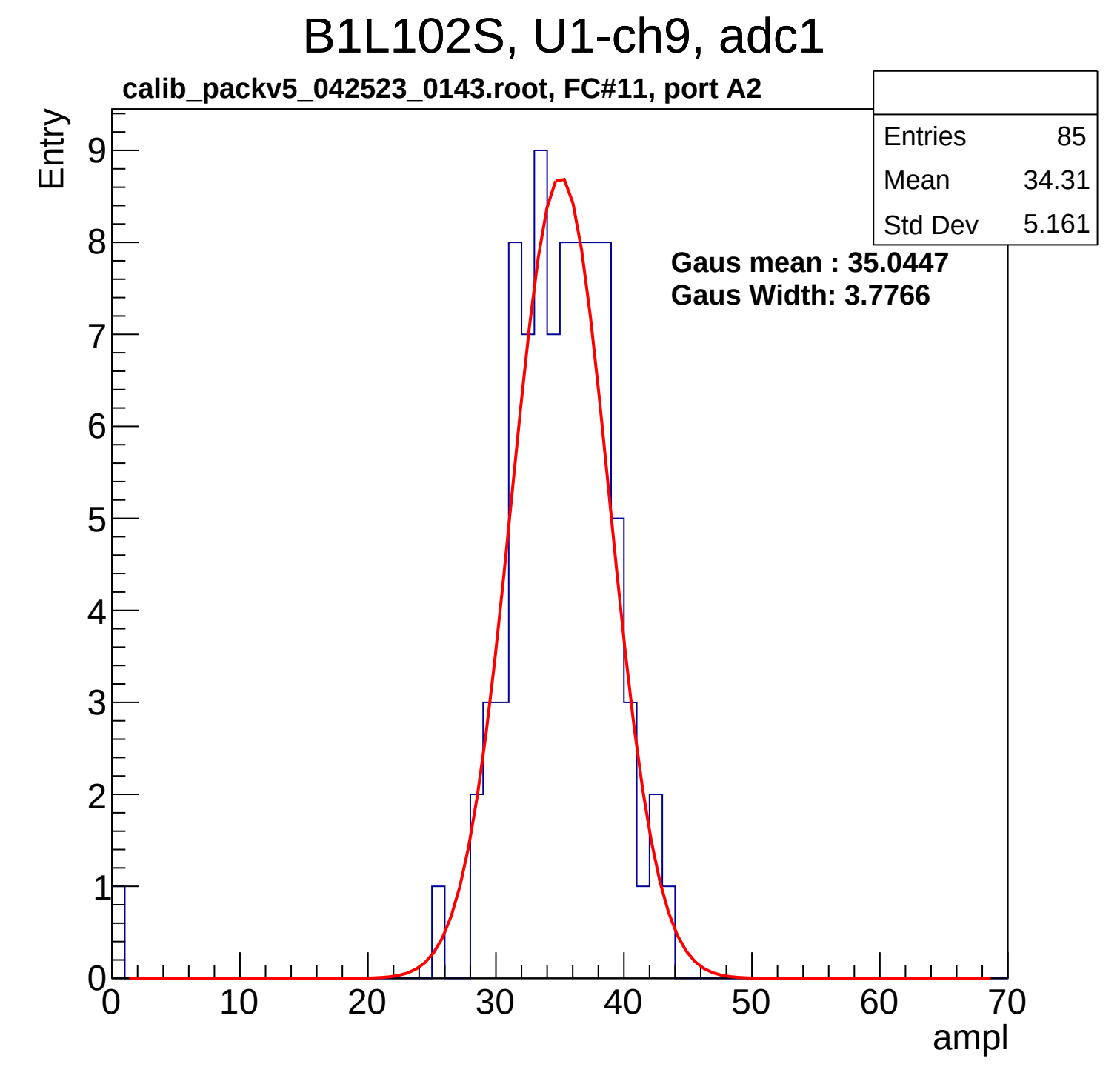

B1L102S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 53.47
Std Dev 3.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

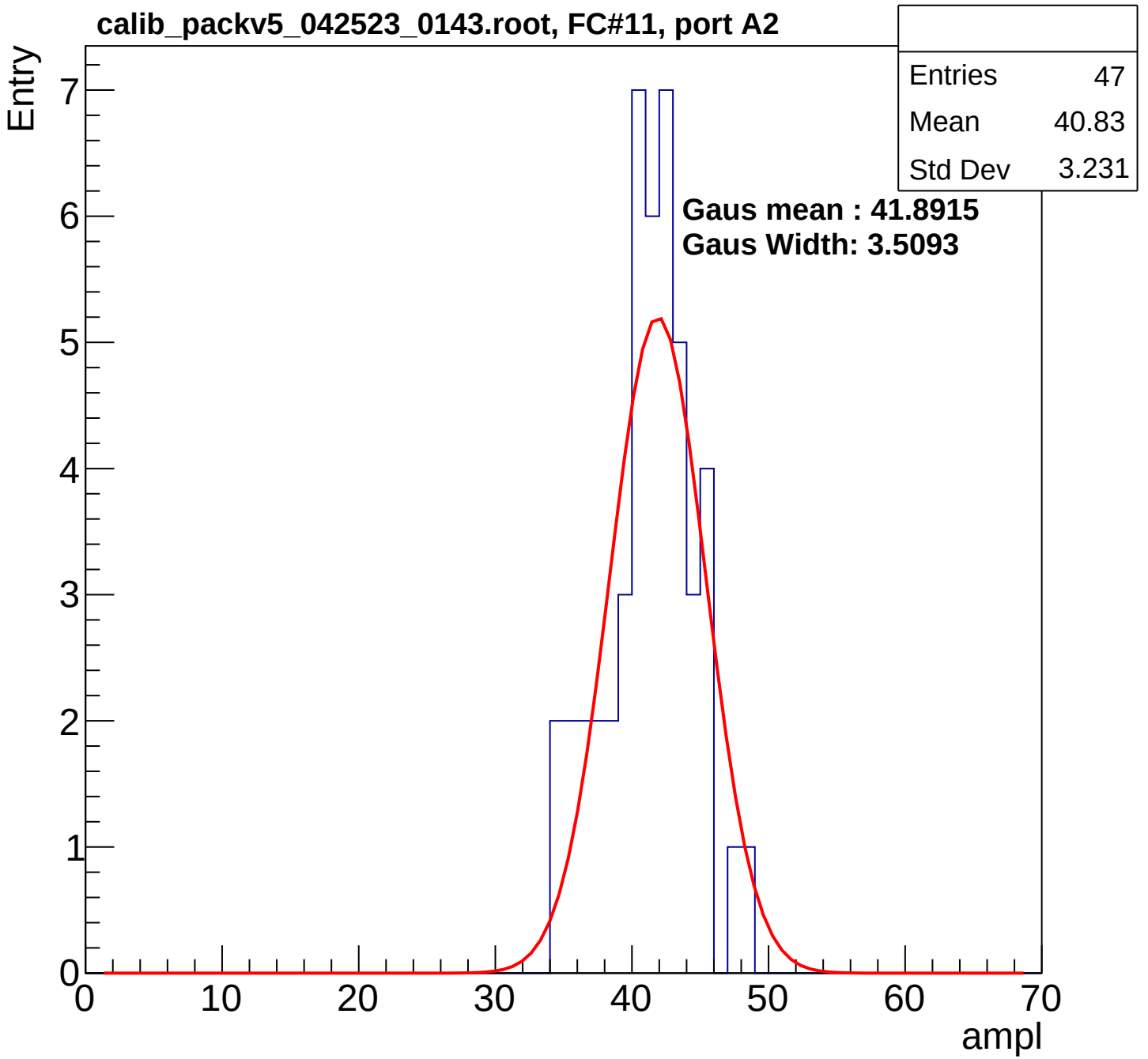

B1L102S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 59.05
Std Dev 2.869
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

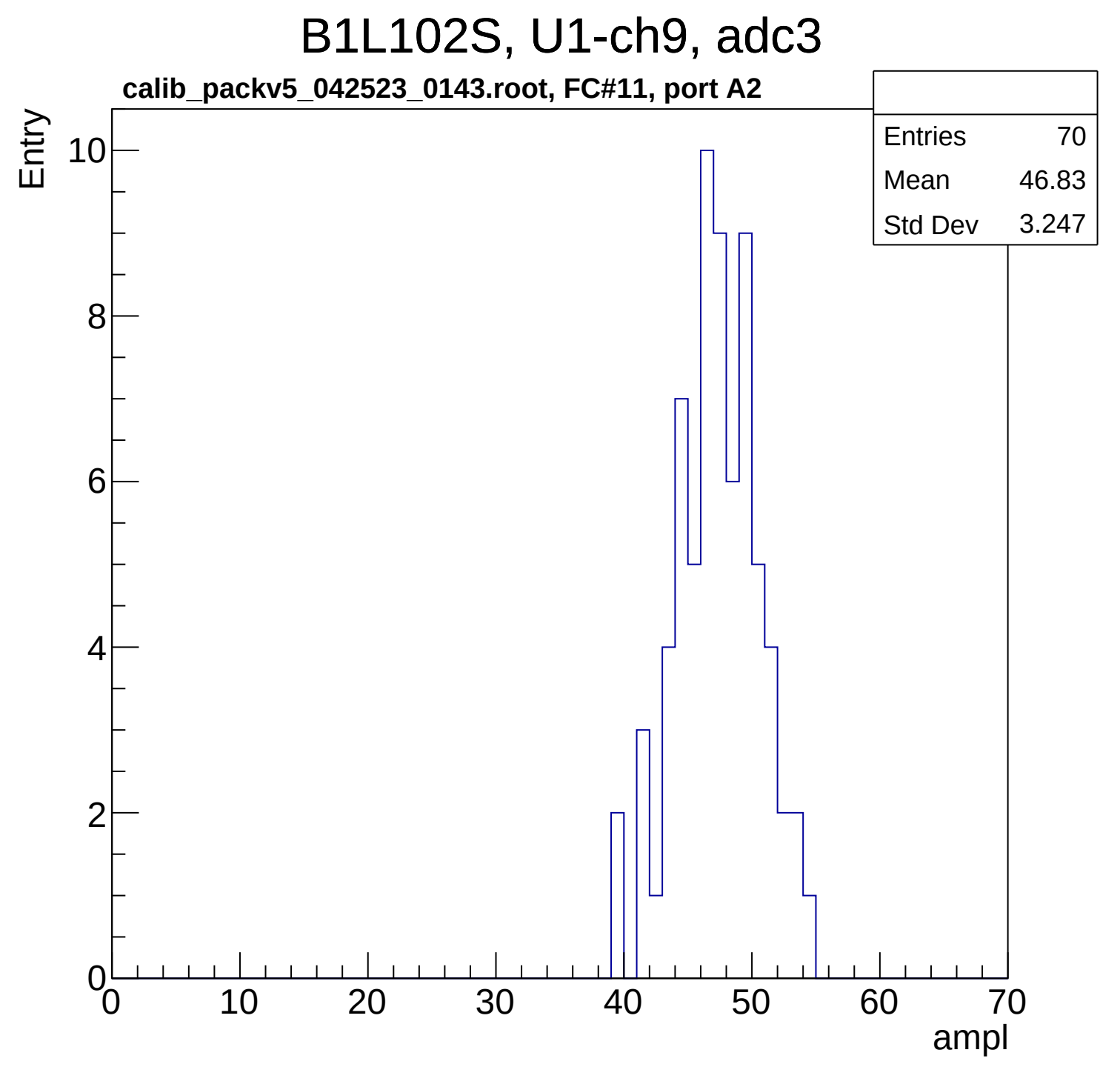

B1L102S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 19
Mean 58
Std Dev 13.78
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

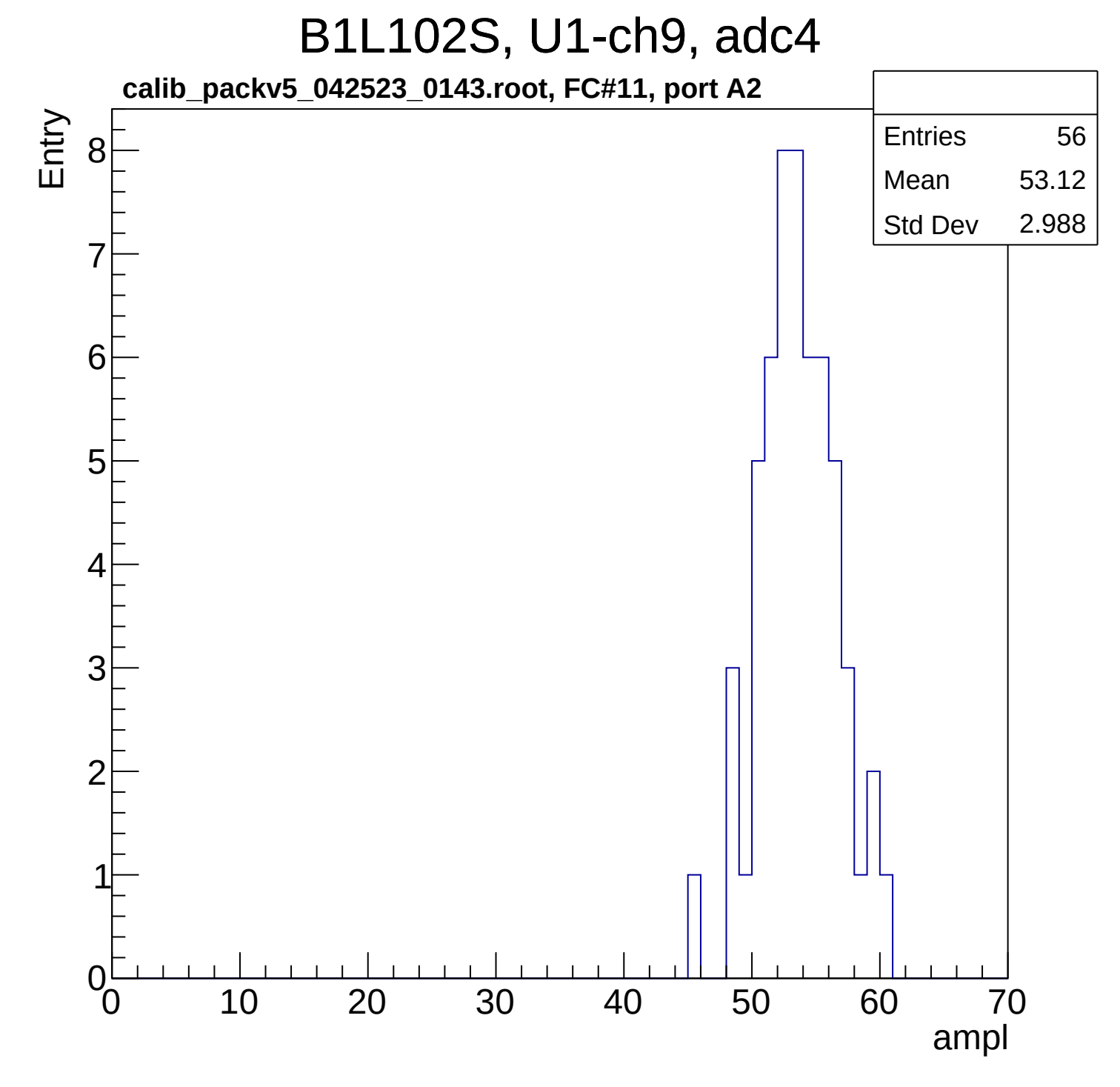

B1L102S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 26.91
Std Dev 3.131
Gaus mean : 27.4487
Gaus Width: 3.2230
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 34.31
Std Dev 5.161
Gaus mean : 35.0447
Gaus Width: 3.7766
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 40.83
Std Dev 3.231
Gaus mean : 41.8915
Gaus Width: 3.5093
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 46.83
Std Dev 3.247
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 53.12
Std Dev 2.988
Entry
ampl
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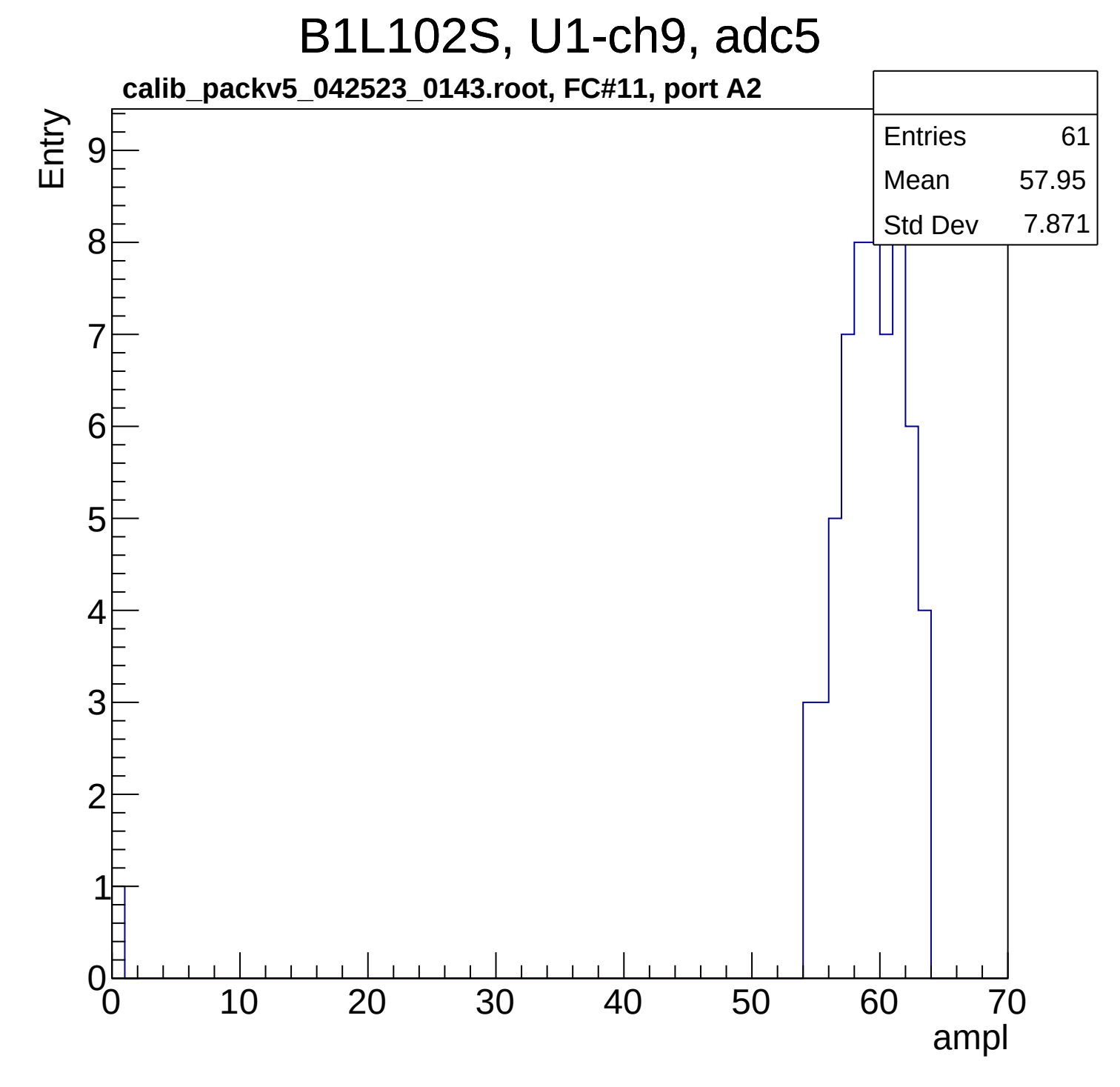
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 57.95
Std Dev 7.871
Entry
ampl
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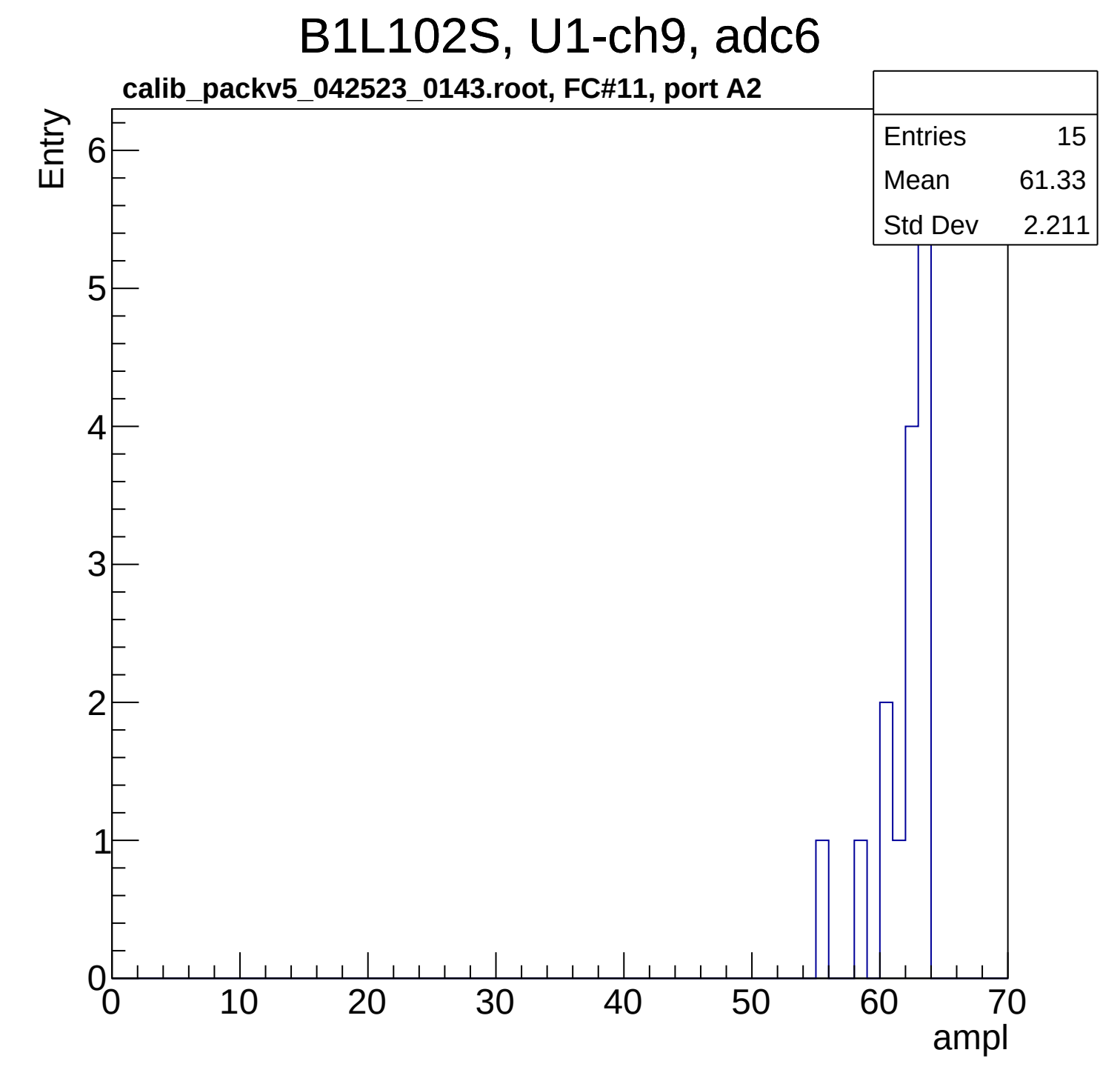
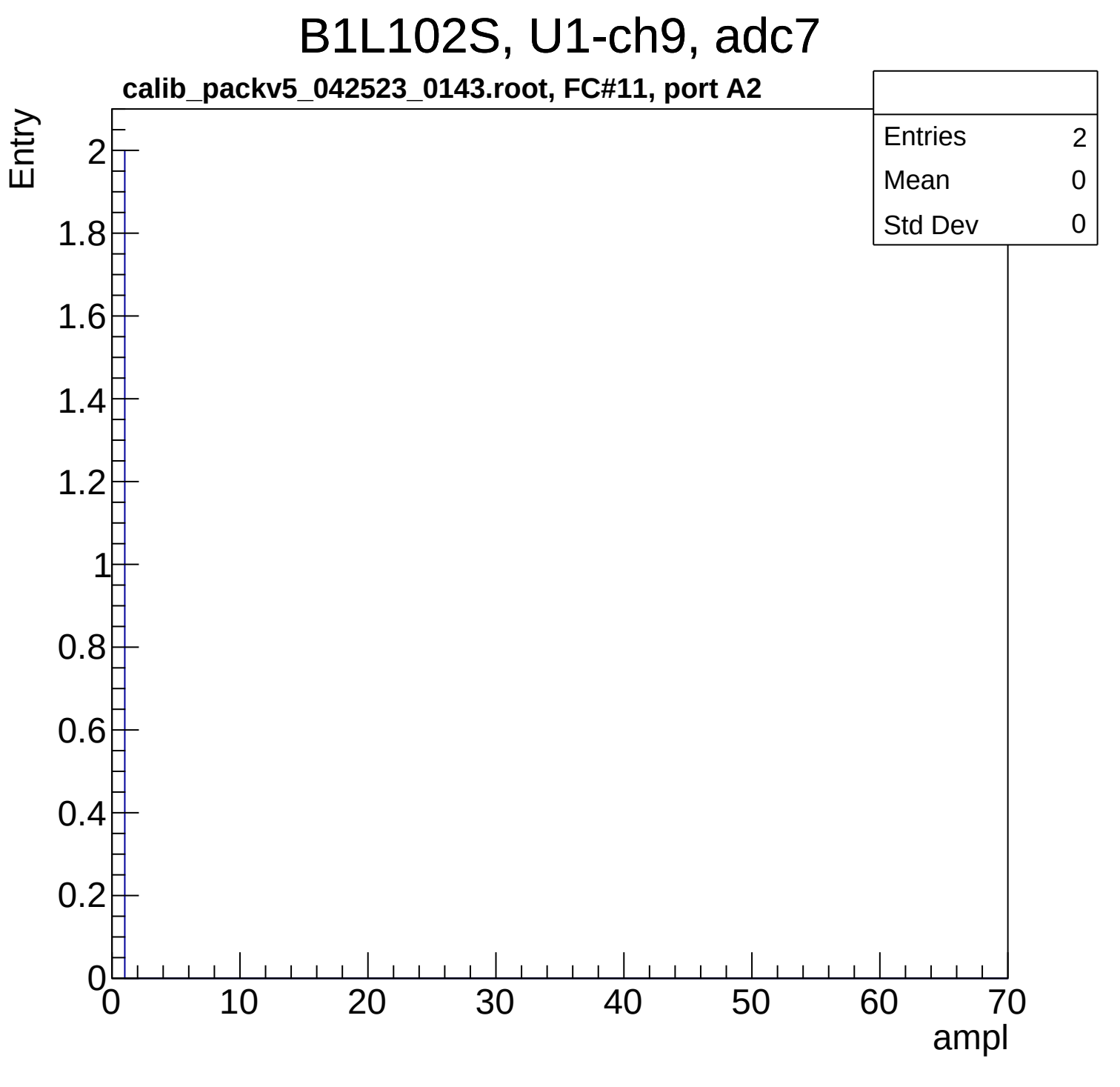
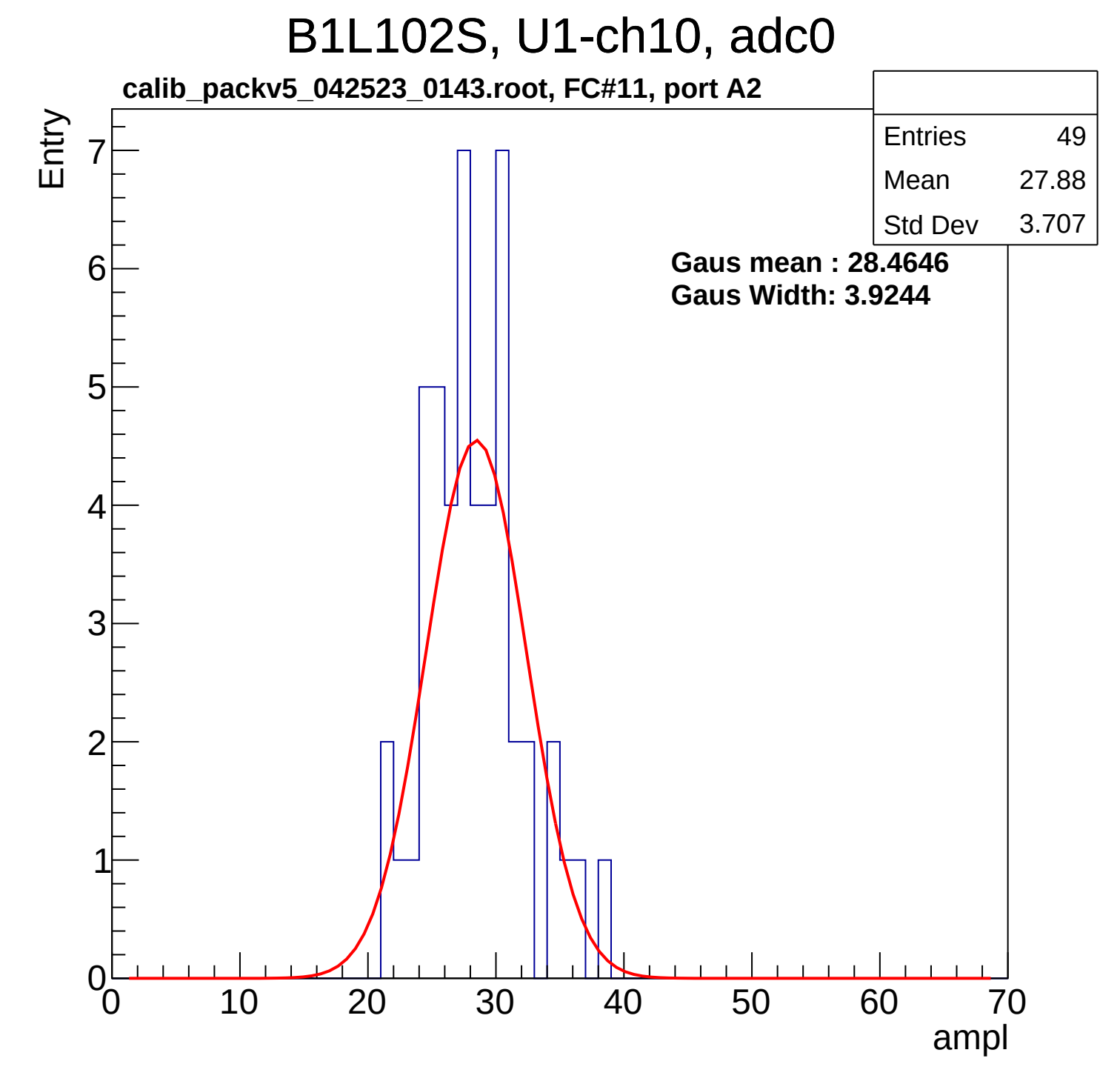
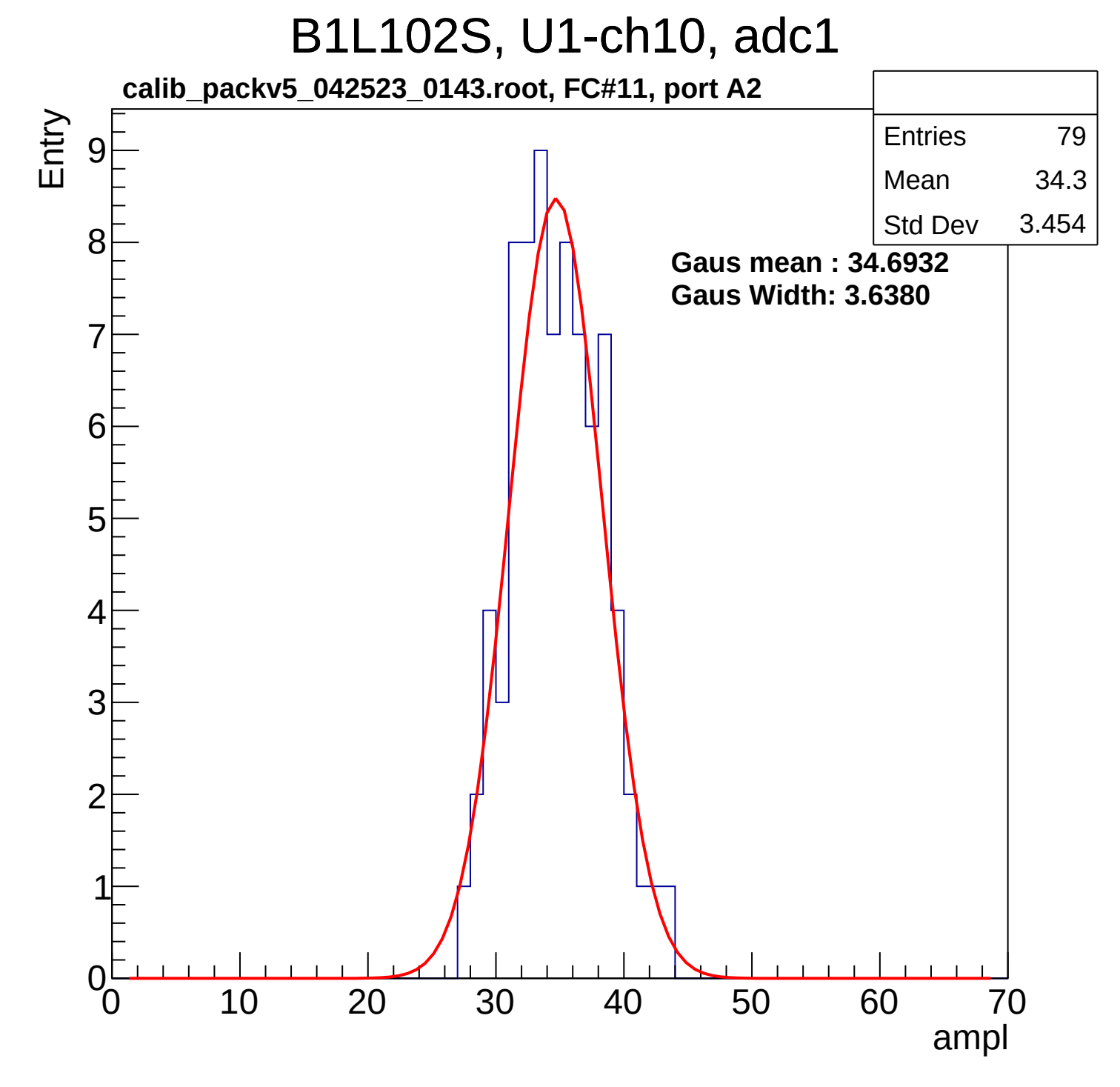
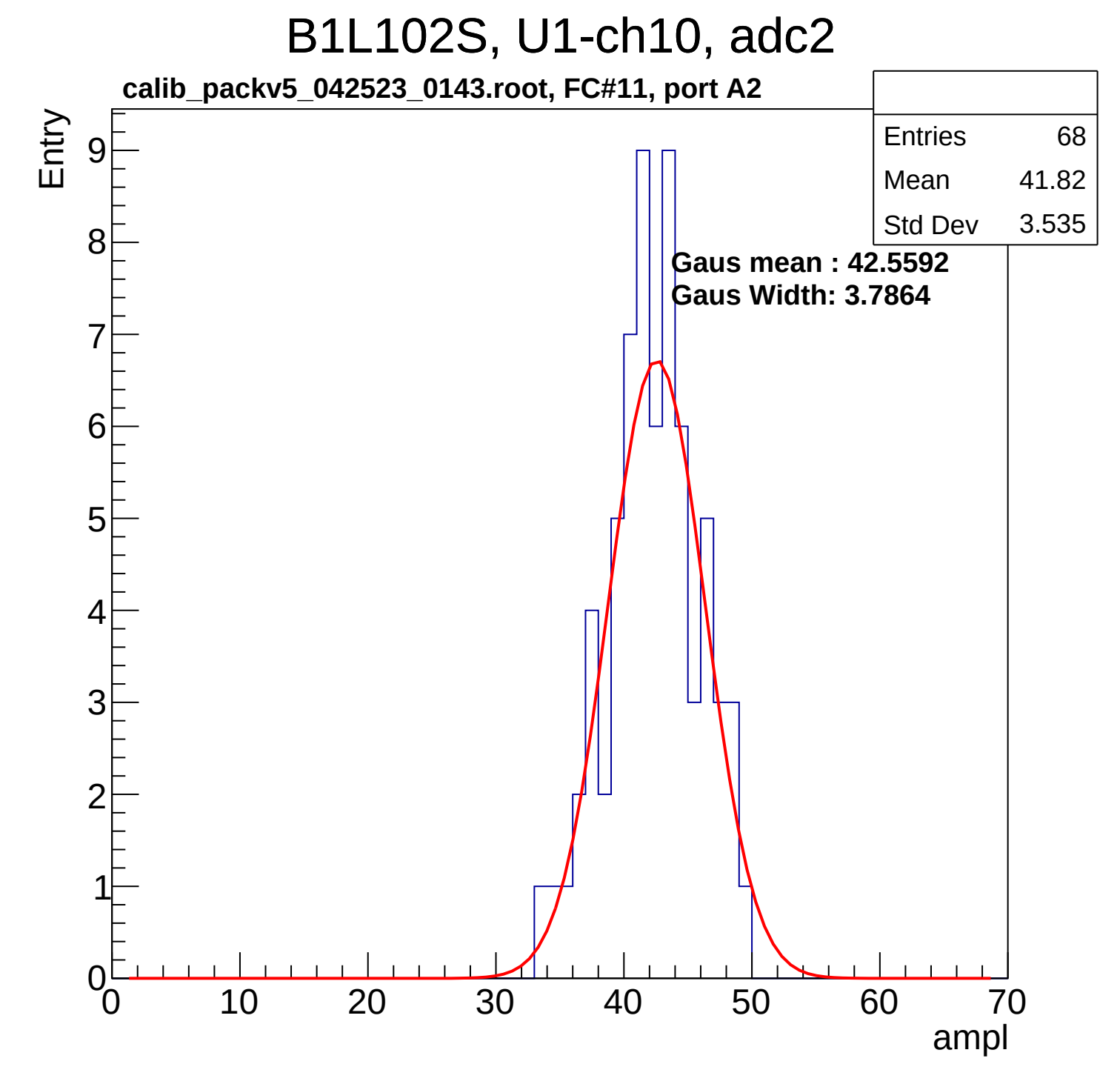
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 61.33
Std Dev 2.211
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 27.88
Std Dev 3.707
Gaus mean : 28.4646
Gaus Width: 3.9244
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 34.3
Std Dev 3.454
Gaus mean : 34.6932
Gaus Width: 3.6380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 41.82
Std Dev 3.535
Gaus mean : 42.5592
Gaus Width: 3.7864
Entry
ampl
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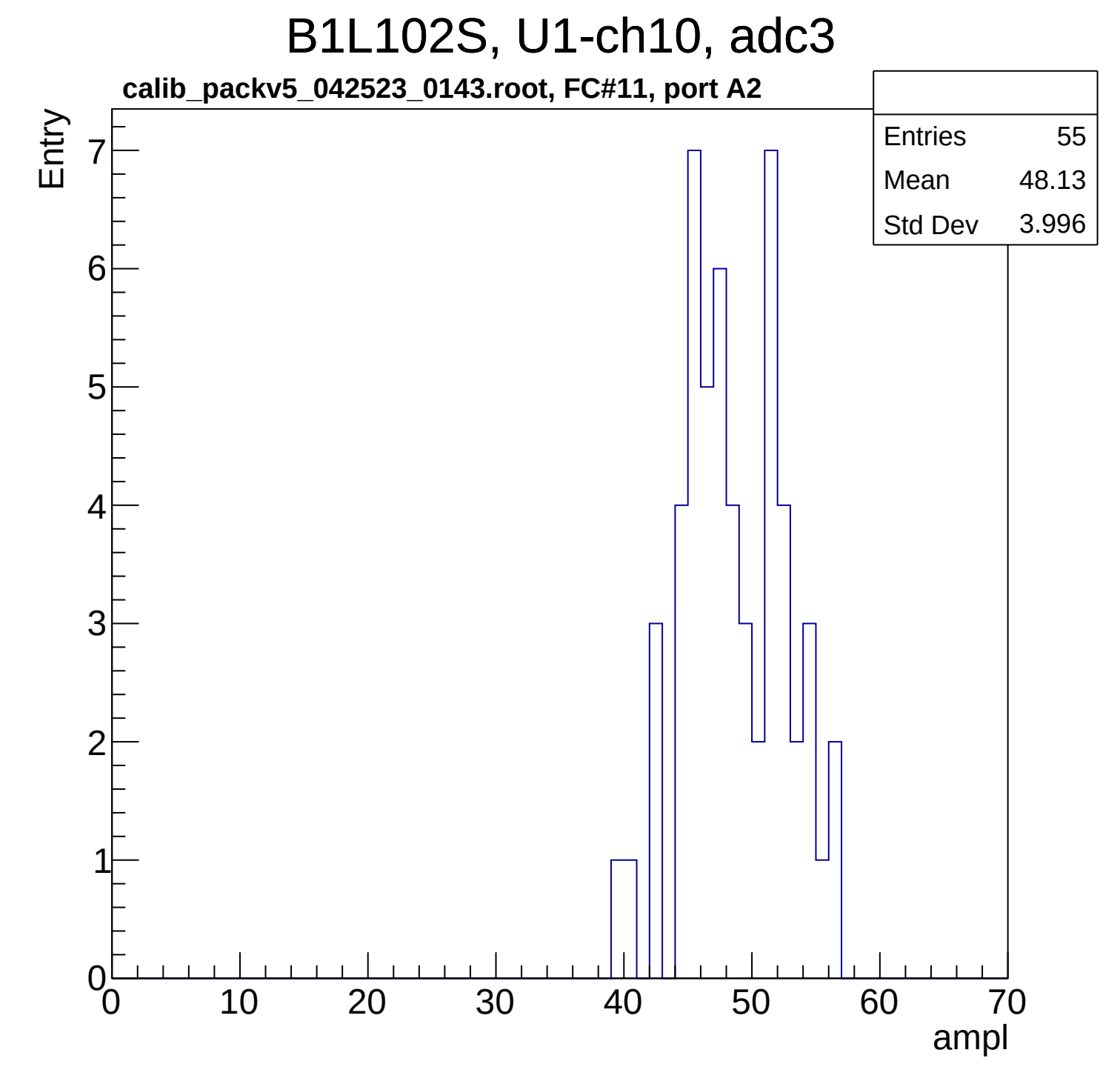
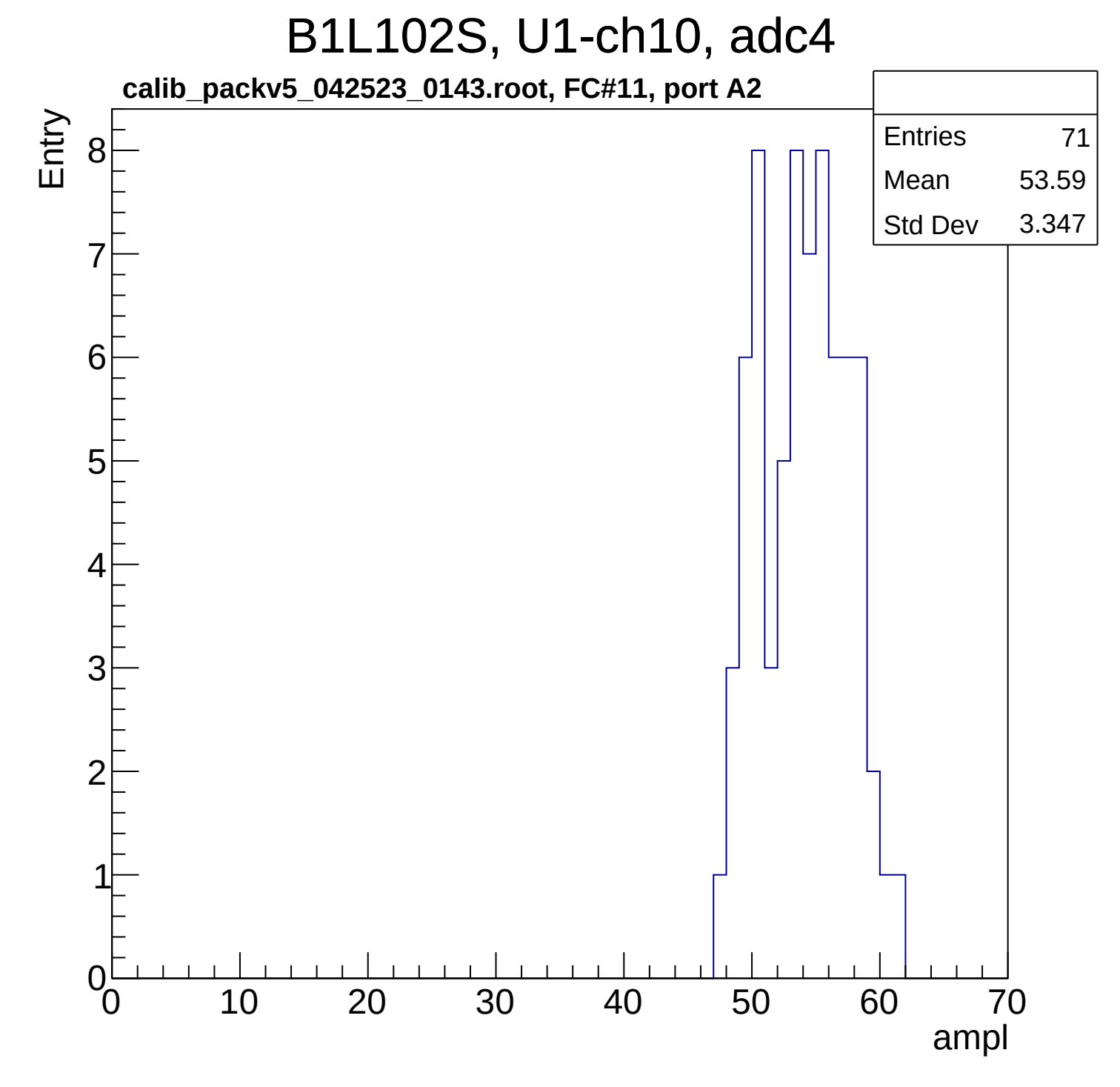
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 48.13
Std Dev 3.996
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 53.59
Std Dev 3.347
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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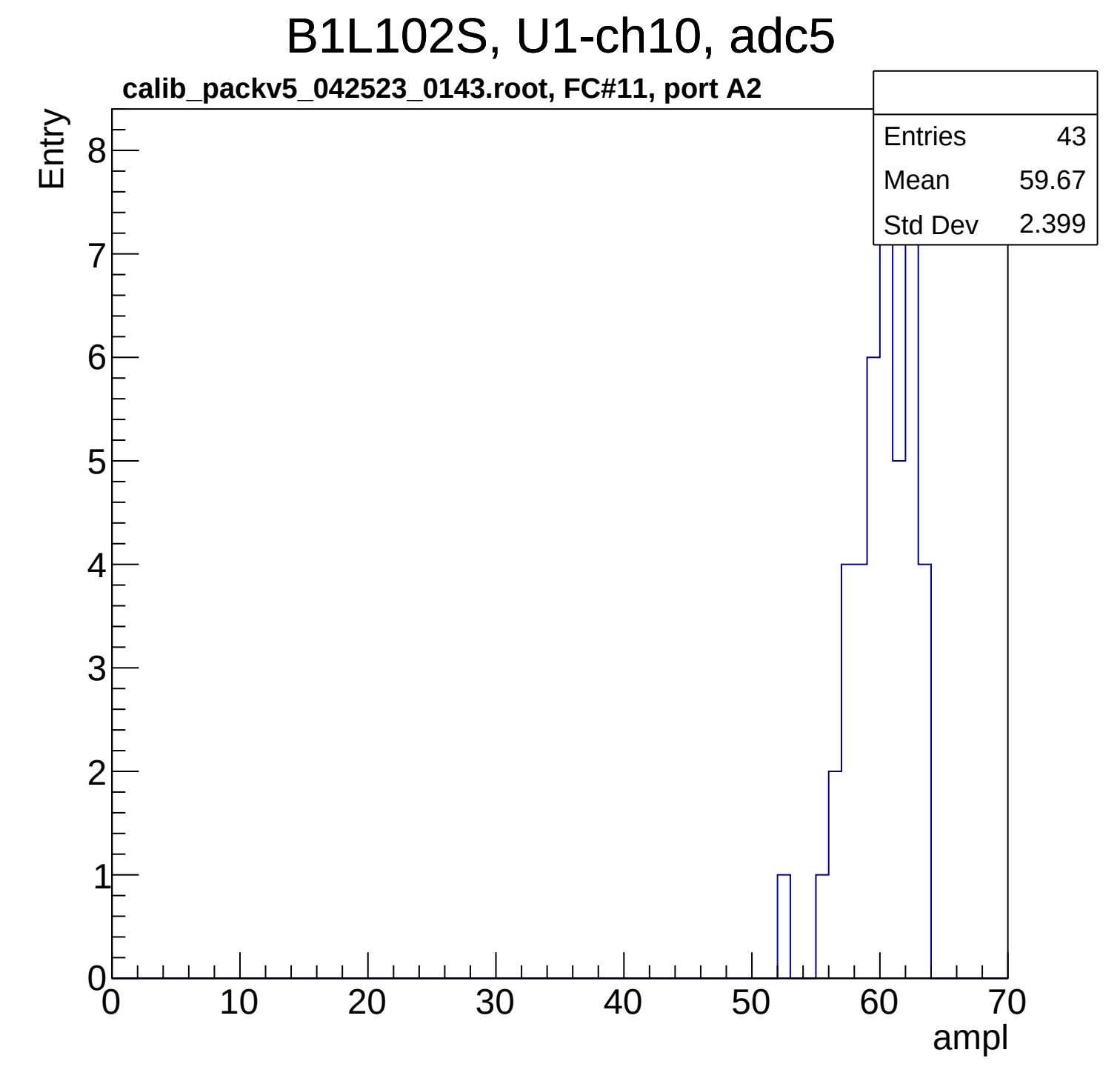
30
40
50
60
70


B1L102S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 59.67
Std Dev 2.399
Entry
ampl
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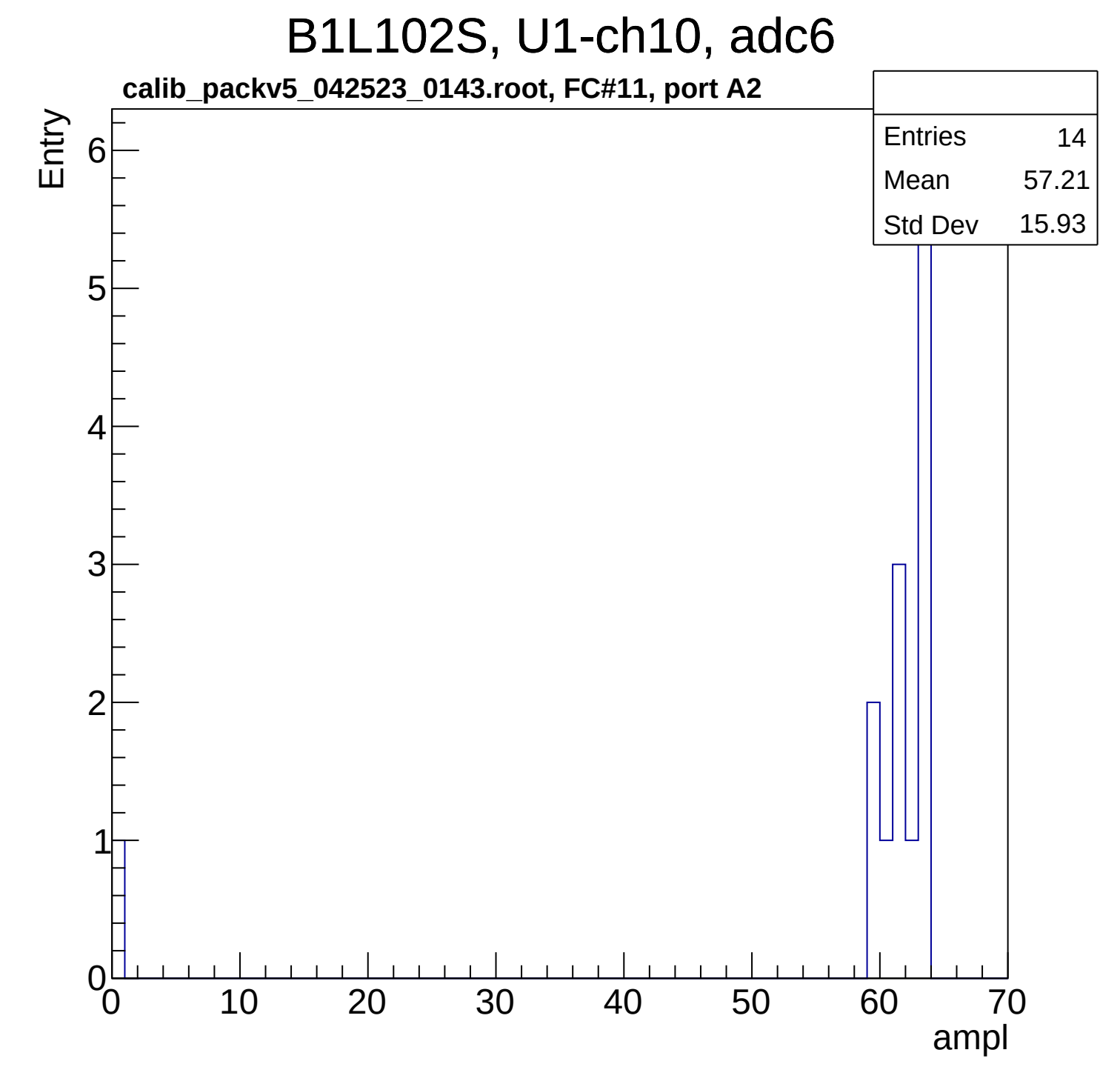
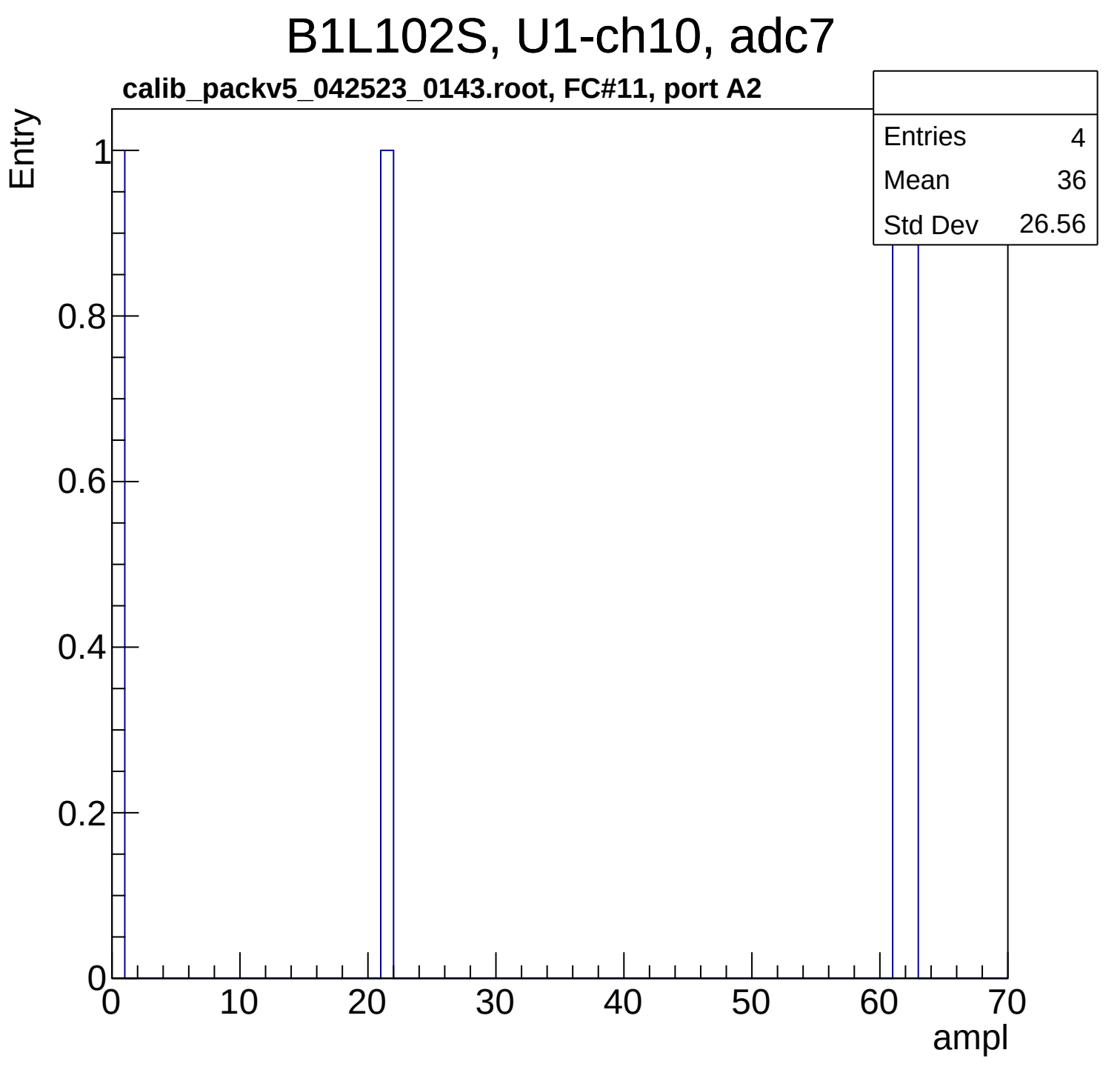
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 57.21
Std Dev 15.93
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 36
Std Dev 26.56
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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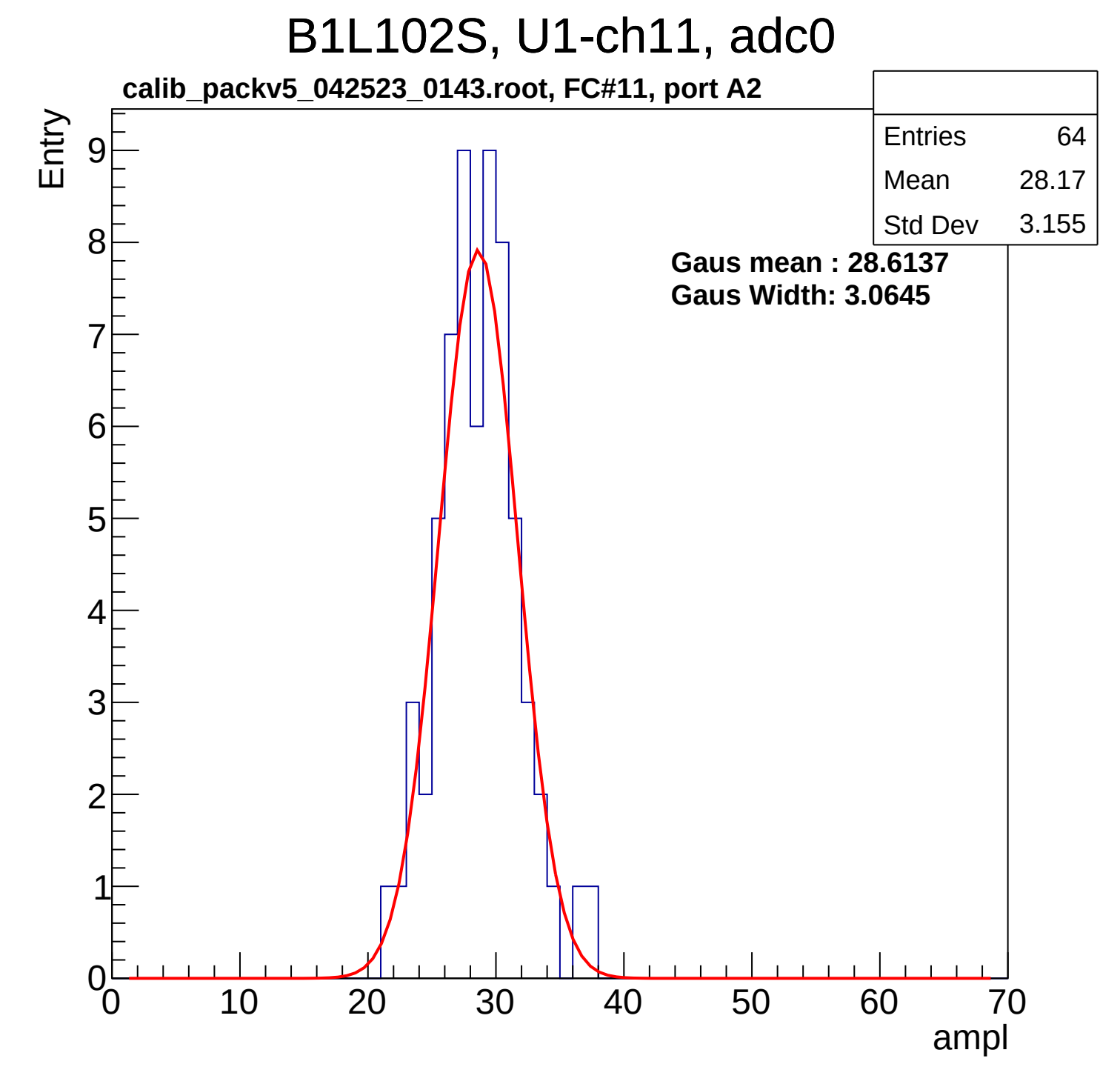
B1L102S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 28.17
Std Dev 3.155
Gaus mean : 28.6137
Gaus Width: 3.0645
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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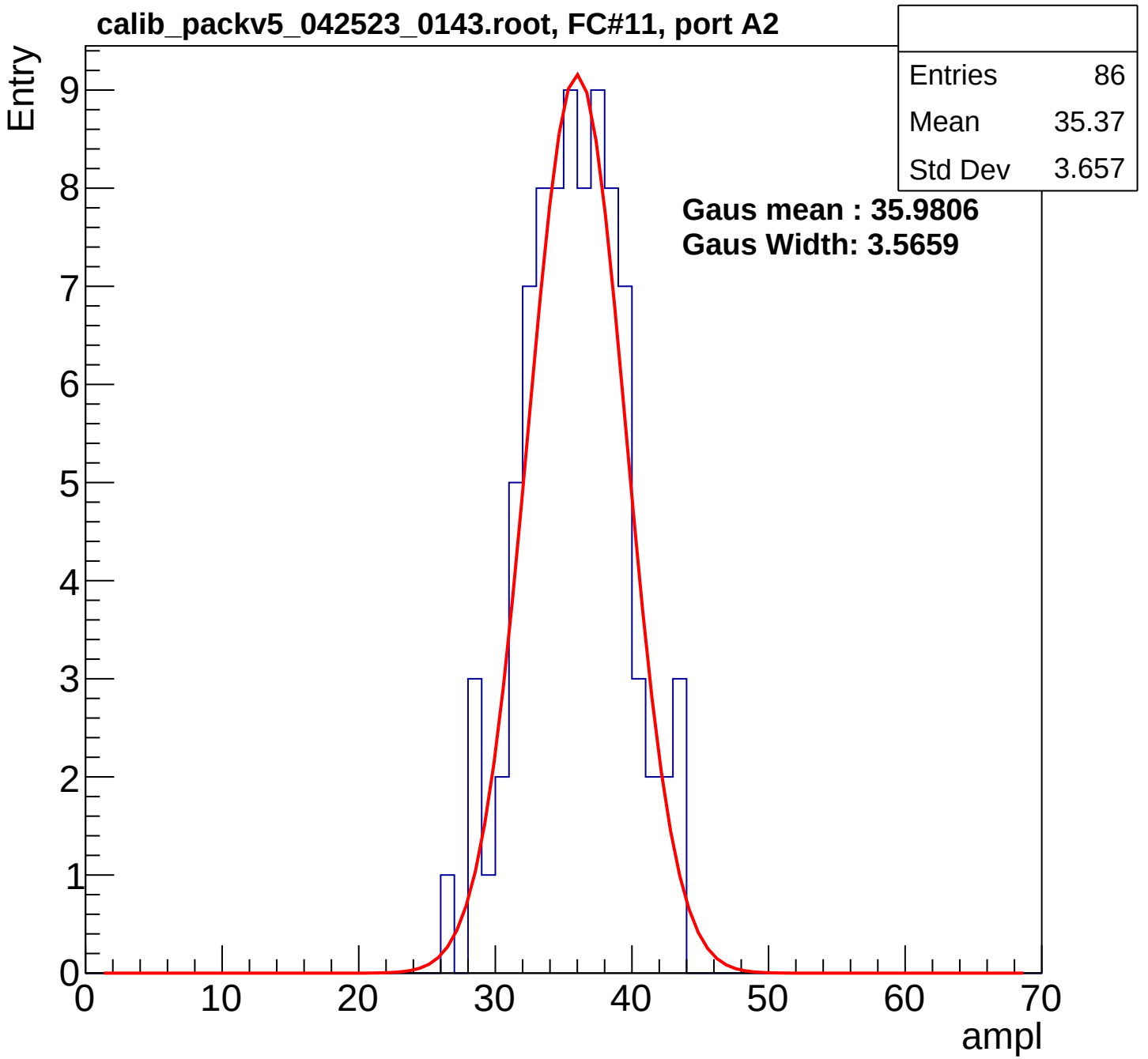
B1L102S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 35.37
Std Dev 3.657
Gaus mean : 35.9806
Gaus Width: 3.5659
Entry
ampl
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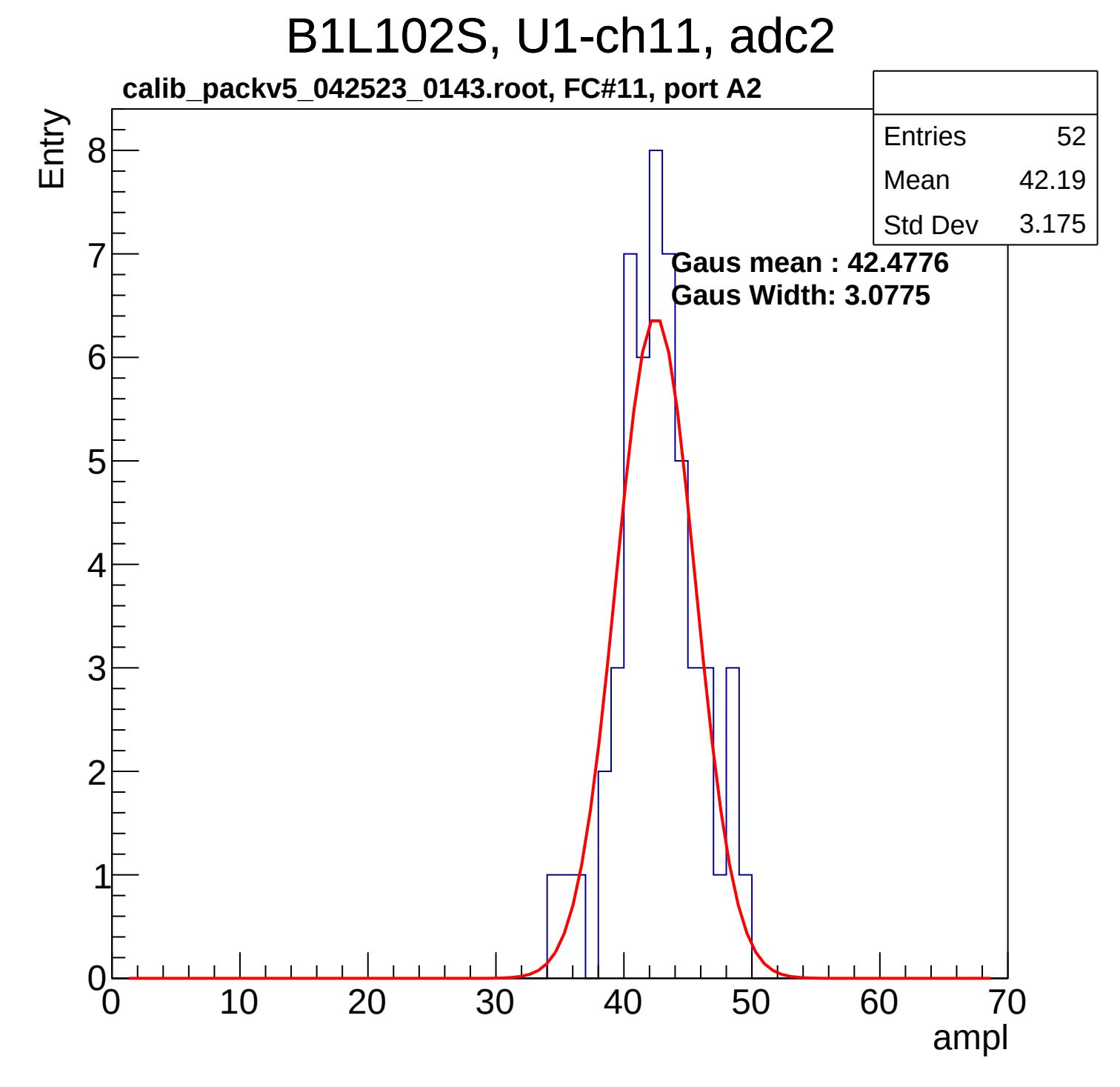
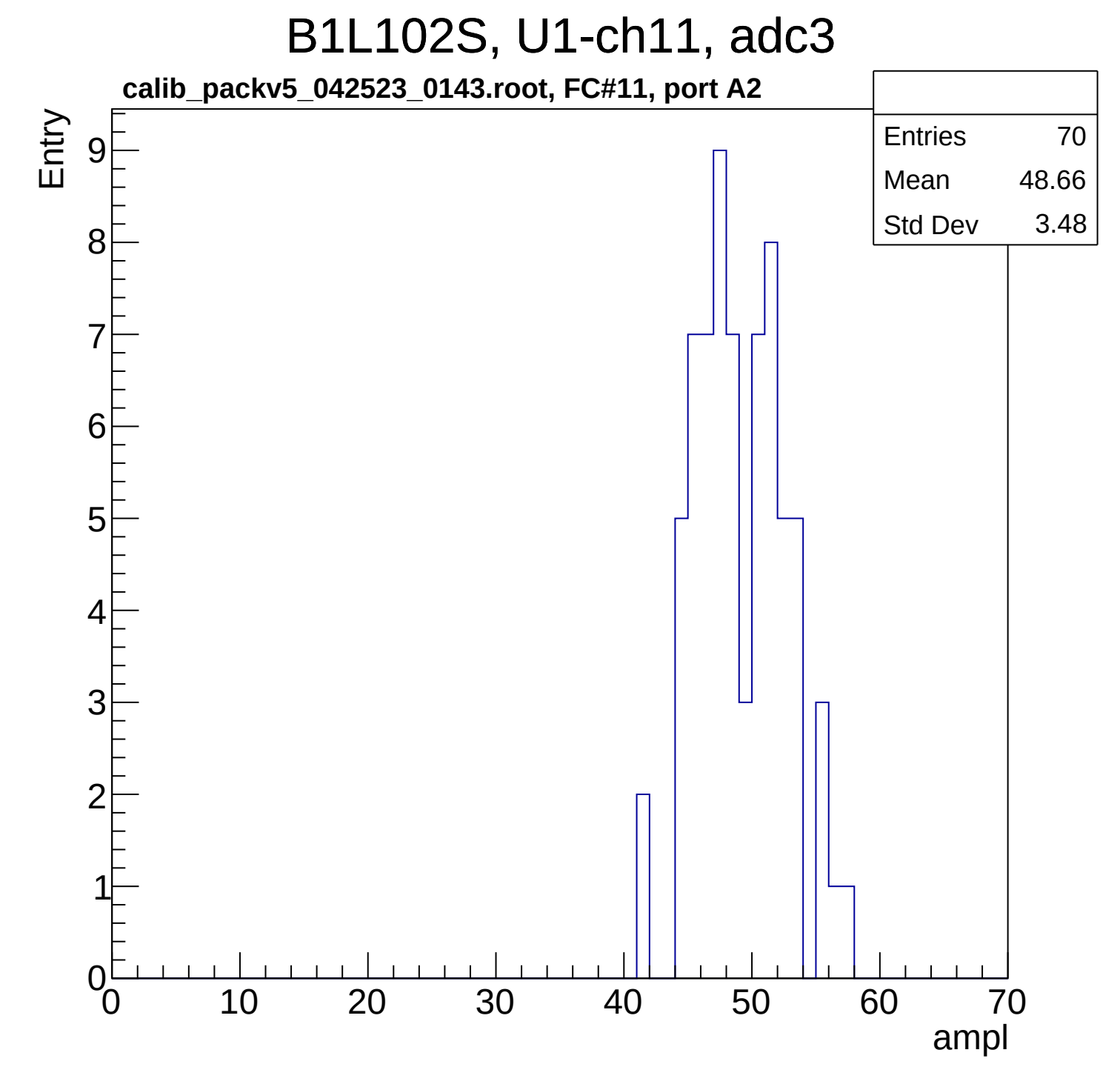
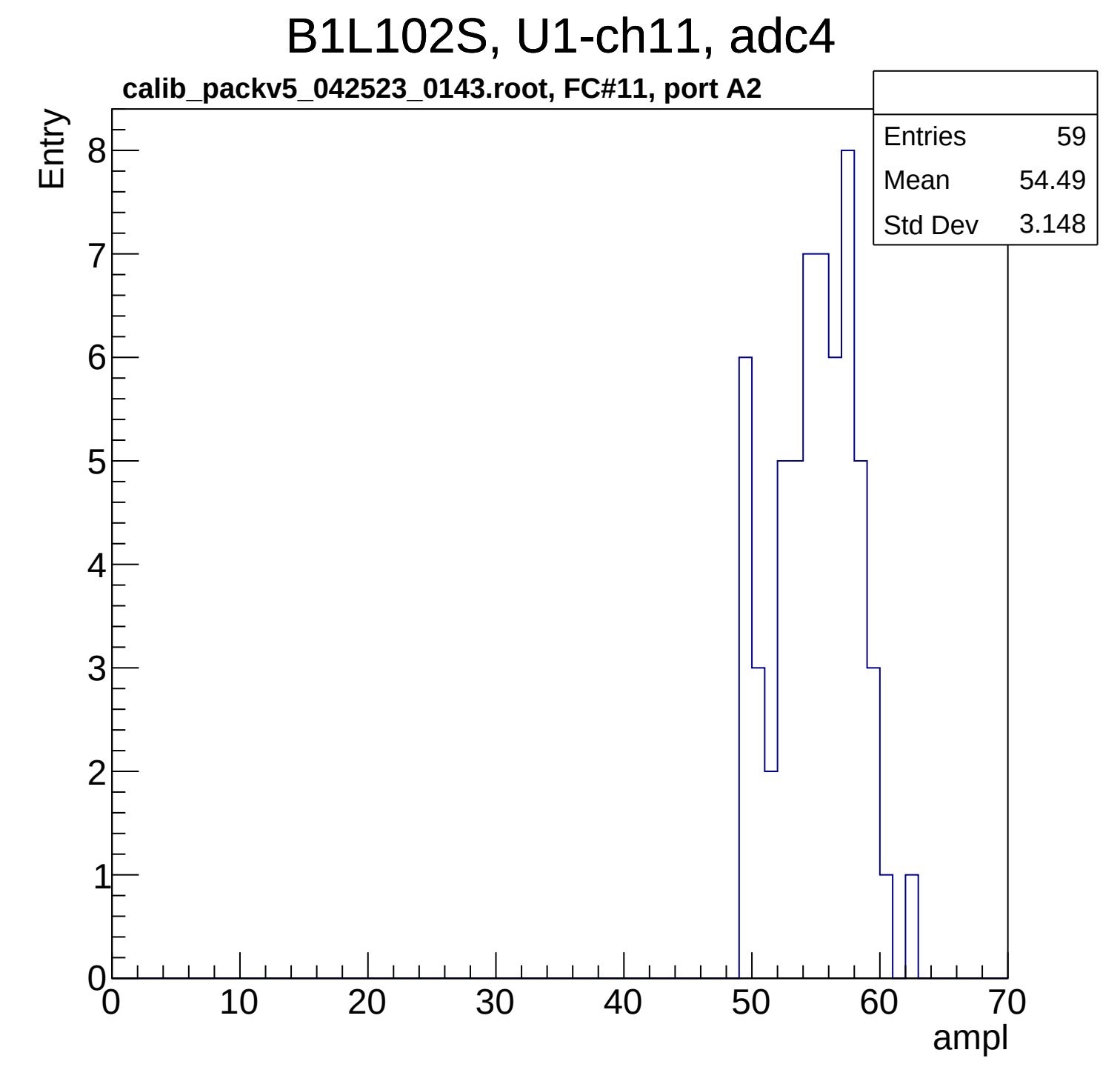
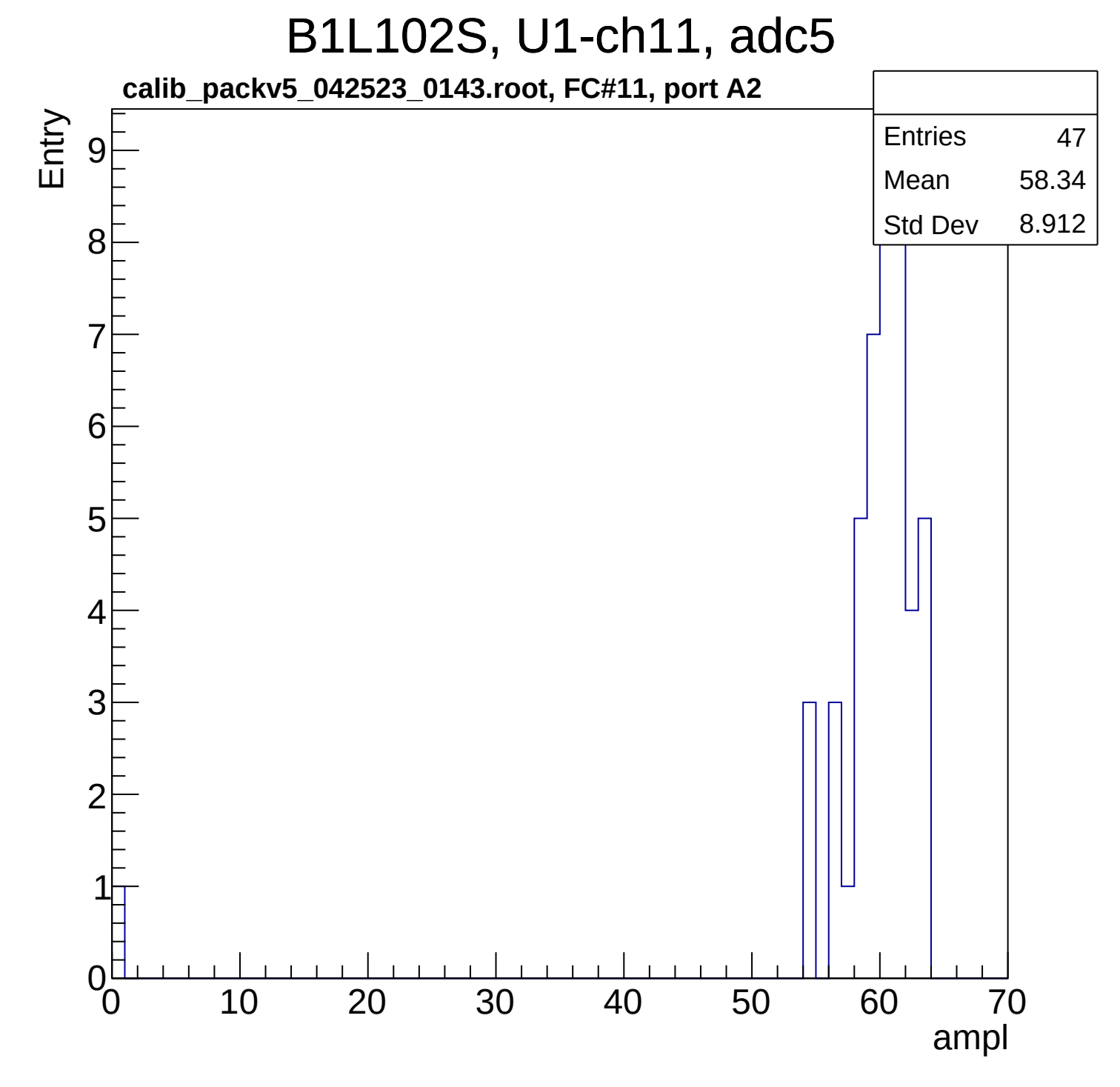
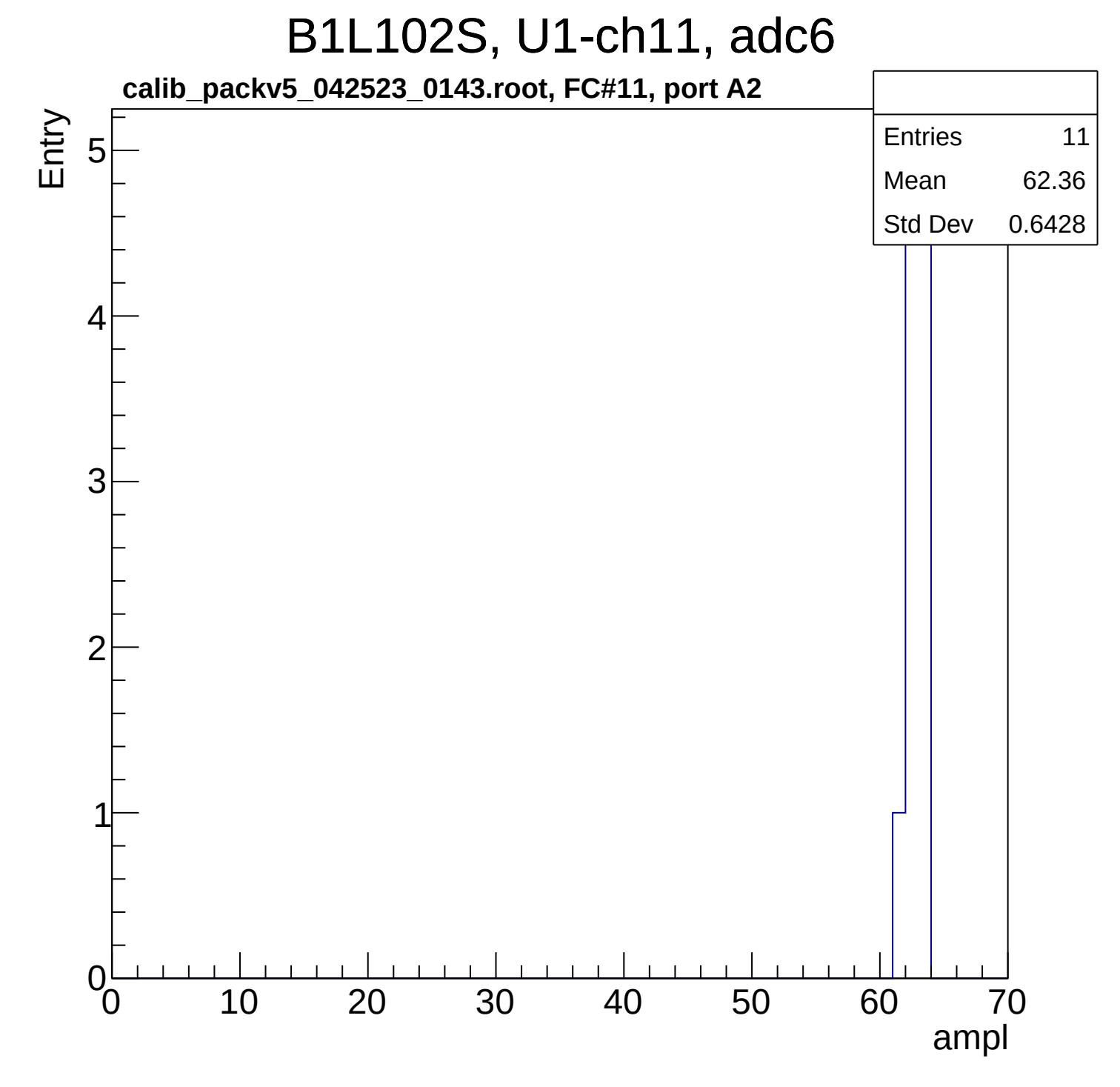
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

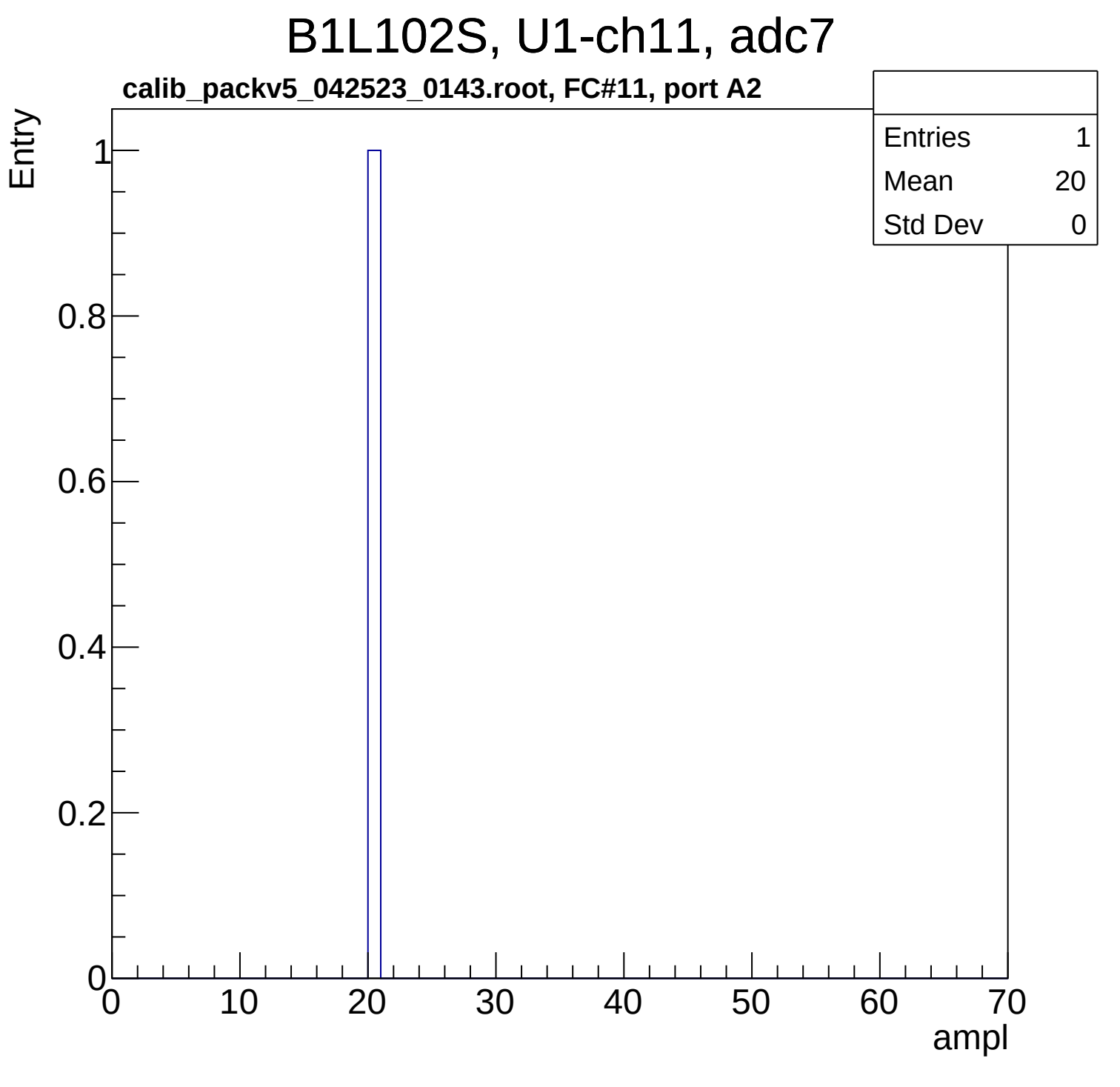

B1L102S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 42.19
Std Dev 3.175
Gaus mean : 42.4776
Gaus Width: 3.0775
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

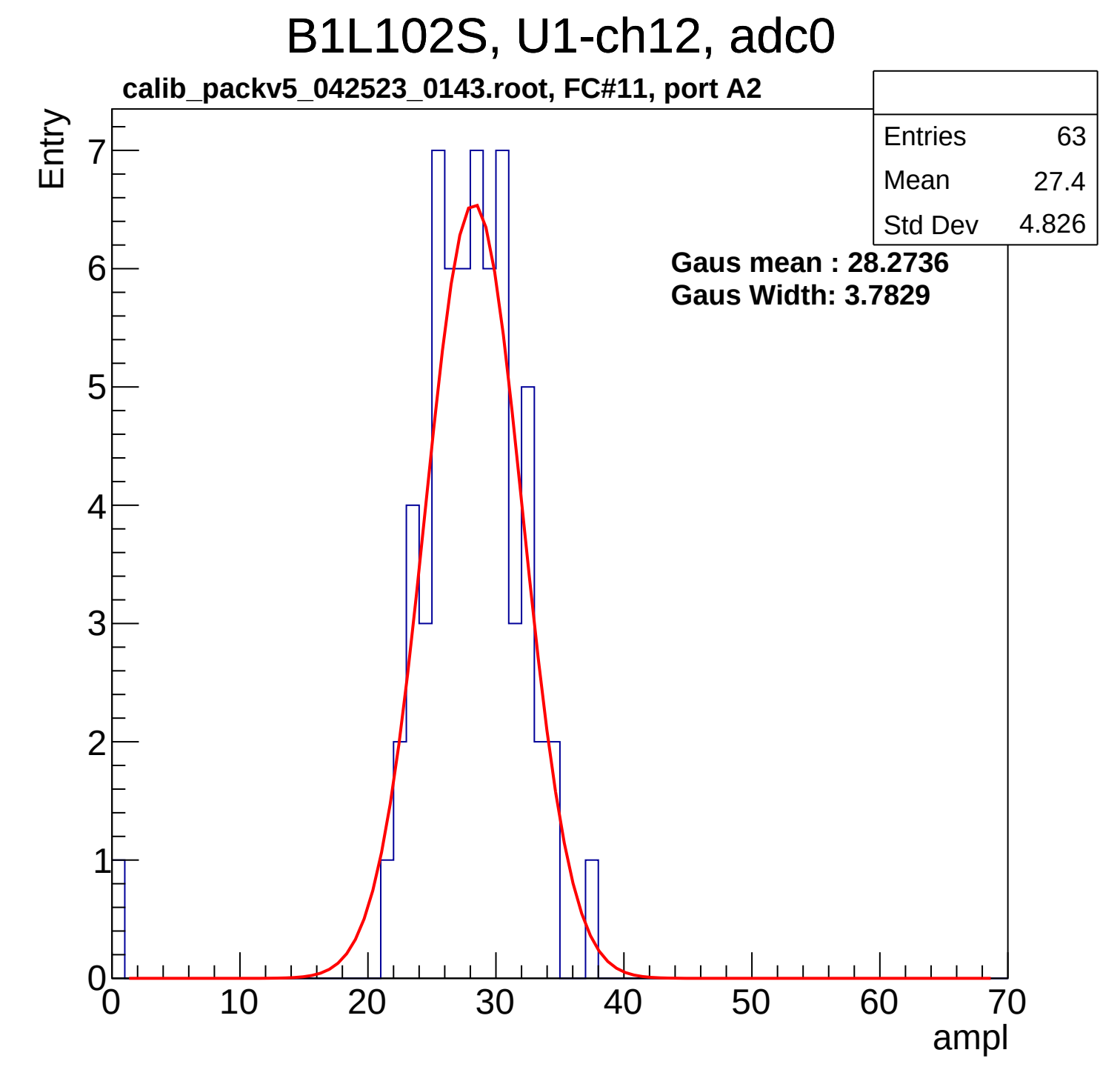

B1L102S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 48.66
Std Dev 3.48
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

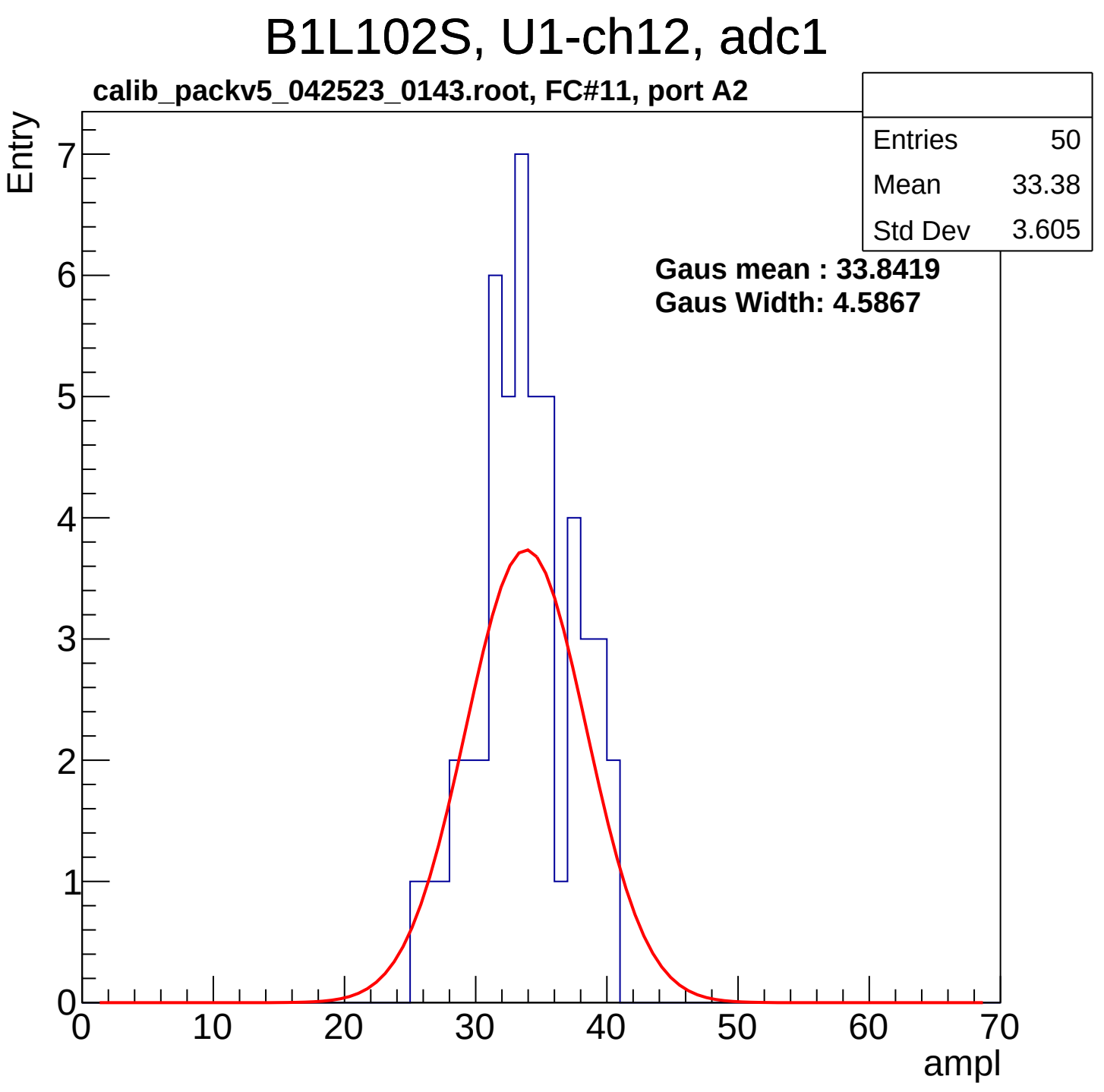

B1L102S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 54.49
Std Dev 3.148
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

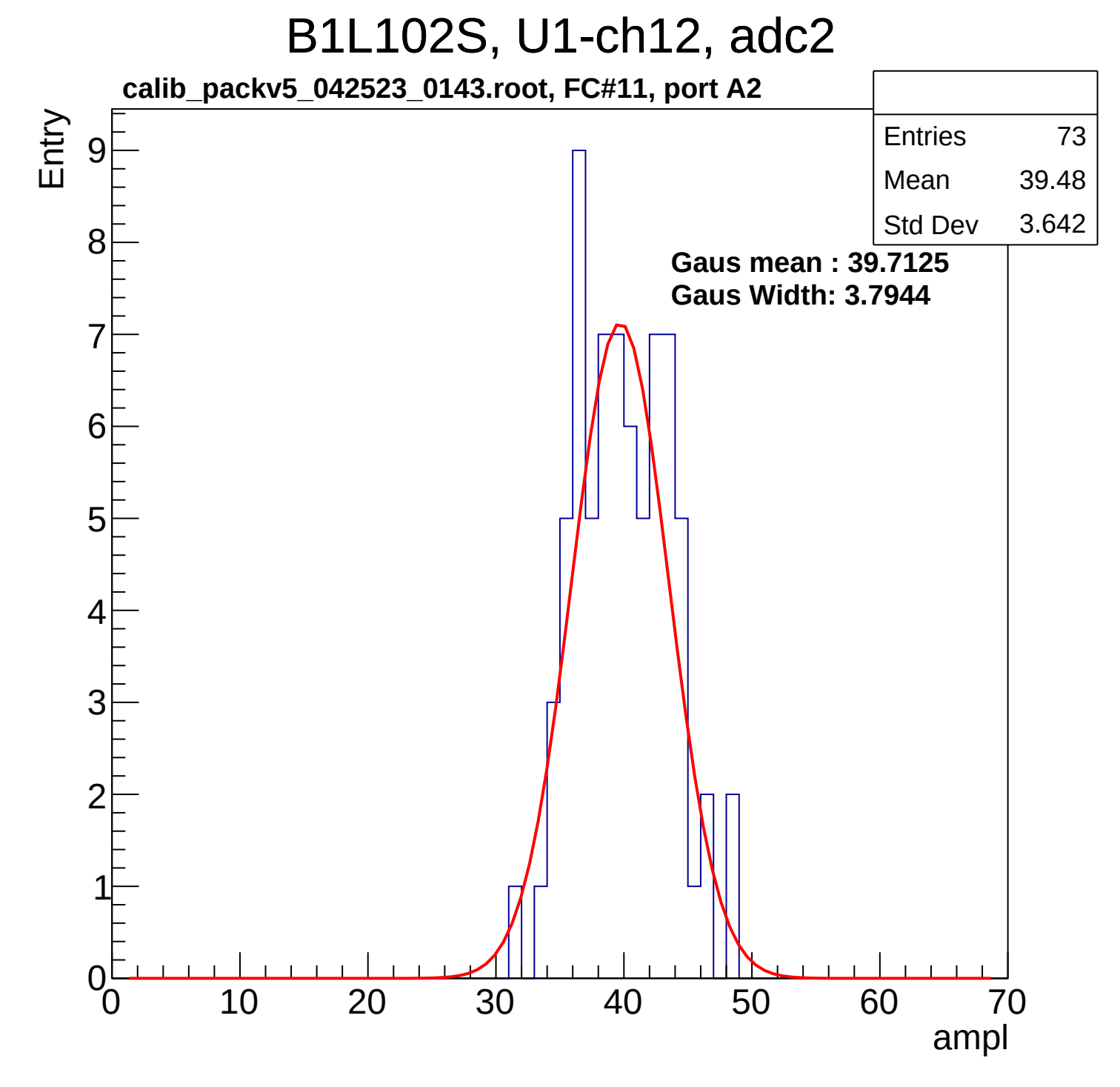

B1L102S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.34
Std Dev 8.912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

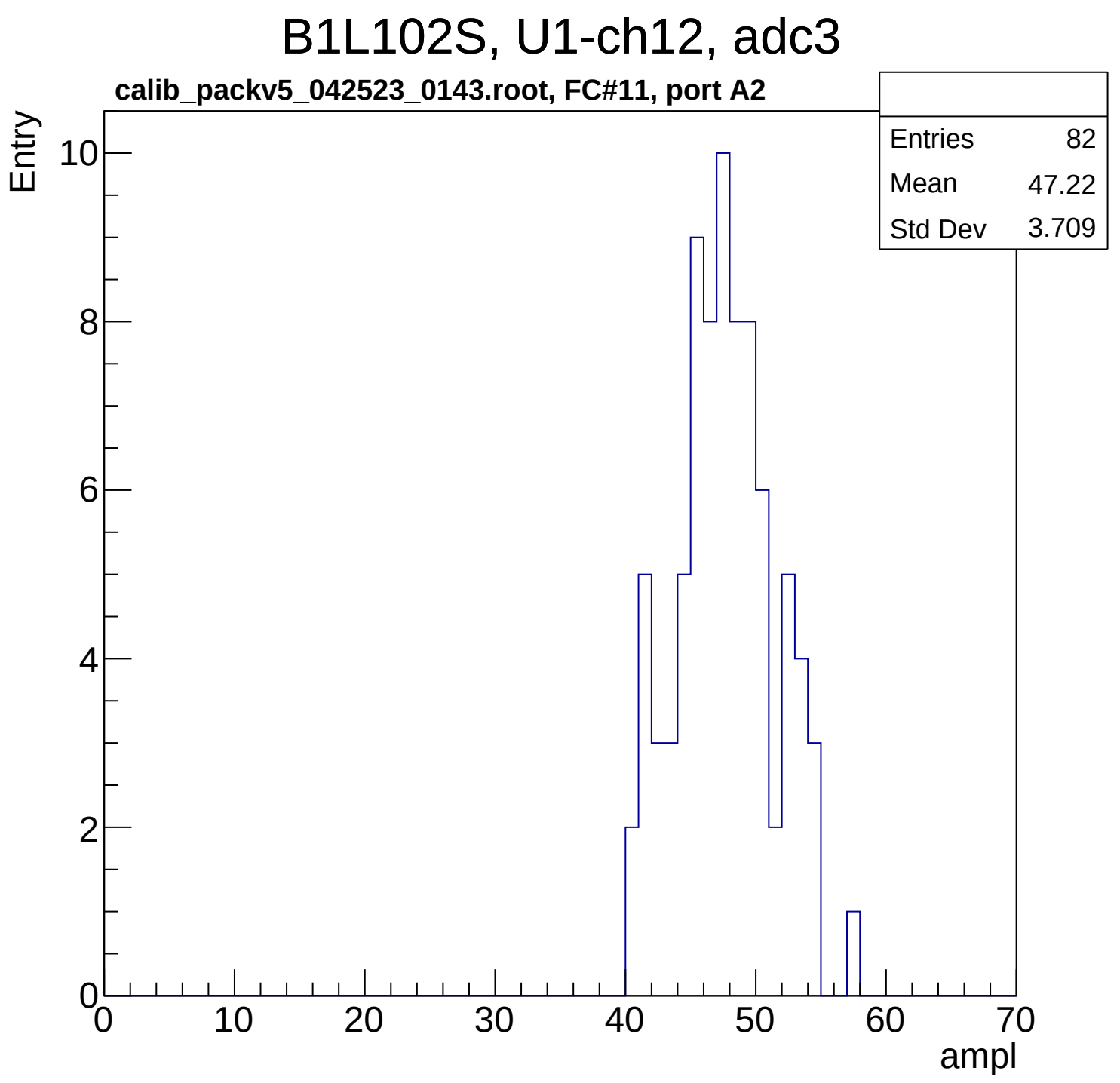

B1L102S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 62.36
Std Dev 0.6428
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

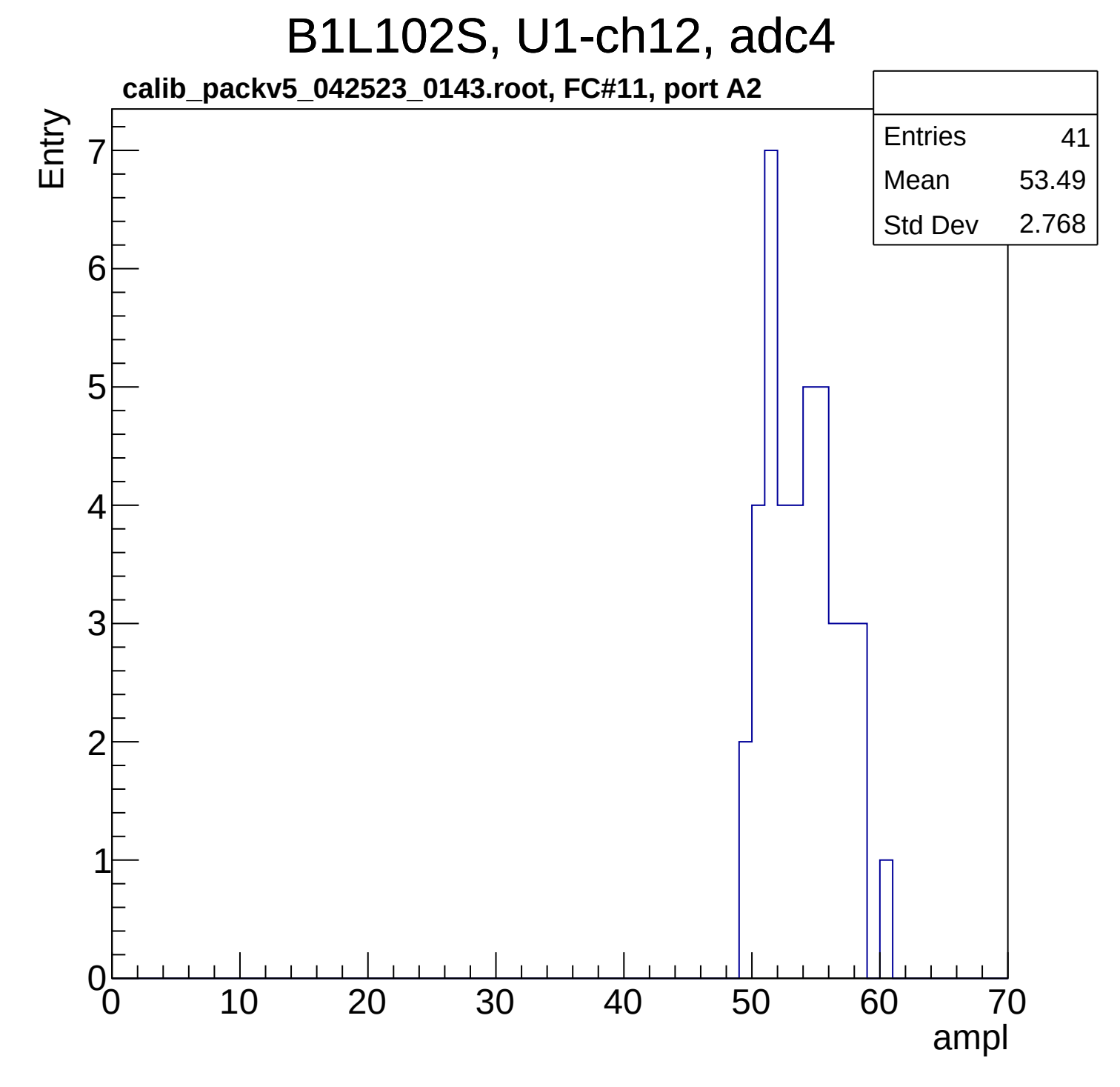

B1L102S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

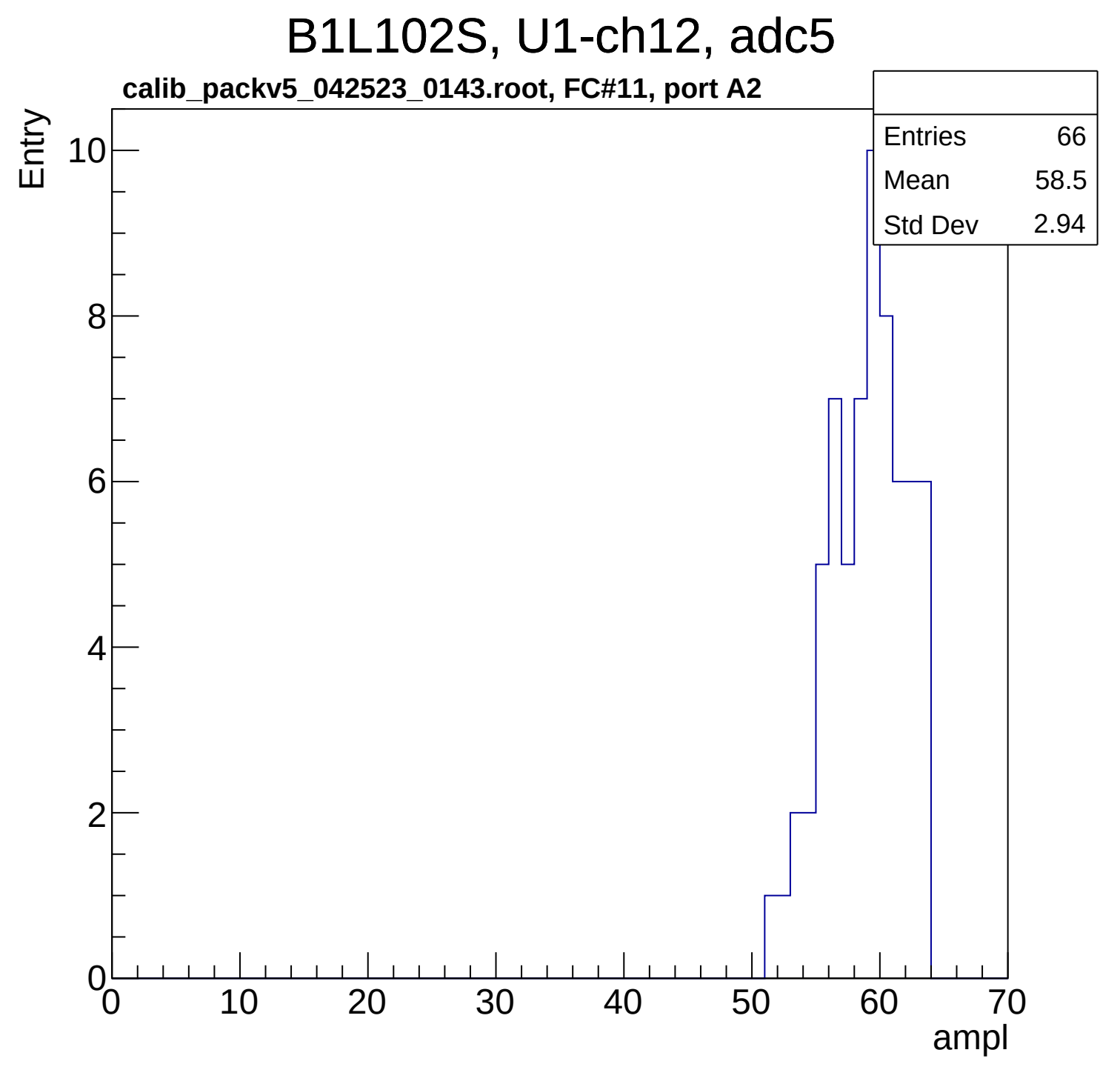

B1L102S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 27.4
Std Dev 4.826
Gaus mean : 28.2736
Gaus Width: 3.7829
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

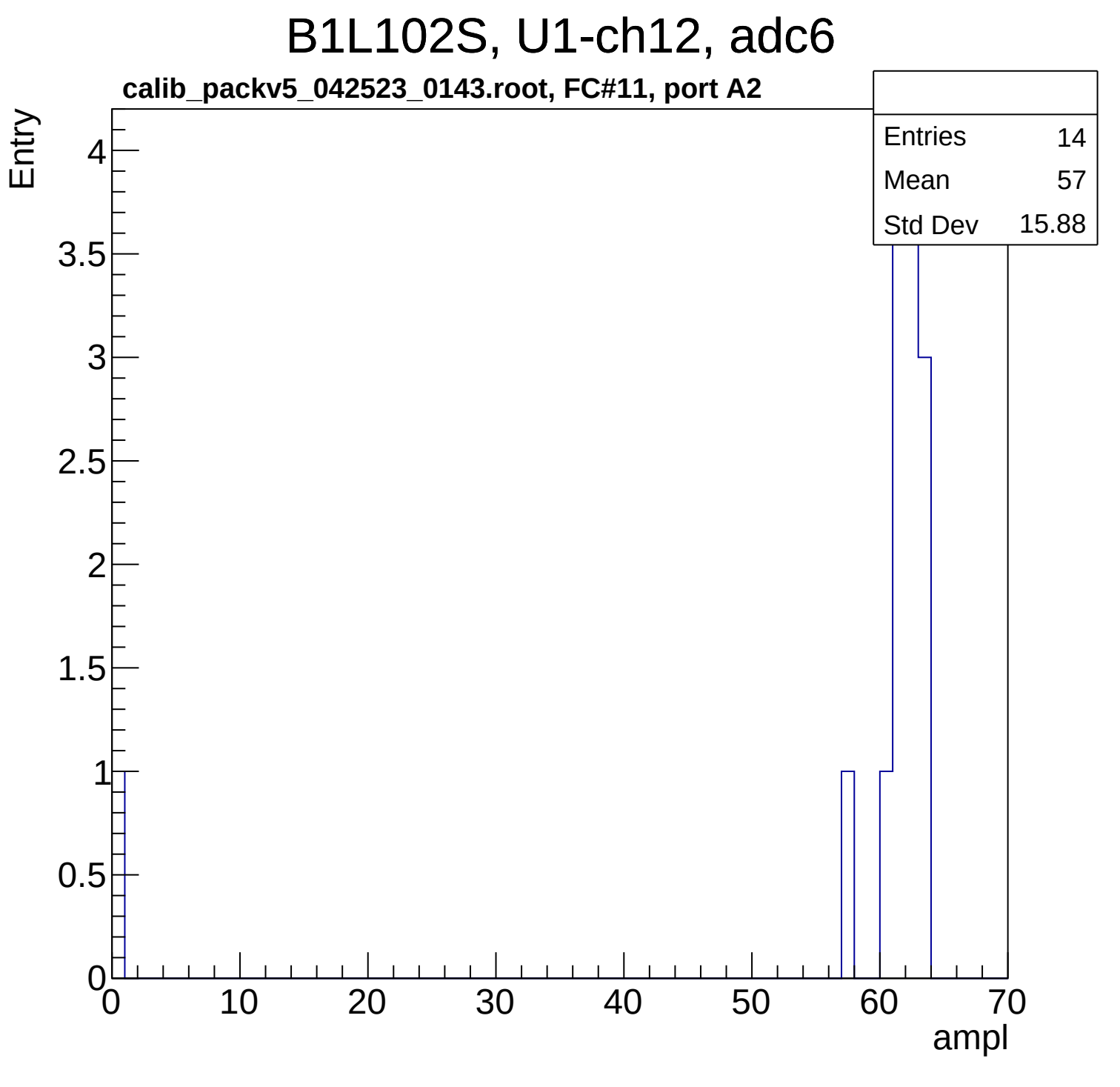

B1L102S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 33.38
Std Dev 3.605
Gaus mean : 33.8419
Gaus Width: 4.5867
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

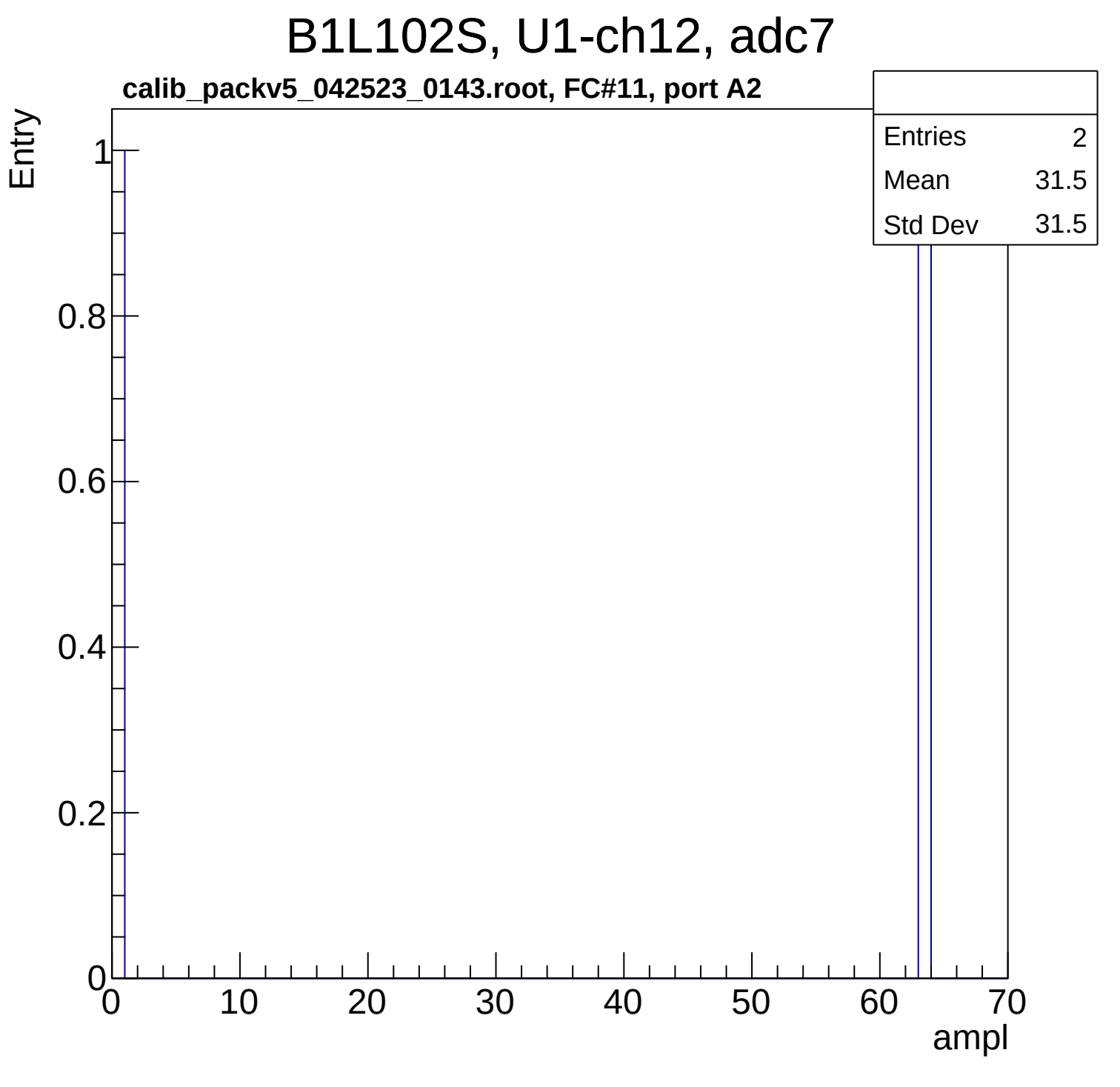

B1L102S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 39.48
Std Dev 3.642
Gaus mean : 39.7125
Gaus Width: 3.7944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

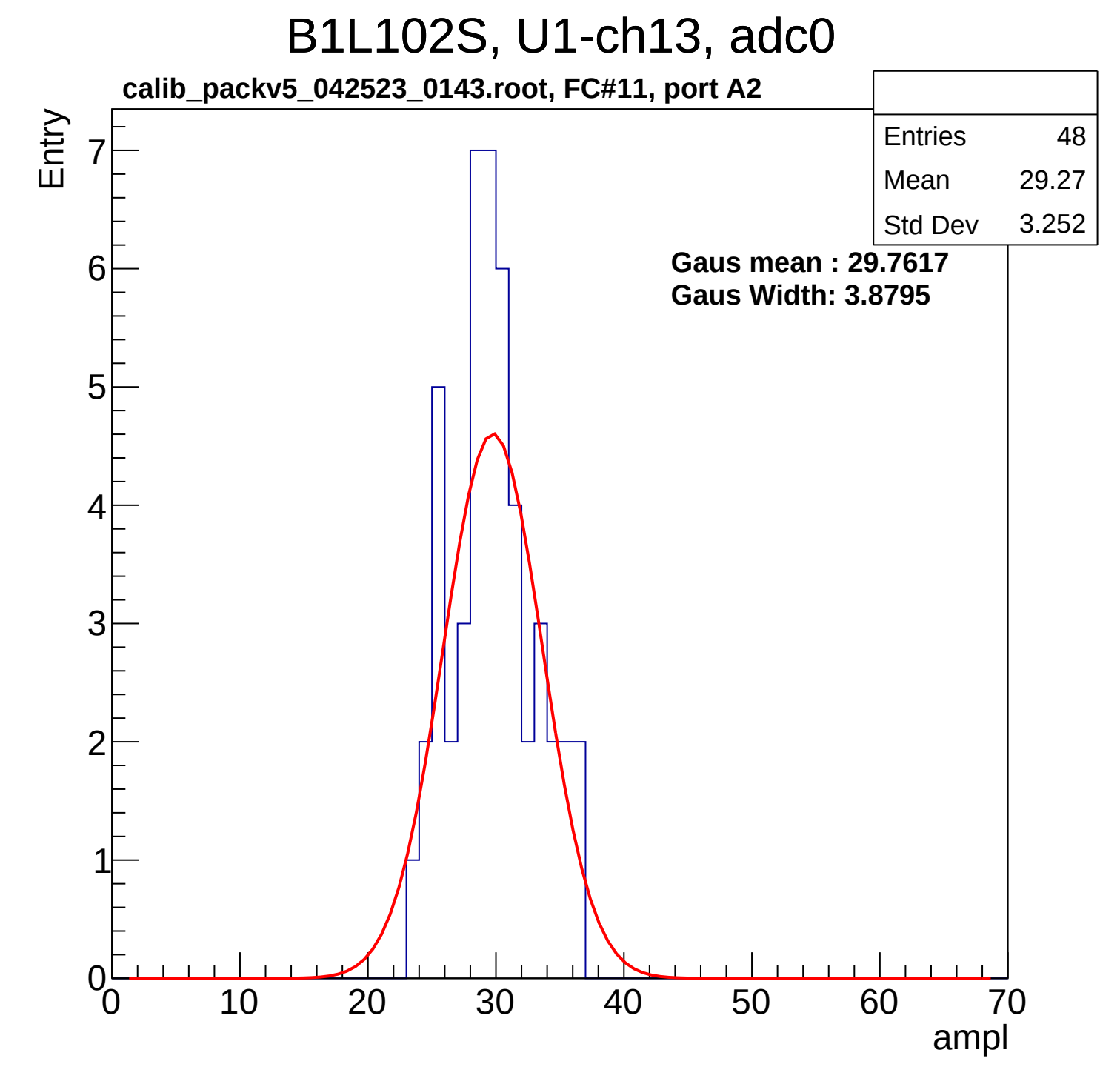

B1L102S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 47.22
Std Dev 3.709
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

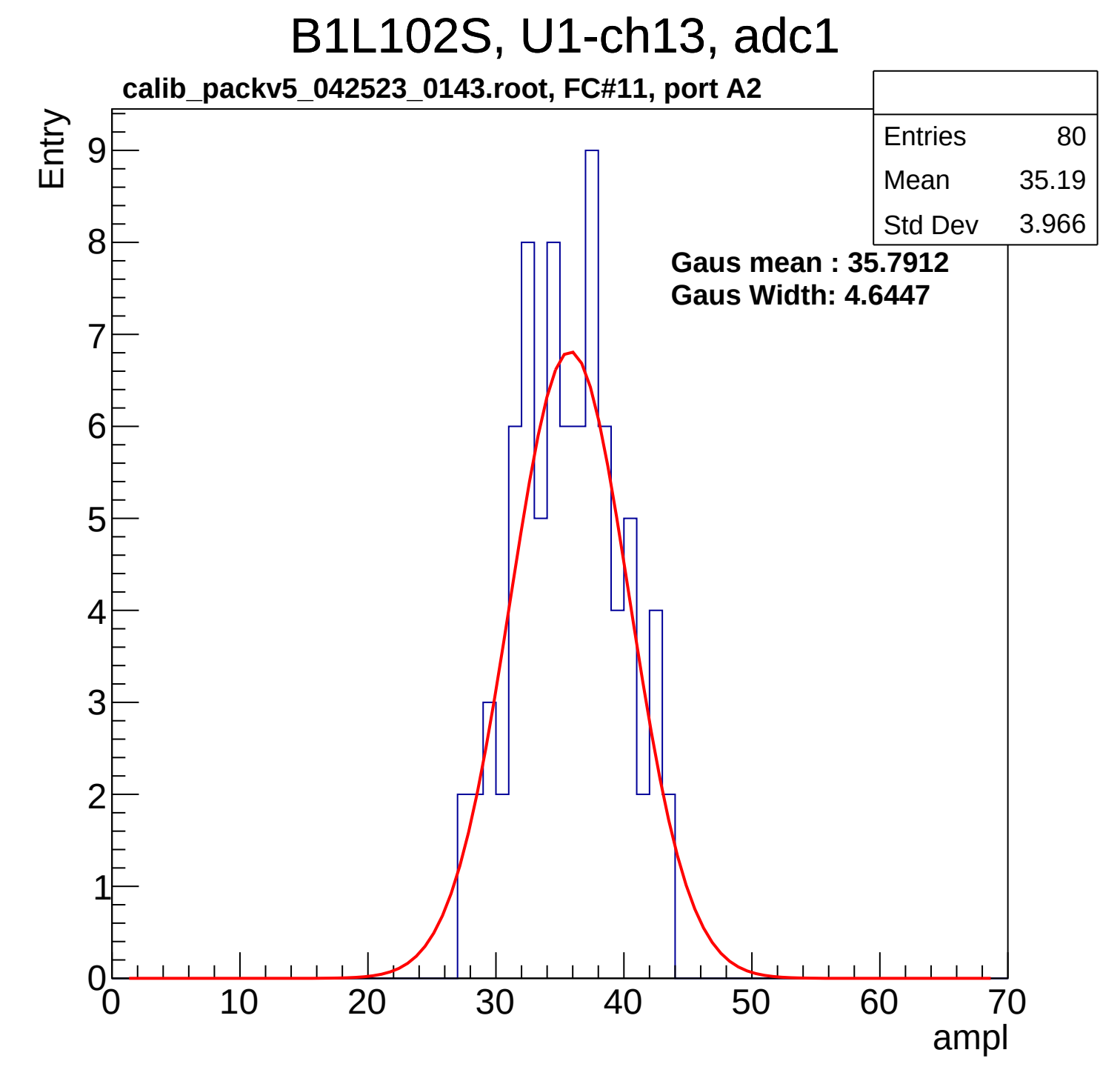

B1L102S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 53.49
Std Dev 2.768
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

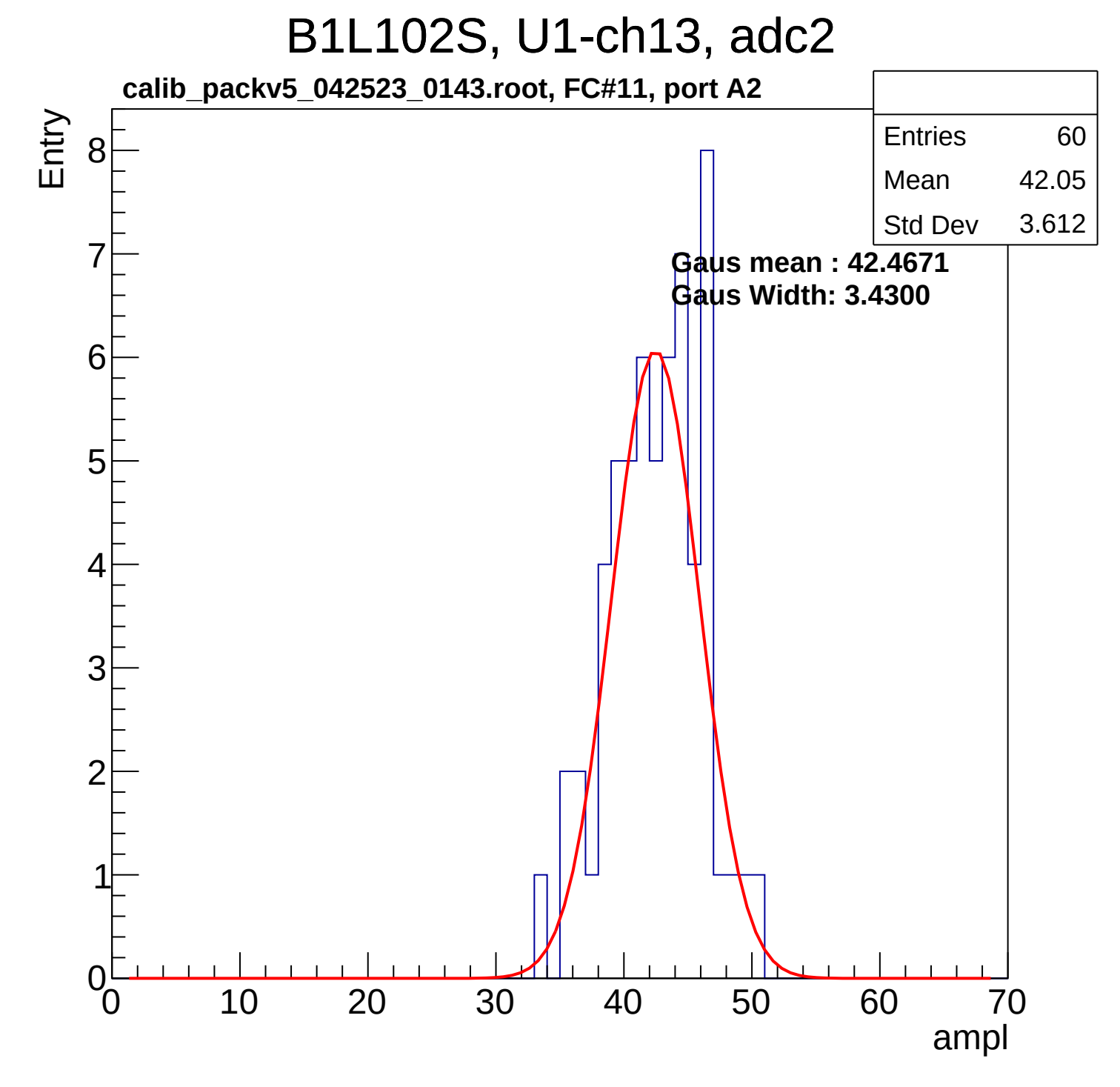

B1L102S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 58.5
Std Dev 2.94
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

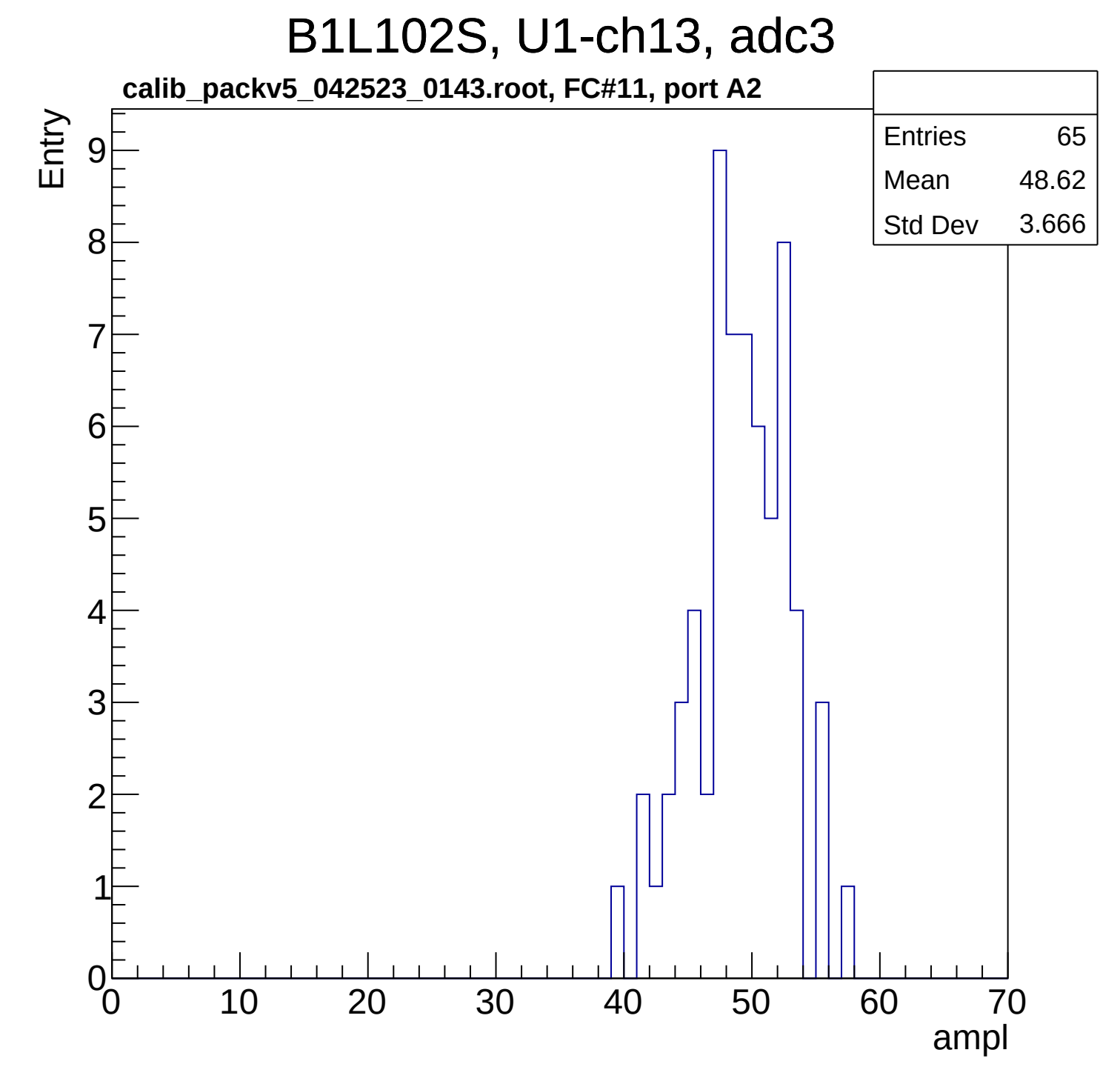

B1L102S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 57
Std Dev 15.88
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 29.27
Std Dev 3.252
Gaus mean : 29.7617
Gaus Width: 3.8795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 35.19
Std Dev 3.966
Gaus mean : 35.7912
Gaus Width: 4.6447
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 42.05
Std Dev 3.612
Gaus mean : 42.4671
Gaus Width: 3.4300
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 48.62
Std Dev 3.666
Entry
ampl
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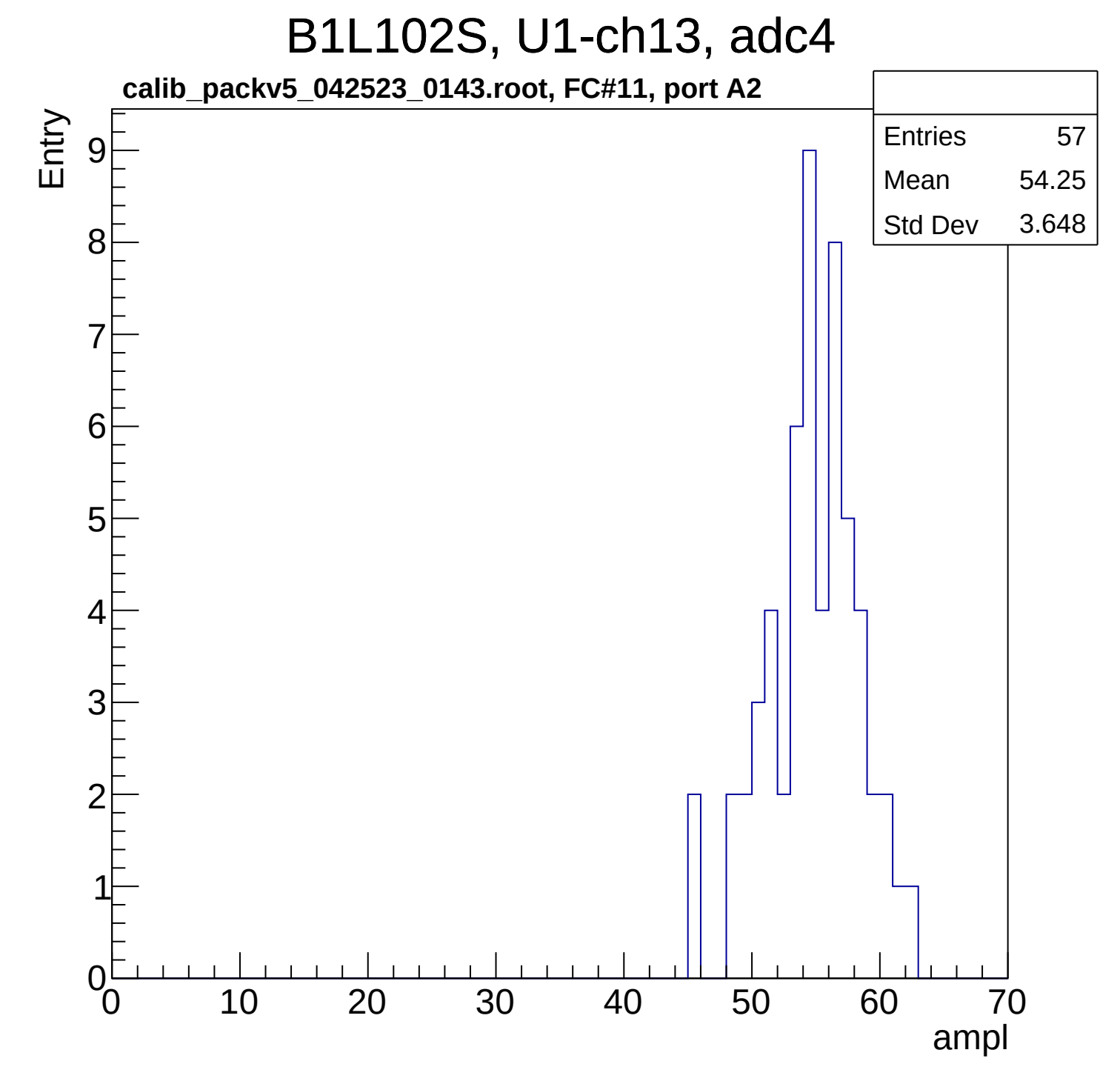
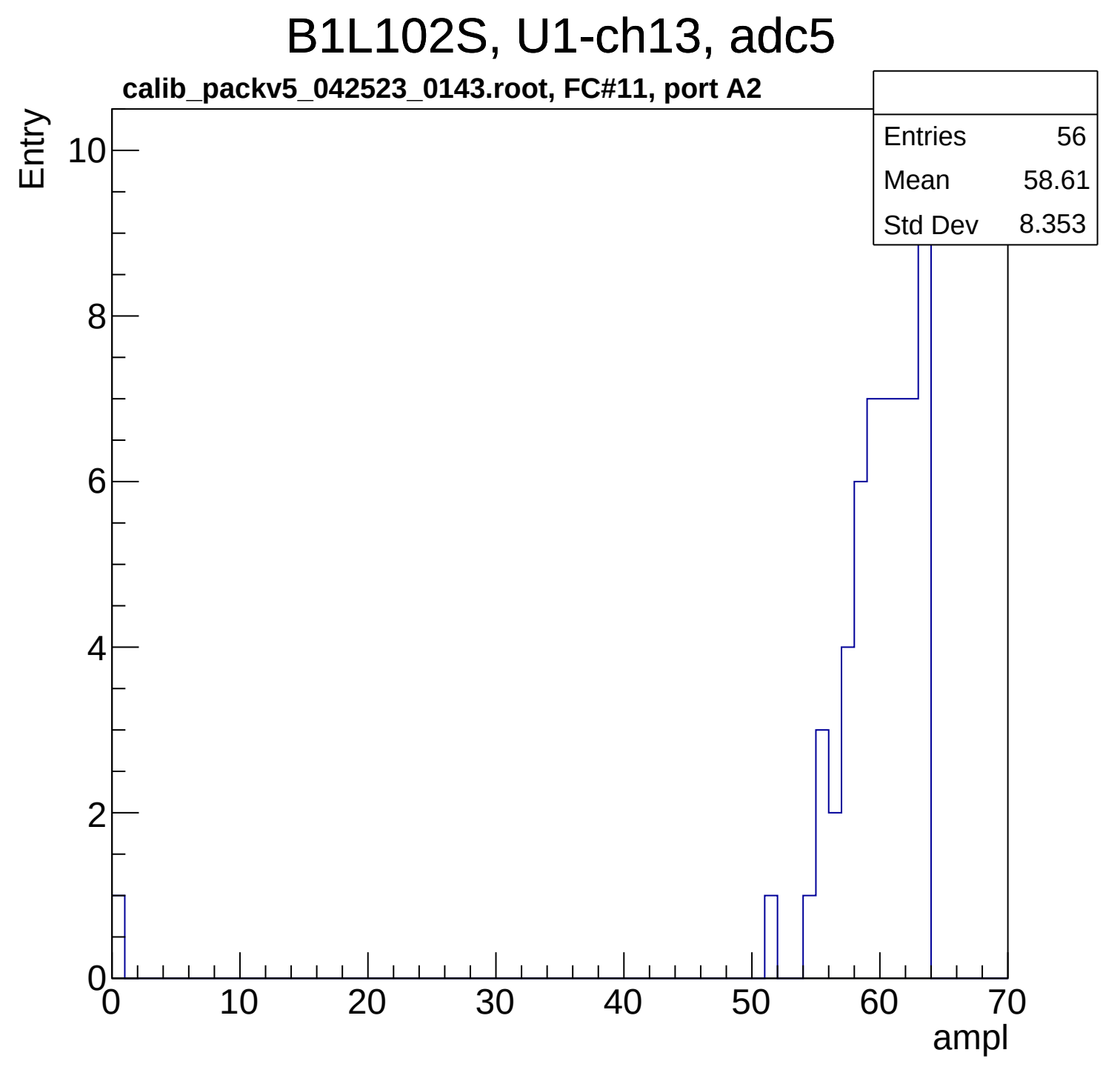
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 54.25
Std Dev 3.648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 58.61
Std Dev 8.353
Entry
0
2
4
6
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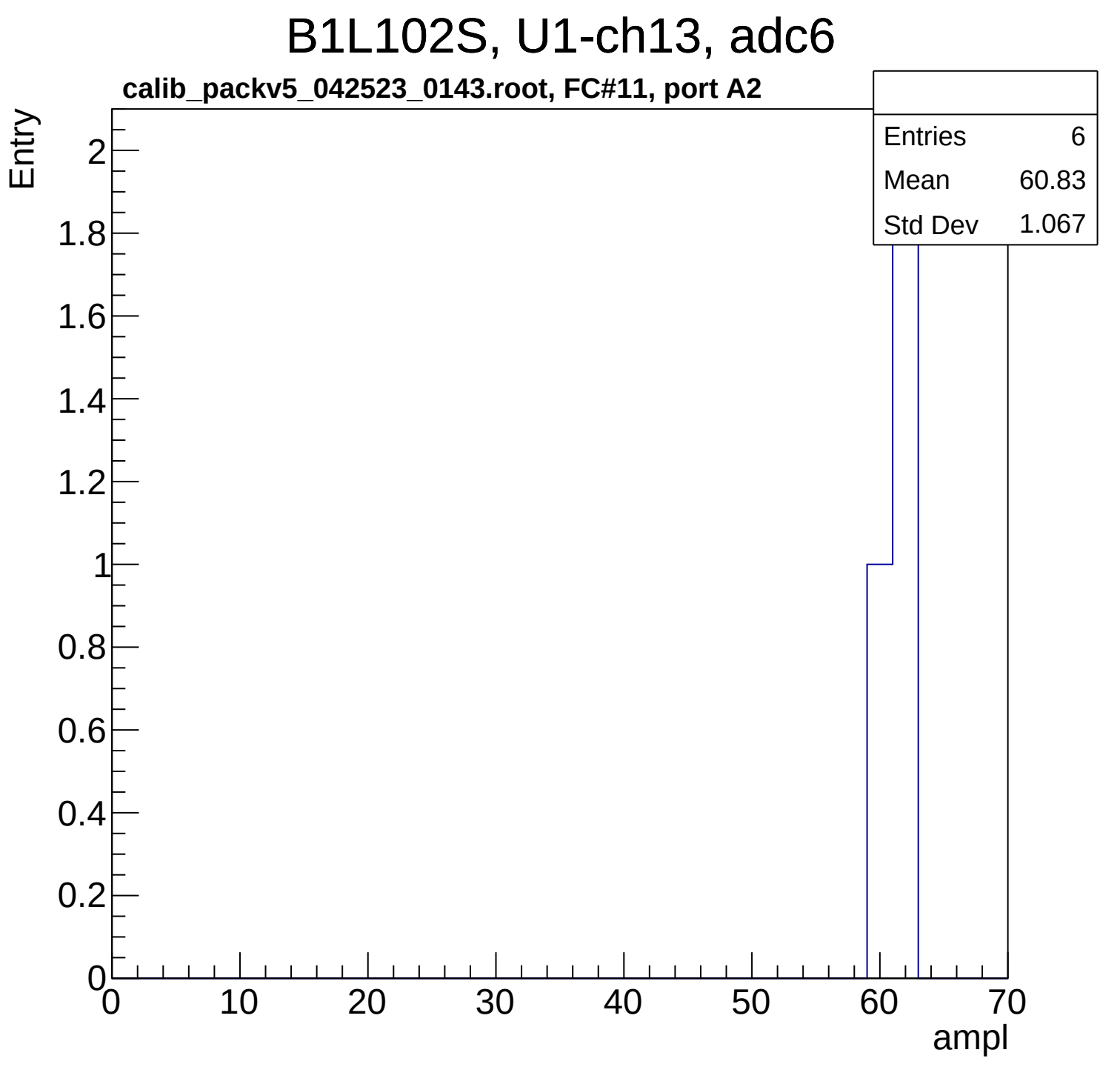
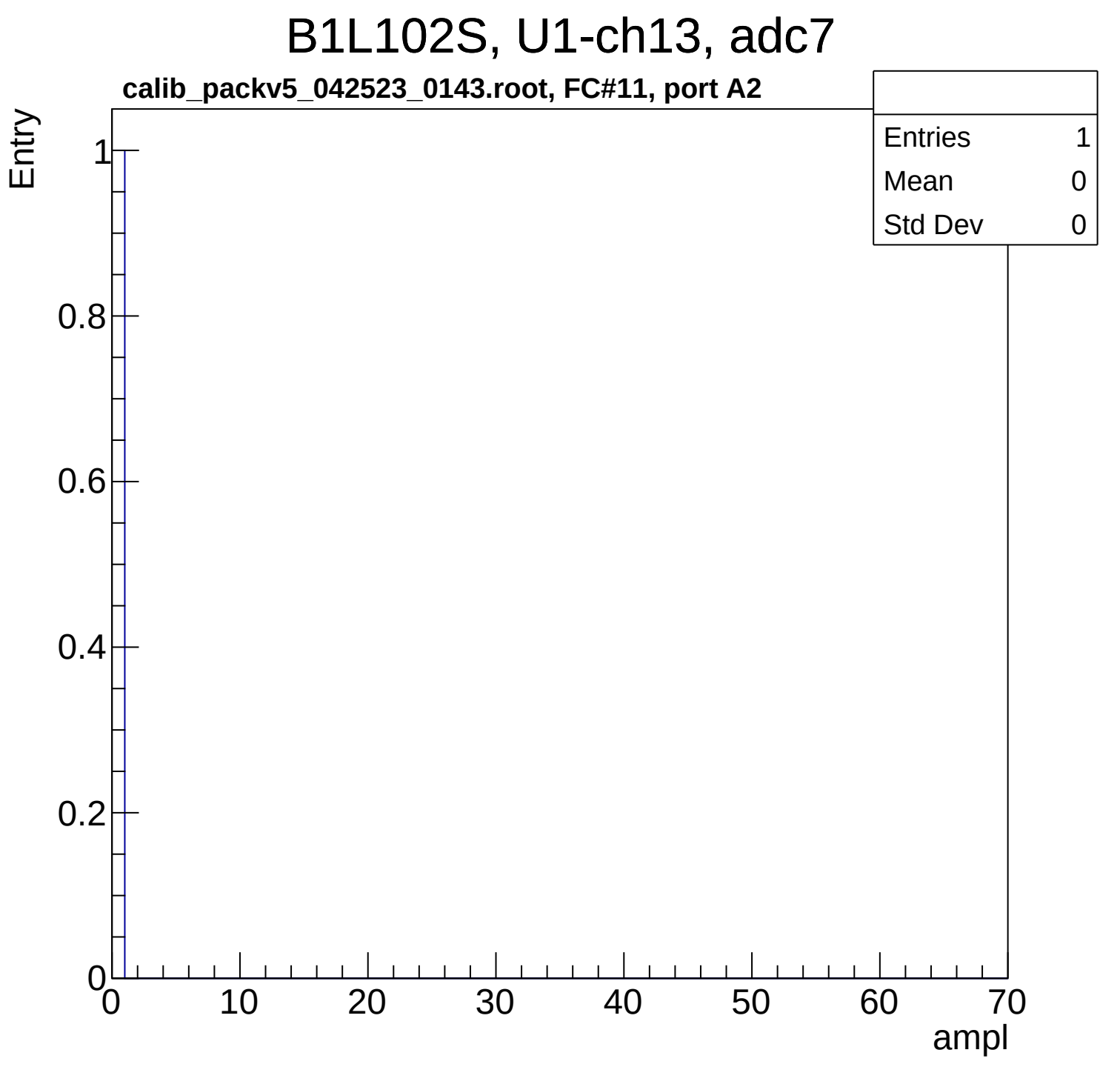
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 60.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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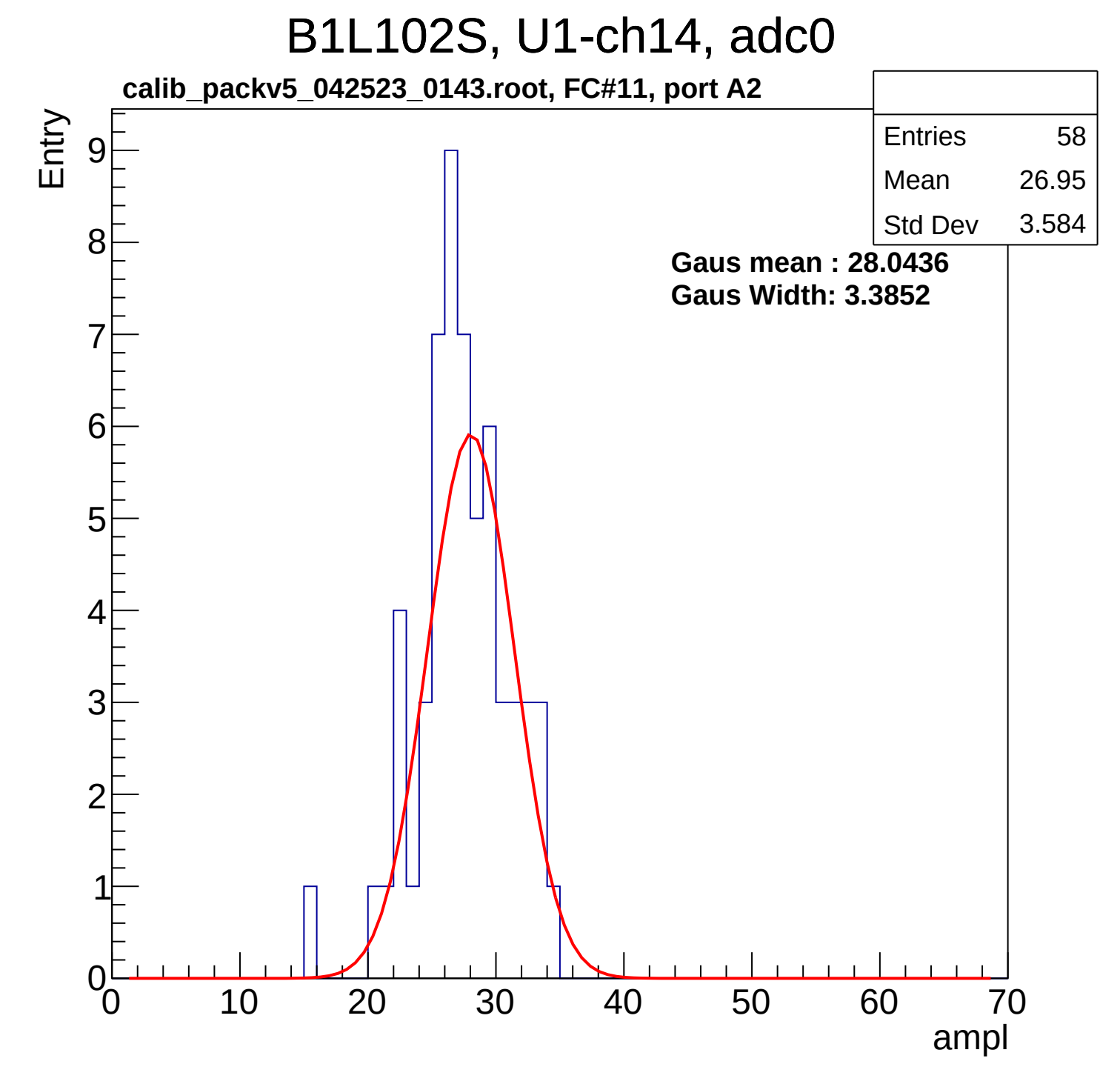
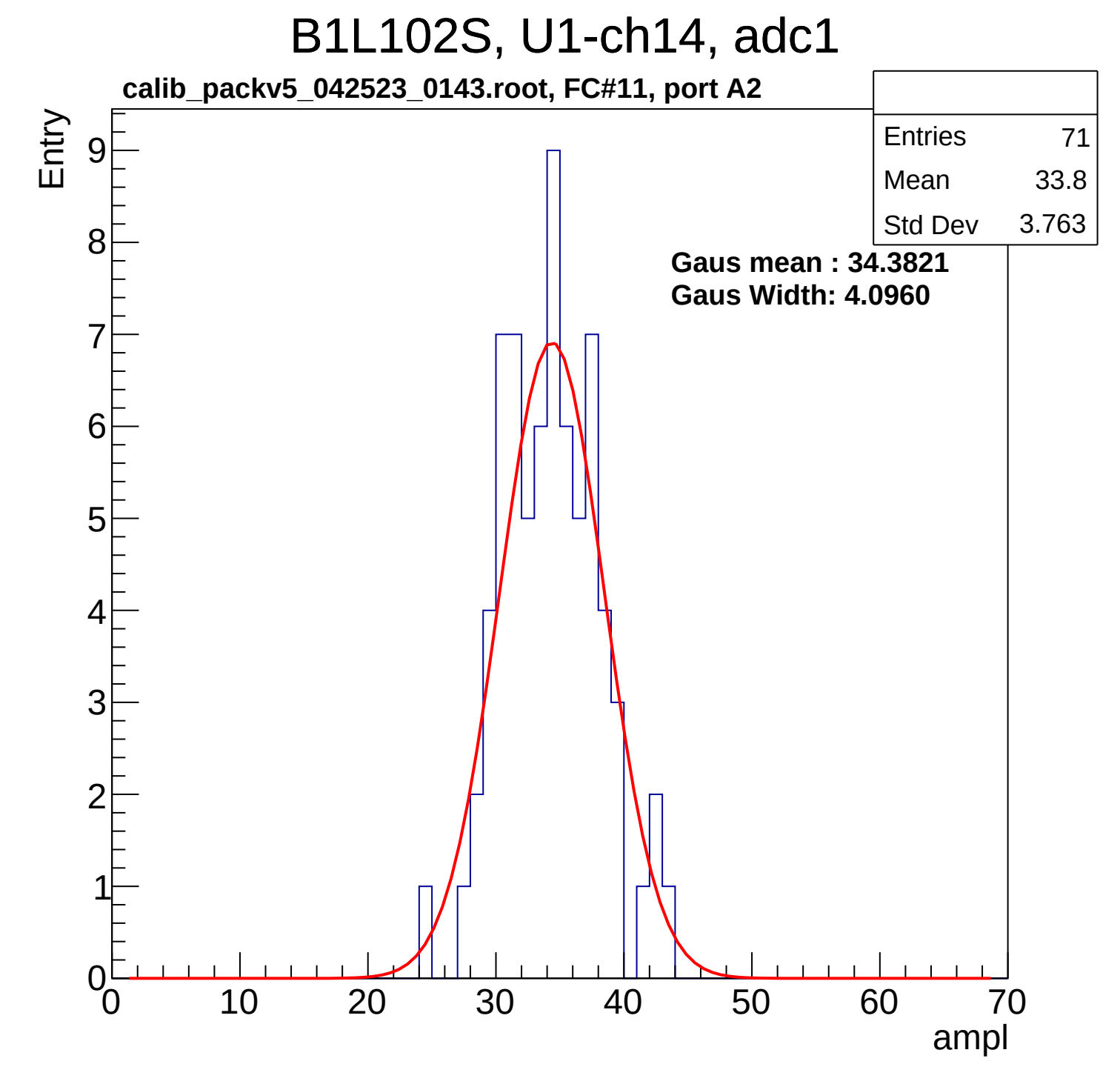
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 26.95
Std Dev 3.584
Gaus mean : 28.0436
Gaus Width: 3.3852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 33.8
Std Dev 3.763
Gaus mean : 34.3821
Gaus Width: 4.0960
Entry
ampl
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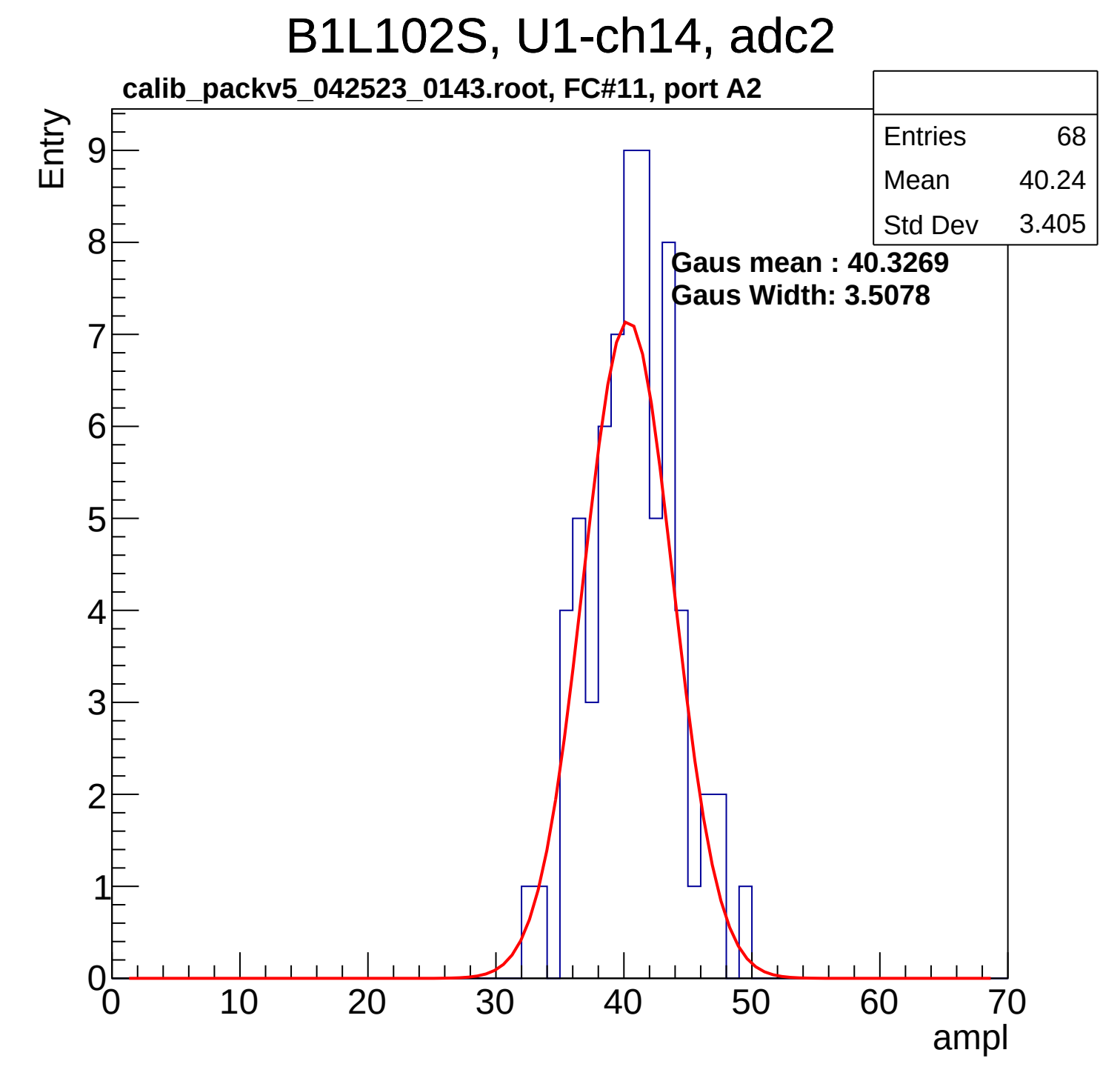
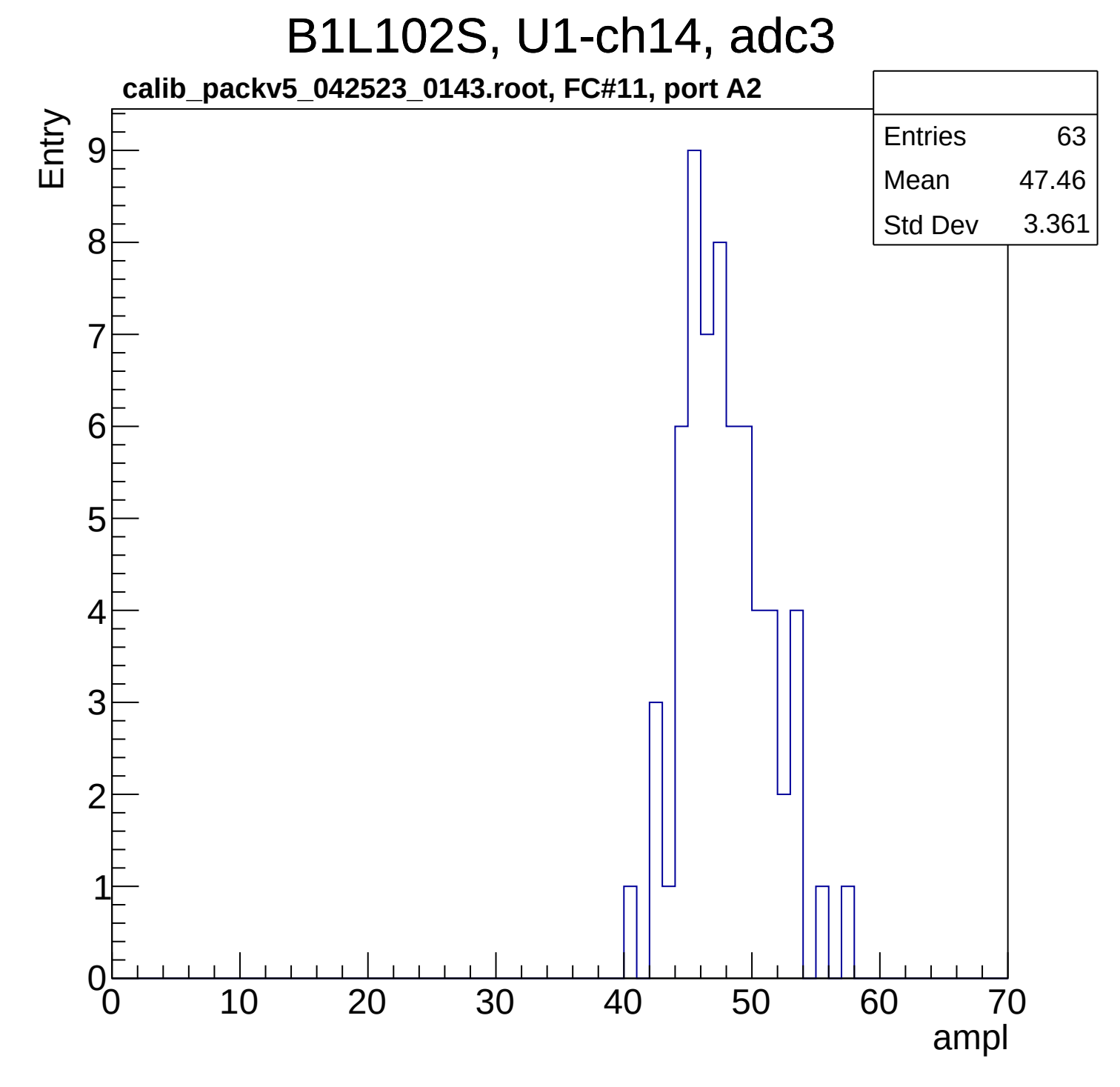
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 40.24
Std Dev 3.405
Gaus mean : 40.3269
Gaus Width: 3.5078
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 47.46
Std Dev 3.361
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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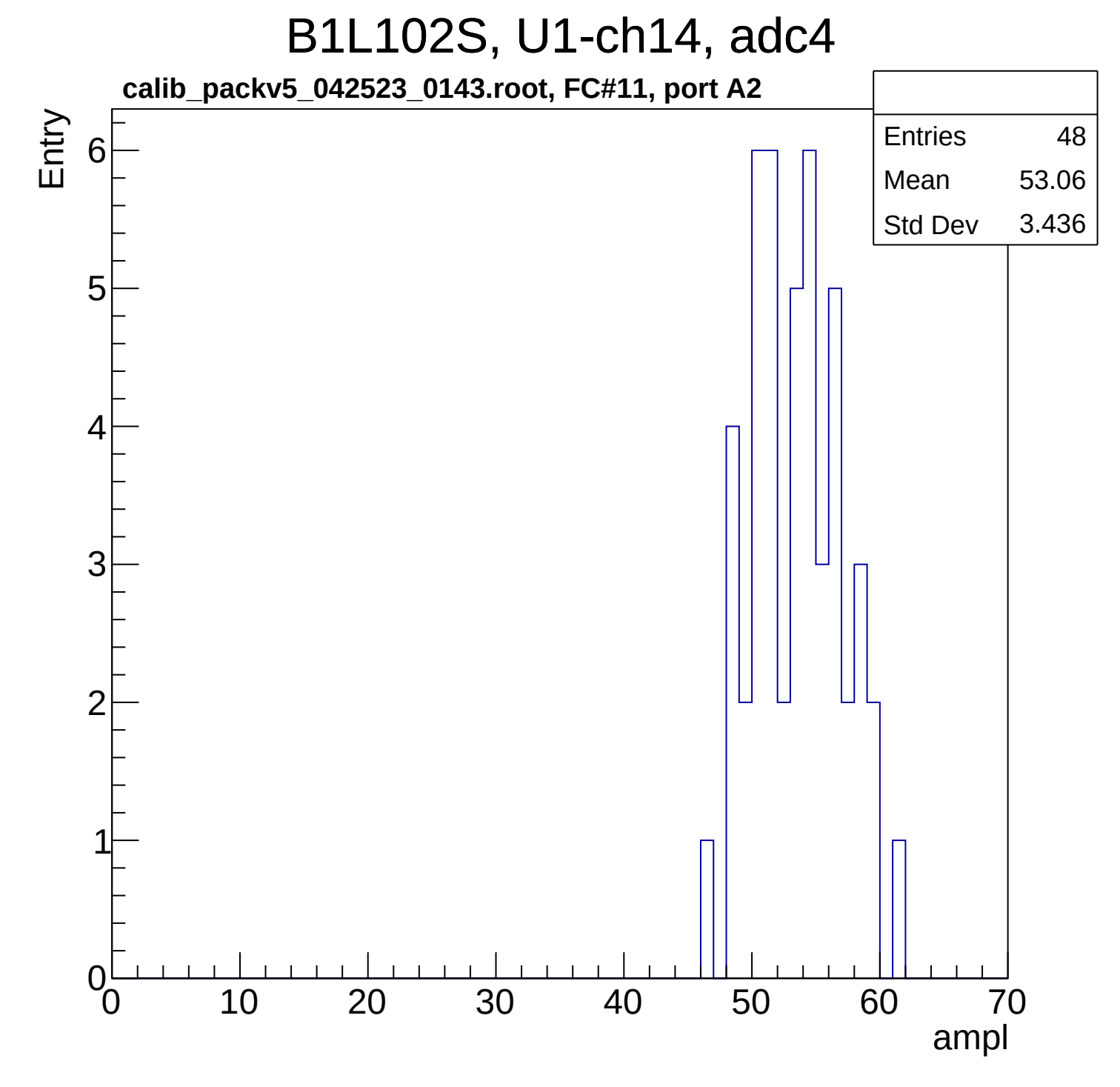
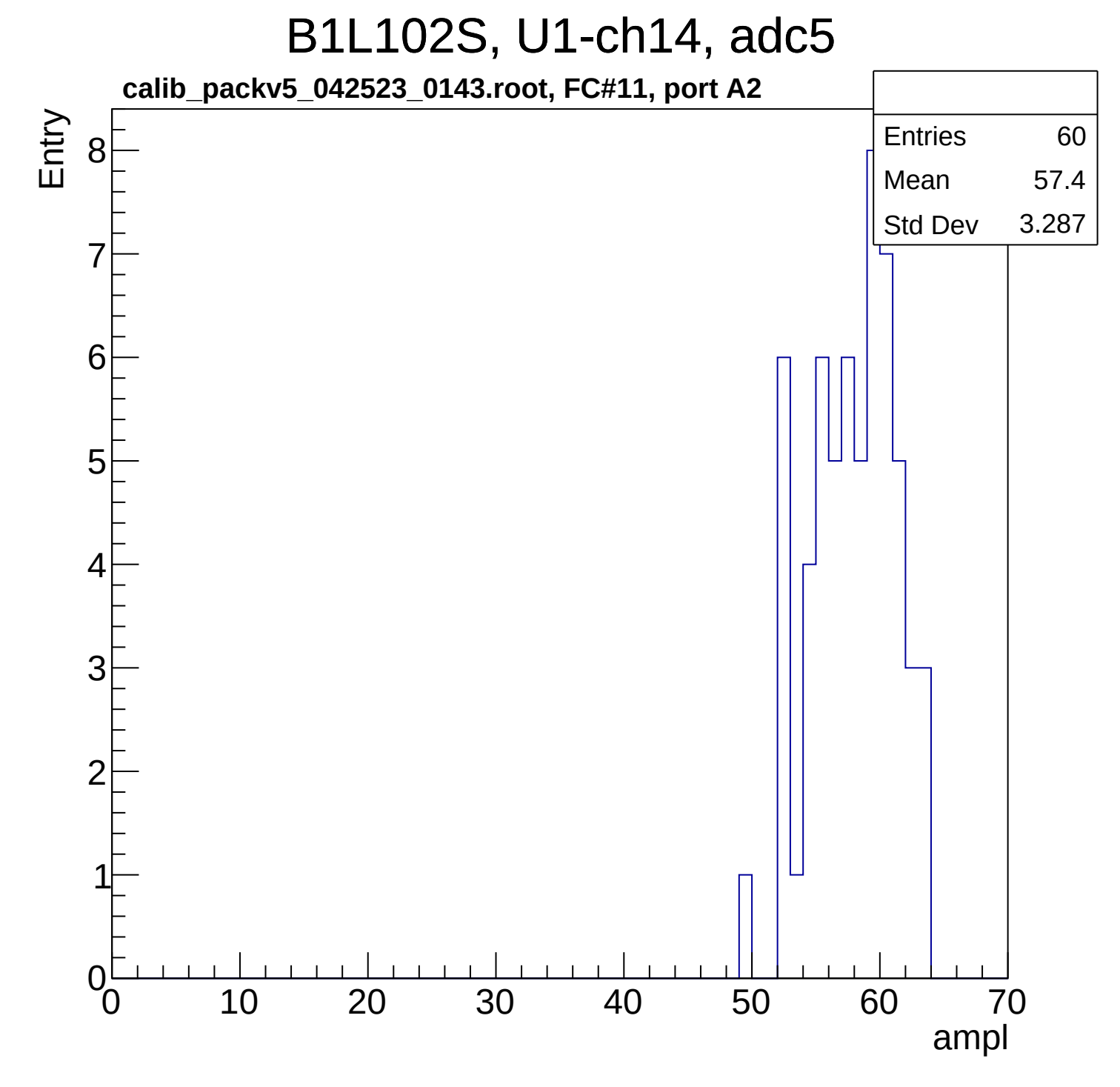
60
70


B1L102S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 53.06
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 57.4
Std Dev 3.287
Entry
ampl
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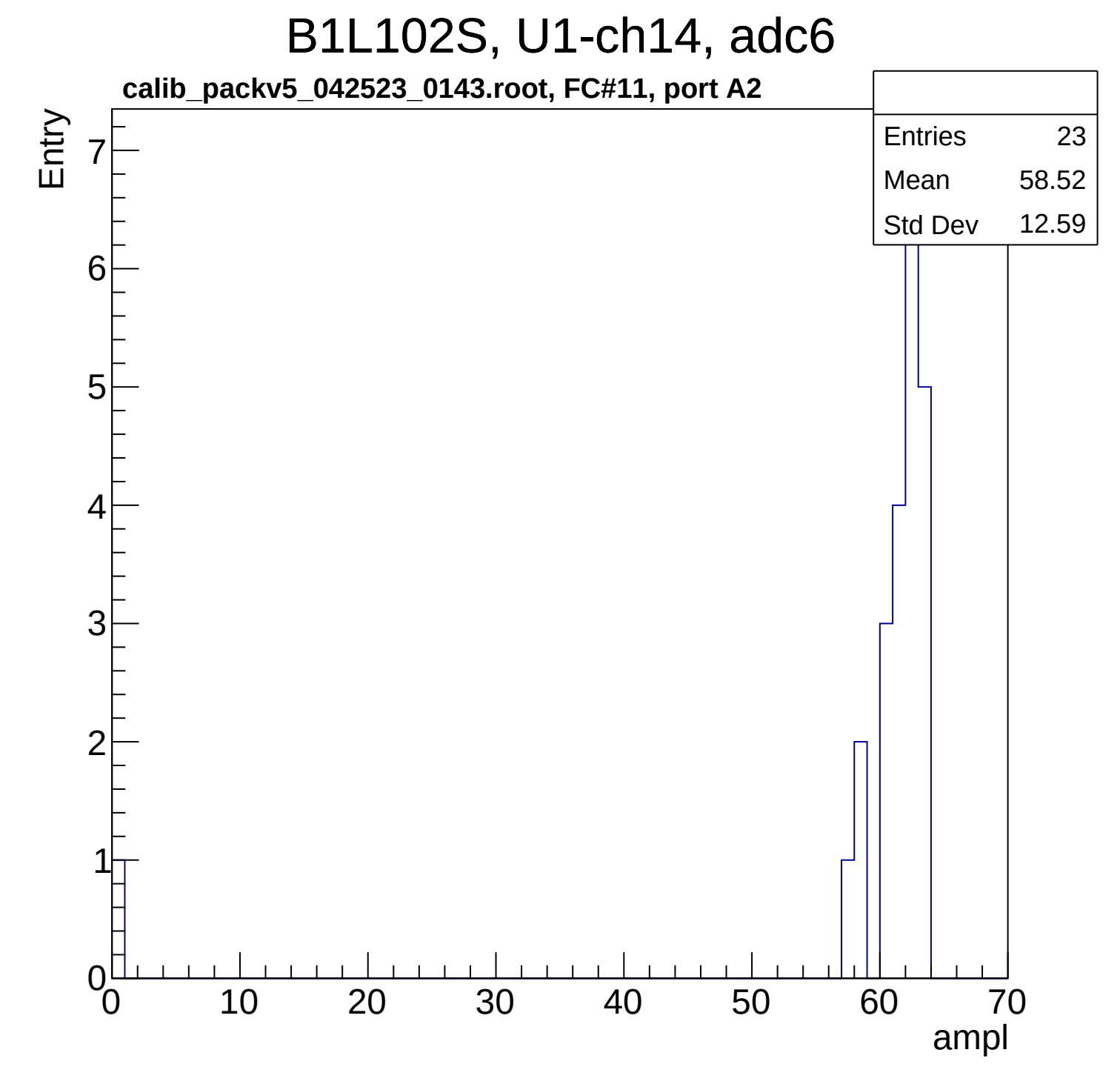
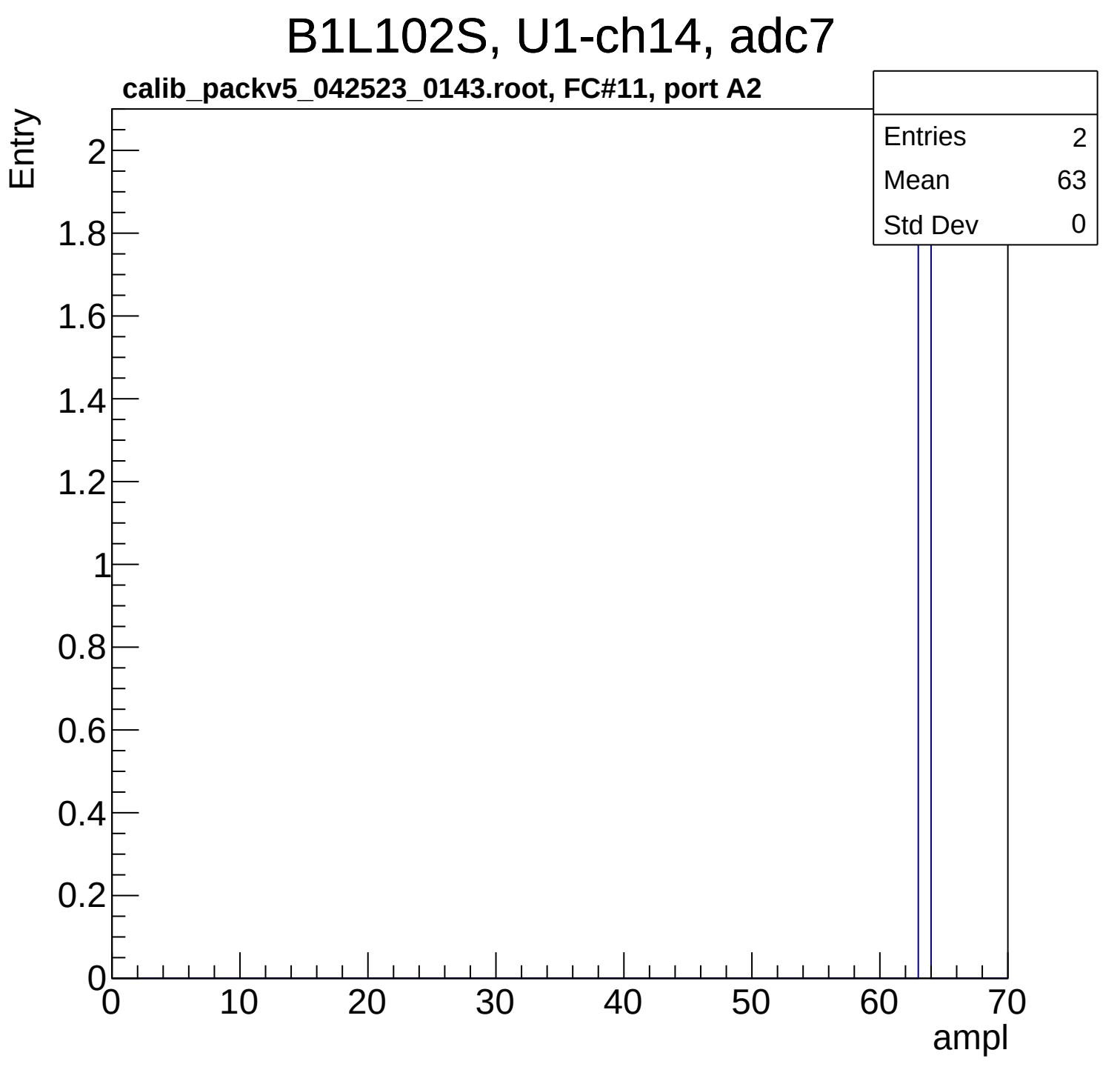
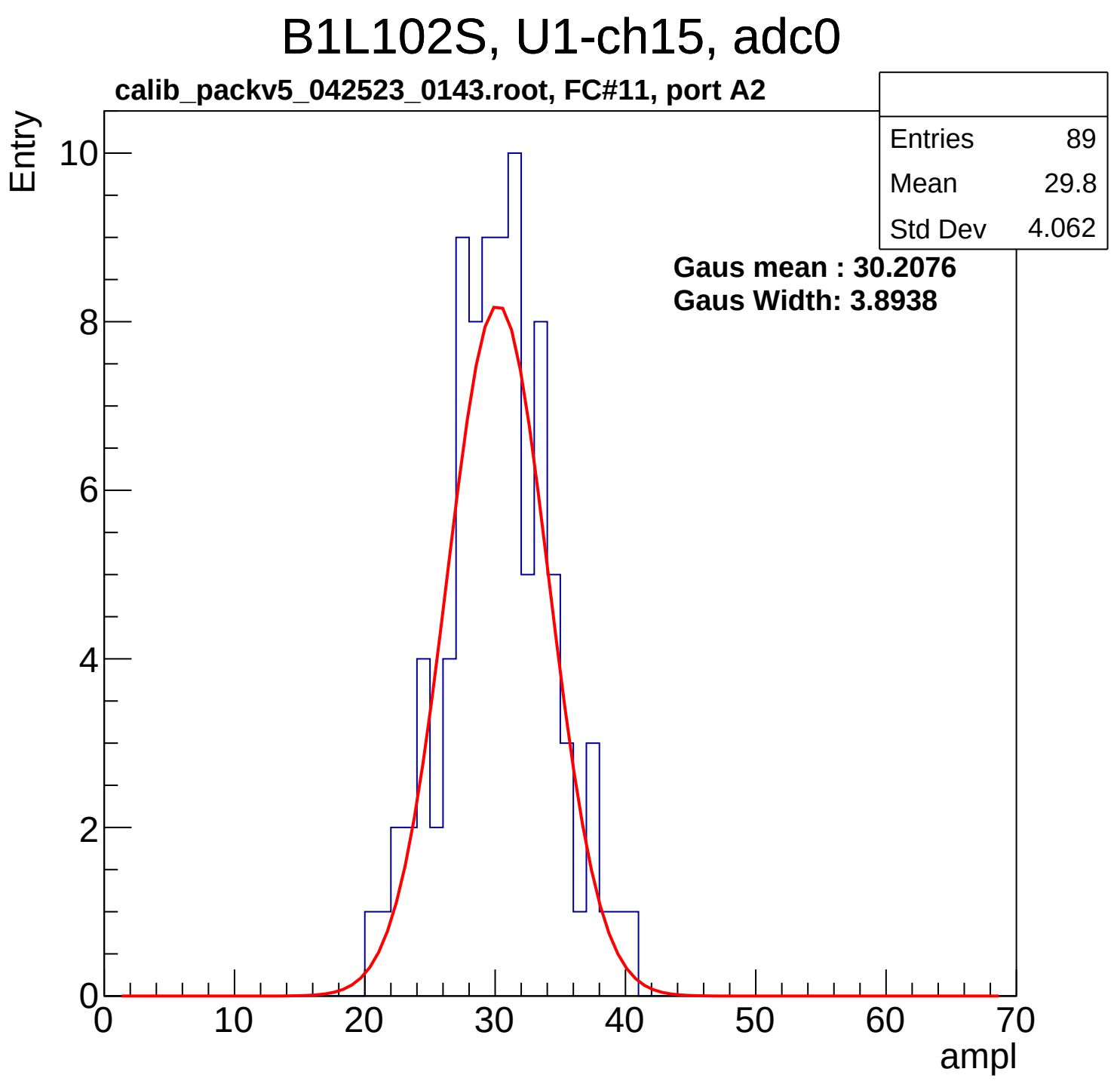
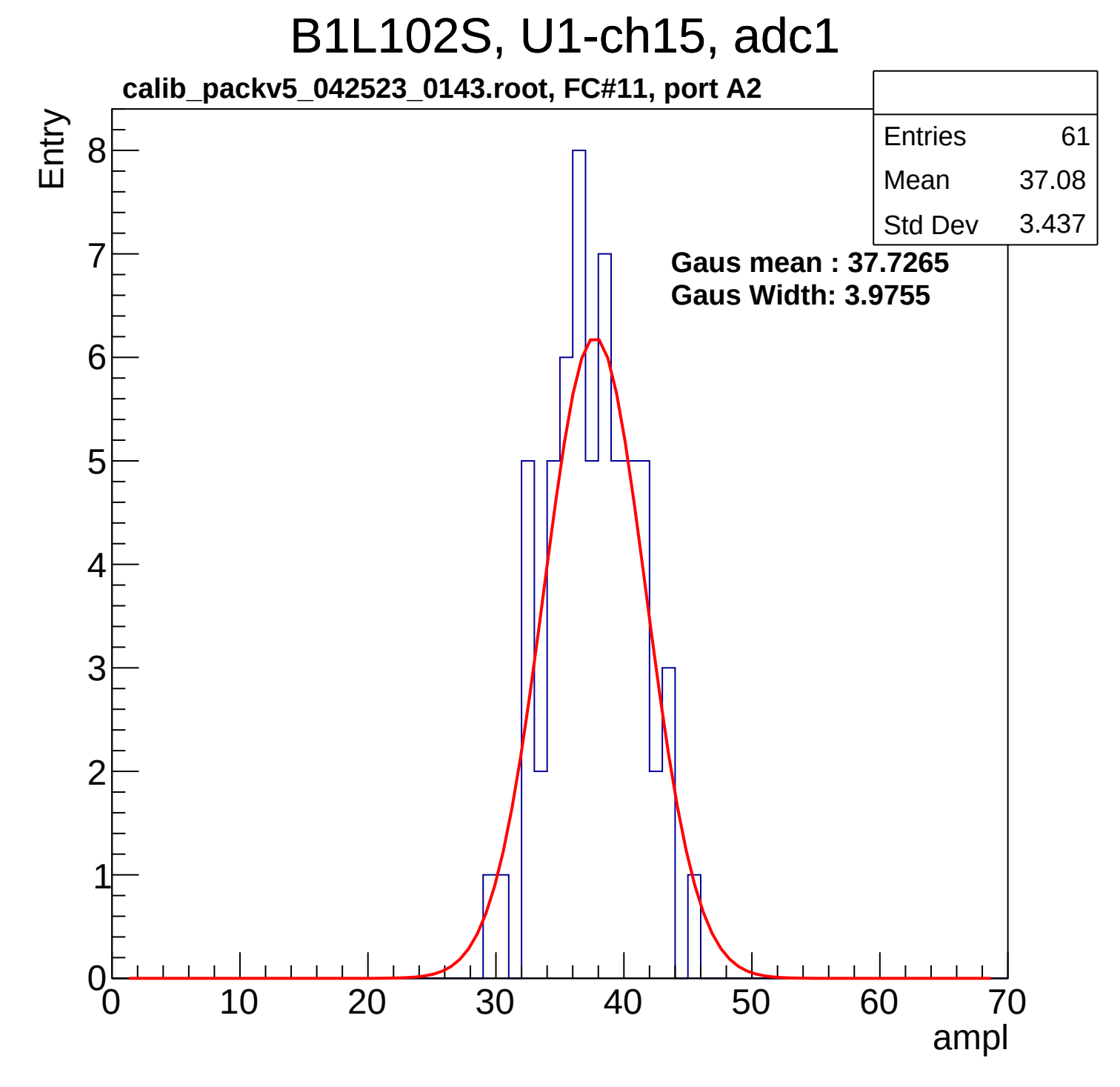
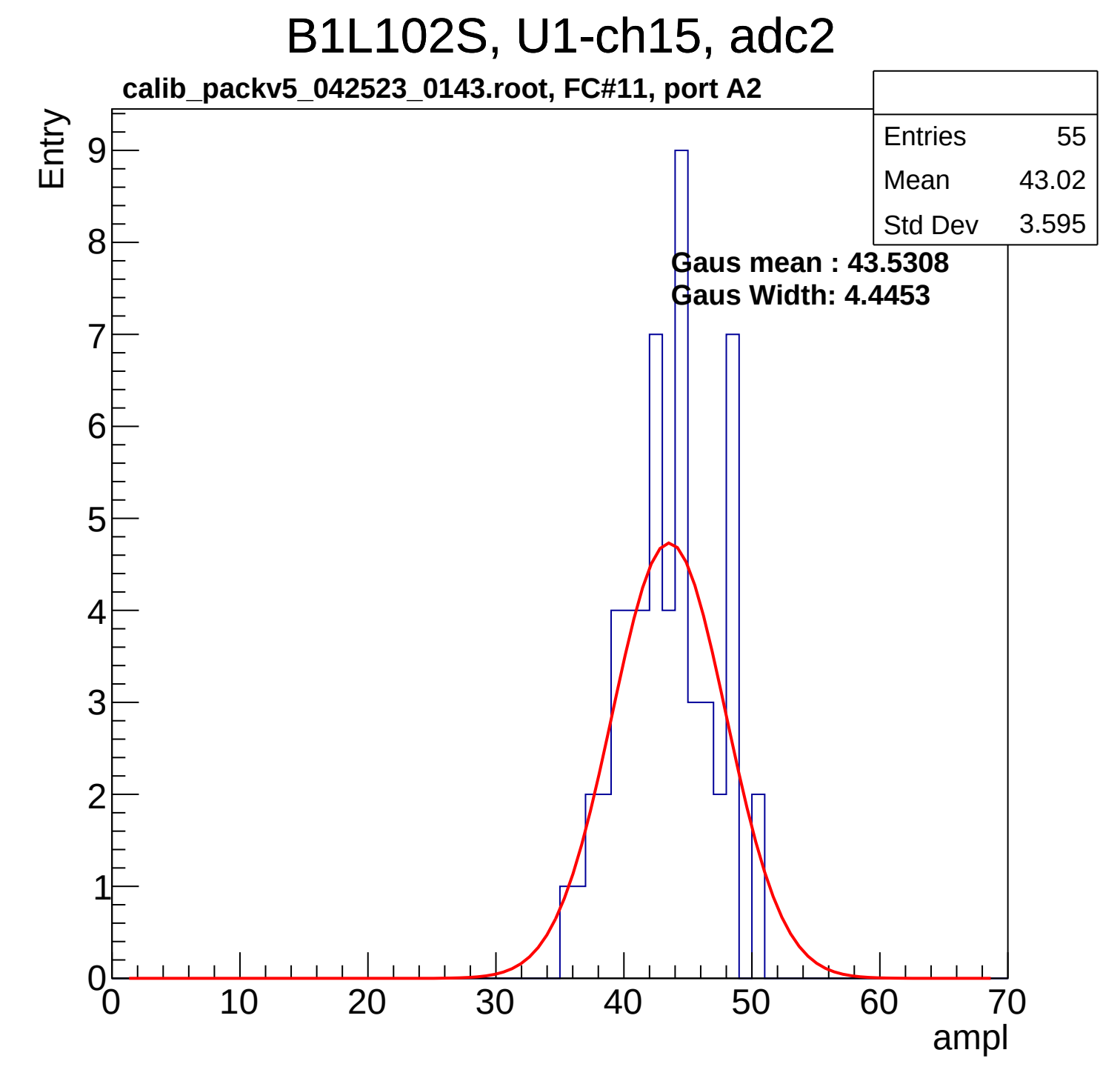
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

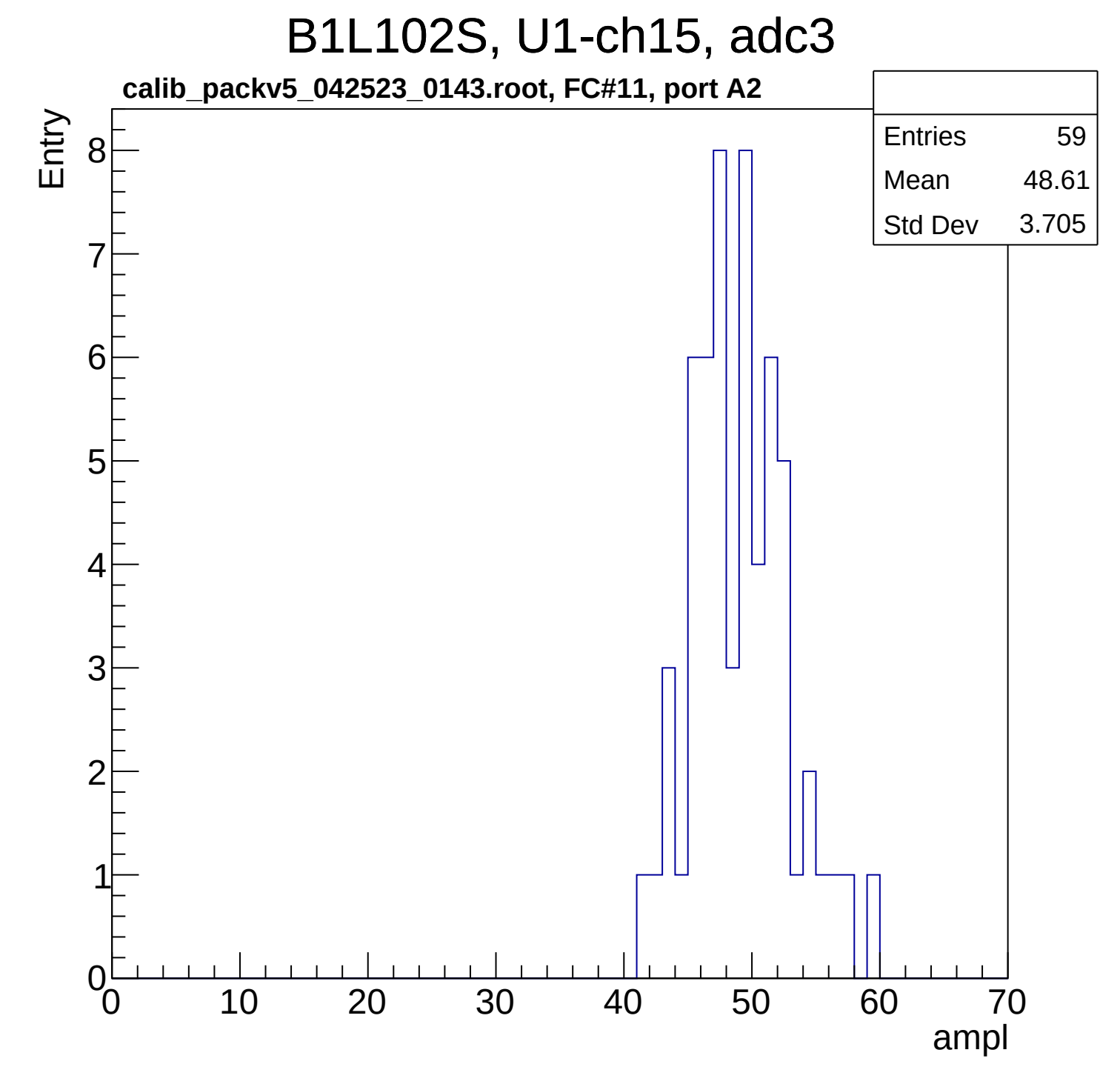

B1L102S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 23
Mean 58.52
Std Dev 12.59
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

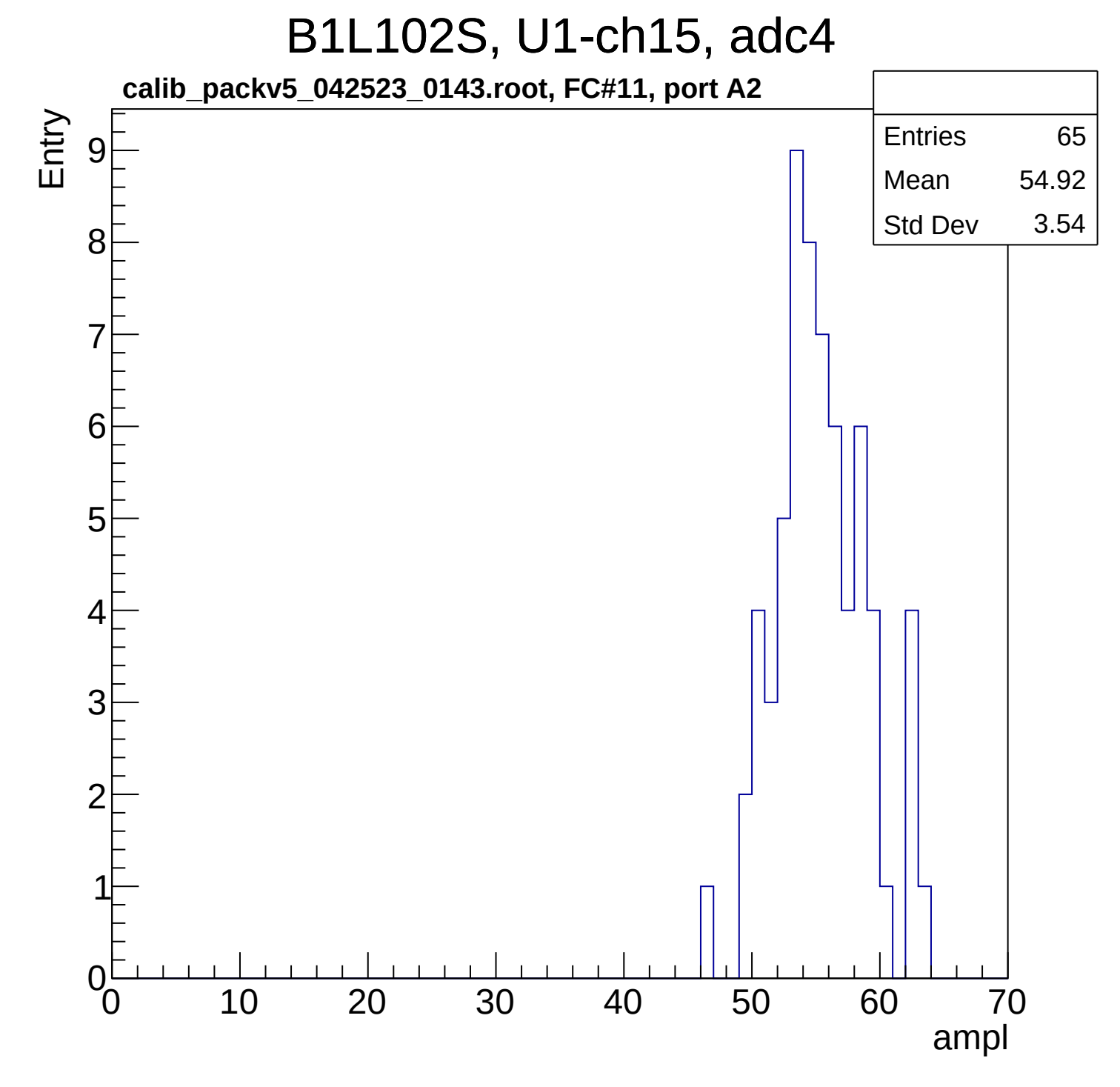

B1L102S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

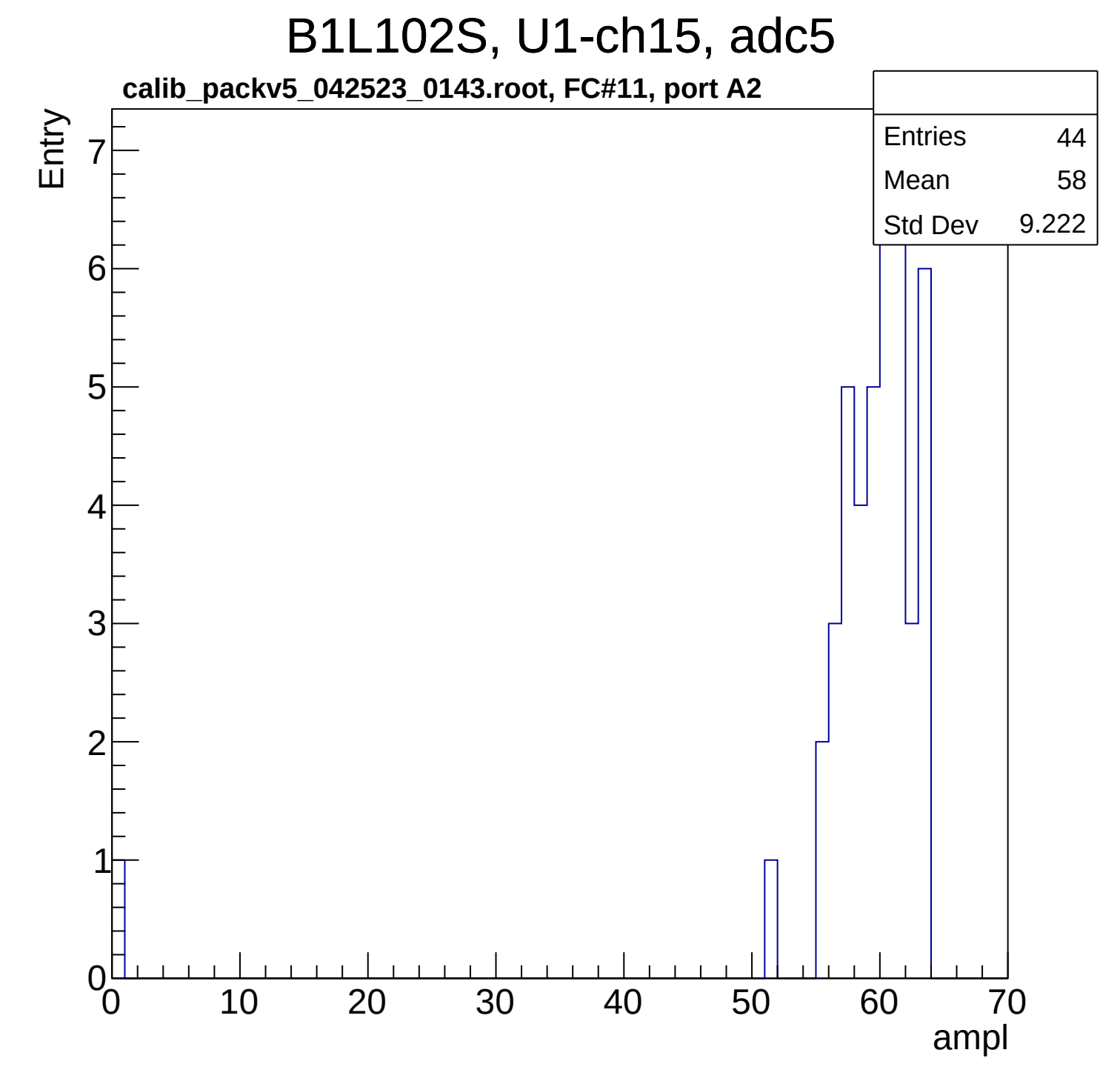

B1L102S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 29.8
Std Dev 4.062
Gaus mean : 30.2076
Gaus Width: 3.8938
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

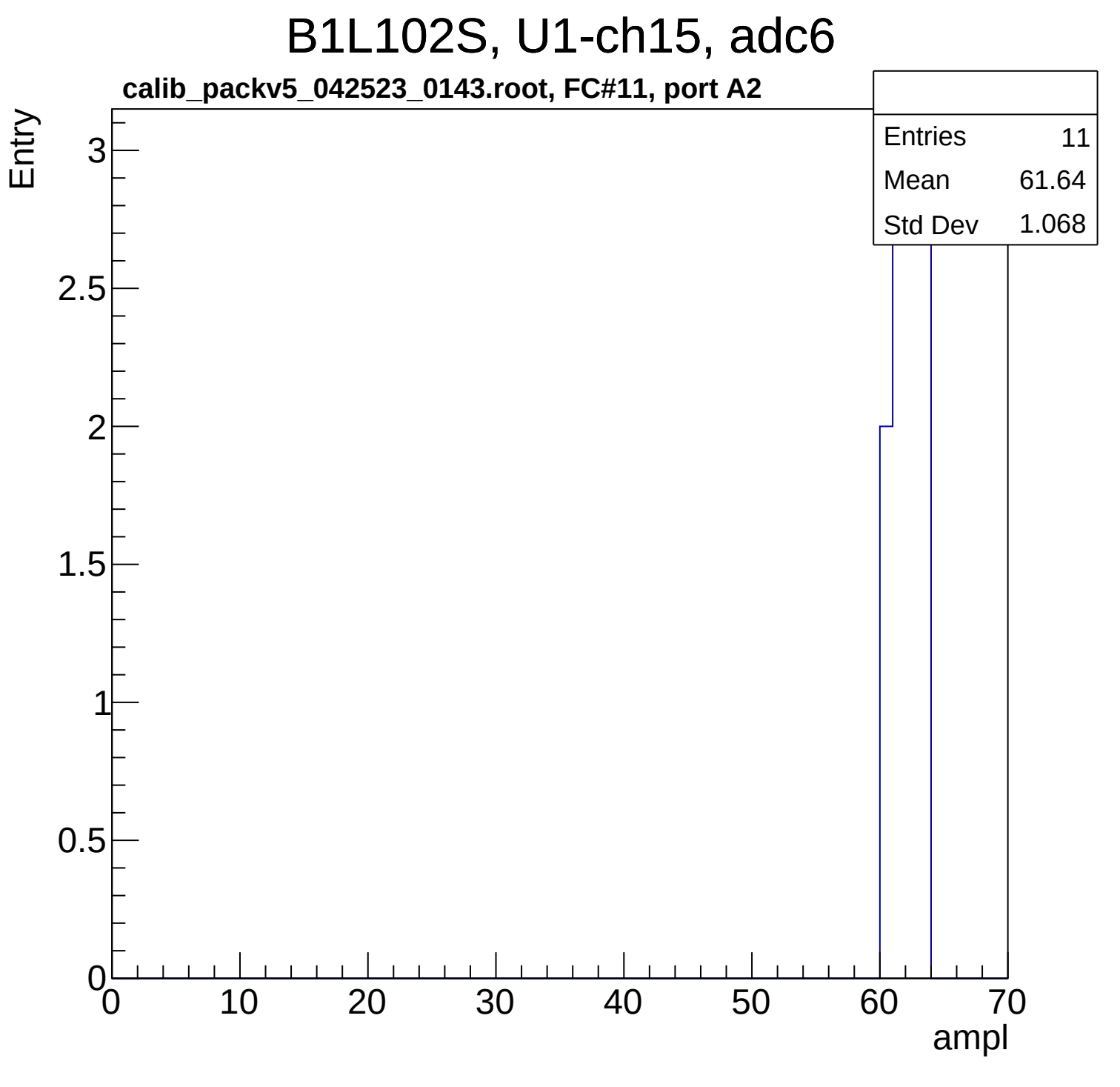

B1L102S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 37.08
Std Dev 3.437
Gaus mean : 37.7265
Gaus Width: 3.9755
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

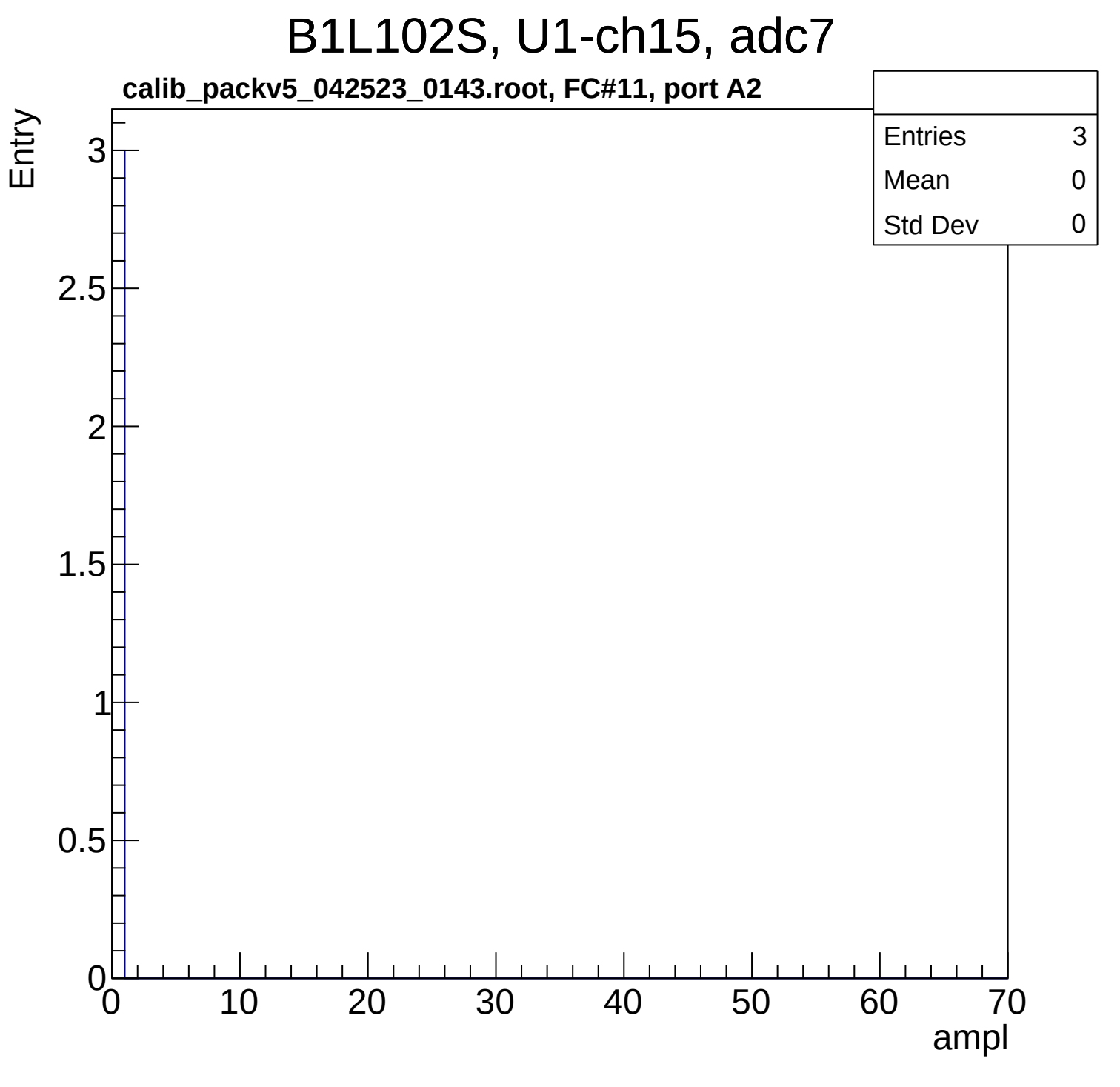

B1L102S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 43.02
Std Dev 3.595
Gaus mean : 43.5308
Gaus Width: 4.4453
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

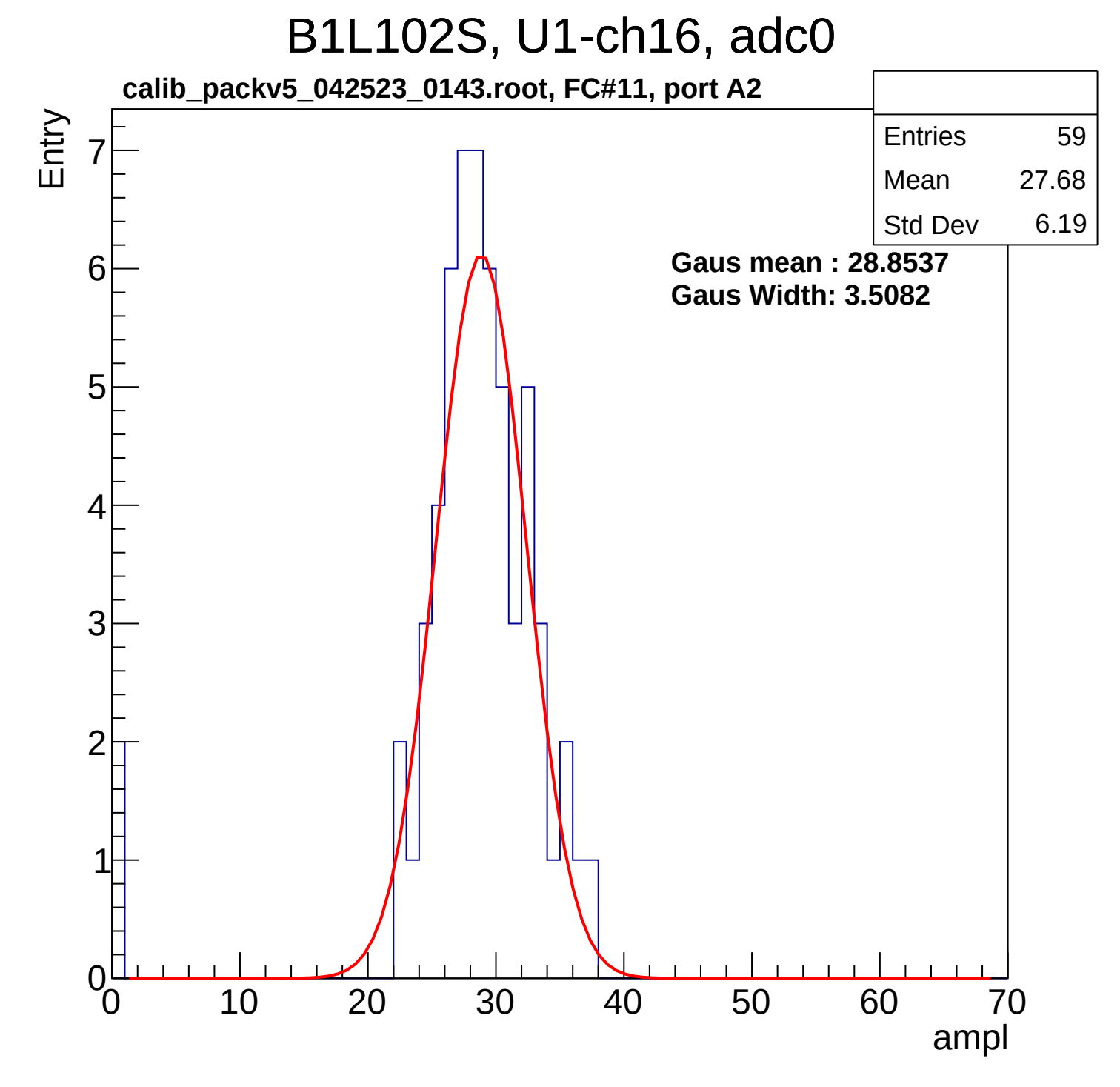

B1L102S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.61
Std Dev 3.705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

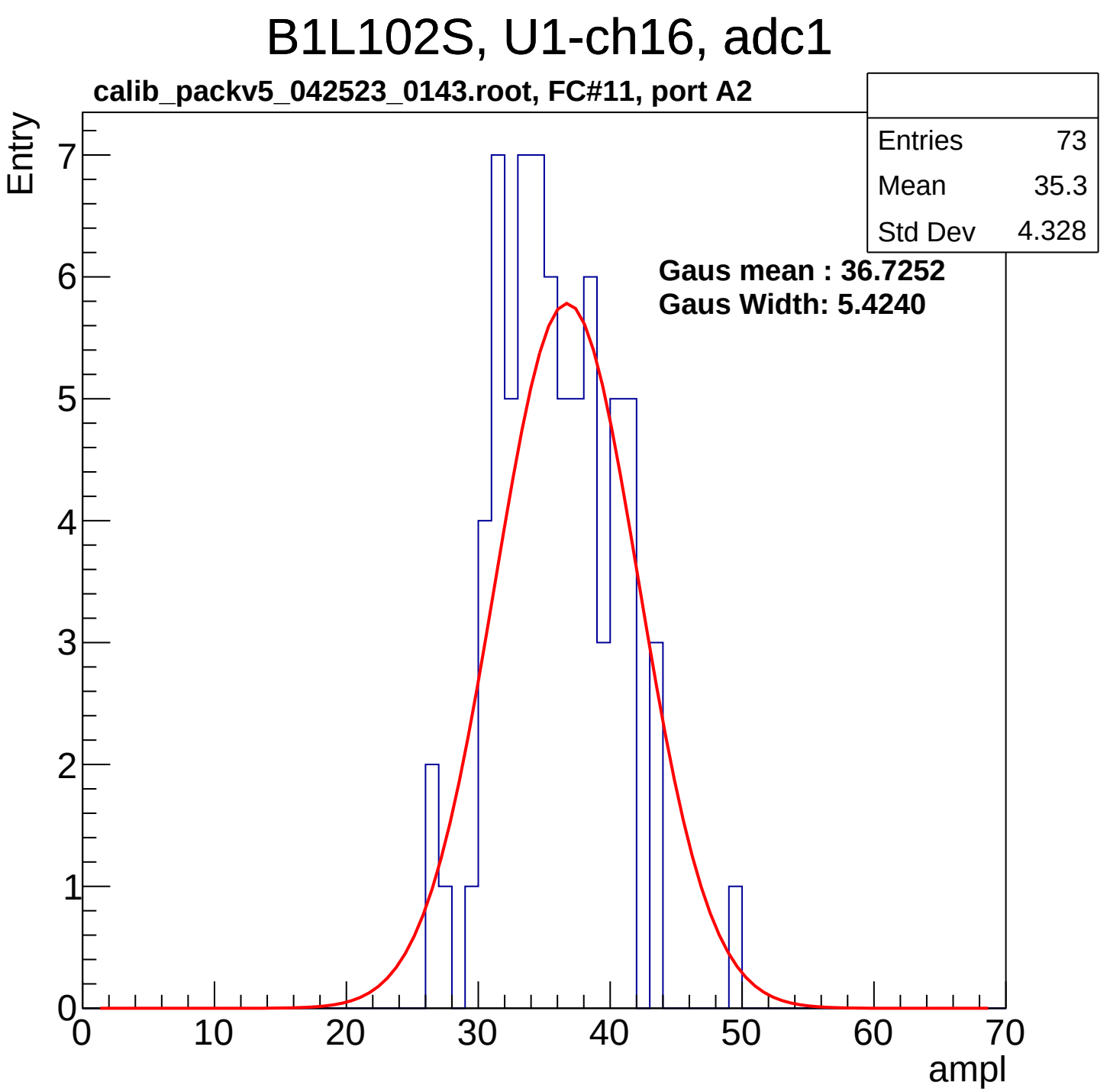

B1L102S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 54.92
Std Dev 3.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

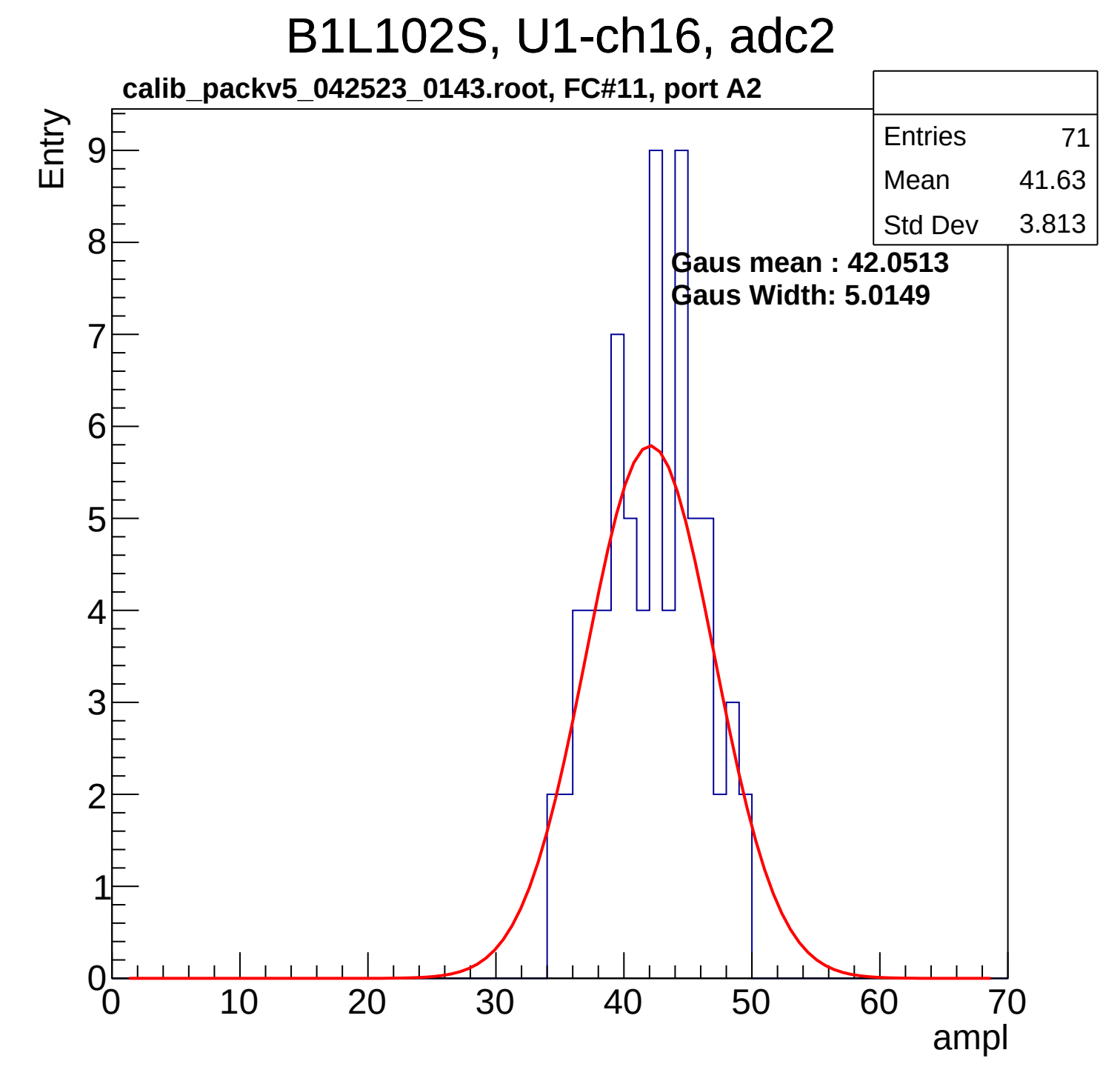

B1L102S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 58
Std Dev 9.222
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

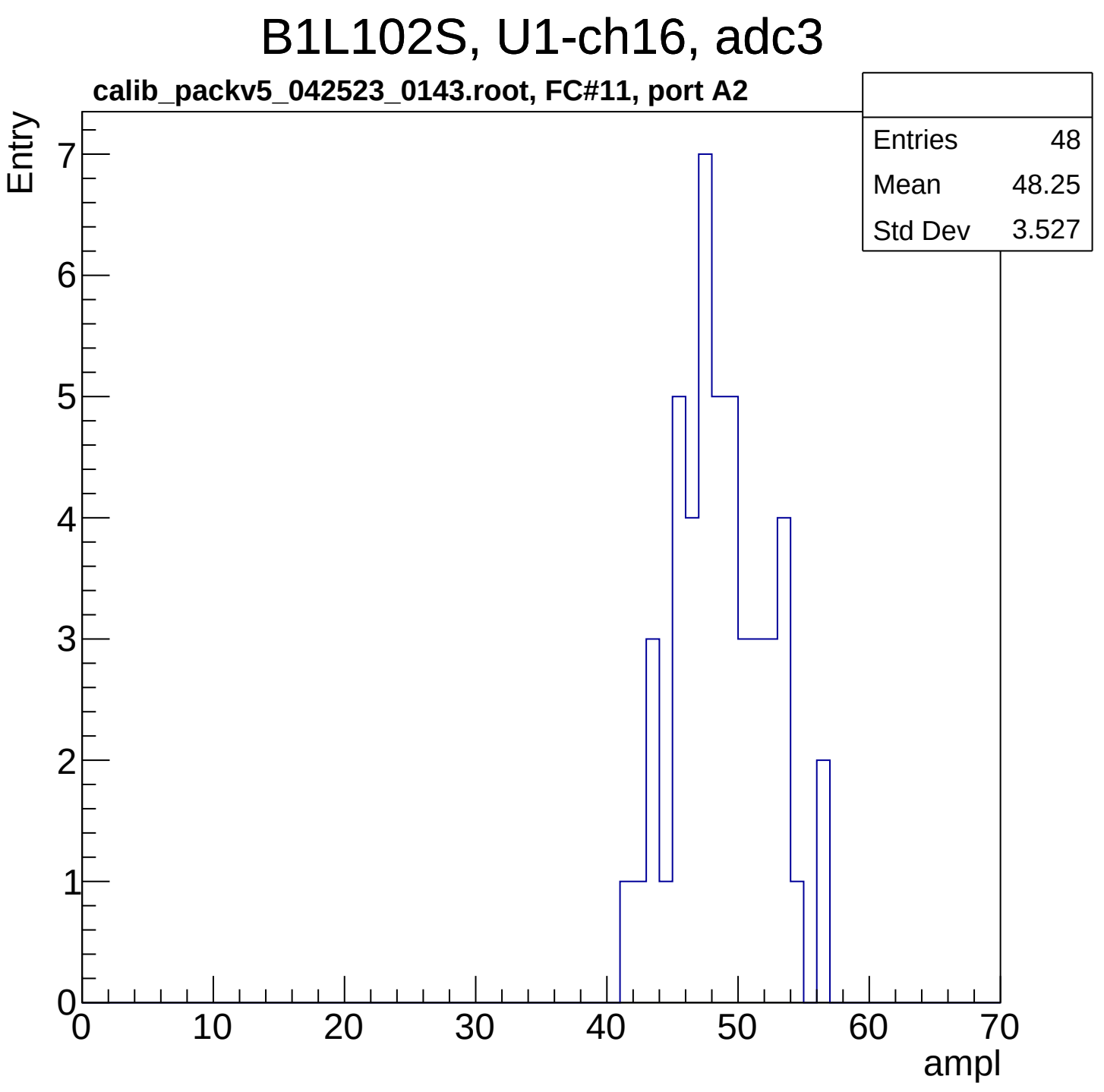

B1L102S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.64
Std Dev 1.068
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

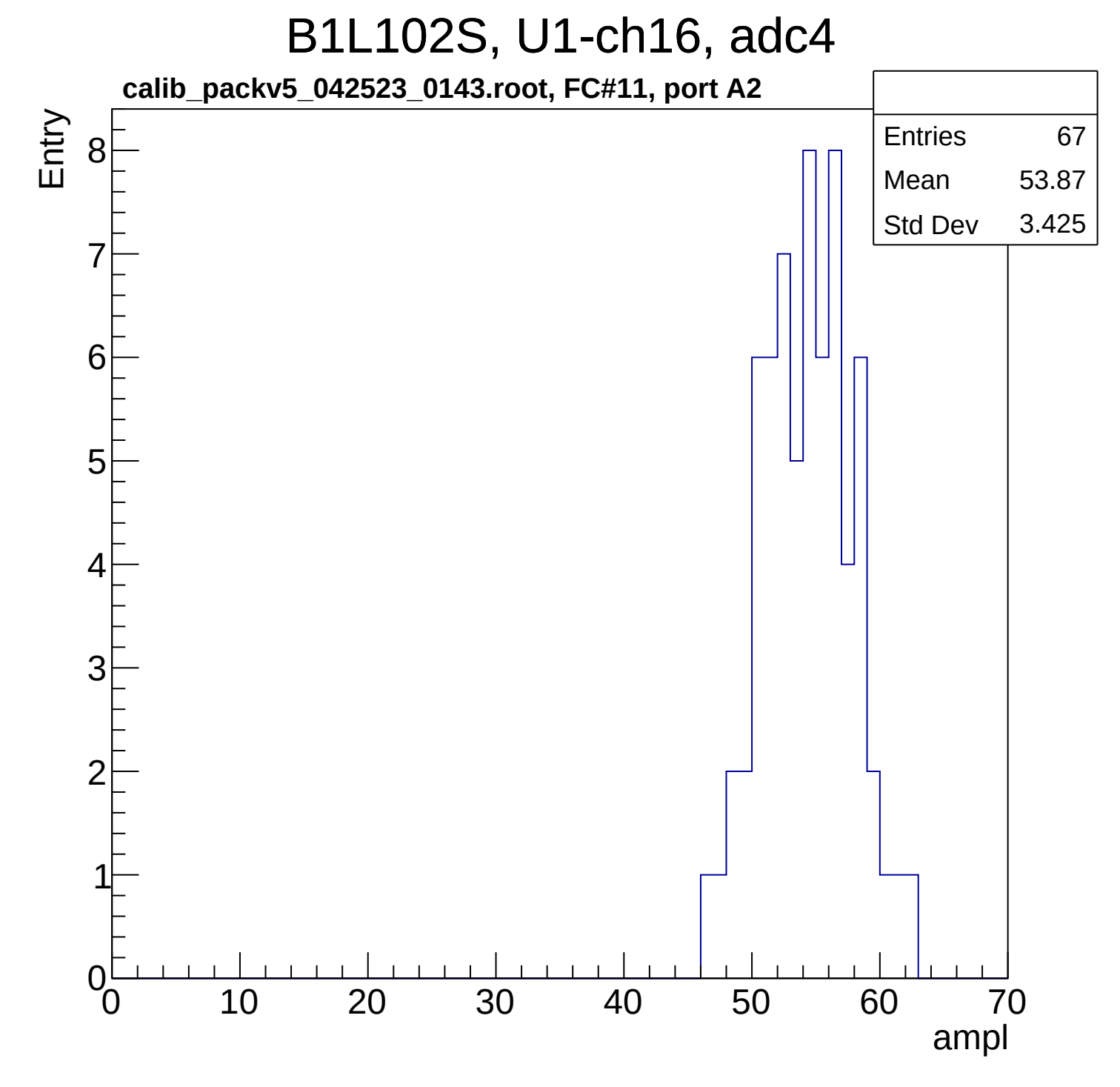

B1L102S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

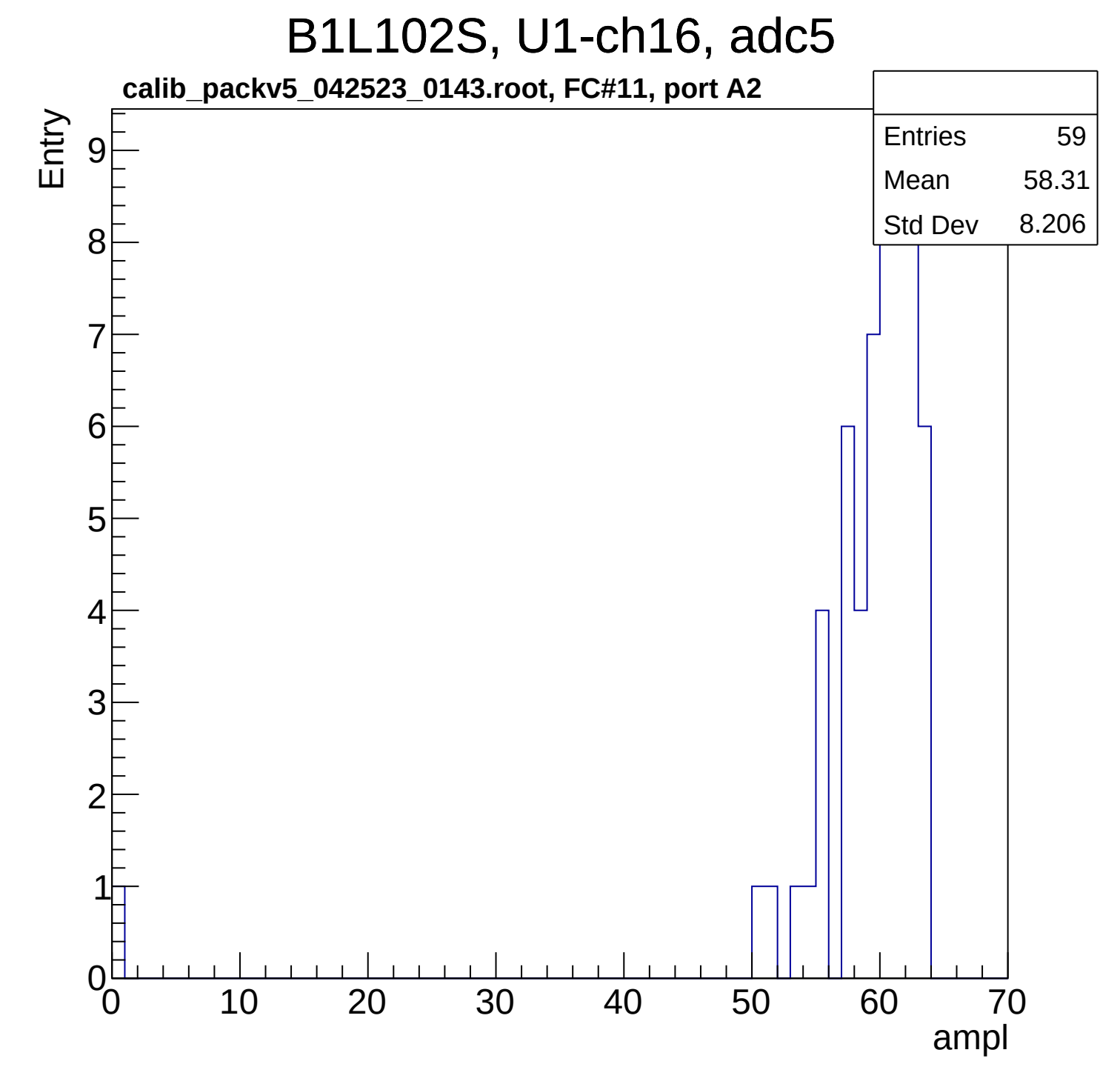

B1L102S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 27.68
Std Dev 6.19
Gaus mean : 28.8537
Gaus Width: 3.5082
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 35.3
Std Dev 4.328
Gaus mean : 36.7252
Gaus Width: 5.4240
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 41.63
Std Dev 3.813
Gaus mean : 42.0513
Gaus Width: 5.0149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 48.25
Std Dev 3.527
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 53.87
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 58.31
Std Dev 8.206
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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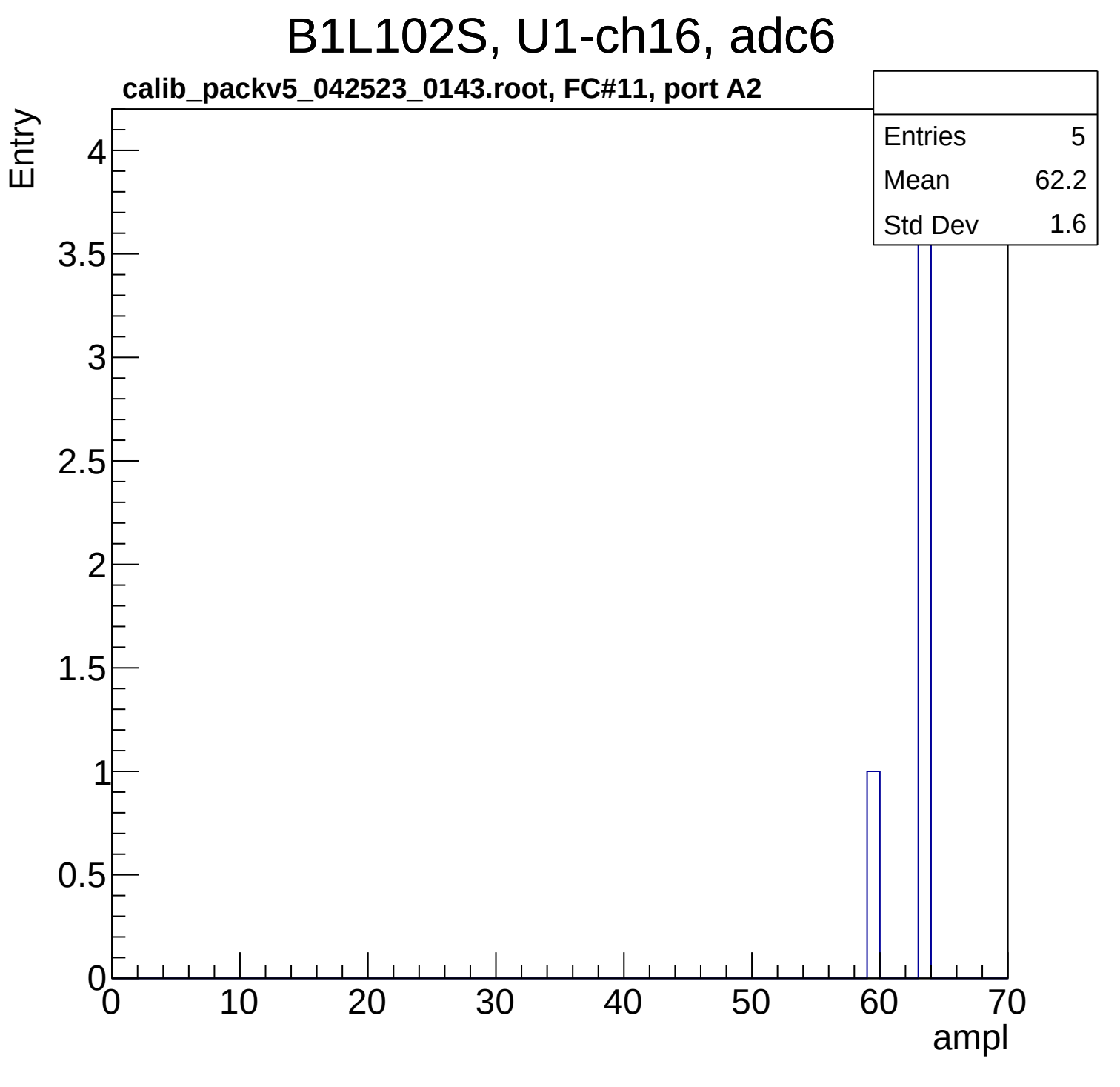
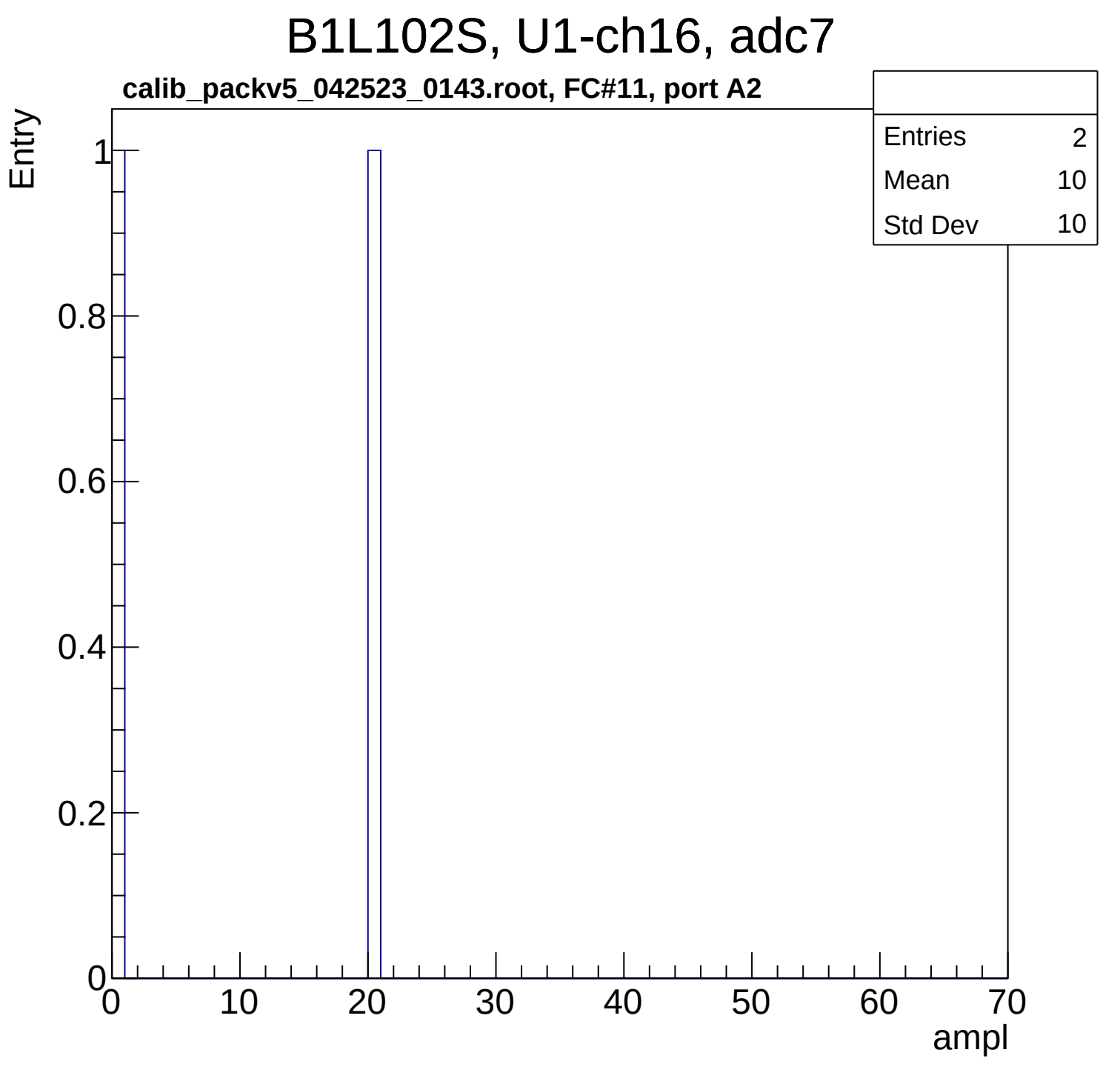
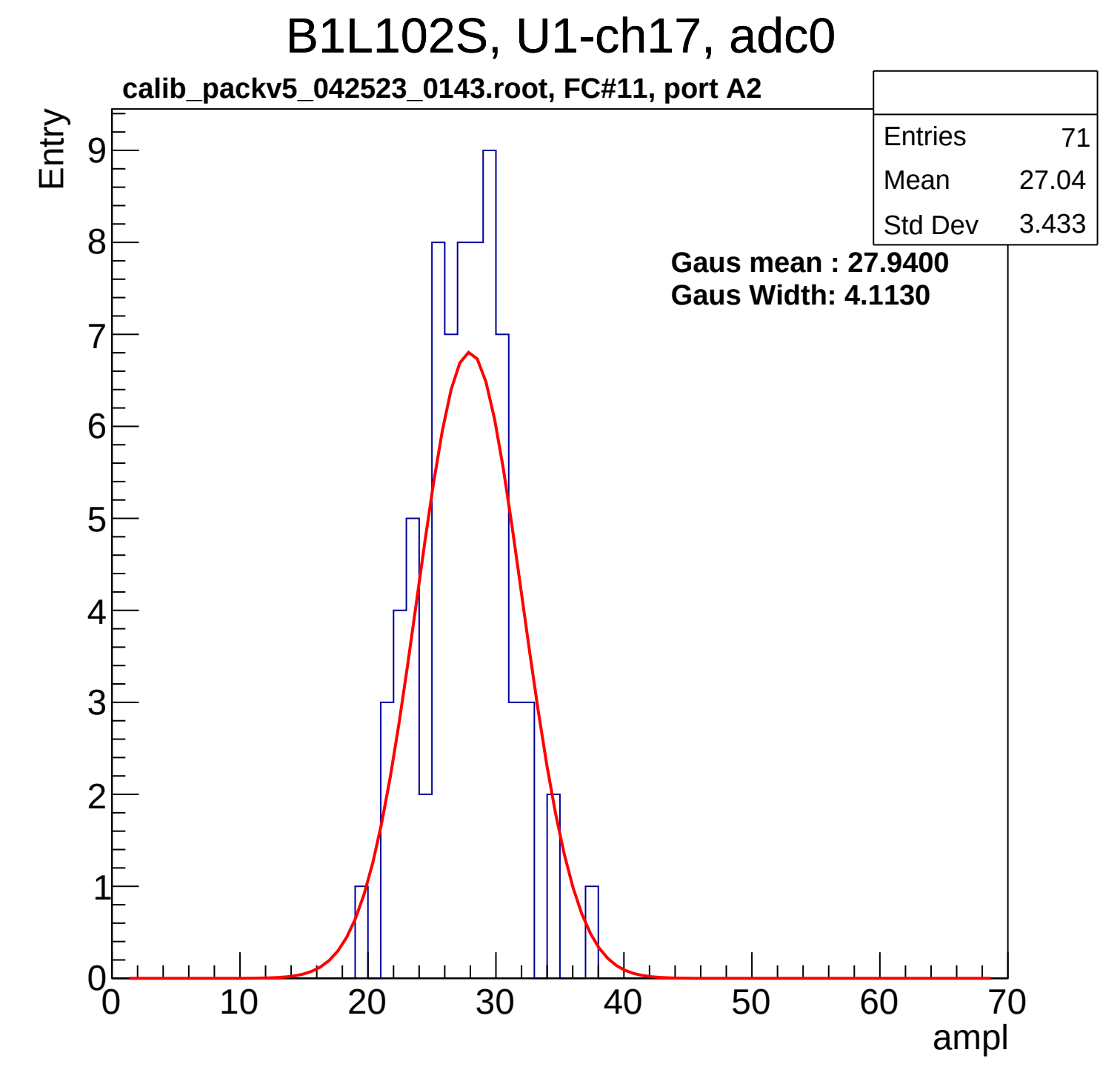
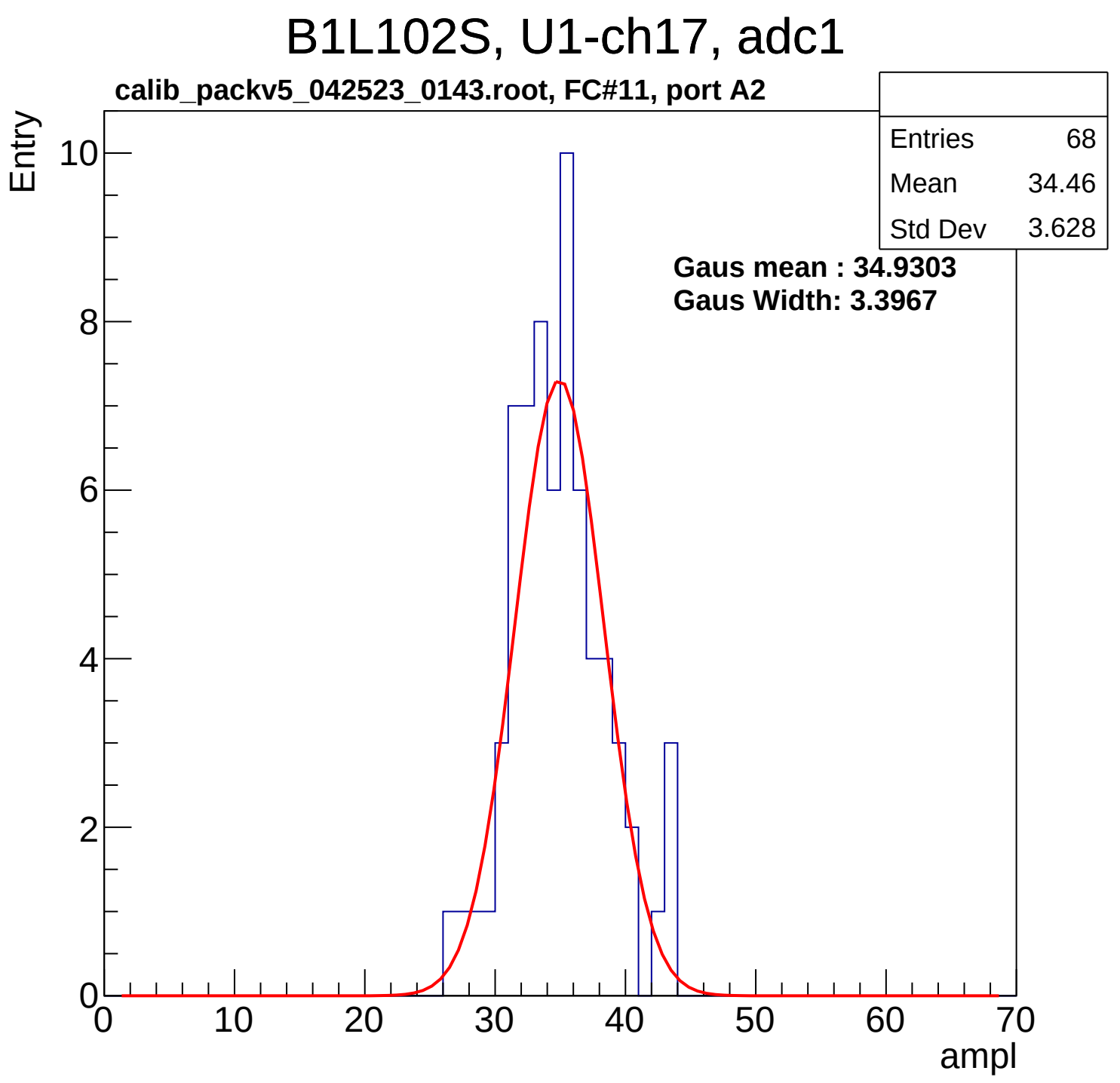
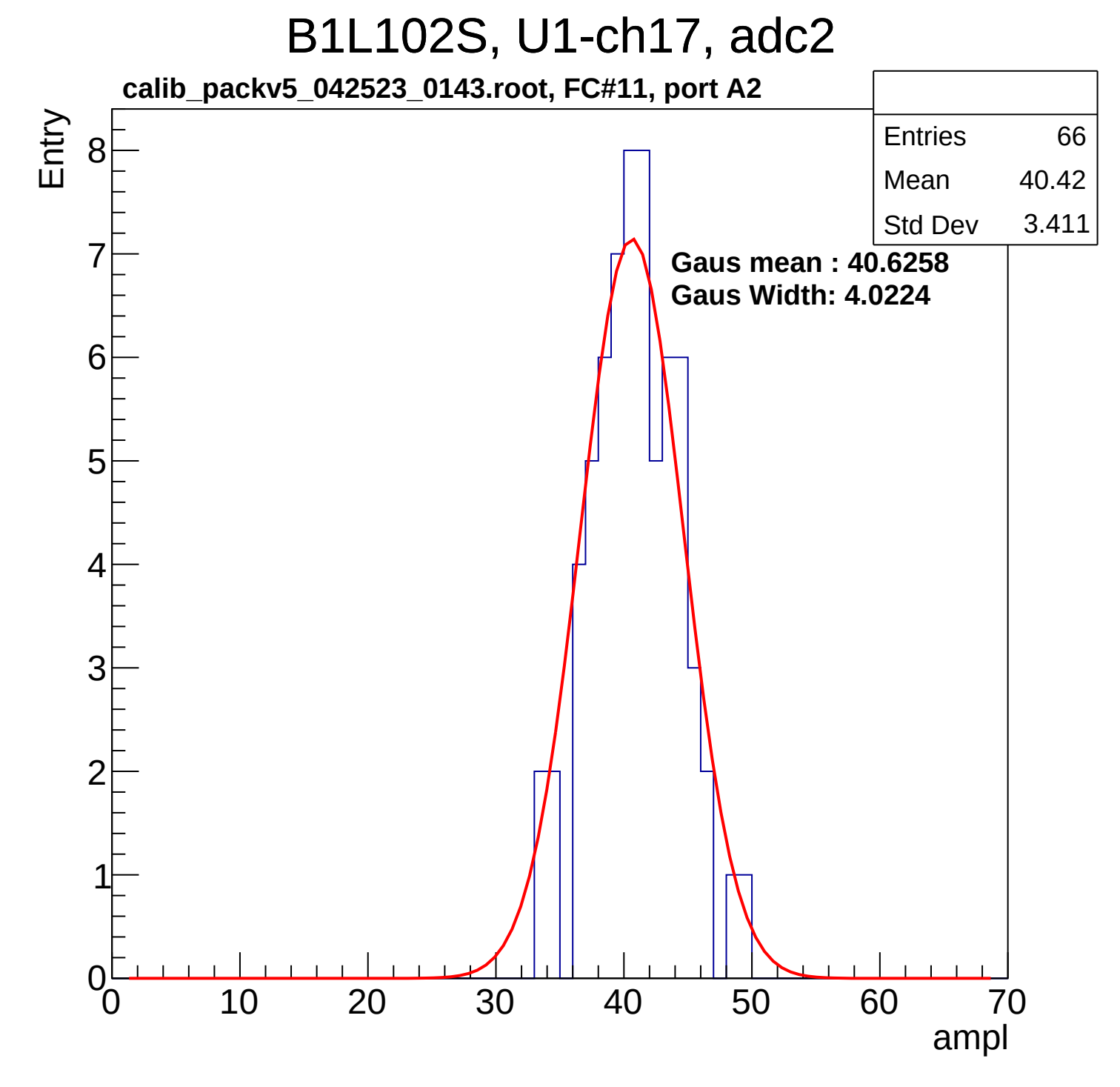
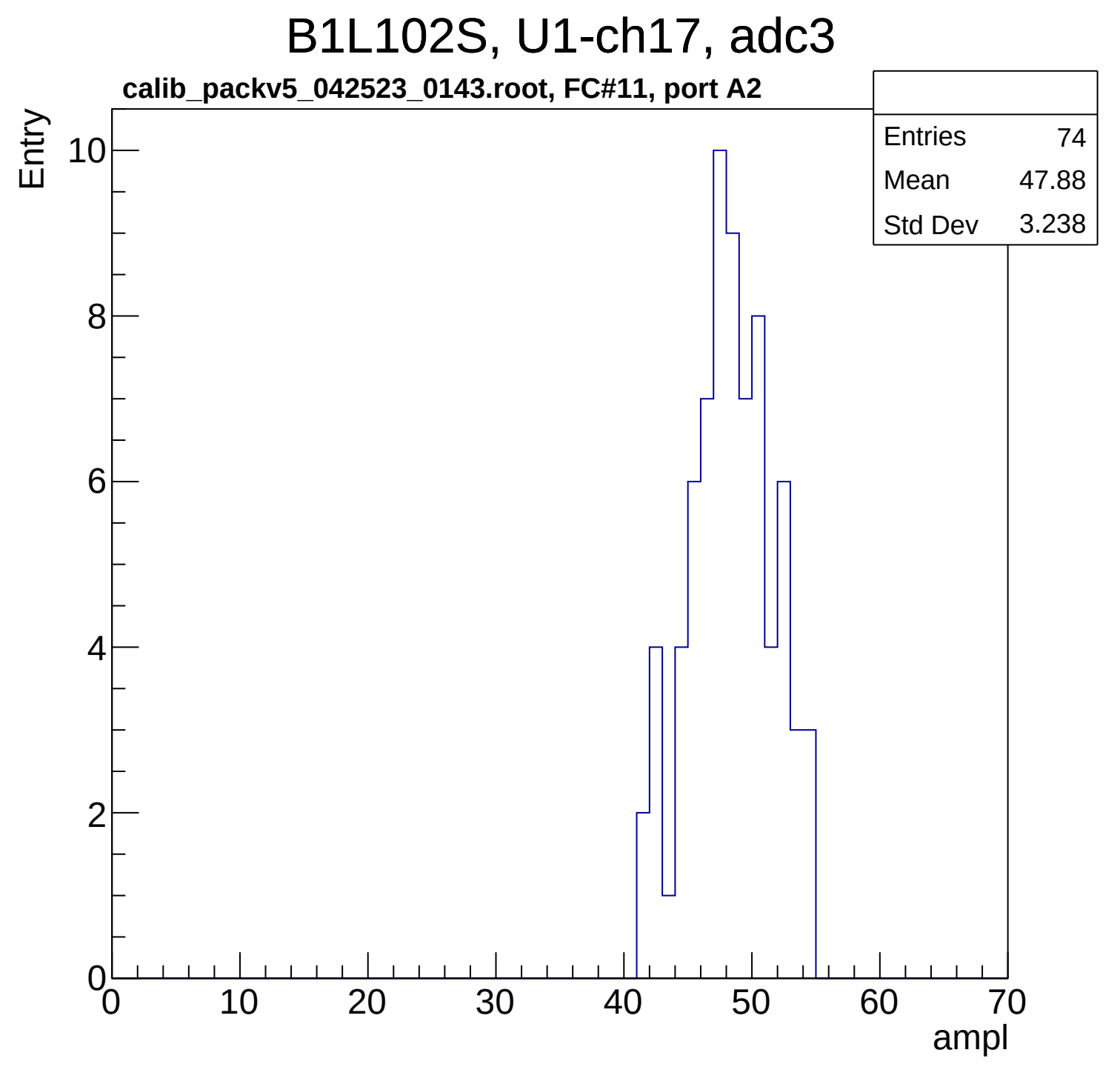

B1L102S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 62.2
Std Dev 1.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

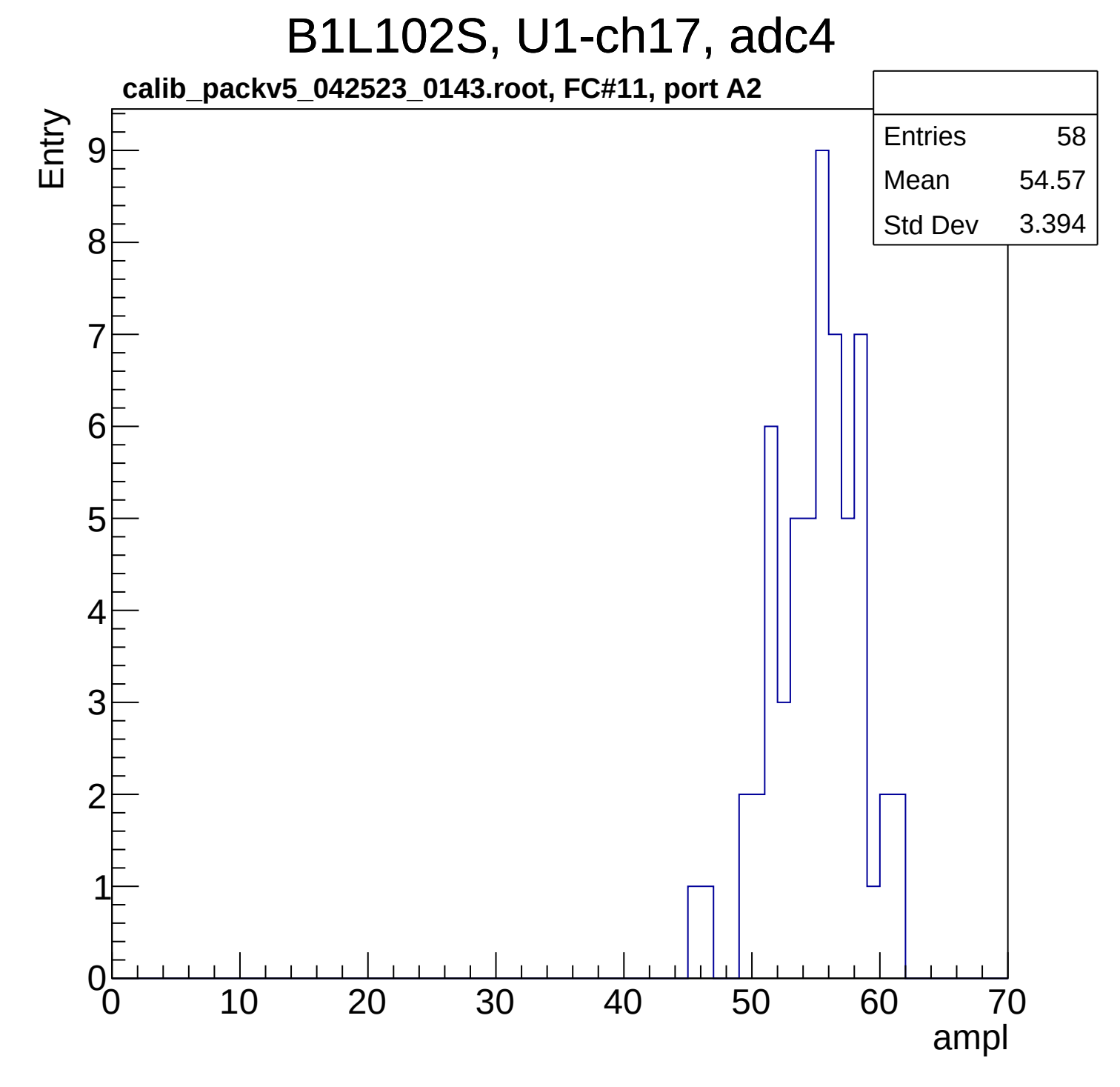

B1L102S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.04
Std Dev 3.433
Gaus mean : 27.9400
Gaus Width: 4.1130
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 34.46
Std Dev 3.628
Gaus mean : 34.9303
Gaus Width: 3.3967
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 40.42
Std Dev 3.411
Gaus mean : 40.6258
Gaus Width: 4.0224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 47.88
Std Dev 3.238
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 54.57
Std Dev 3.394
Entry
ampl
0
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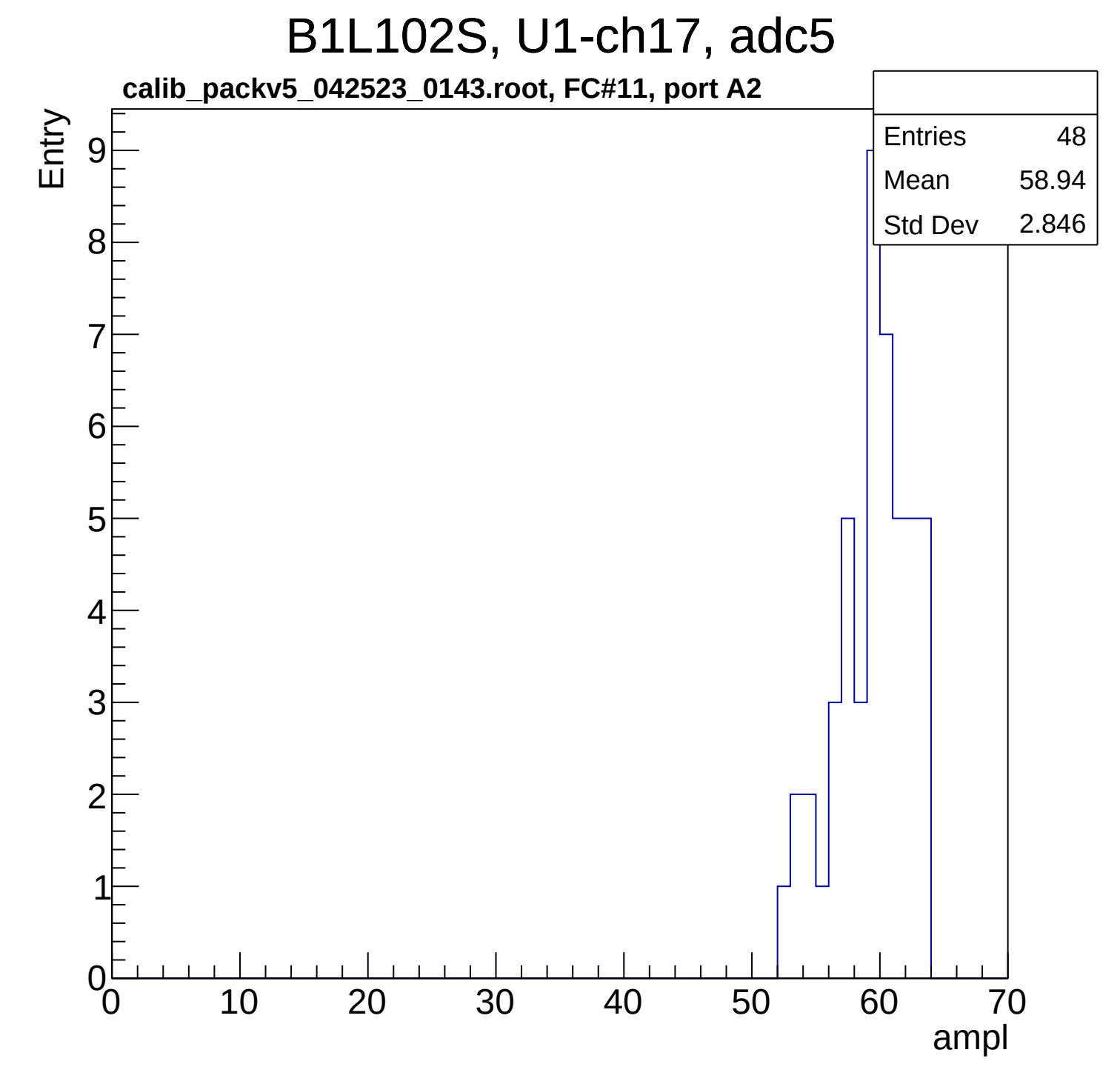
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 58.94
Std Dev 2.846
Entry
ampl
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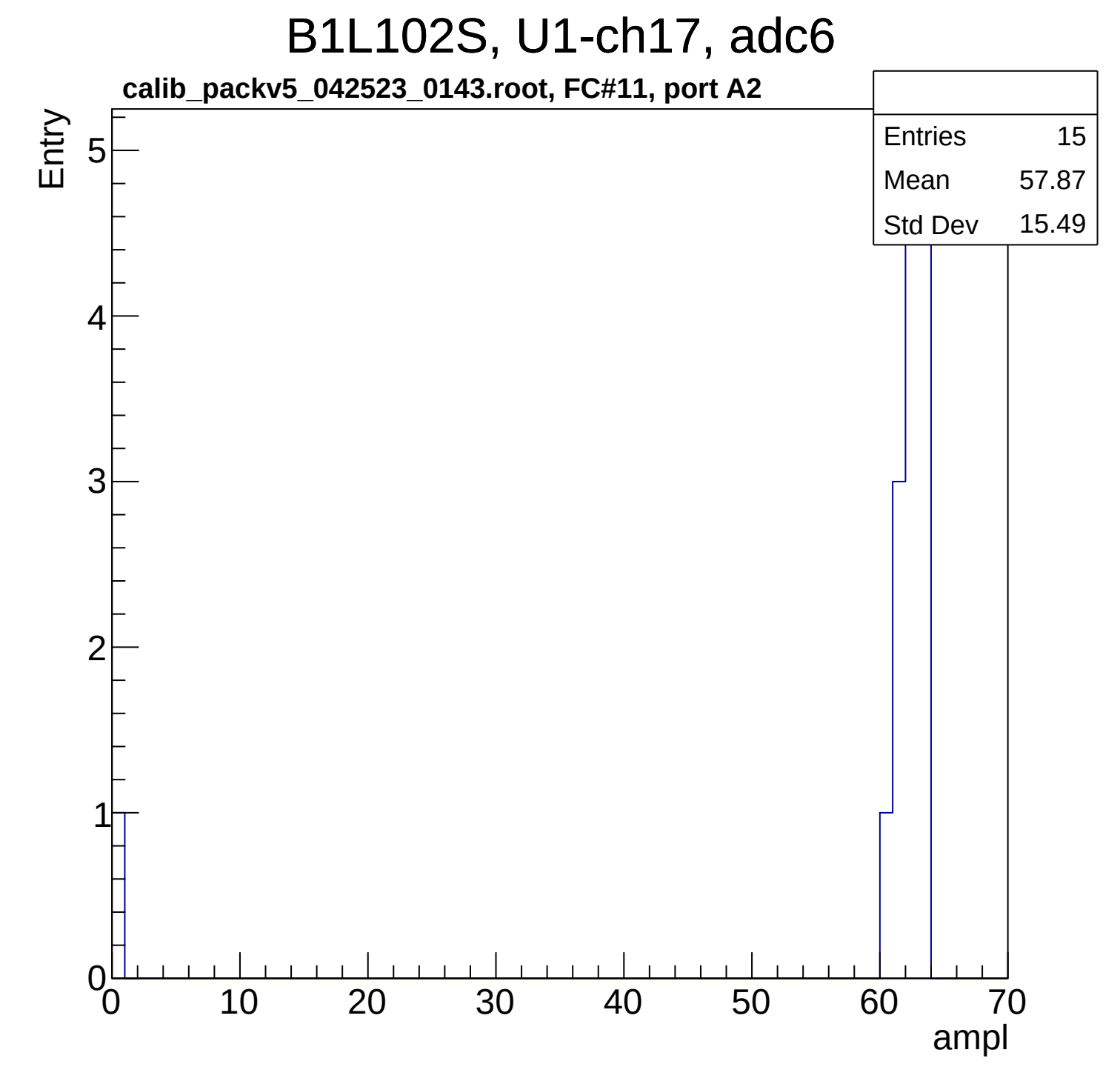
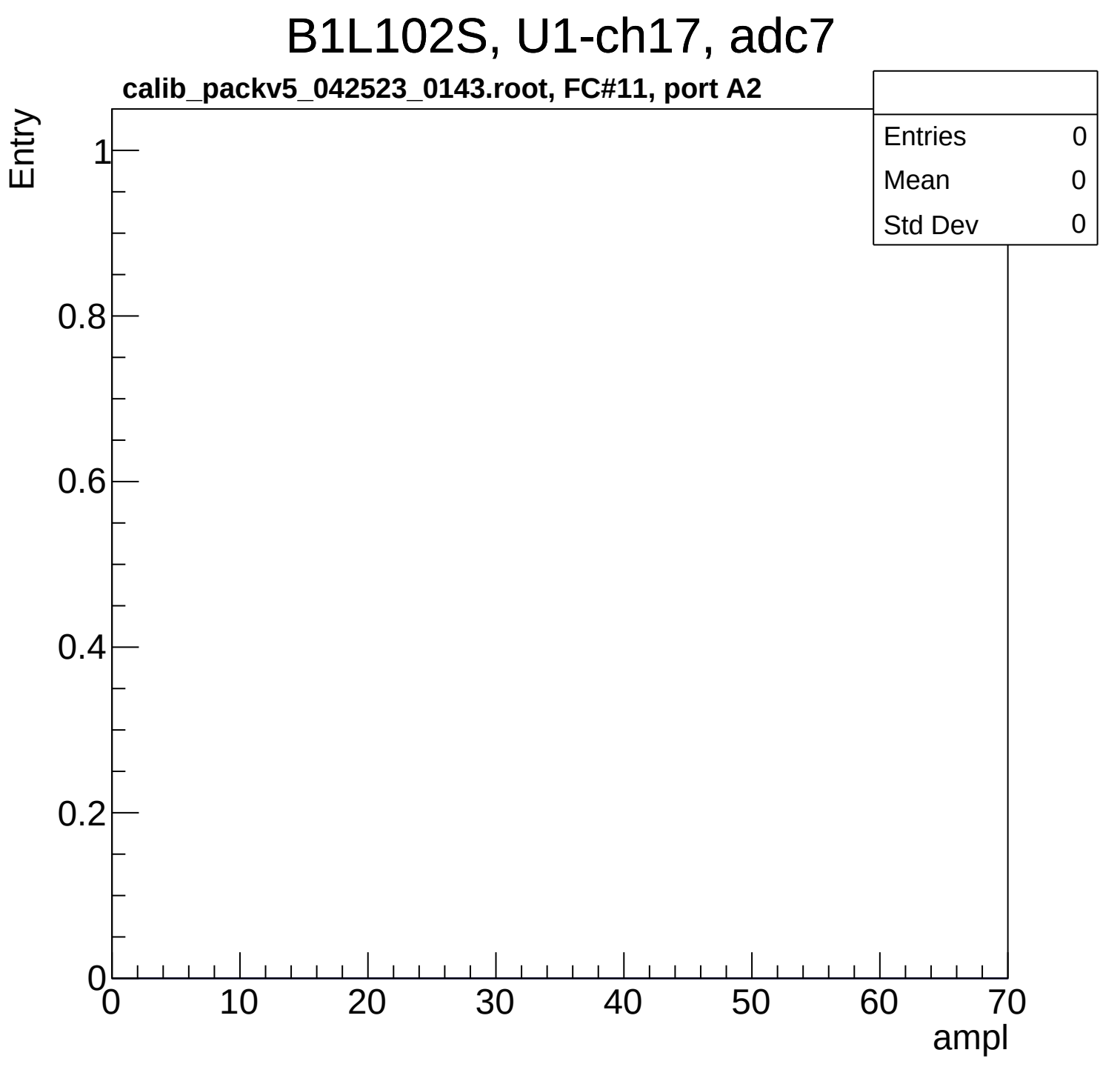
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 57.87
Std Dev 15.49
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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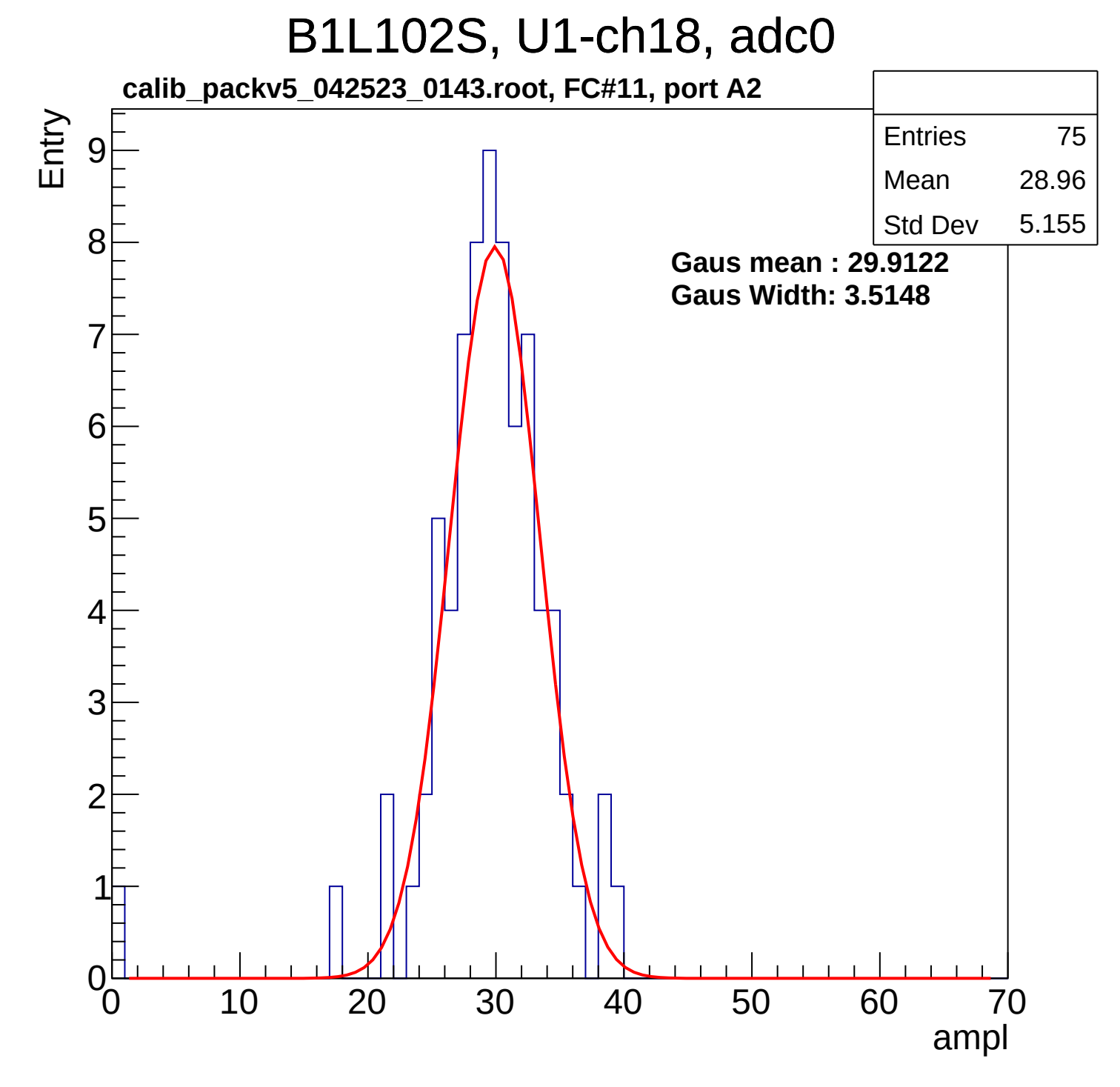
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 28.96
Std Dev 5.155
Gaus mean : 29.9122
Gaus Width: 3.5148
Entry
ampl
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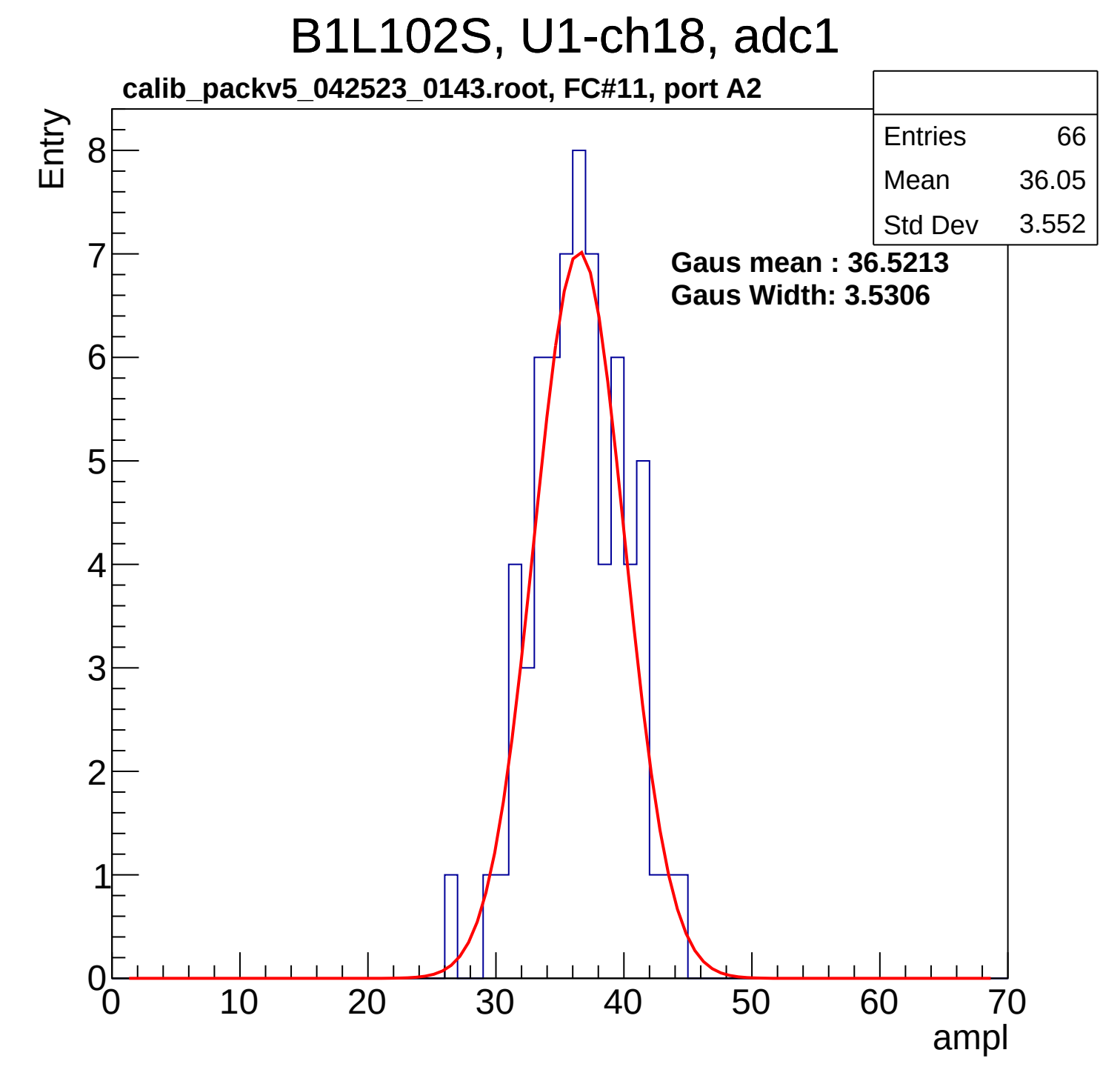
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 36.05
Std Dev 3.552
Gaus mean : 36.5213
Gaus Width: 3.5306
Entry
ampl
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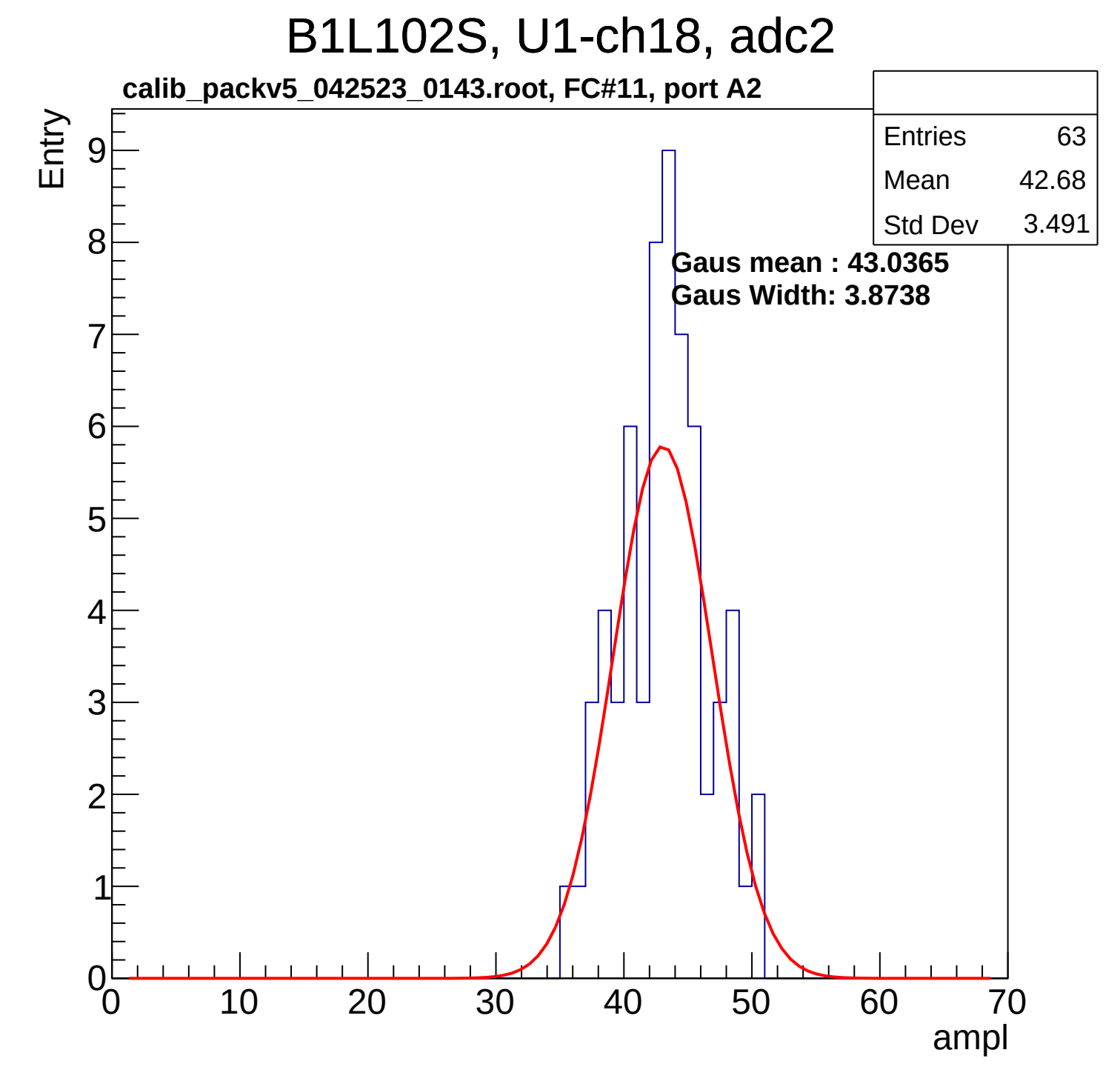
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 42.68
Std Dev 3.491
Gaus mean : 43.0365
Gaus Width: 3.8738
Entry
ampl
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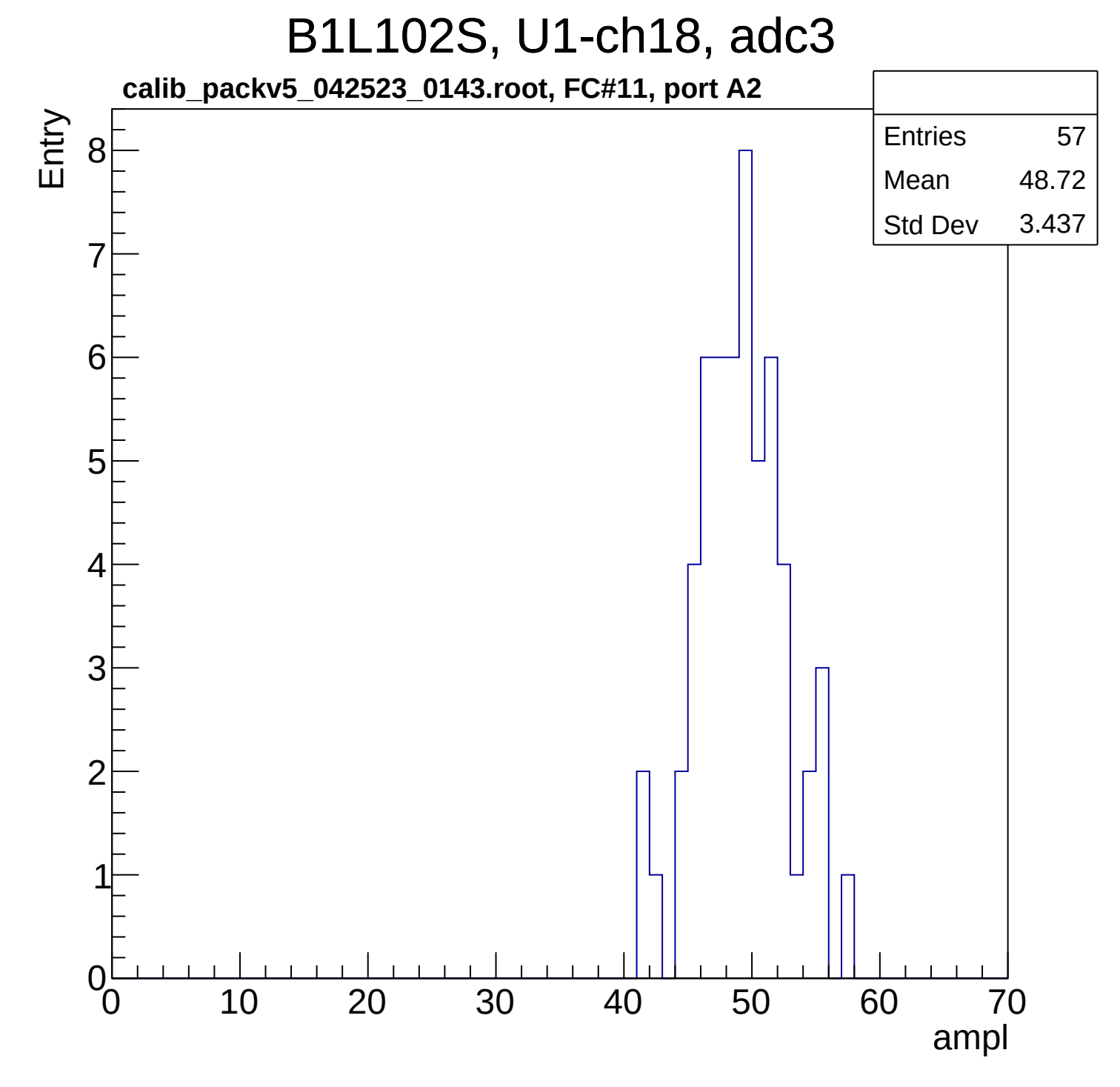
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 48.72
Std Dev 3.437
Entry
ampl
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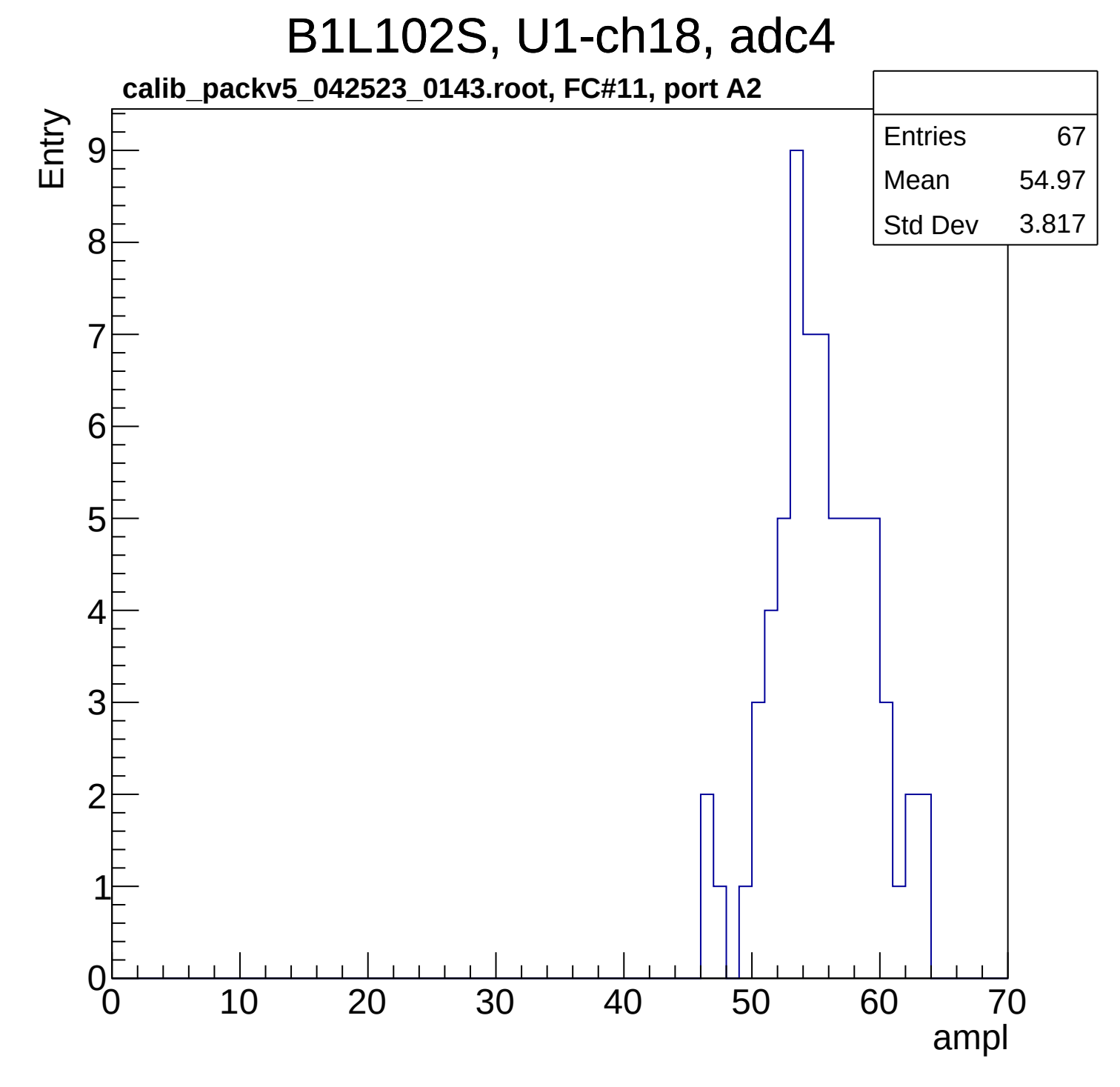
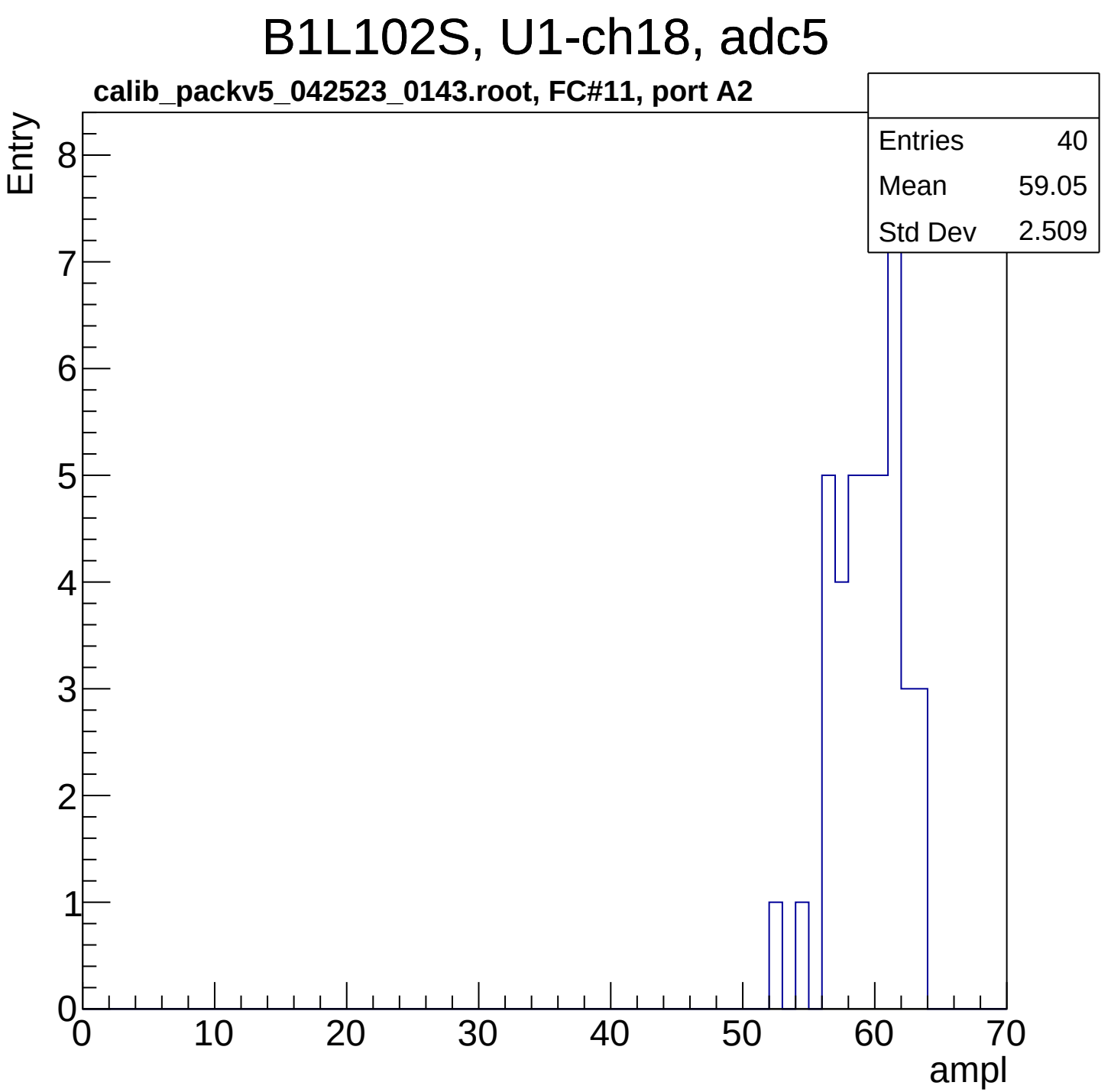
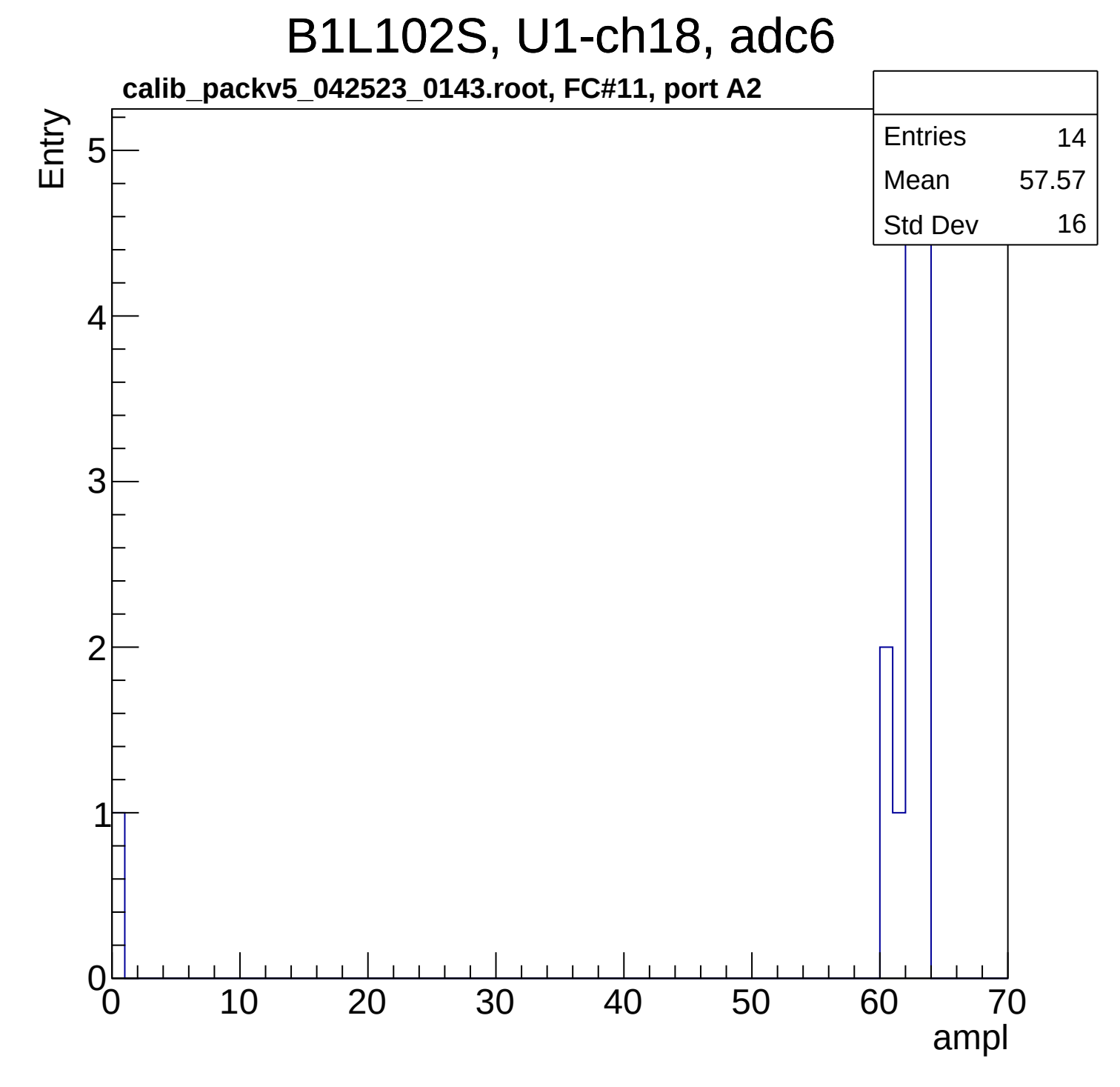
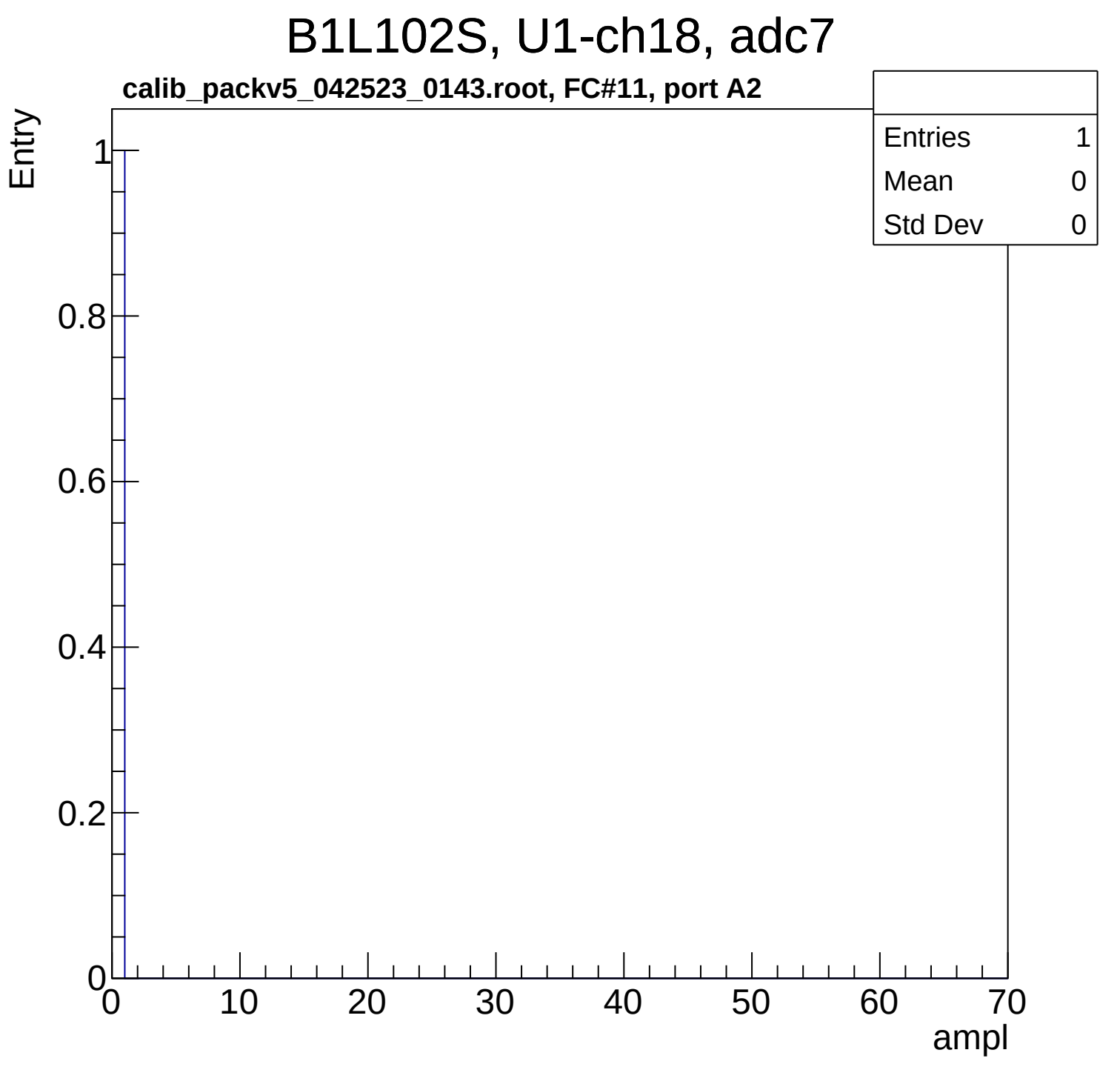
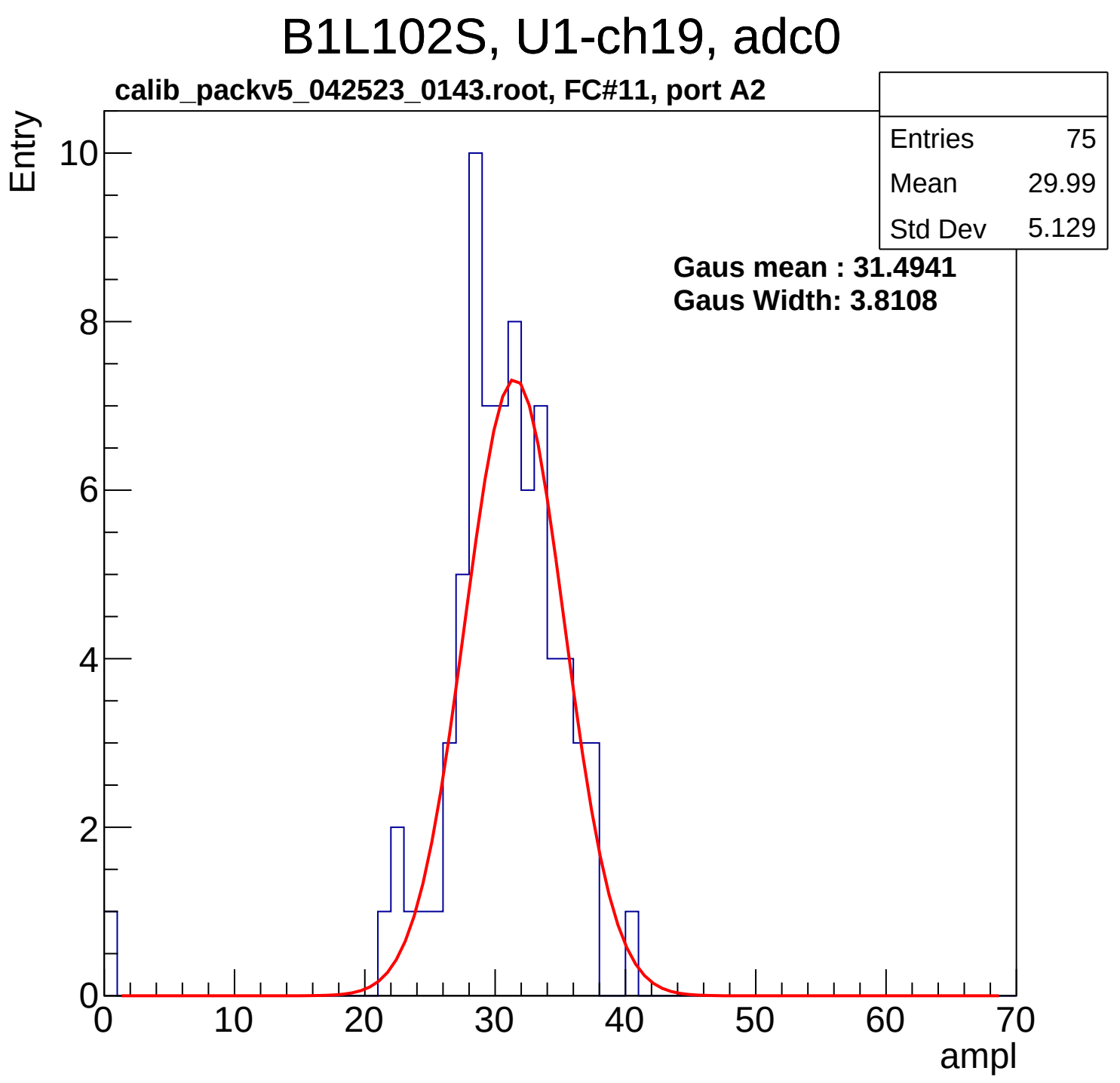
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

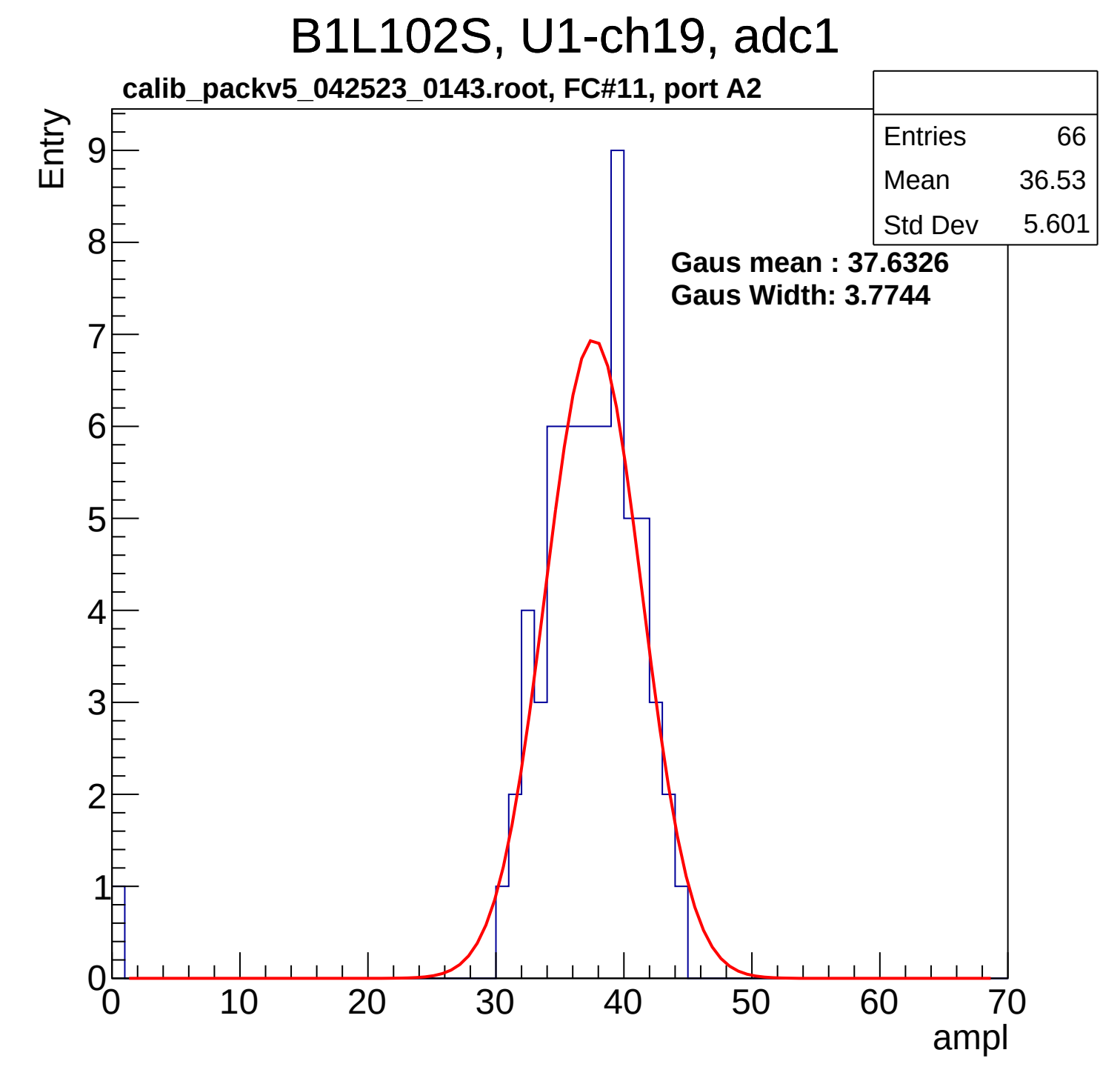

B1L102S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 54.97
Std Dev 3.817
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

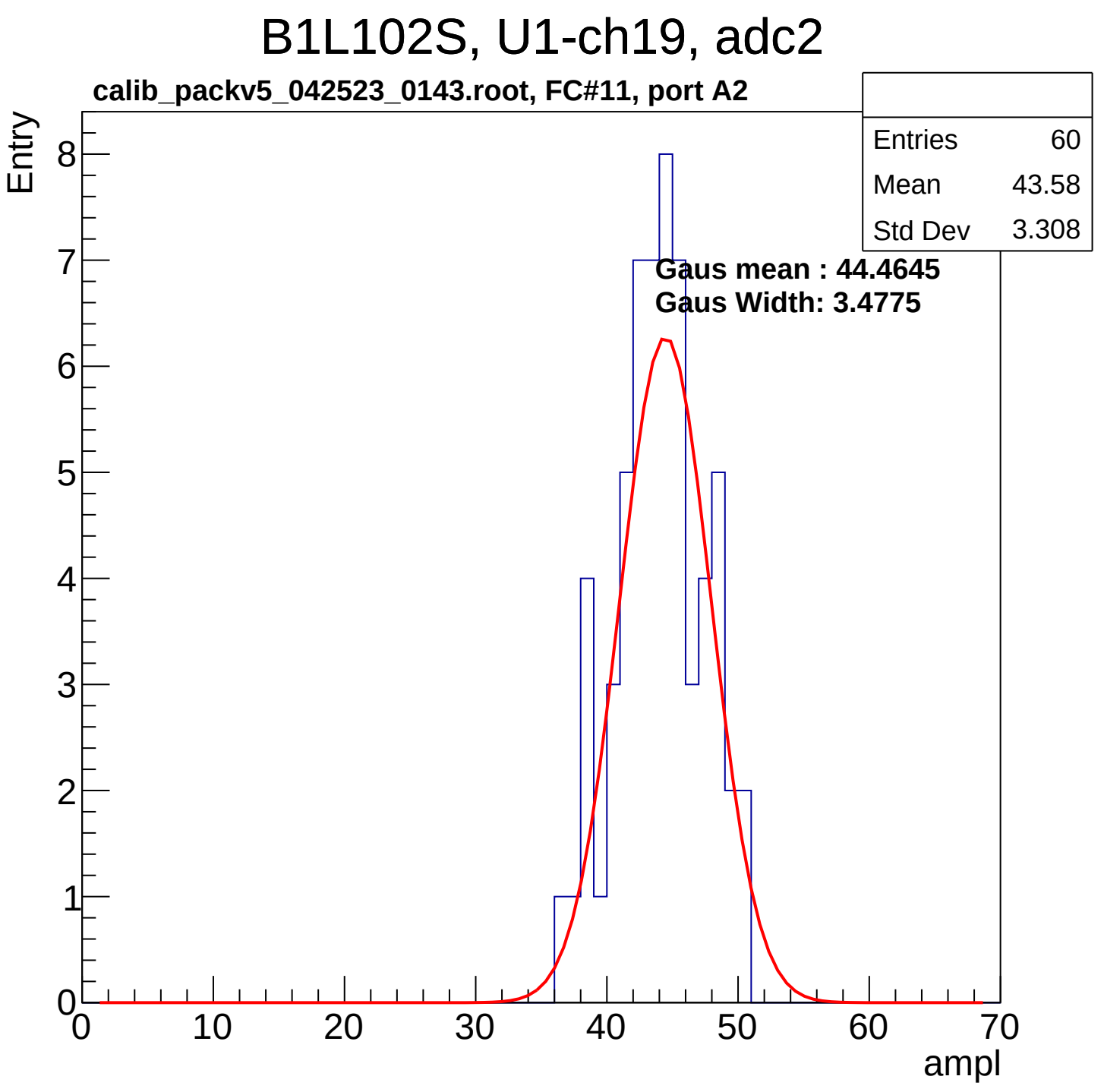

B1L102S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 59.05
Std Dev 2.509
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

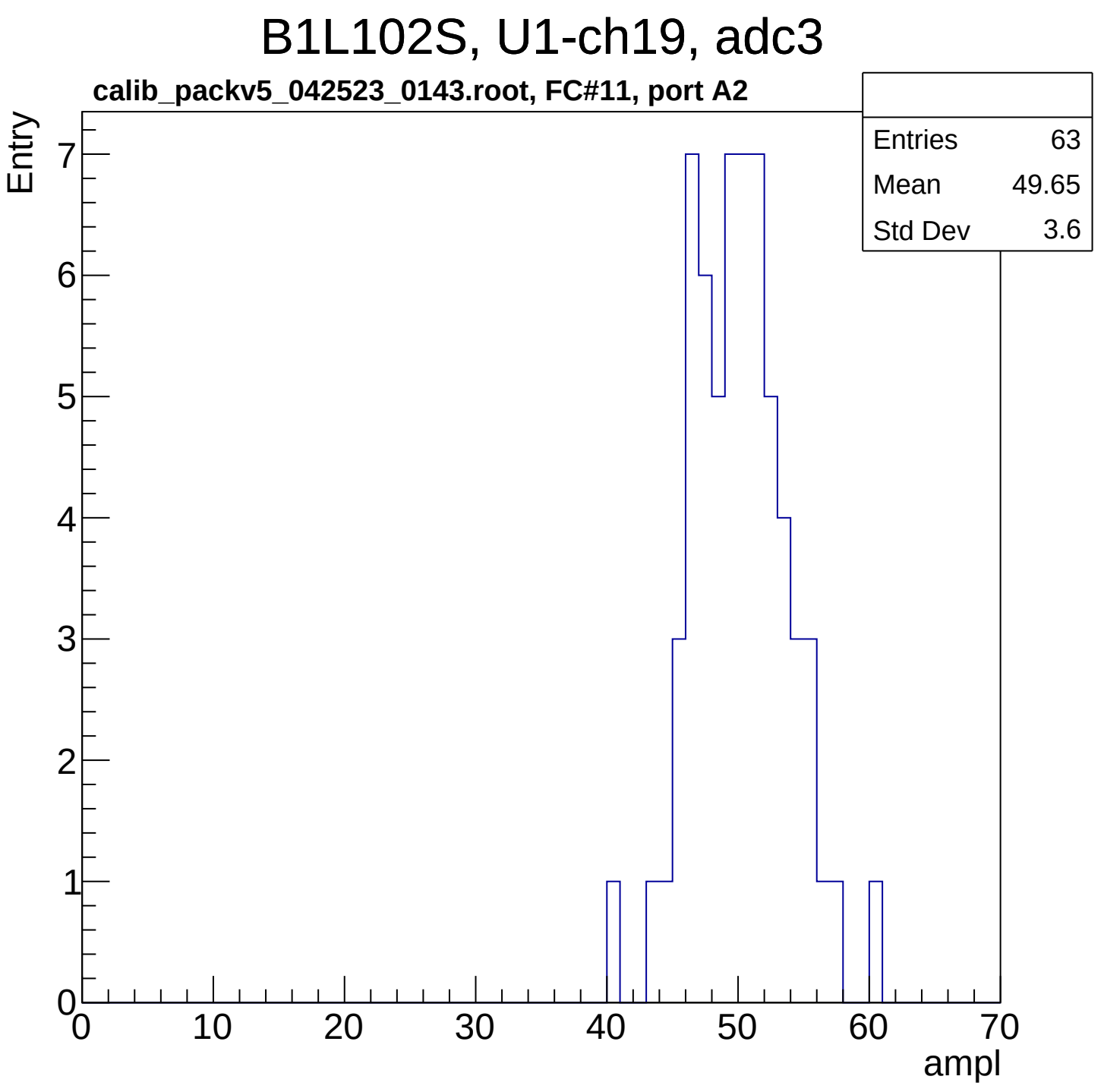

B1L102S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 57.57
Std Dev 16
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

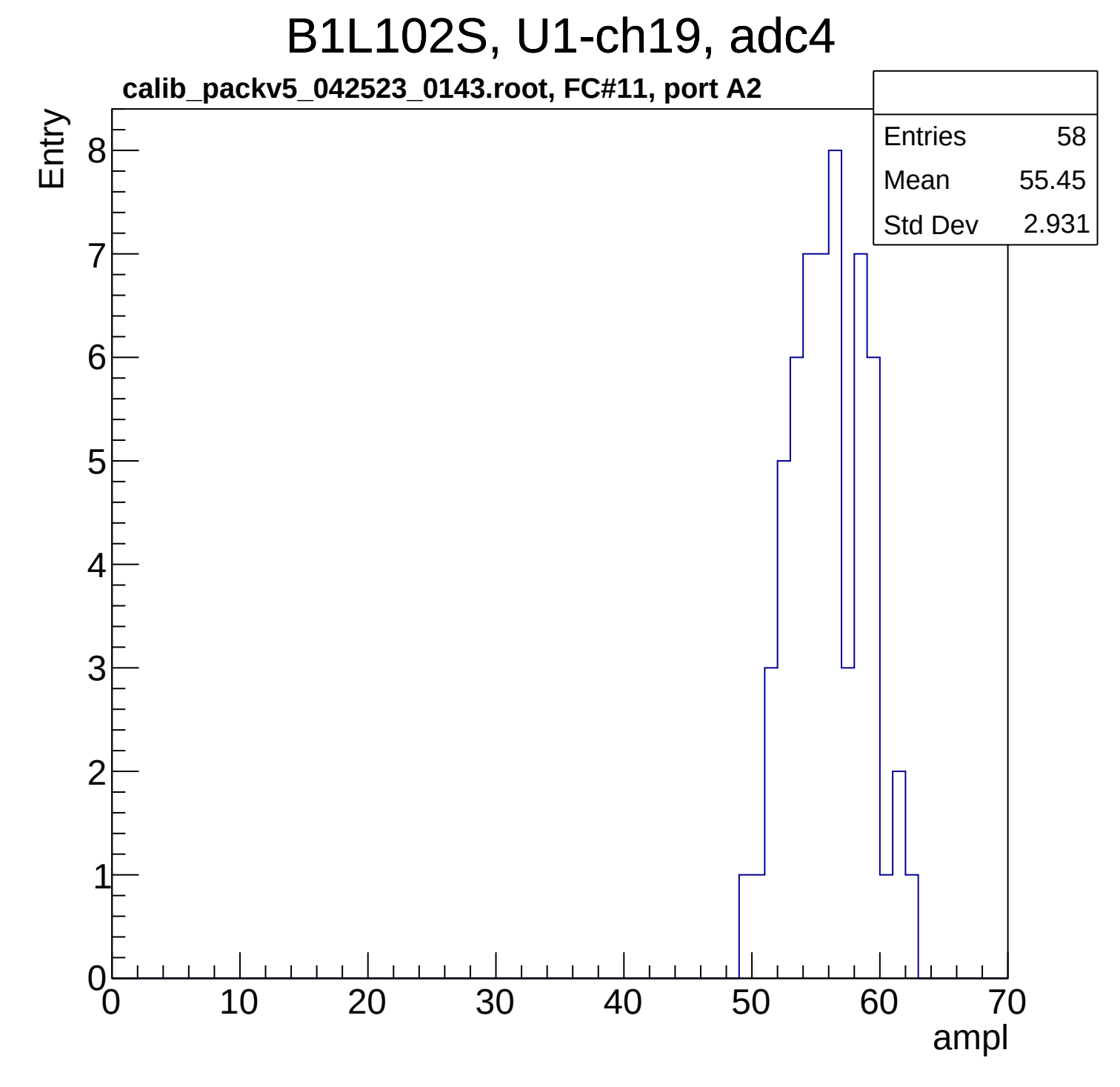

B1L102S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

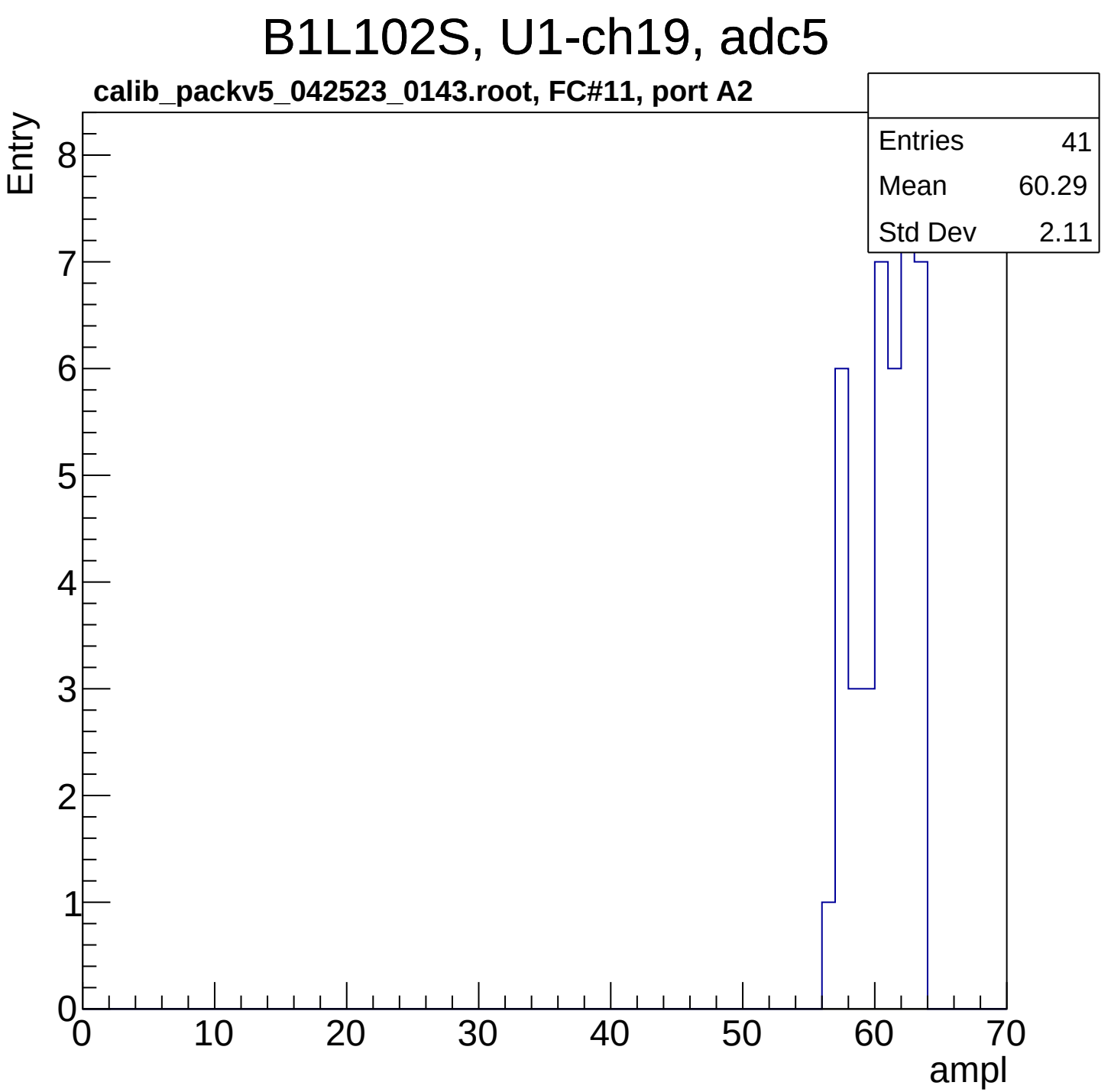

B1L102S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 29.99
Std Dev 5.129
Gaus mean : 31.4941
Gaus Width: 3.8108
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

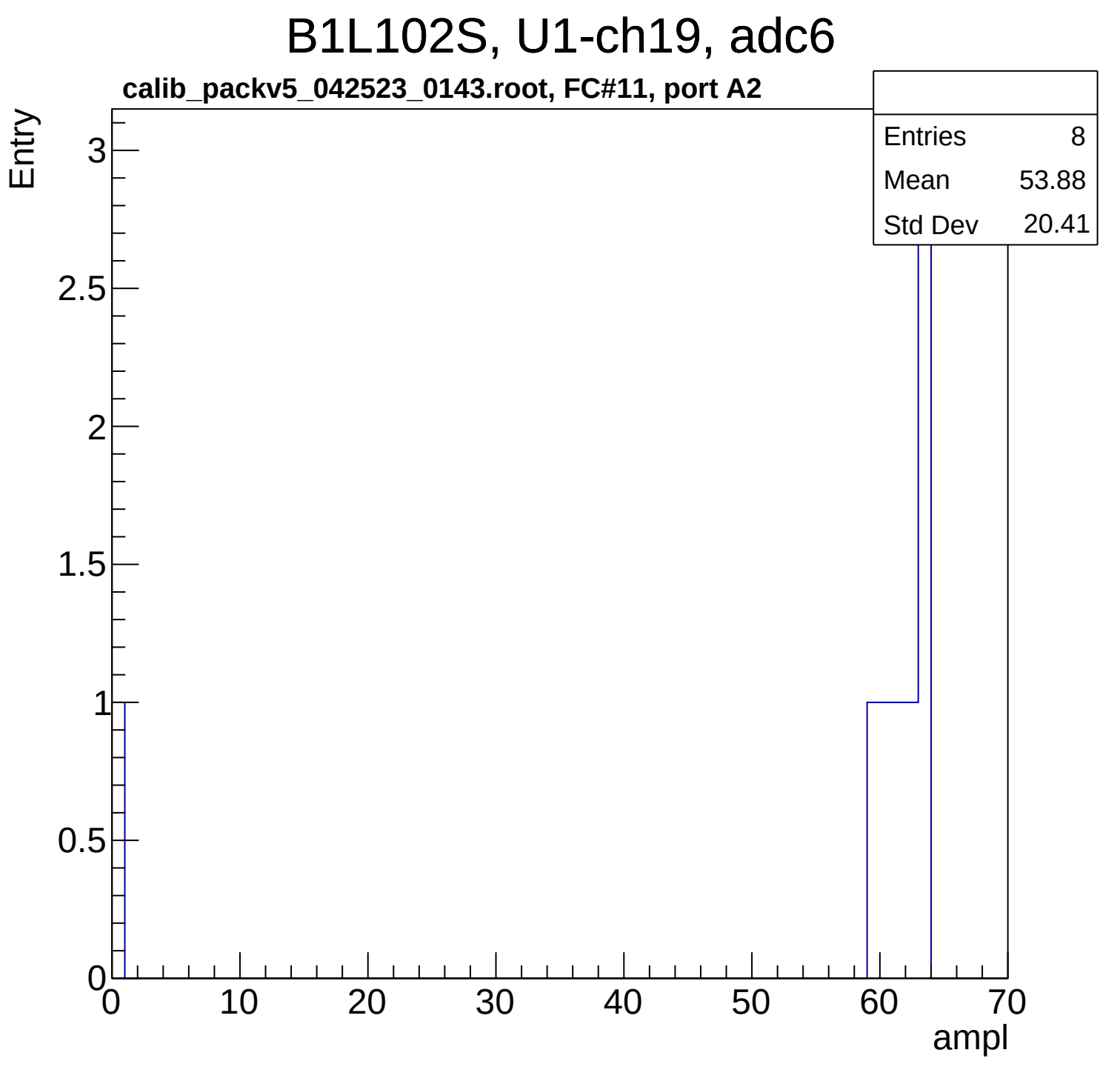

B1L102S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 36.53
Std Dev 5.601
Gaus mean : 37.6326
Gaus Width: 3.7744
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

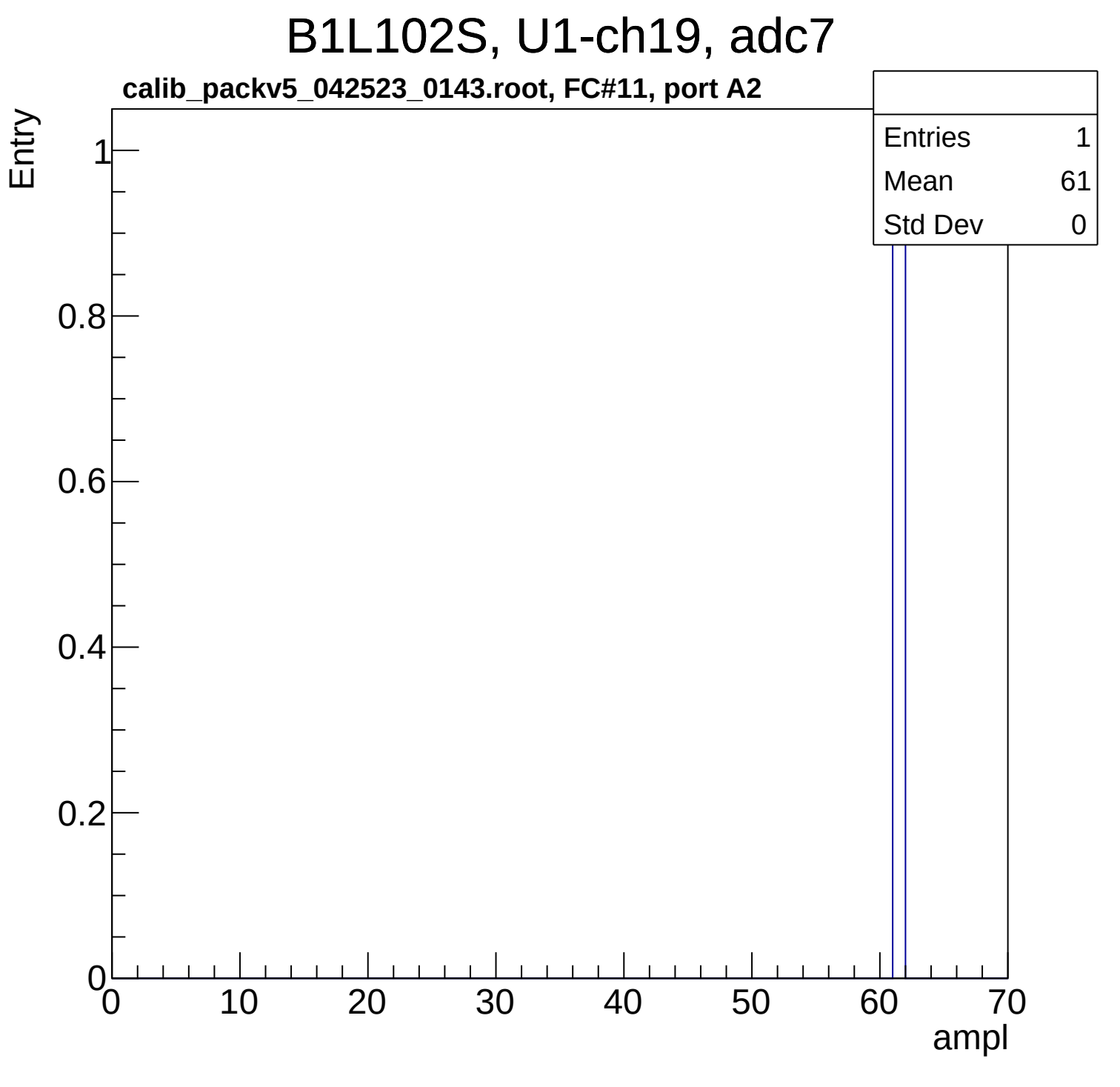

B1L102S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 43.58
Std Dev 3.308
Gaus mean : 44.4645
Gaus Width: 3.4775
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

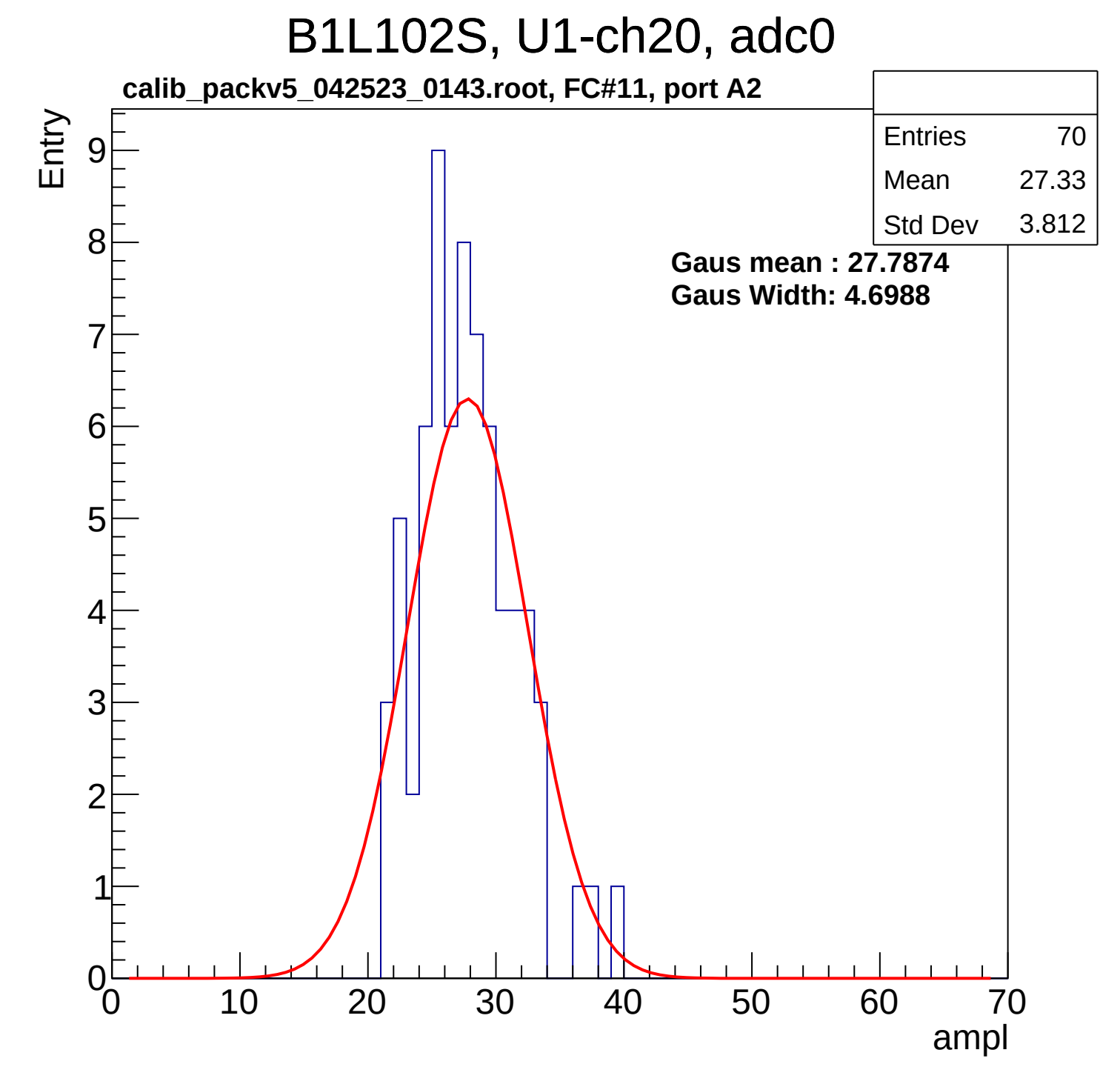

B1L102S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 49.65
Std Dev 3.6
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

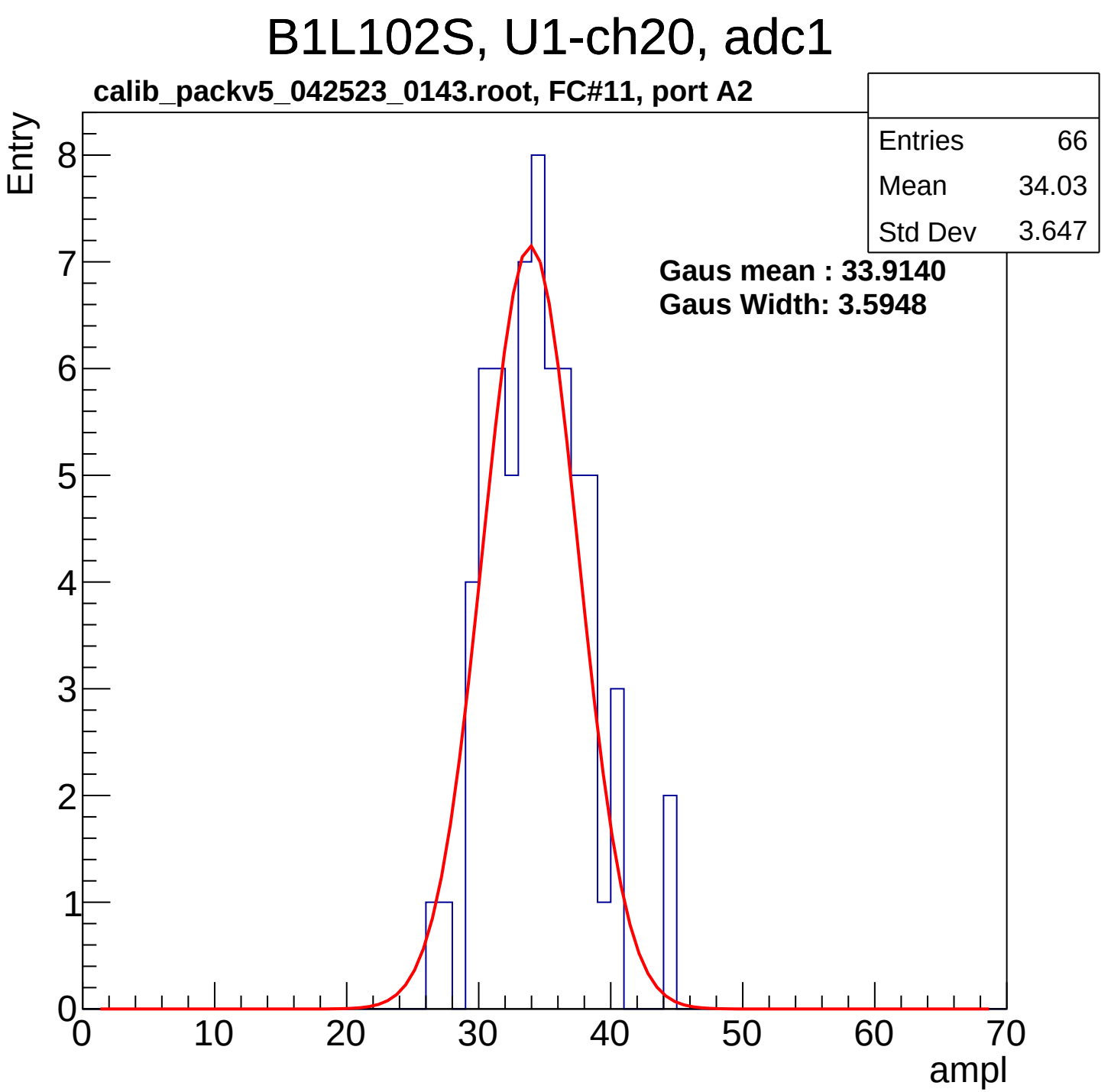

B1L102S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.45
Std Dev 2.931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

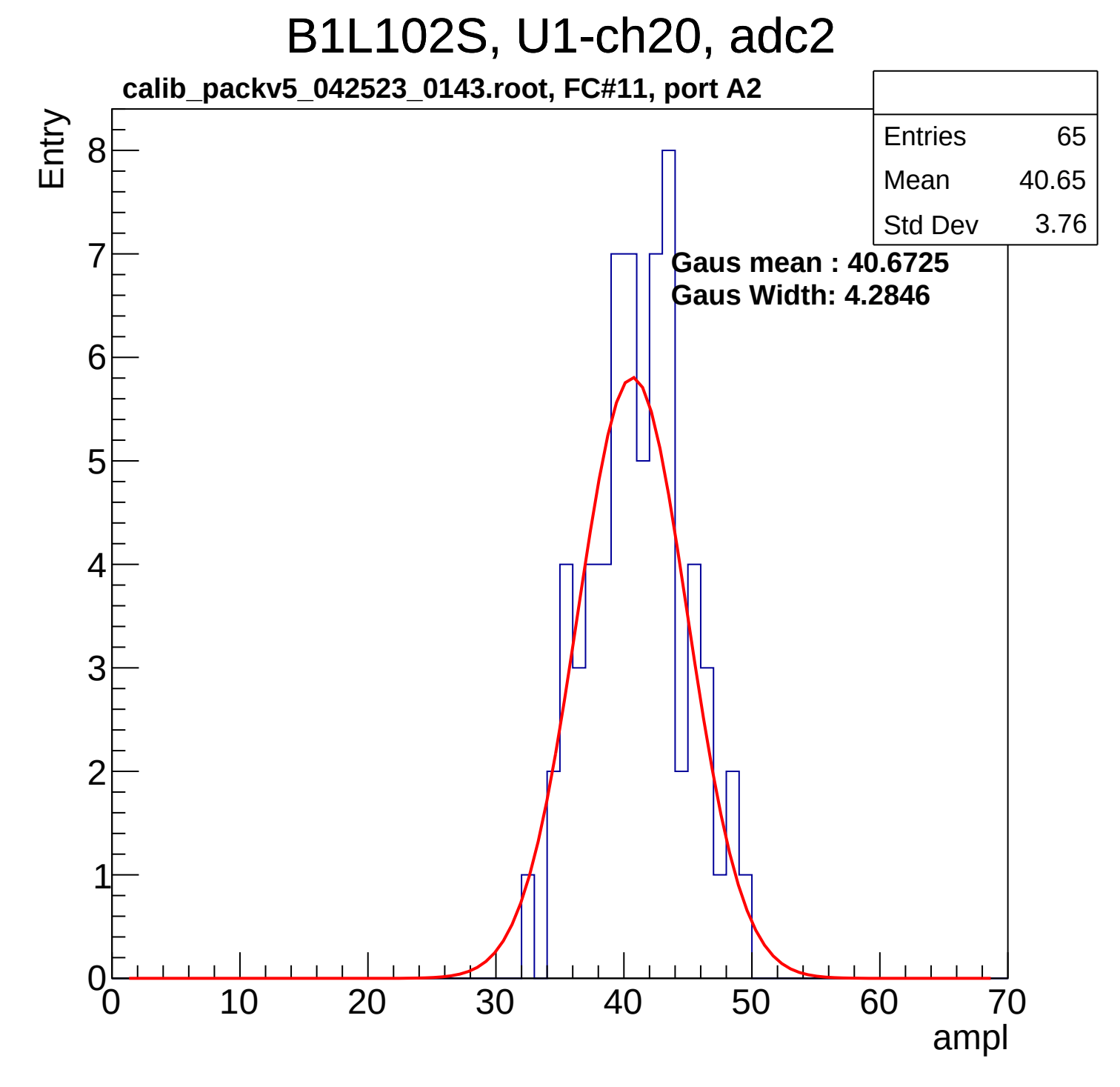

B1L102S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 60.29
Std Dev 2.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

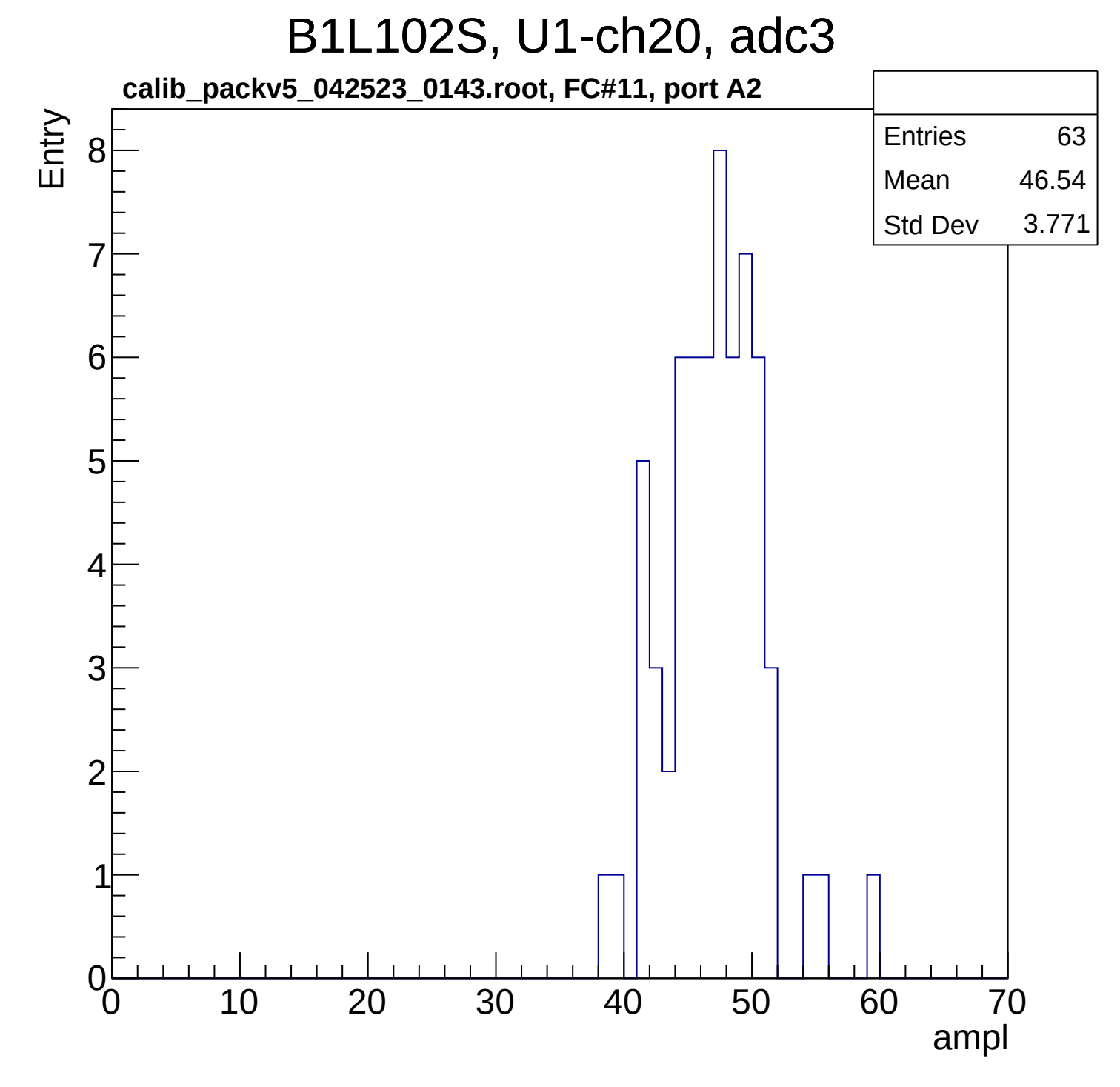

B1L102S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 53.88
Std Dev 20.41
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

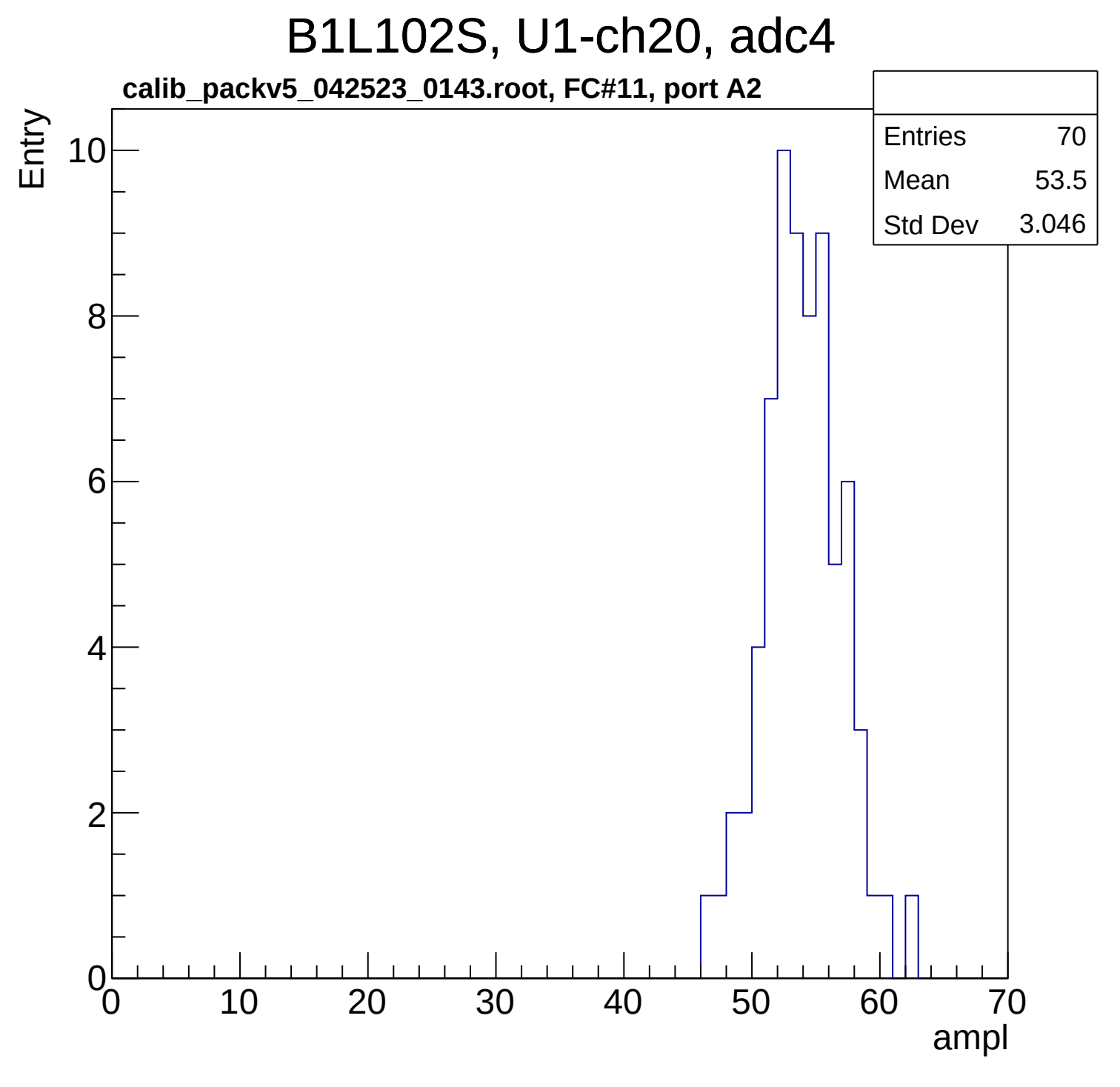

B1L102S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

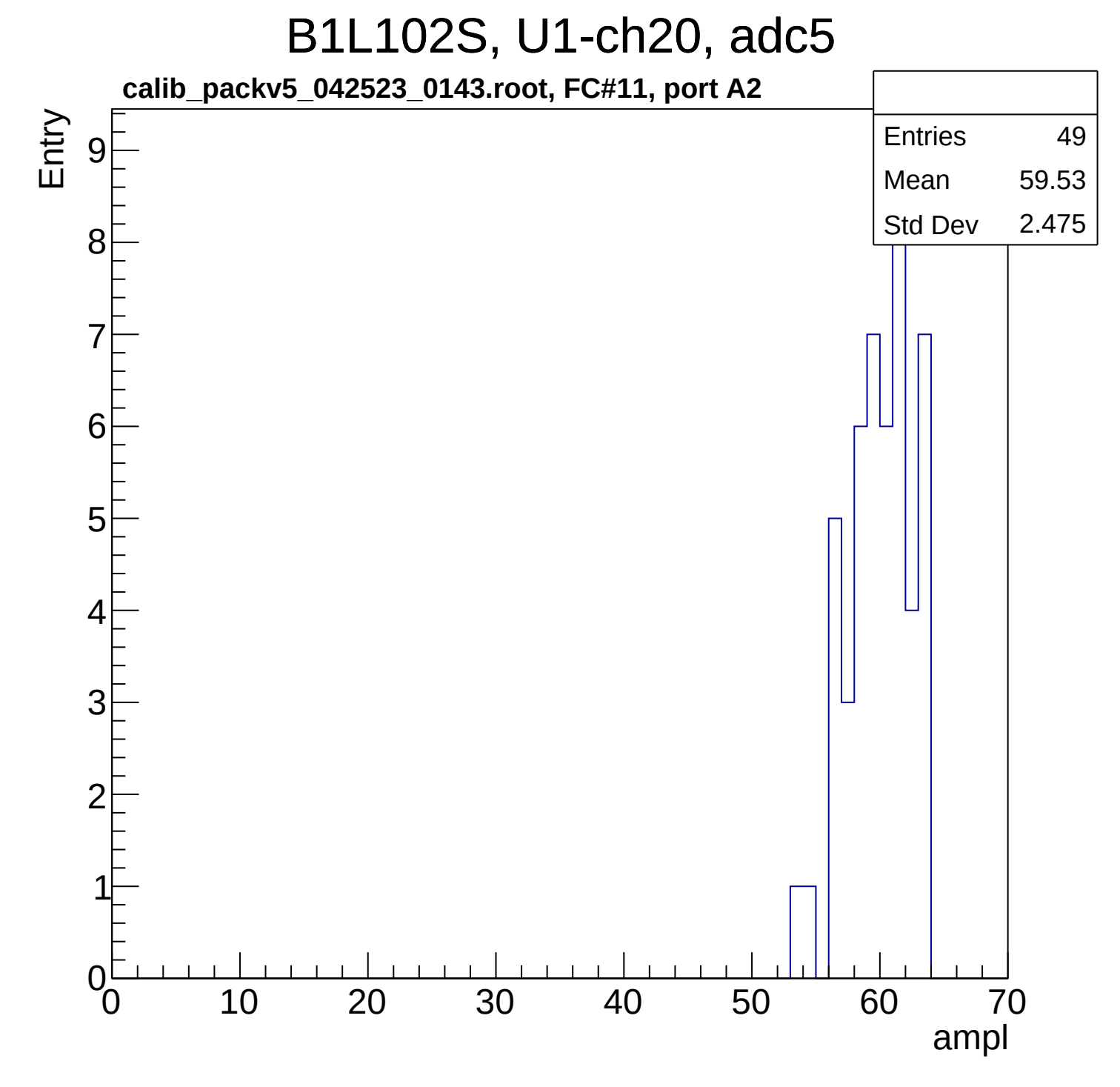

B1L102S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 27.33
Std Dev 3.812
Gaus mean : 27.7874
Gaus Width: 4.6988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

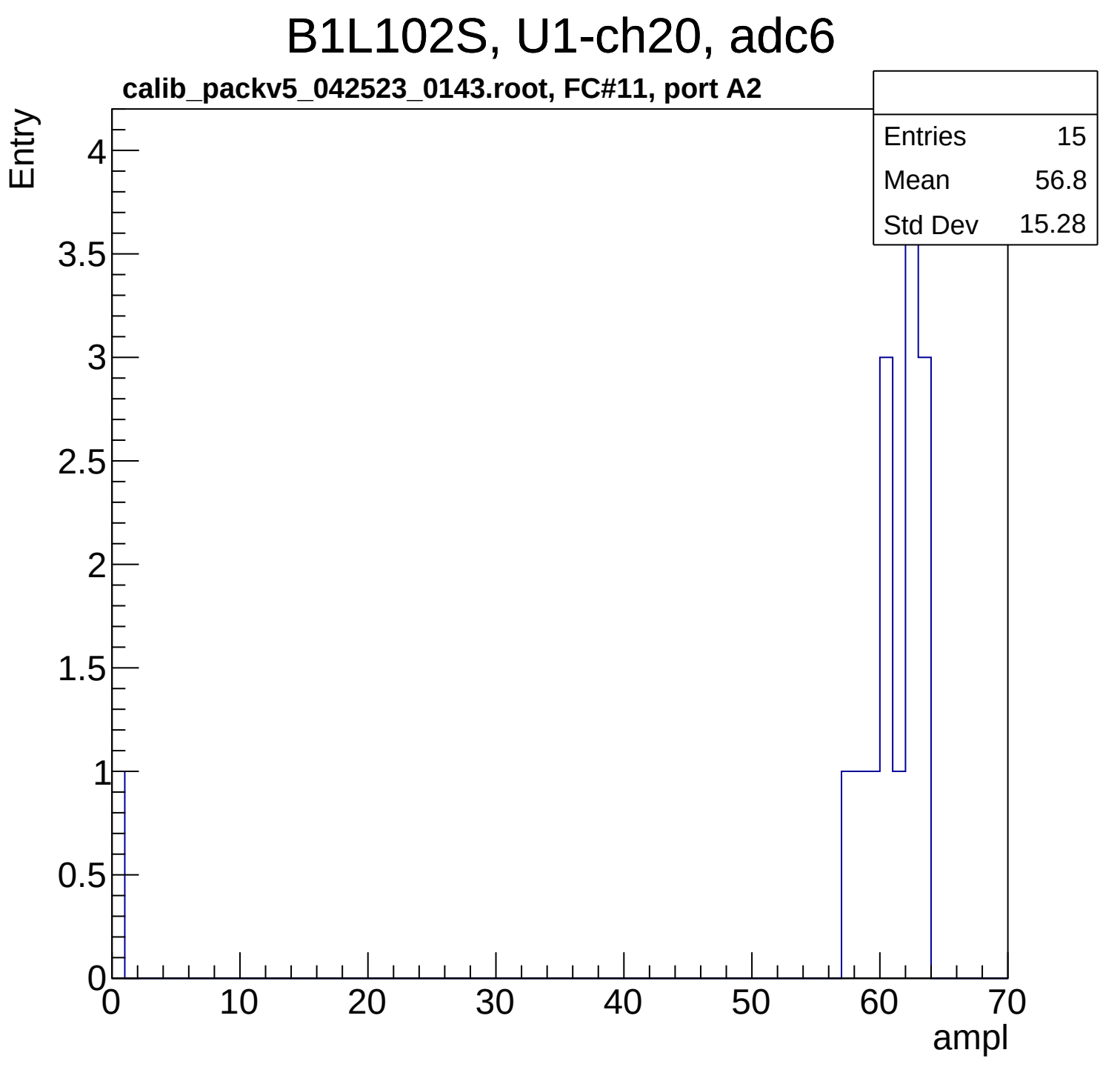

B1L102S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 34.03
Std Dev 3.647
Gaus mean : 33.9140
Gaus Width: 3.5948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

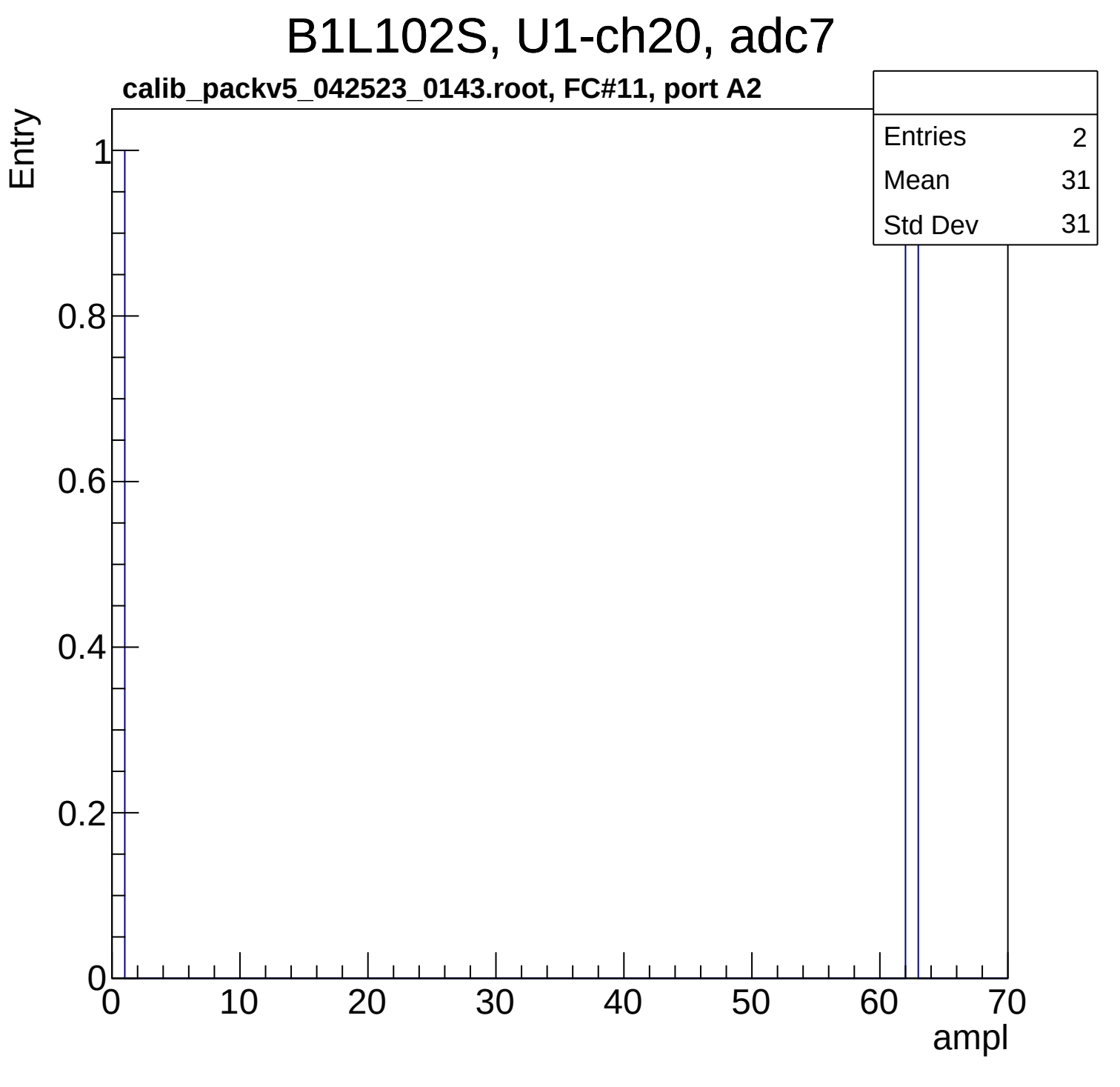

B1L102S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 40.65
Std Dev 3.76
Gaus mean : 40.6725
Gaus Width: 4.2846
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

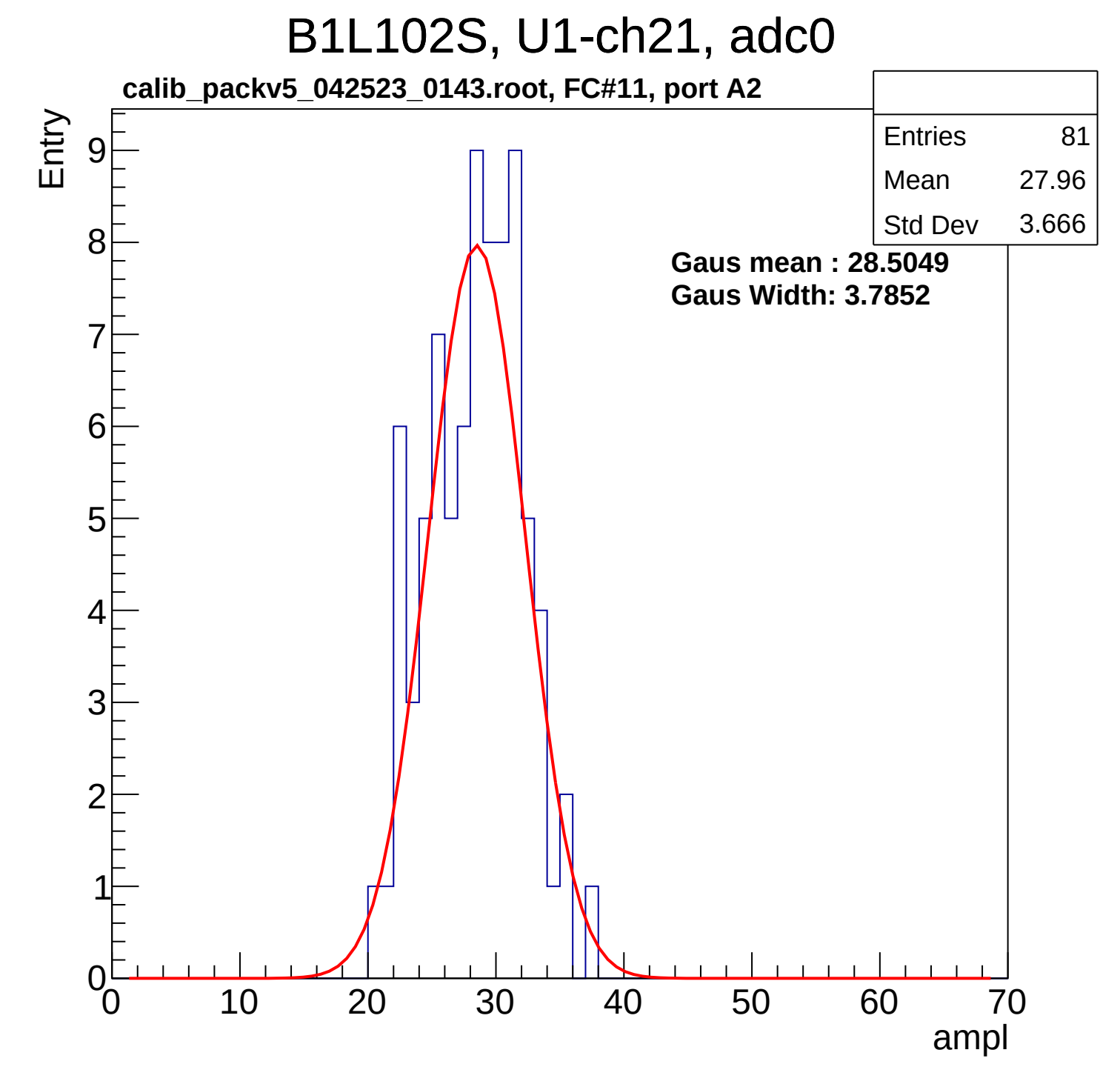

B1L102S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 46.54
Std Dev 3.771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

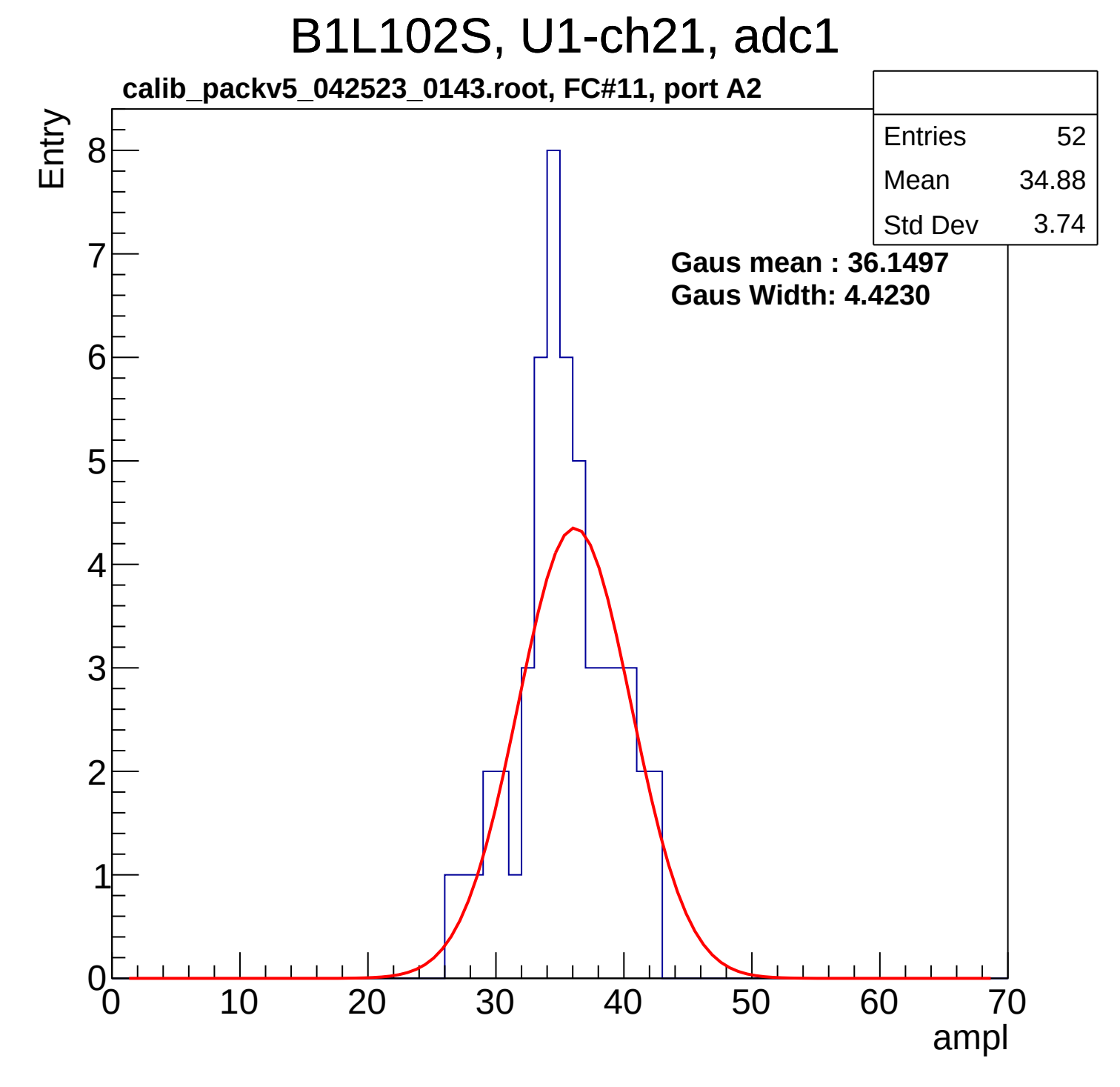

B1L102S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 53.5
Std Dev 3.046
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

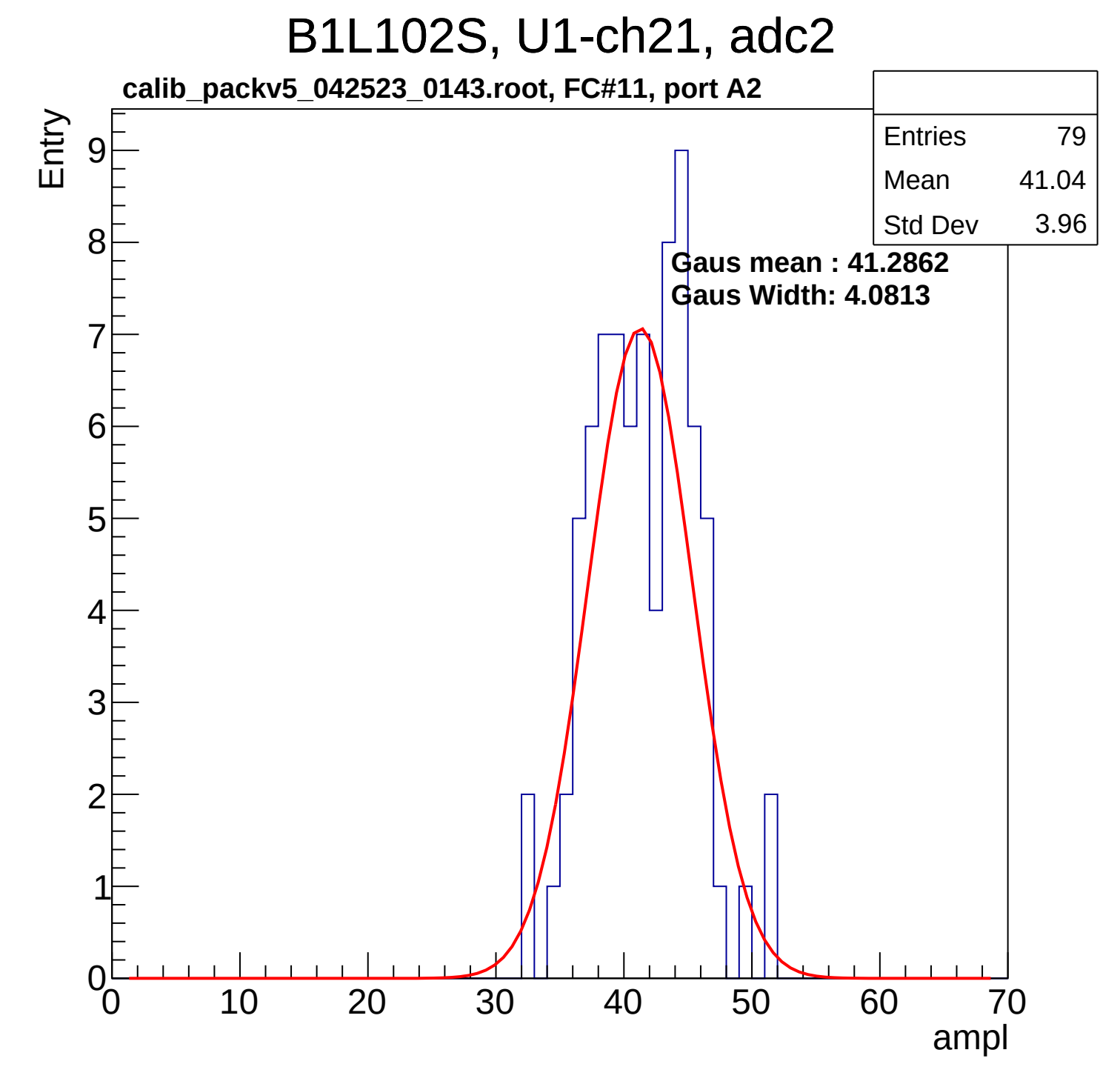

B1L102S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.53
Std Dev 2.475
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

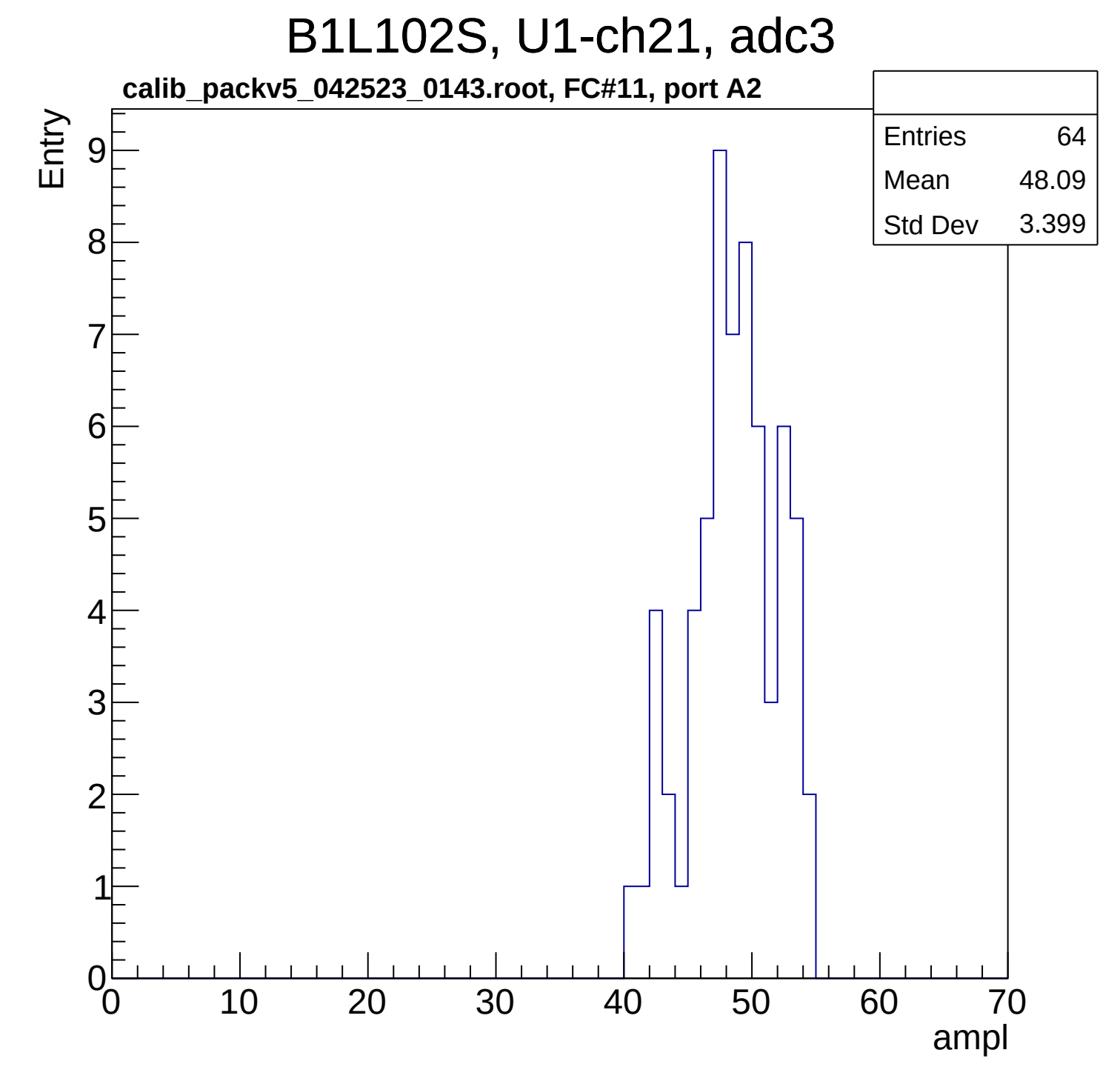

B1L102S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 56.8
Std Dev 15.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

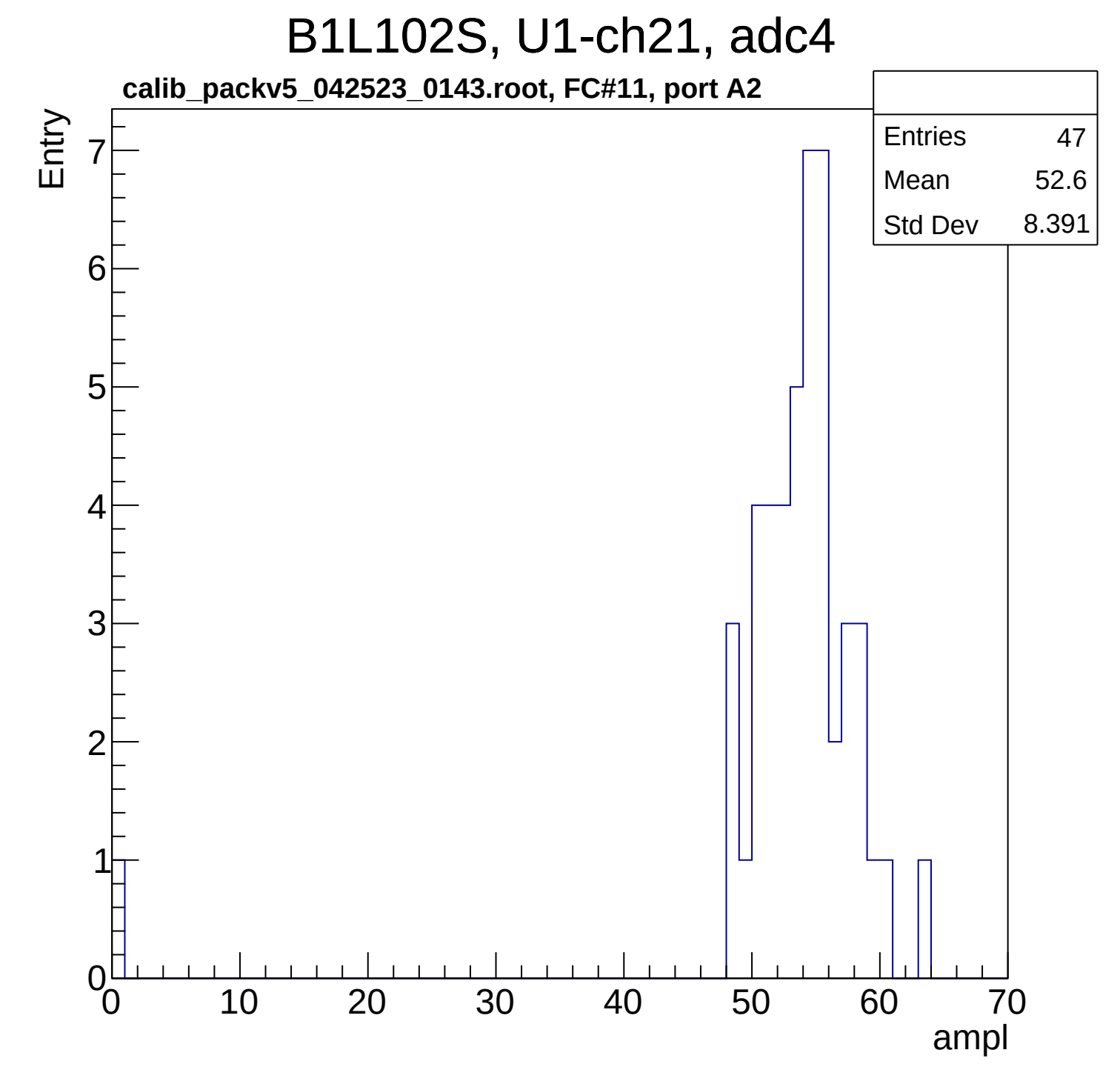

B1L102S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

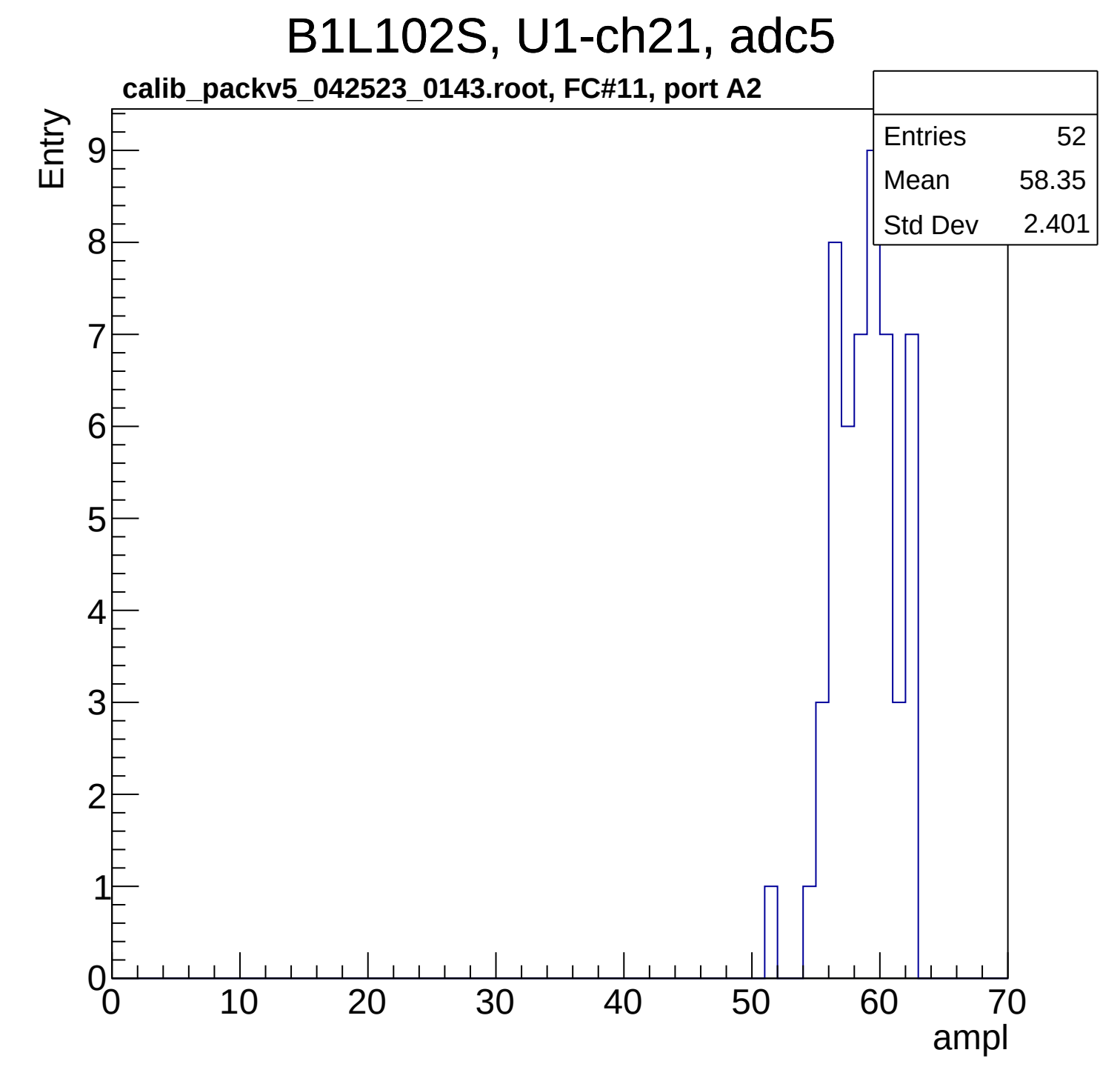

B1L102S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 27.96
Std Dev 3.666
Gaus mean : 28.5049
Gaus Width: 3.7852
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 34.88
Std Dev 3.74
Gaus mean : 36.1497
Gaus Width: 4.4230
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 41.04
Std Dev 3.96
Gaus mean : 41.2862
Gaus Width: 4.0813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 48.09
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 52.6
Std Dev 8.391
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.35
Std Dev 2.401
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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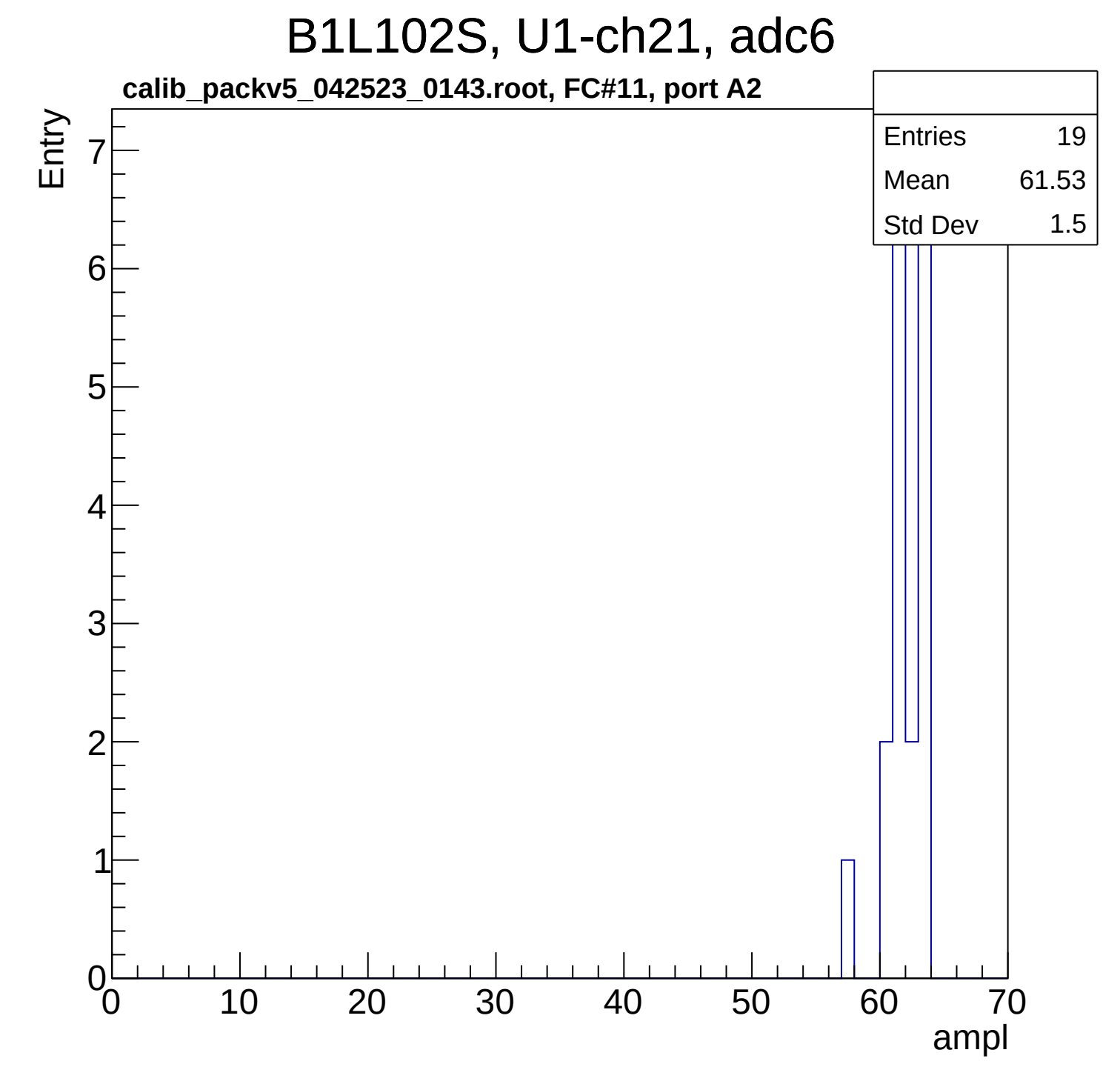
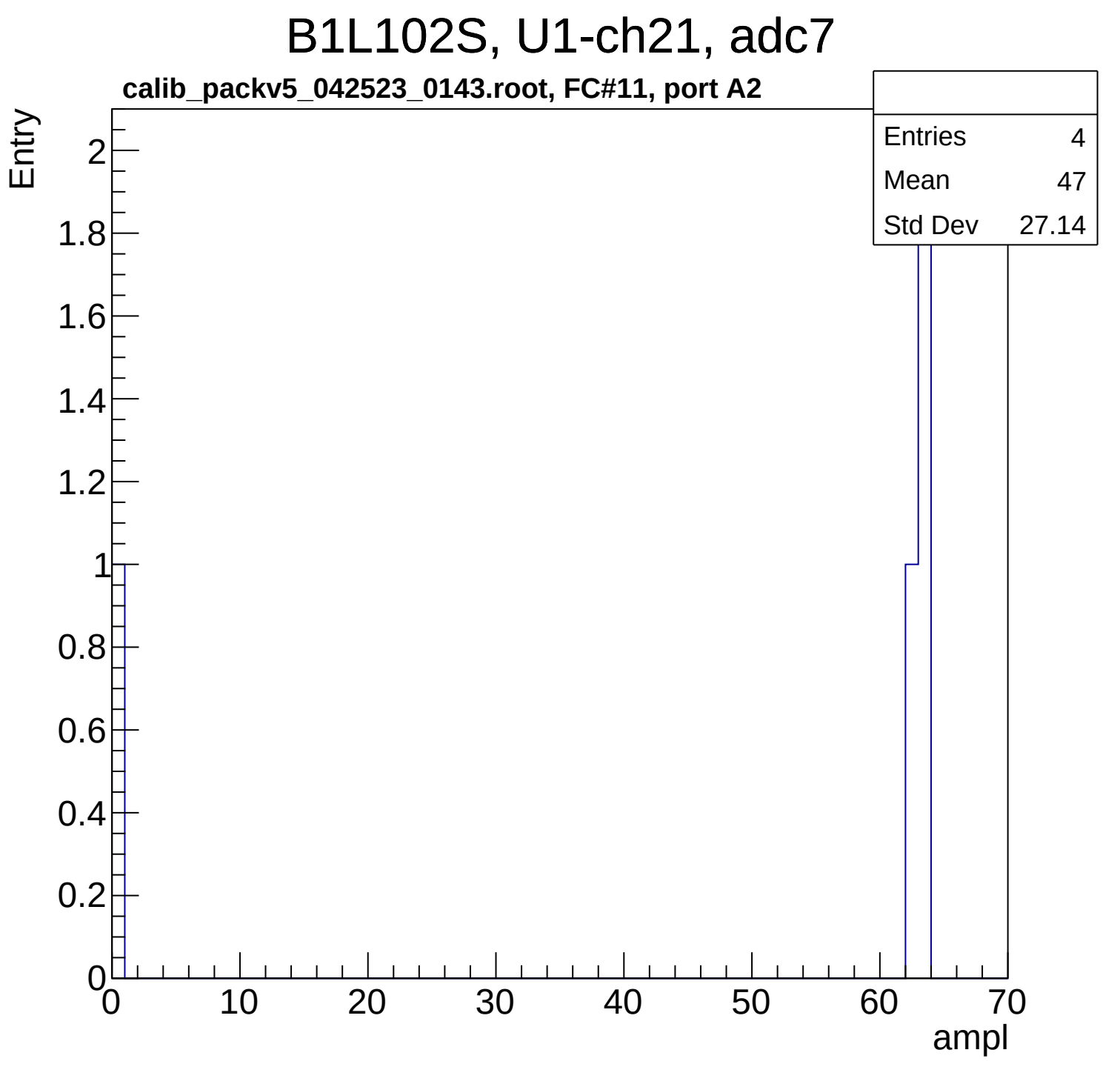
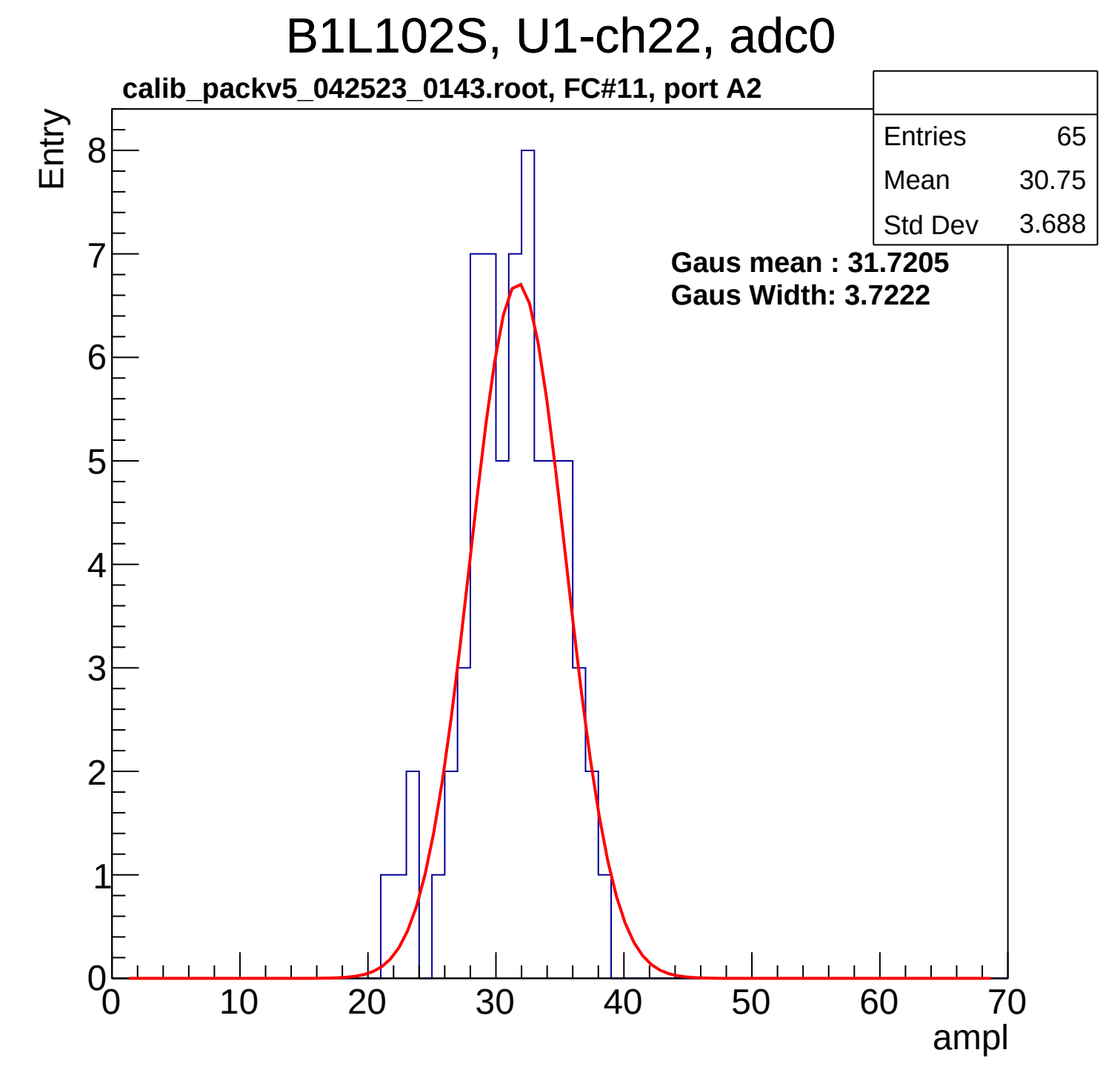
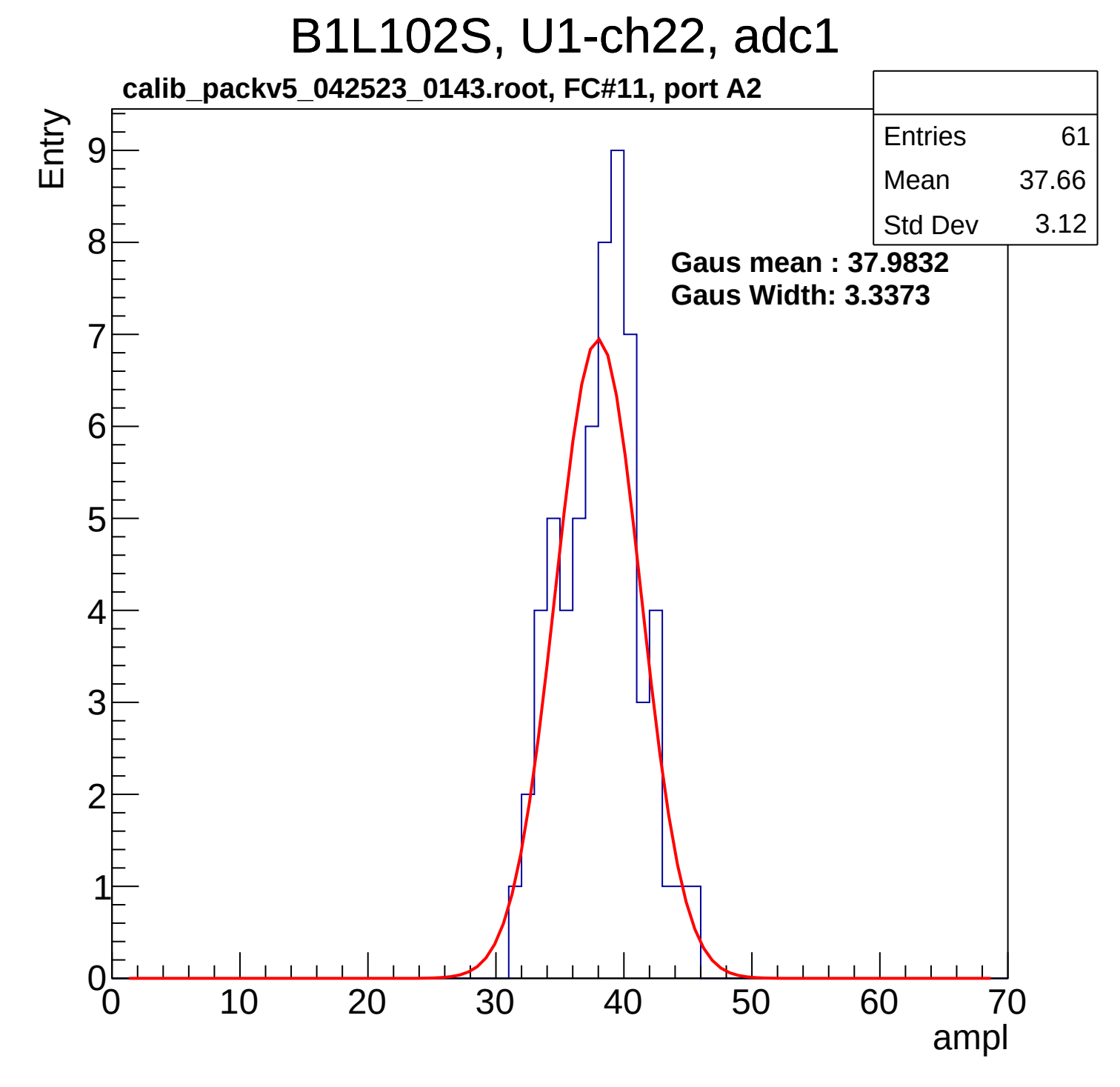
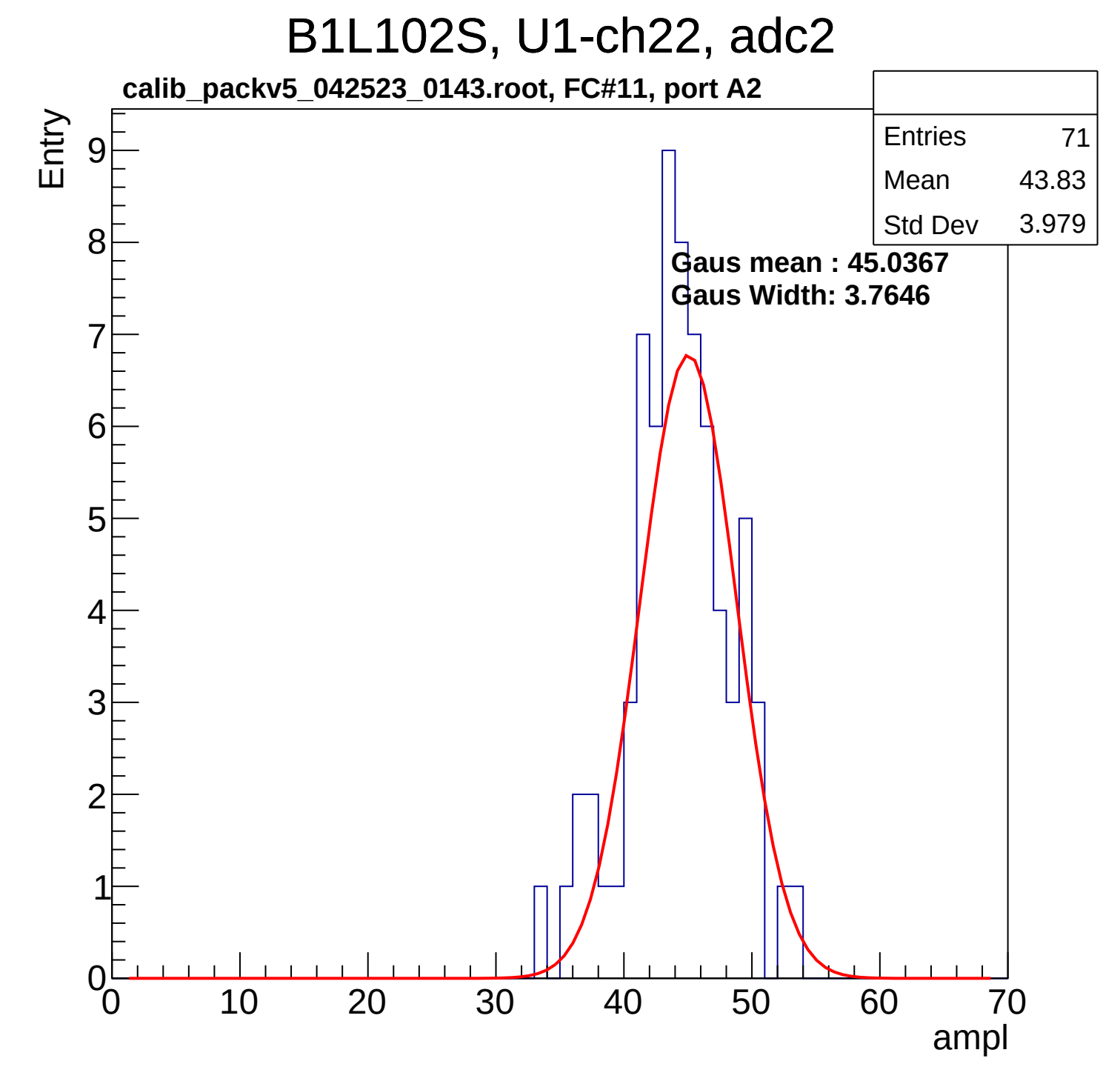
30
40
50
60
70

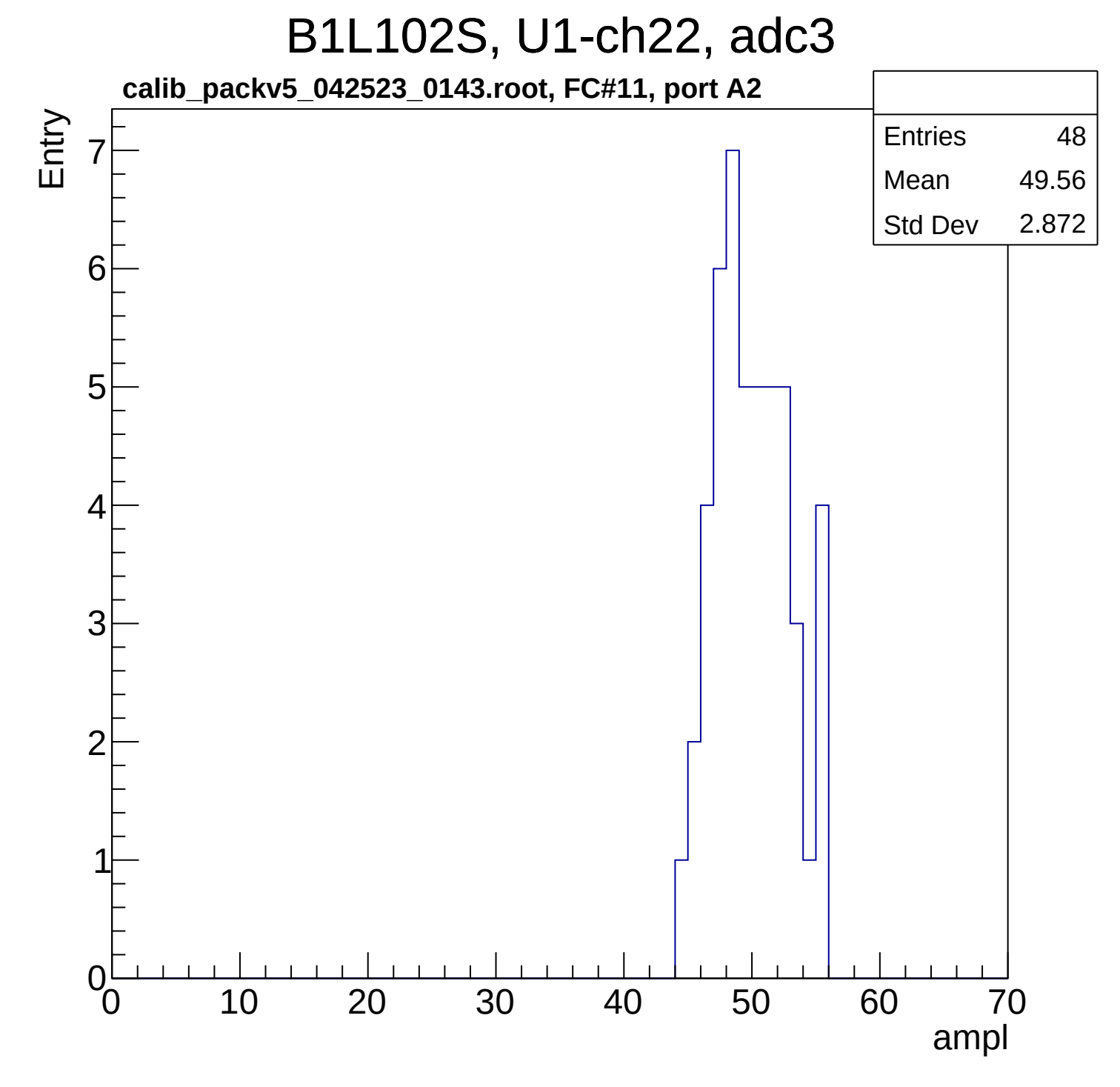

B1L102S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 19
Mean 61.53
Std Dev 1.5
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

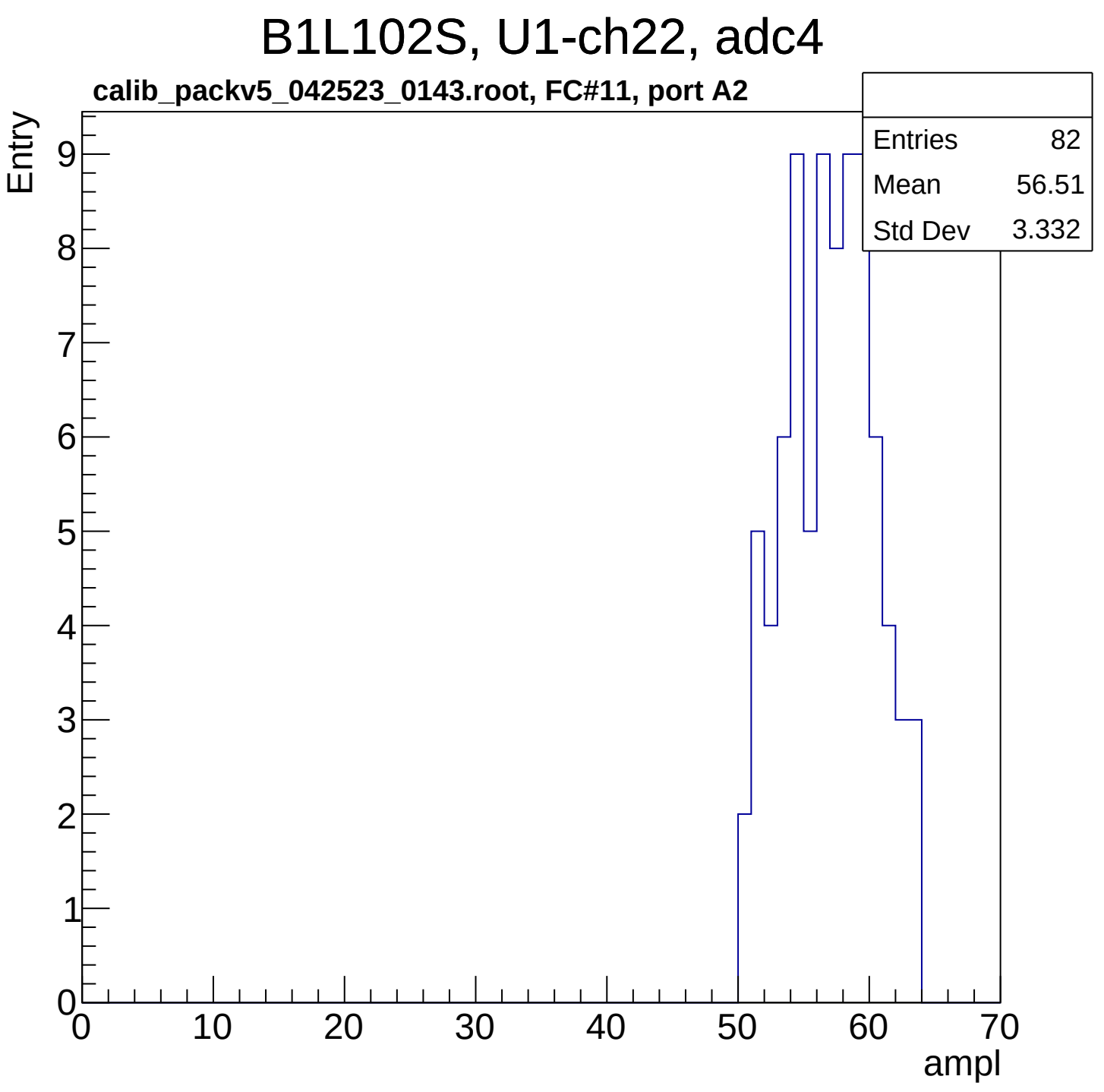

B1L102S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 30.75
Std Dev 3.688
Gaus mean : 31.7205
Gaus Width: 3.7222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 37.66
Std Dev 3.12
Gaus mean : 37.9832
Gaus Width: 3.3373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 43.83
Std Dev 3.979
Gaus mean : 45.0367
Gaus Width: 3.7646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 49.56
Std Dev 2.872
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 56.51
Std Dev 3.332
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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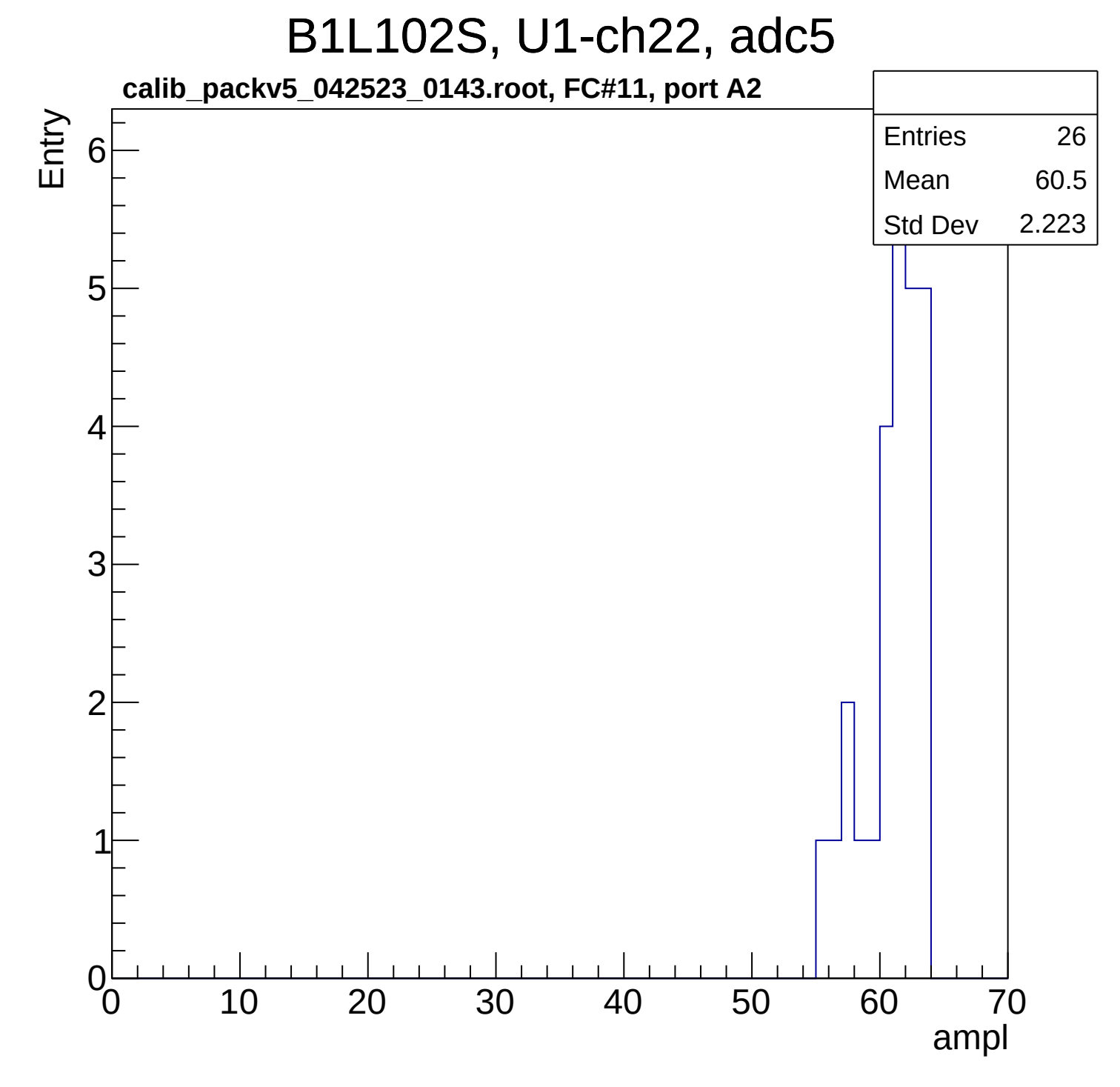
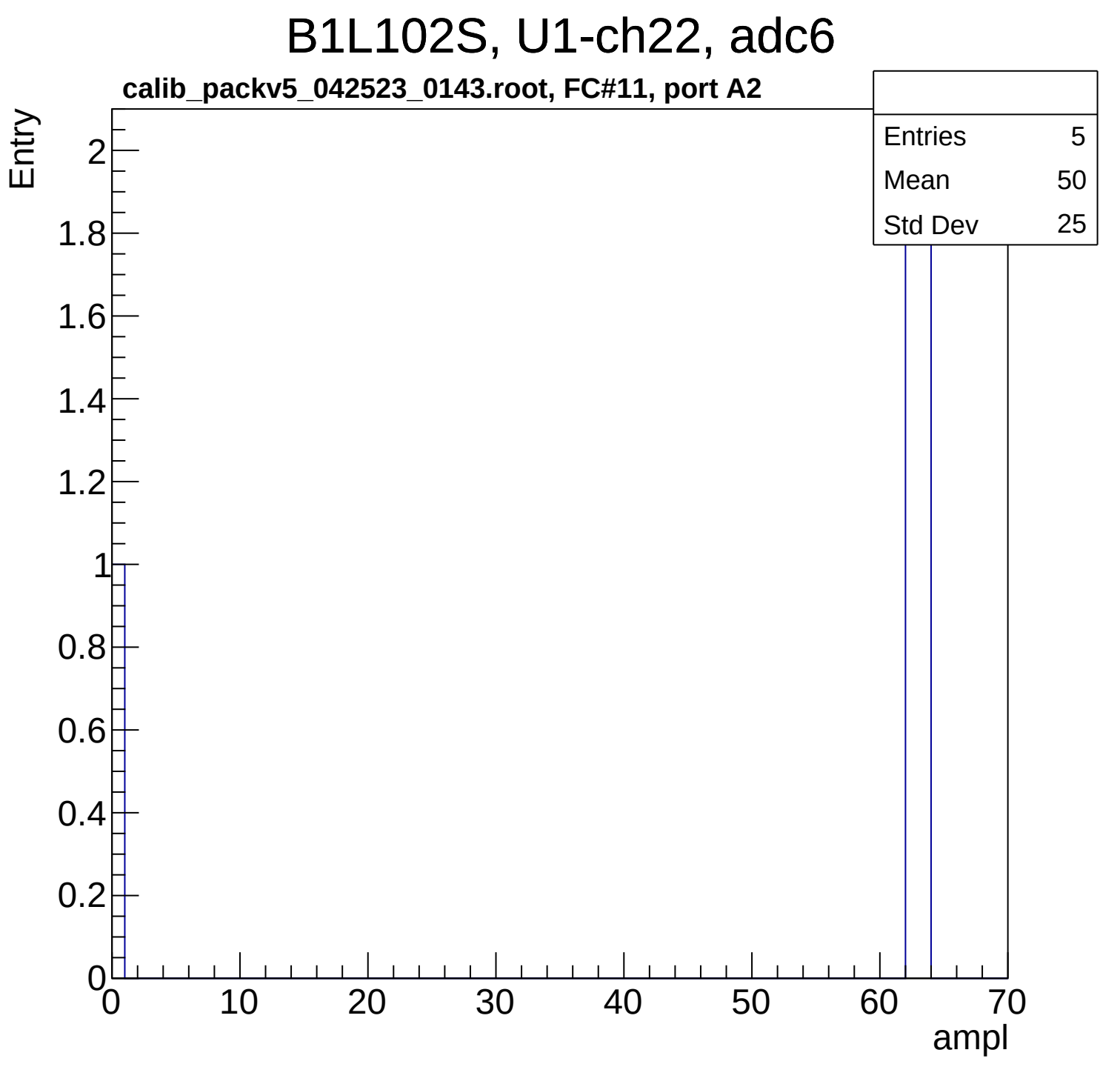
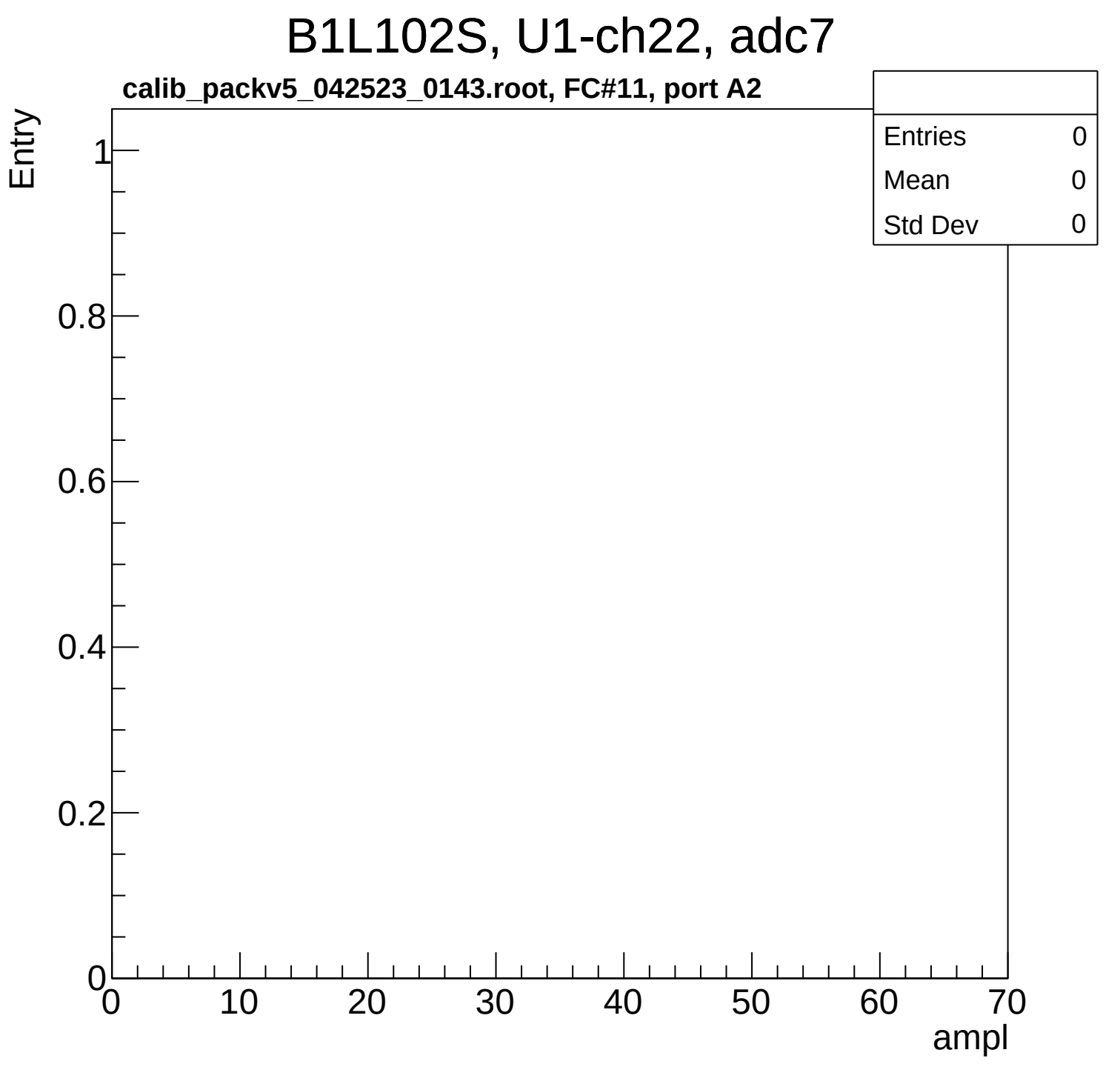

B1L102S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 26
Mean 60.5
Std Dev 2.223
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 50
Std Dev 25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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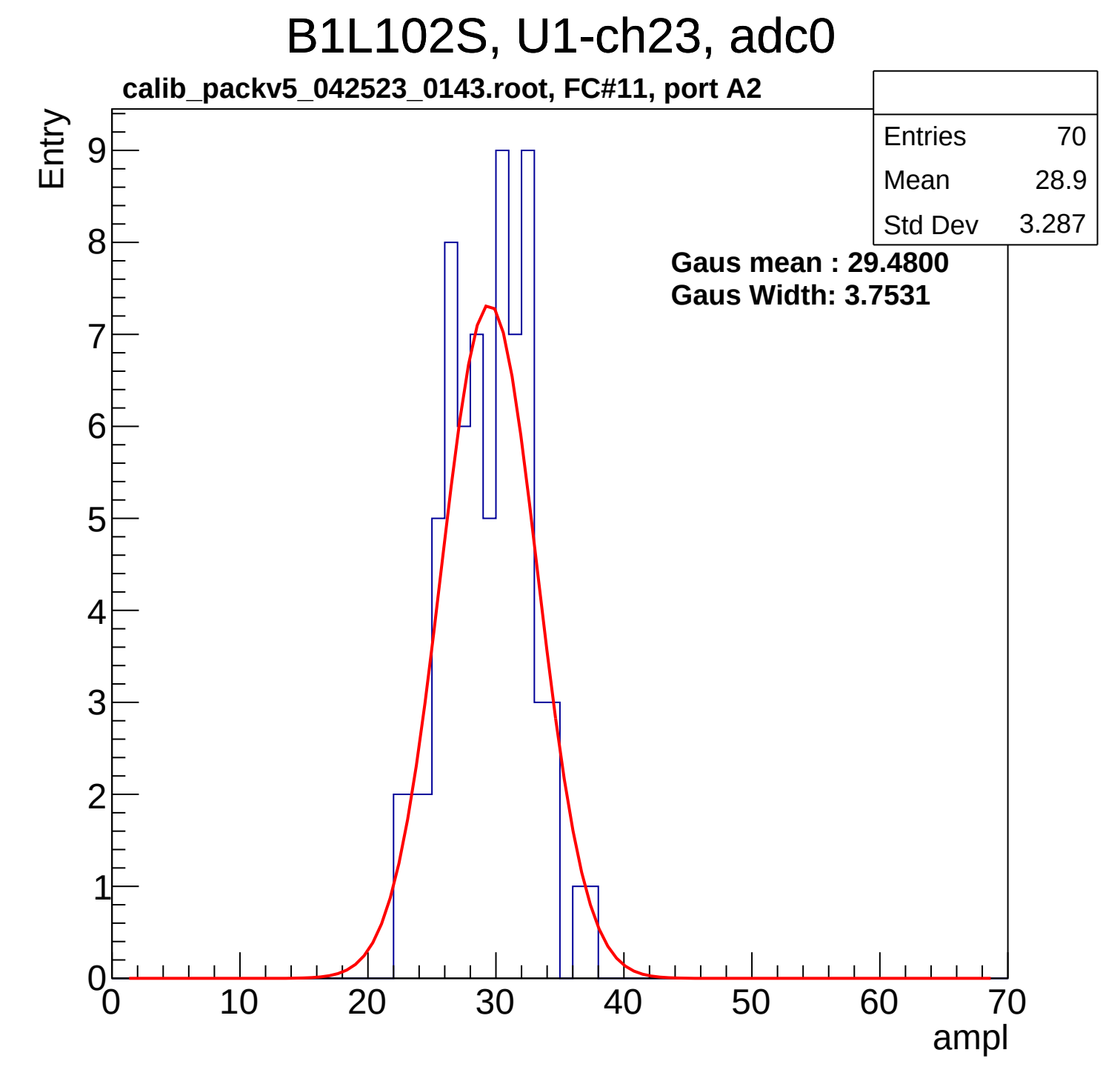
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.9
Std Dev 3.287
Gaus mean : 29.4800
Gaus Width: 3.7531
Entry
ampl
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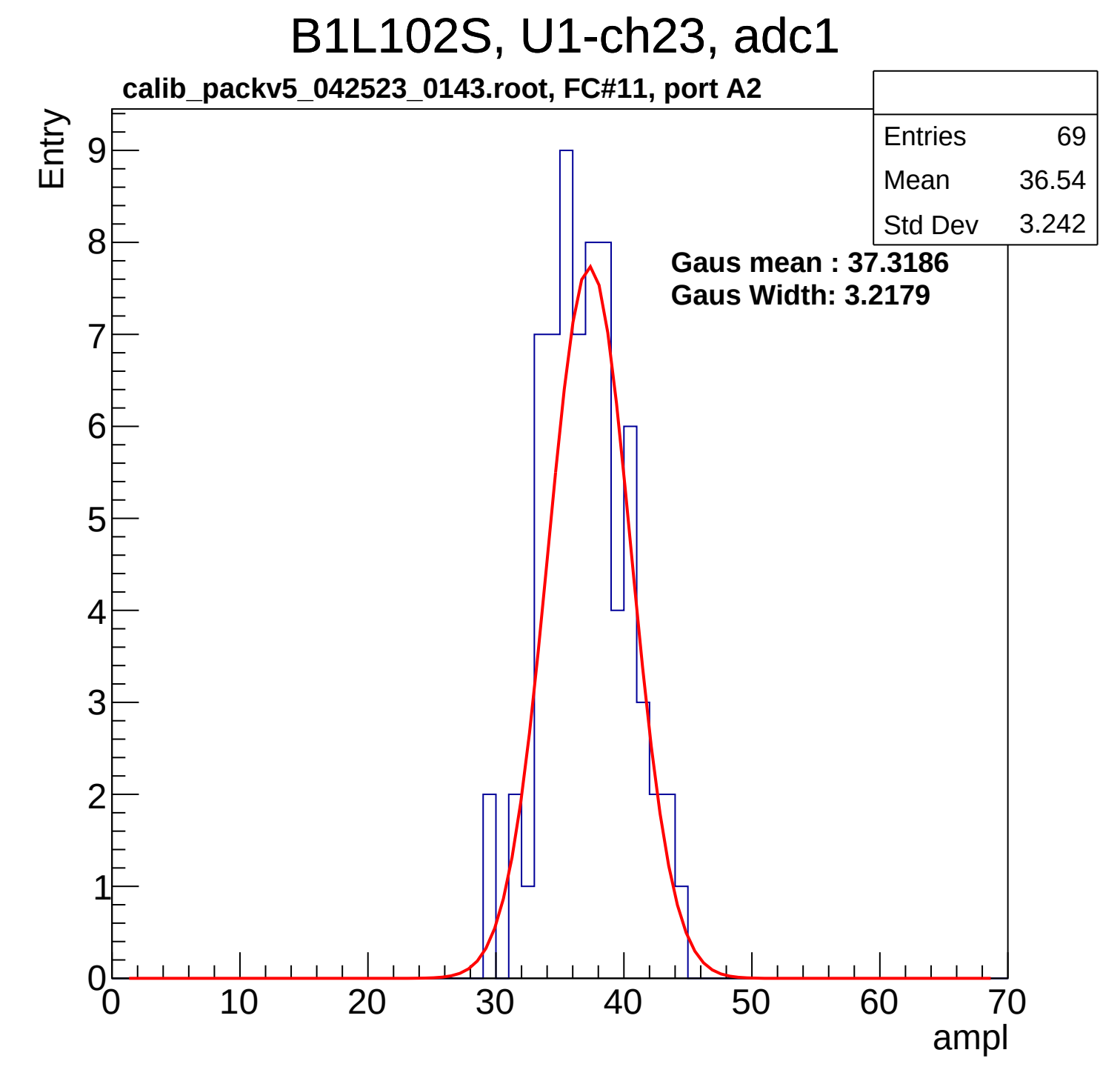
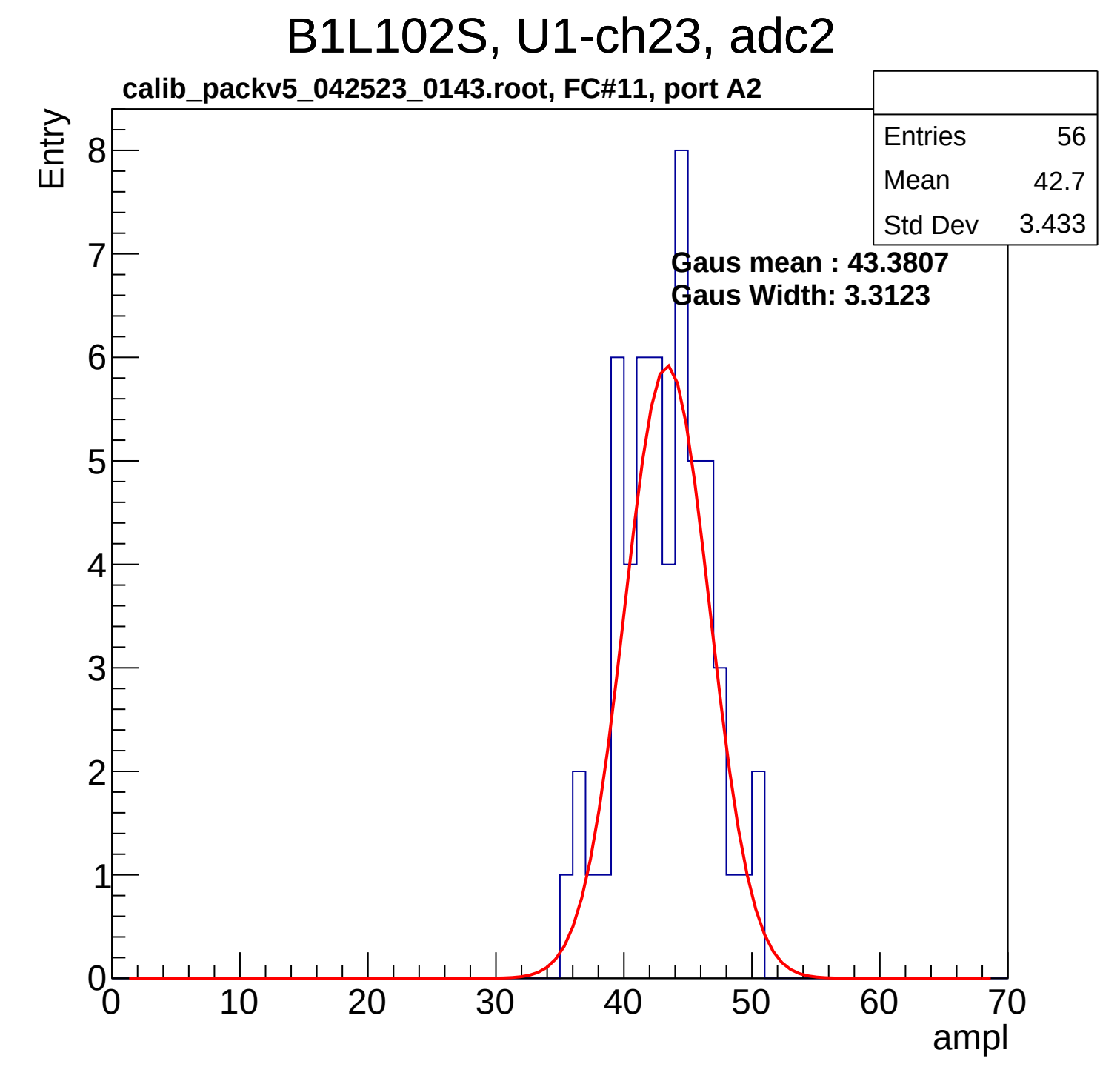
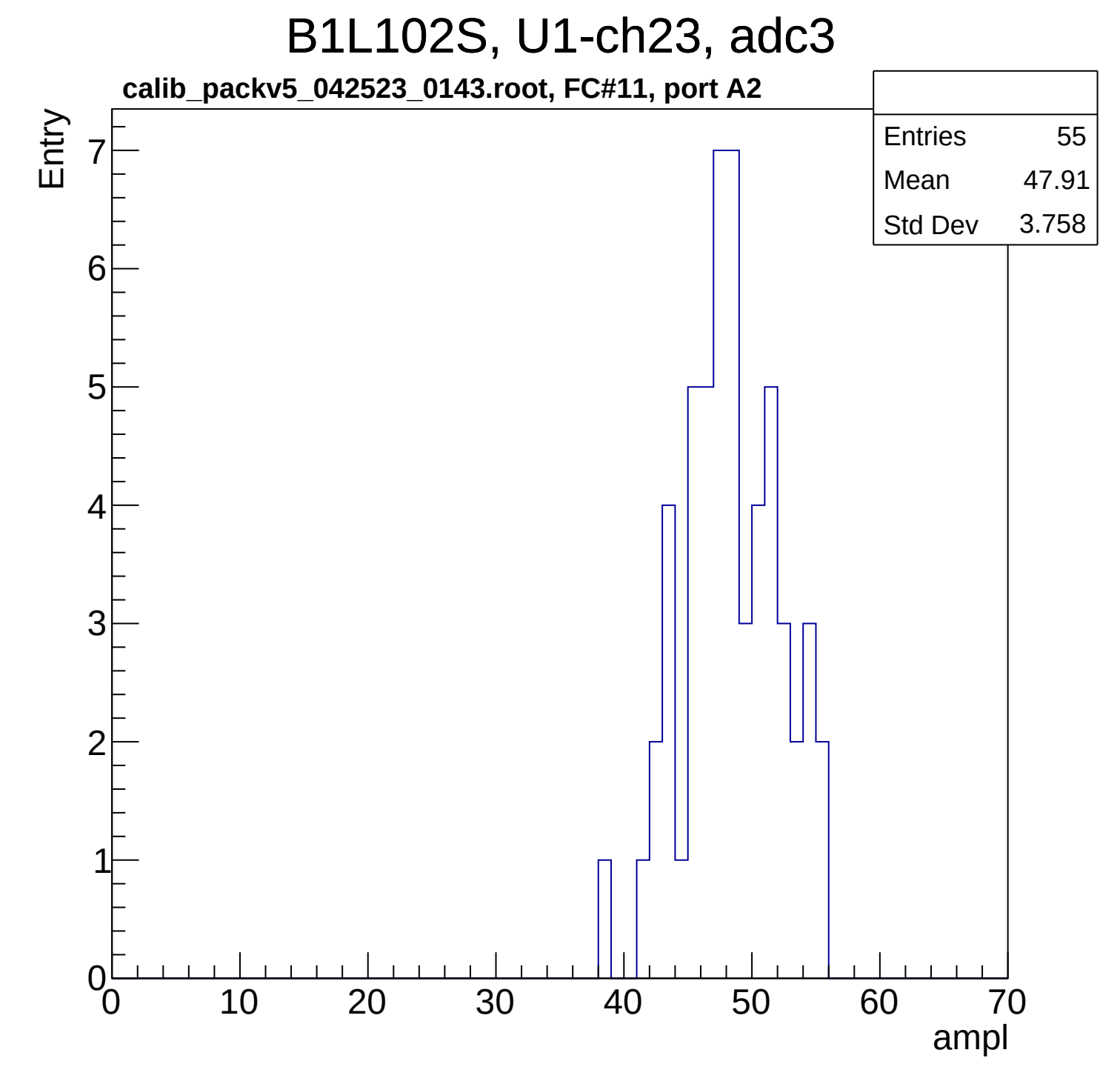
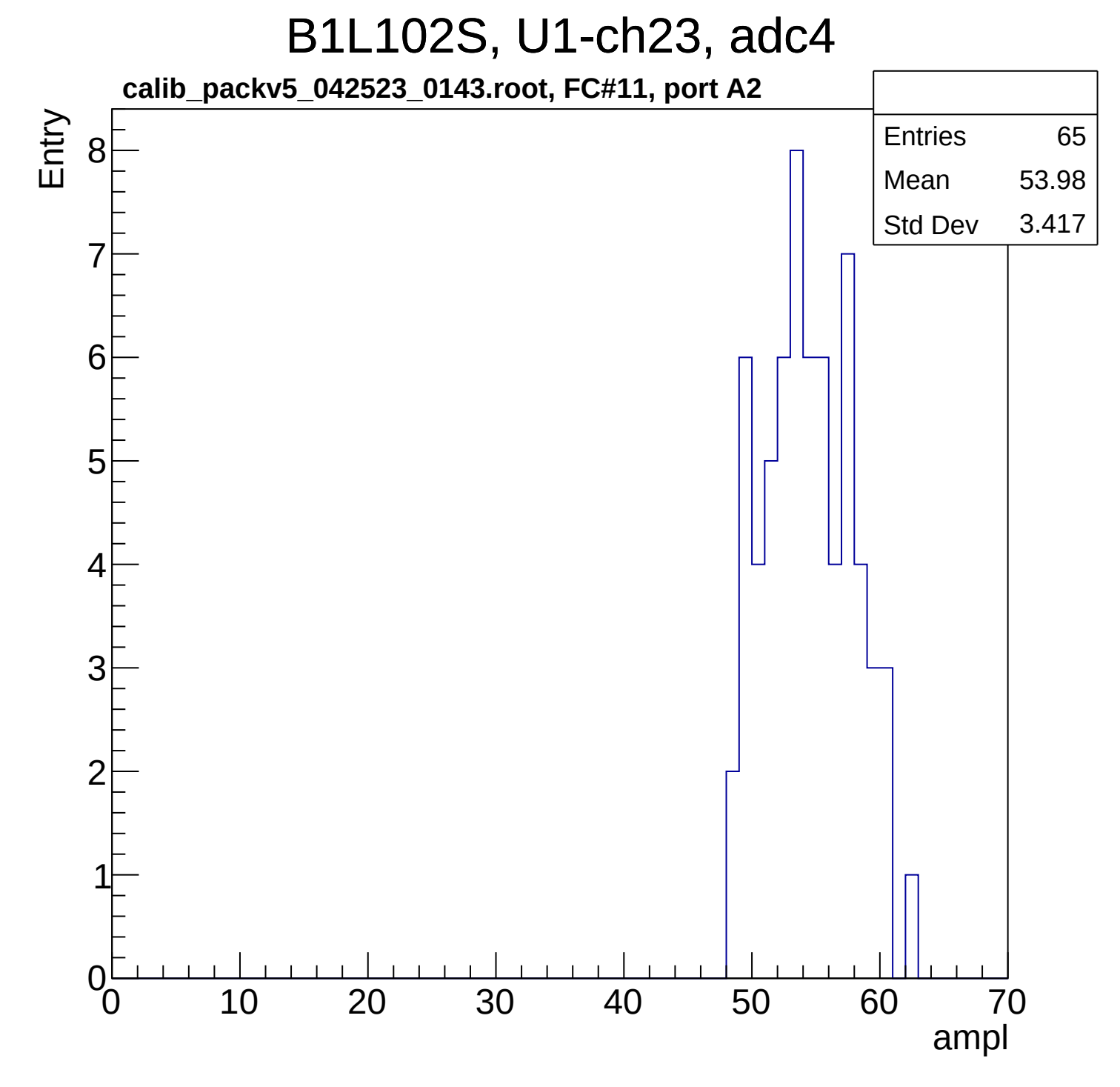
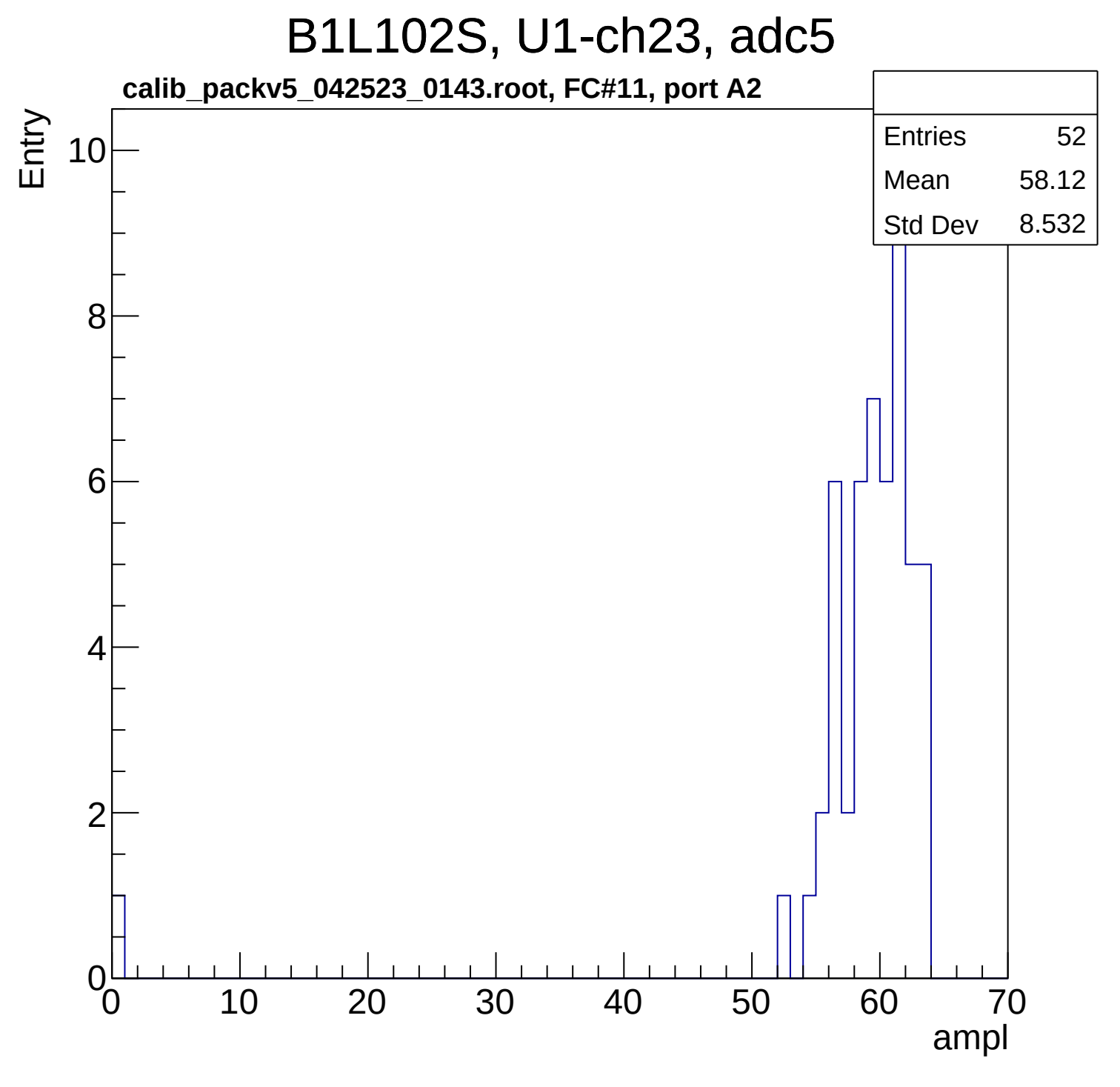
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

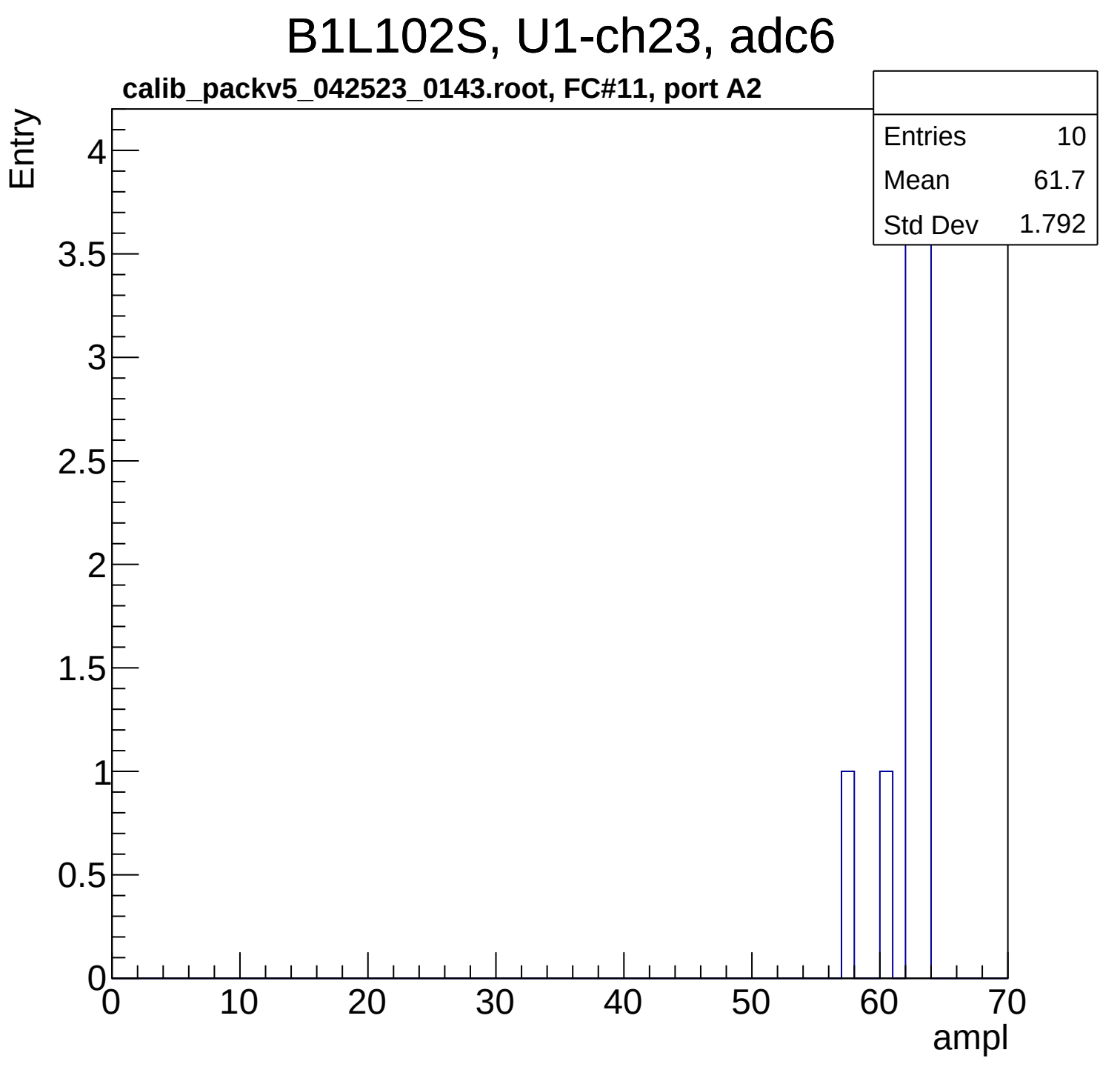

B1L102S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 36.54
Std Dev 3.242
Gaus mean : 37.3186
Gaus Width: 3.2179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

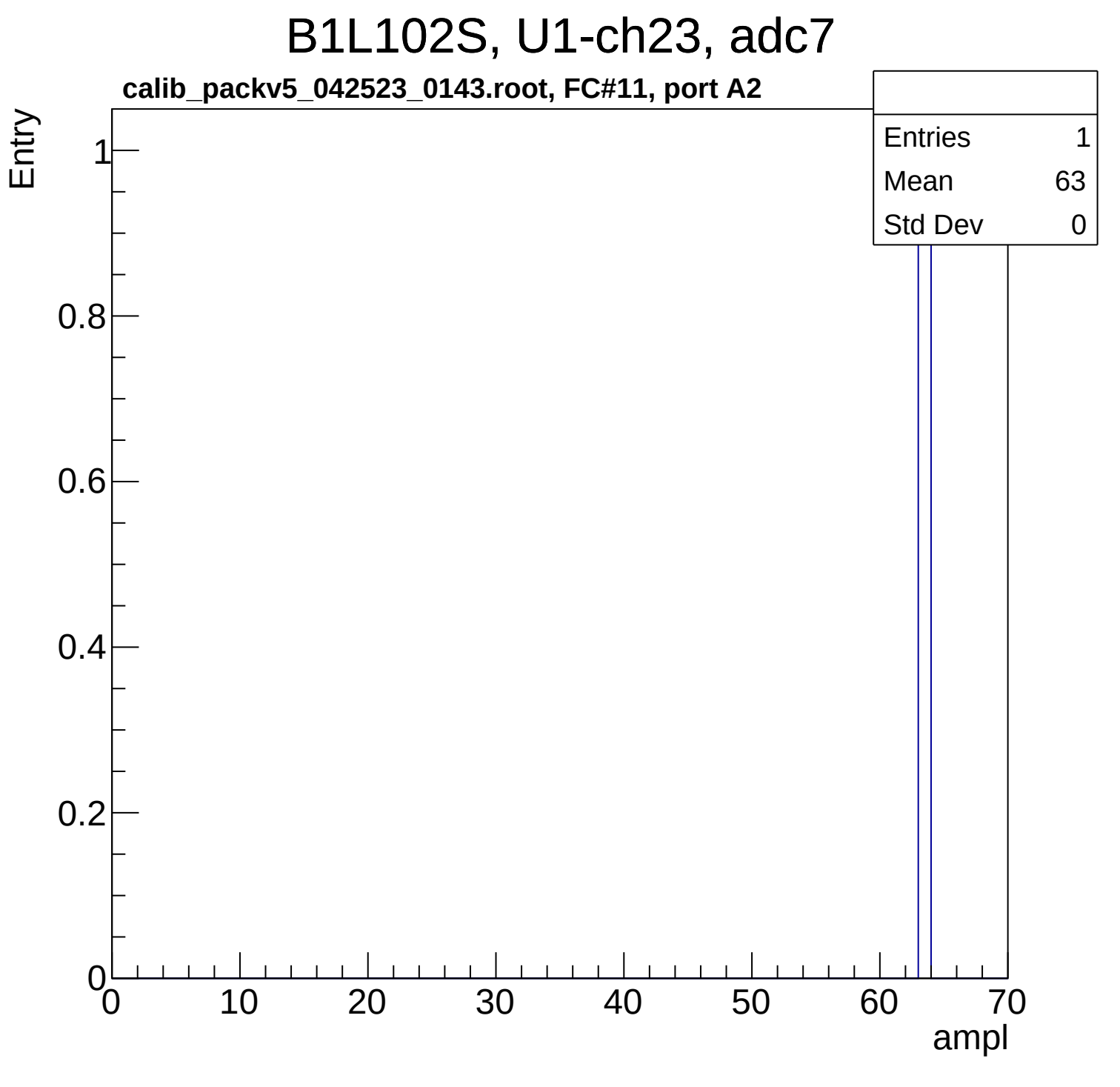

B1L102S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 42.7
Std Dev 3.433
Gaus mean : 43.3807
Gaus Width: 3.3123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

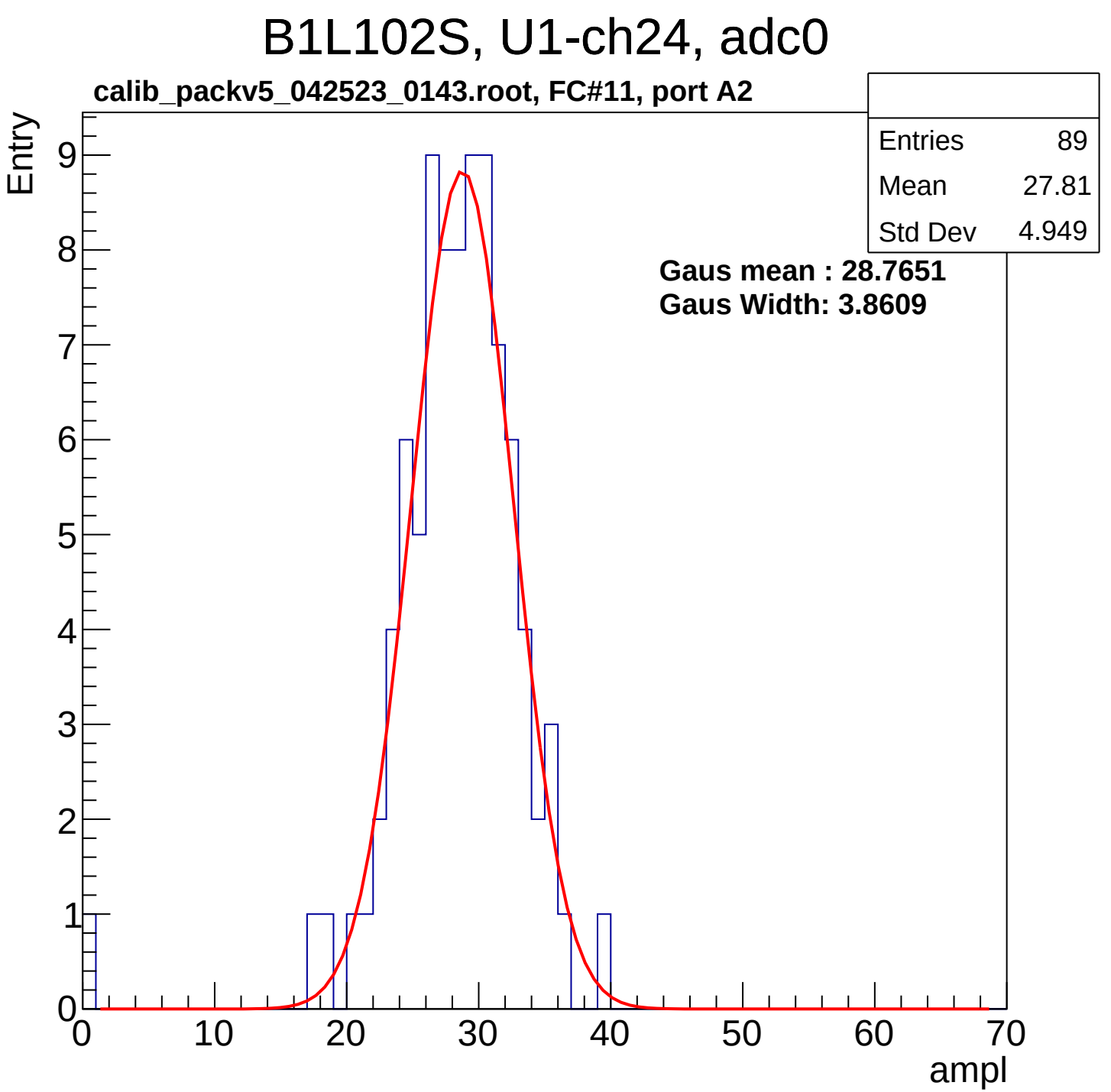

B1L102S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 47.91
Std Dev 3.758
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

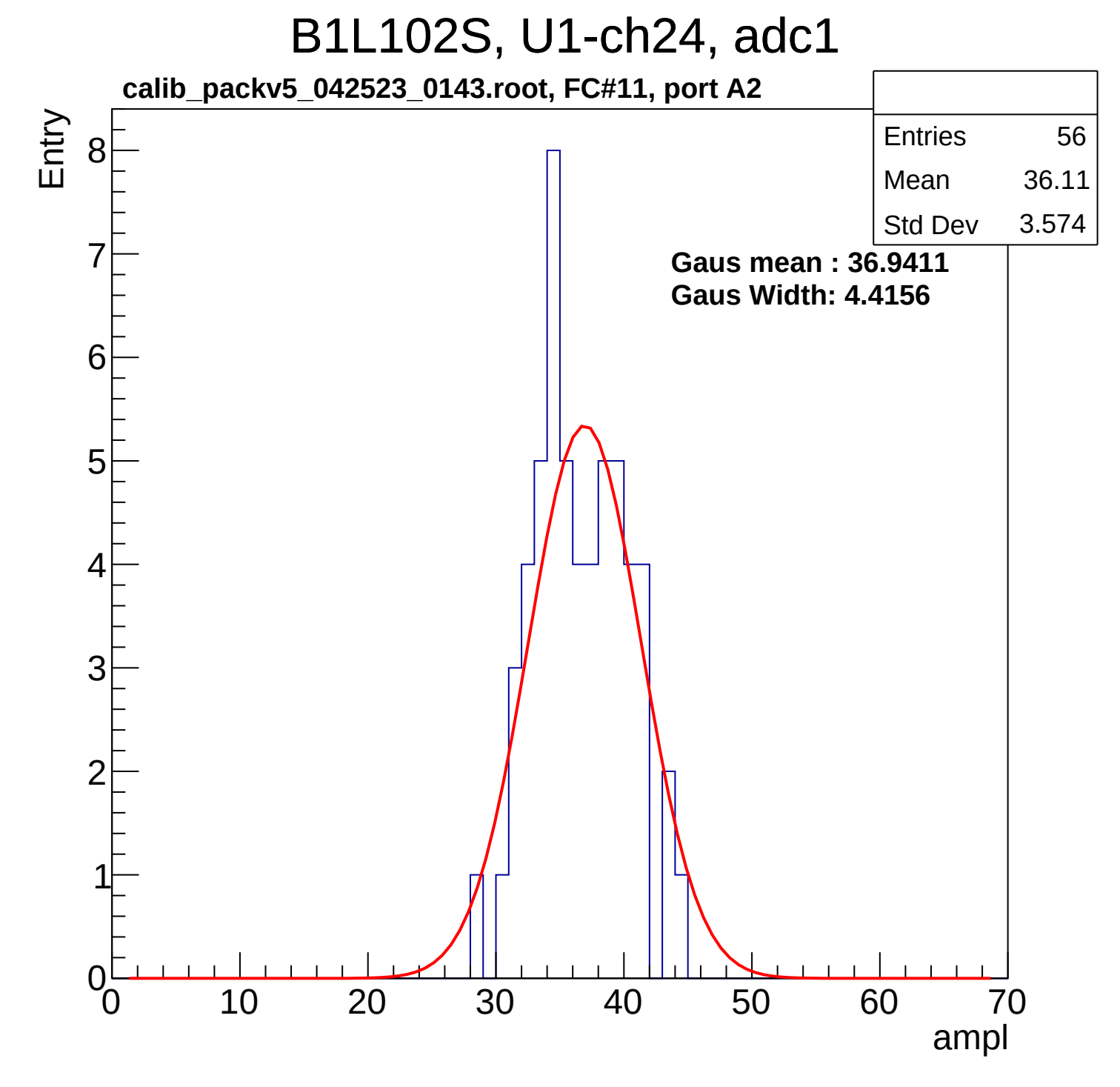

B1L102S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 53.98
Std Dev 3.417
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.12
Std Dev 8.532
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 61.7
Std Dev 1.792
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 27.81
Std Dev 4.949
Gaus mean : 28.7651
Gaus Width: 3.8609
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 36.11
Std Dev 3.574
Gaus mean : 36.9411
Gaus Width: 4.4156
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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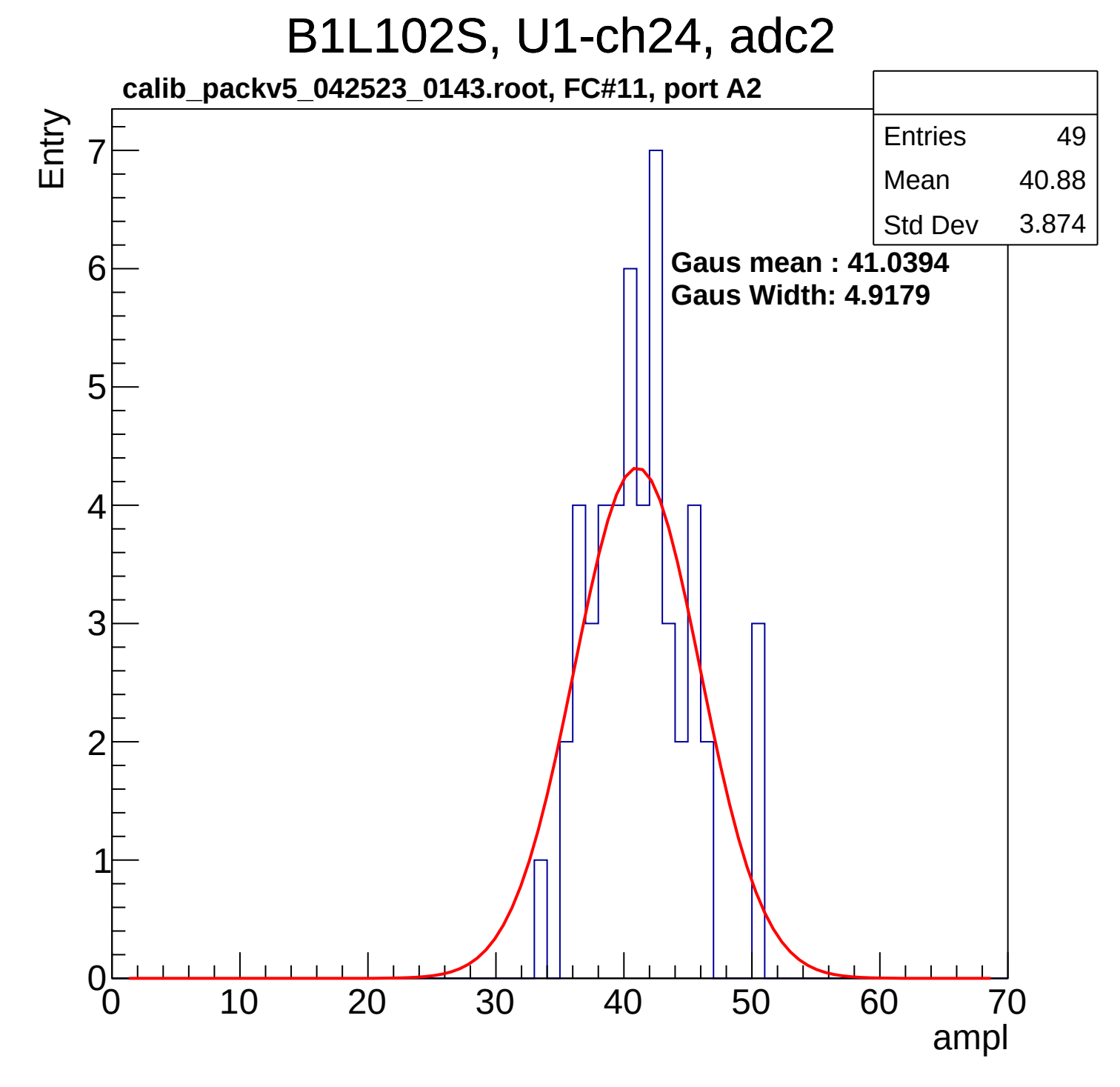
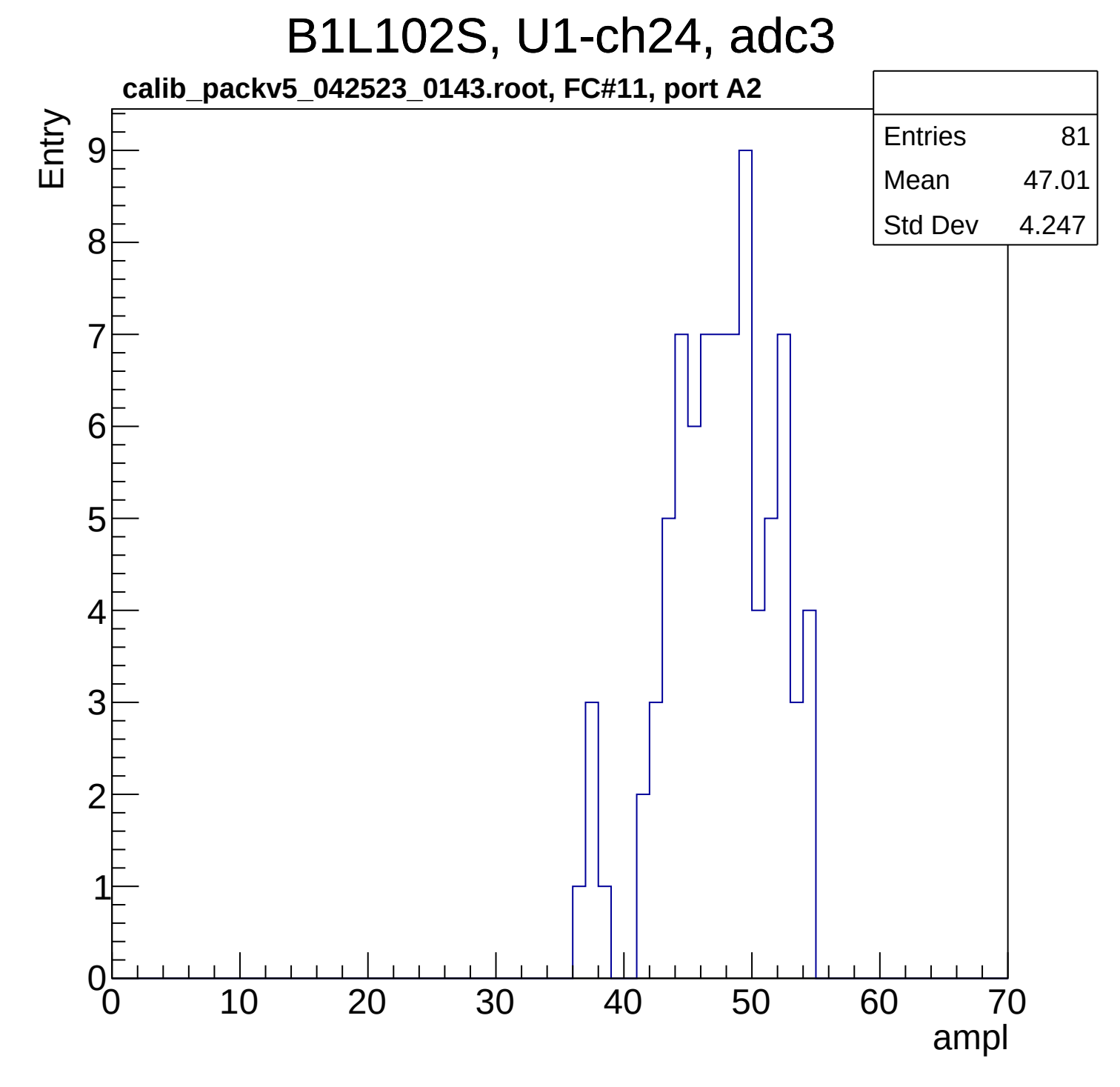
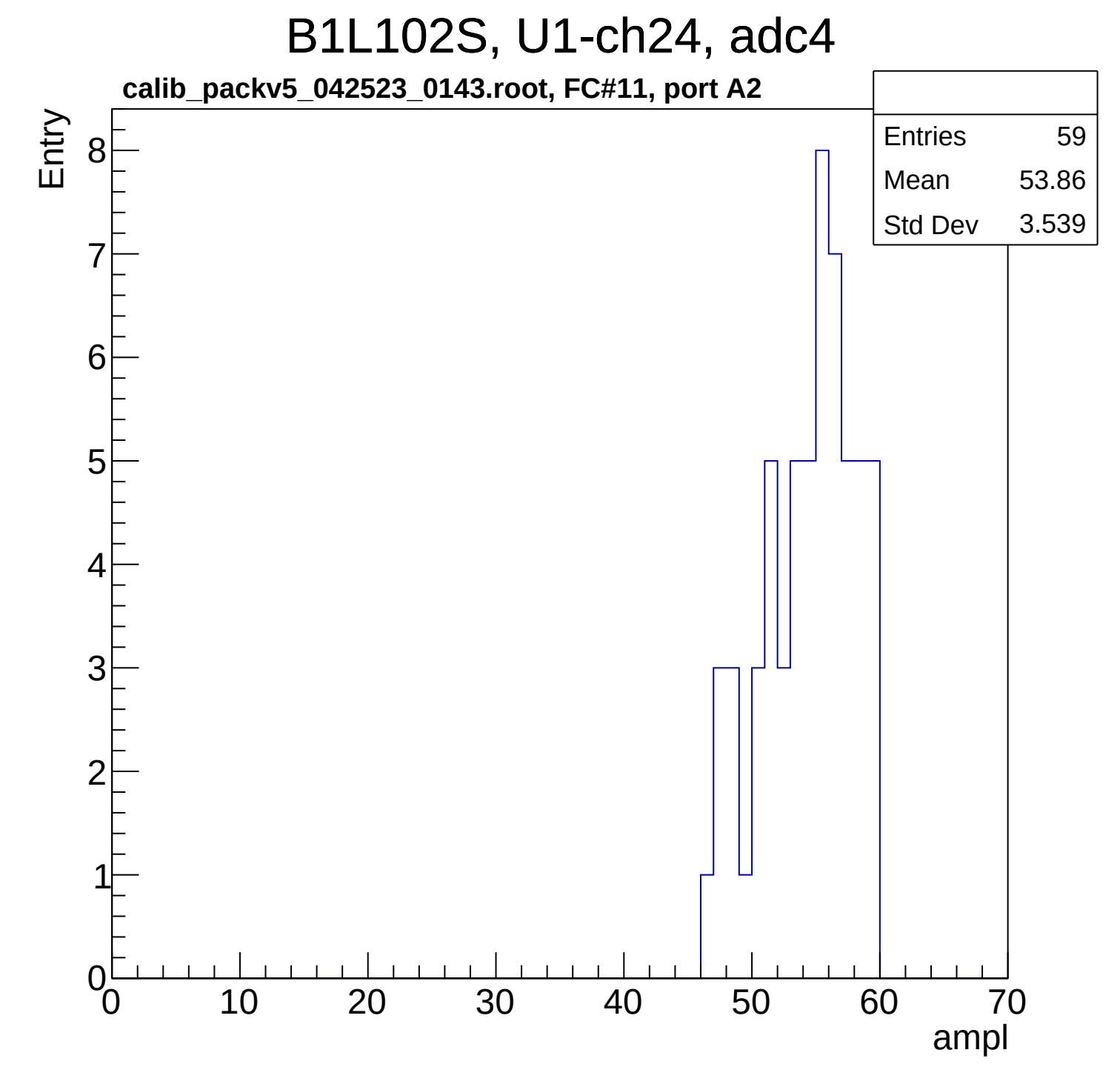
40
50
60
70


B1L102S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 40.88
Std Dev 3.874
Gaus mean : 41.0394
Gaus Width: 4.9179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 47.01
Std Dev 4.247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 53.86
Std Dev 3.539
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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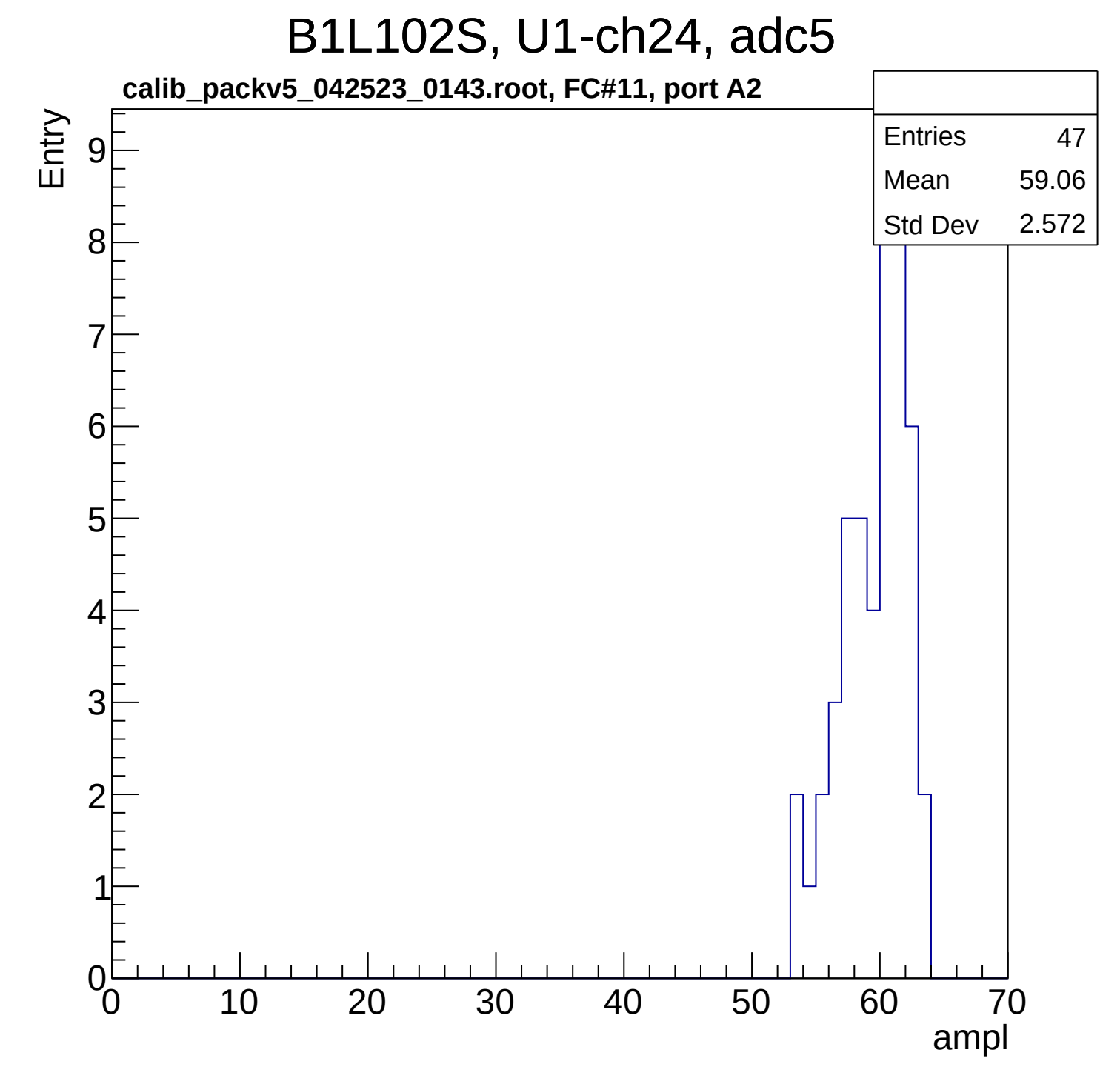
40
50
60
70


B1L102S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 59.06
Std Dev 2.572
Entry
ampl
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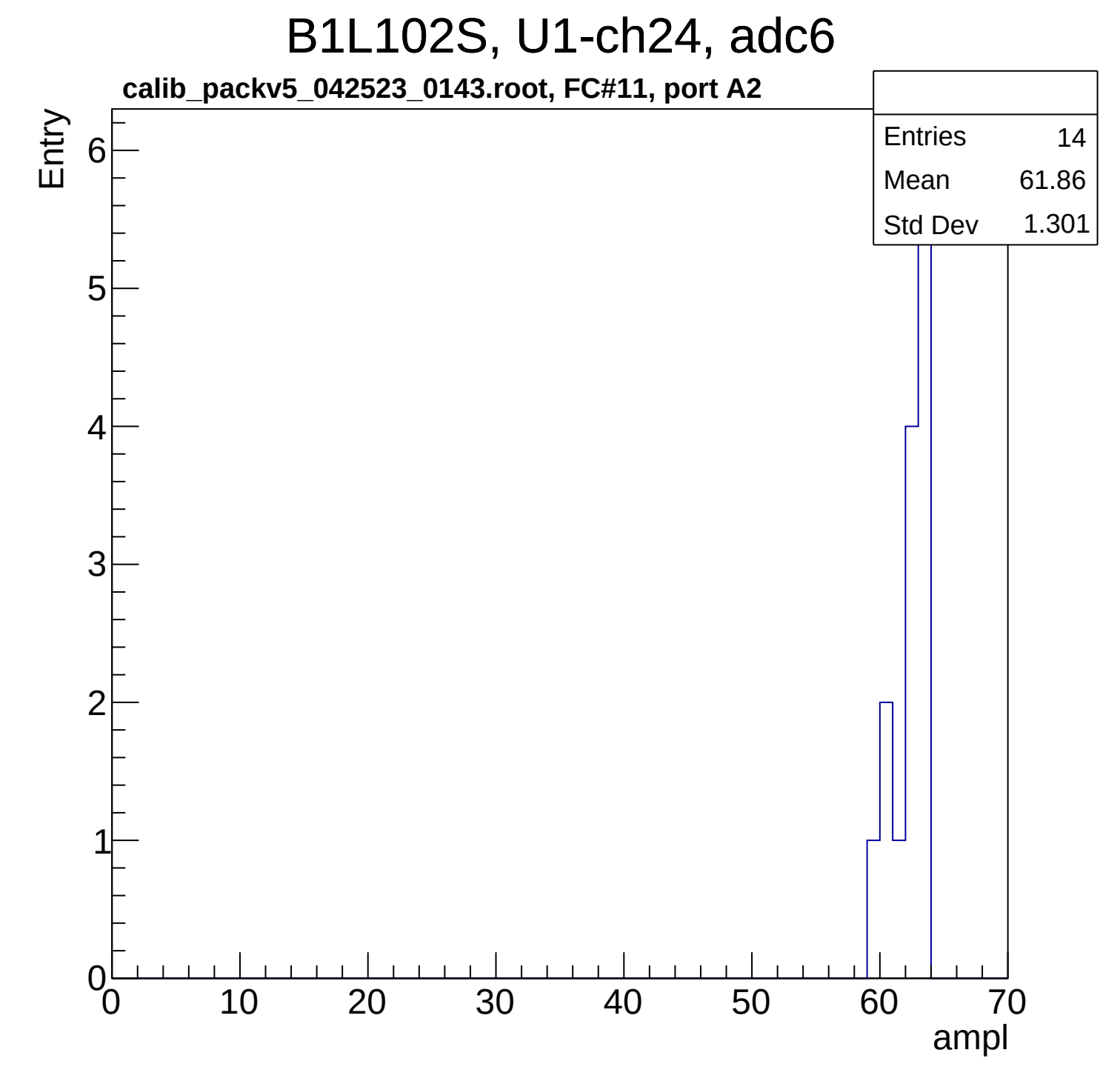
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61.86
Std Dev 1.301
Entry
ampl
0
1
2
3
4
5
6
0
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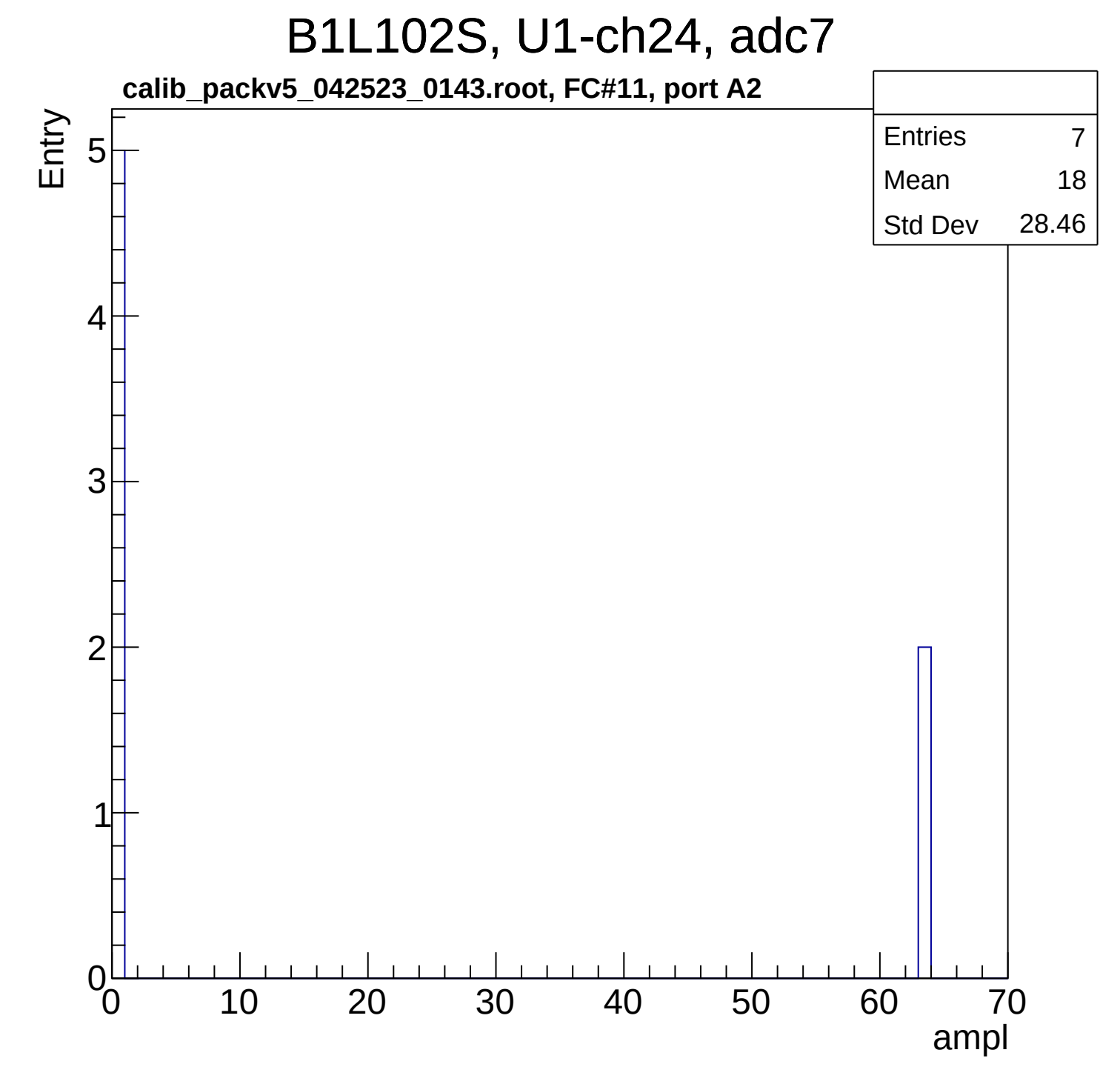
20
30
40
50
60
70


B1L102S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 18
Std Dev 28.46
Entry
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2
3
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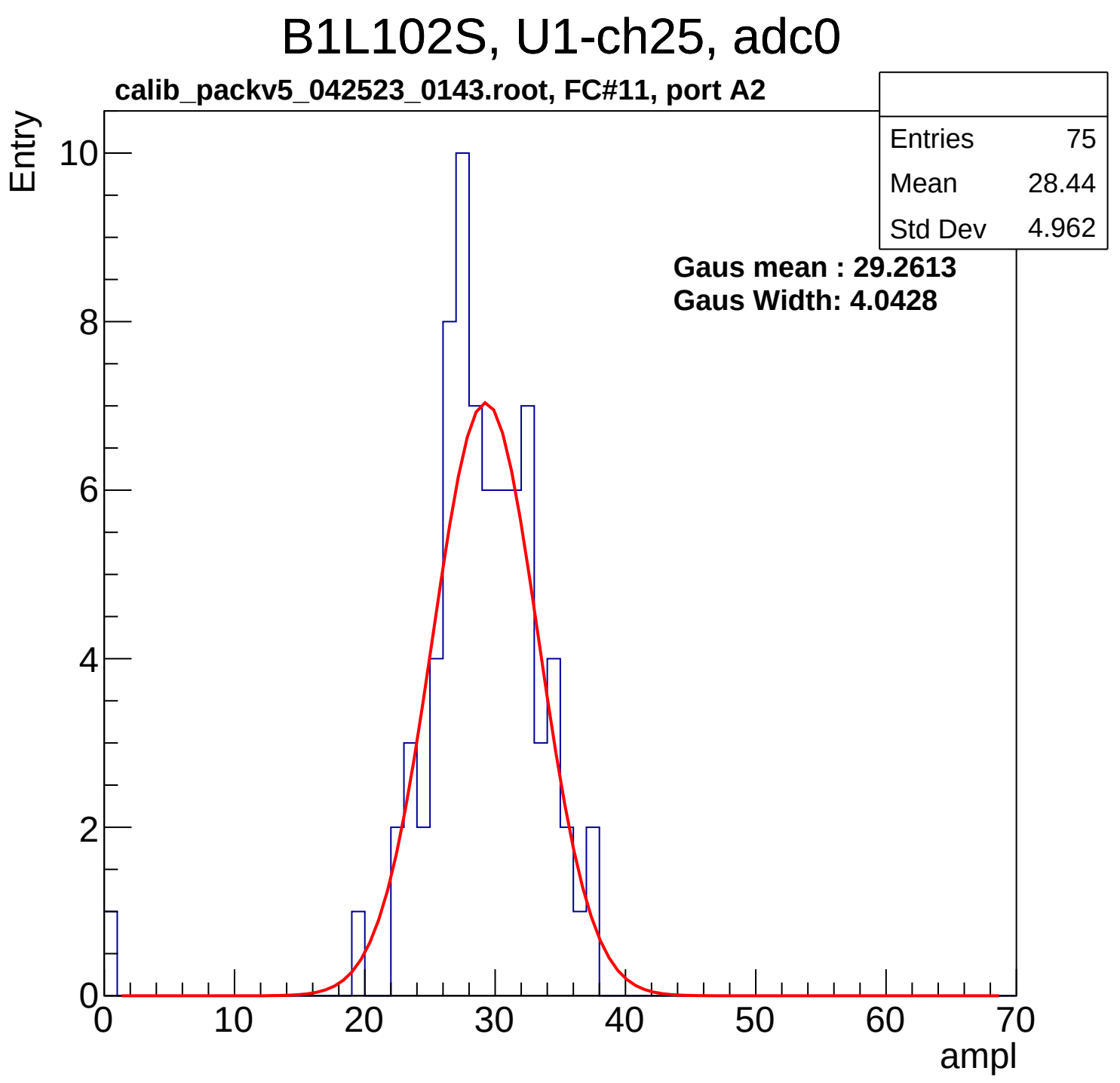
50
60
70
ampl


B1L102S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 28.44
Std Dev 4.962
Gaus mean : 29.2613
Gaus Width: 4.0428
Entry
ampl
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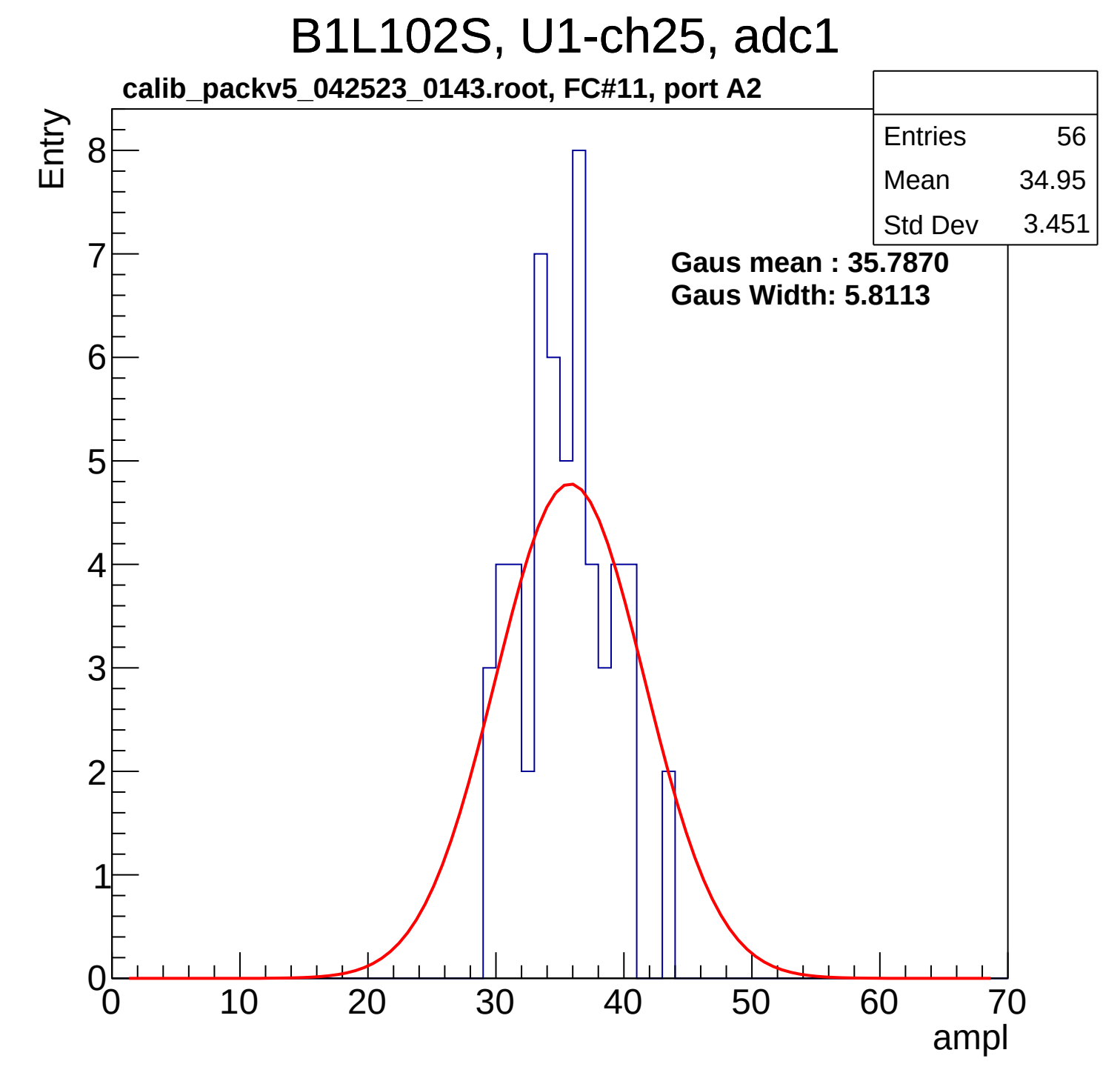
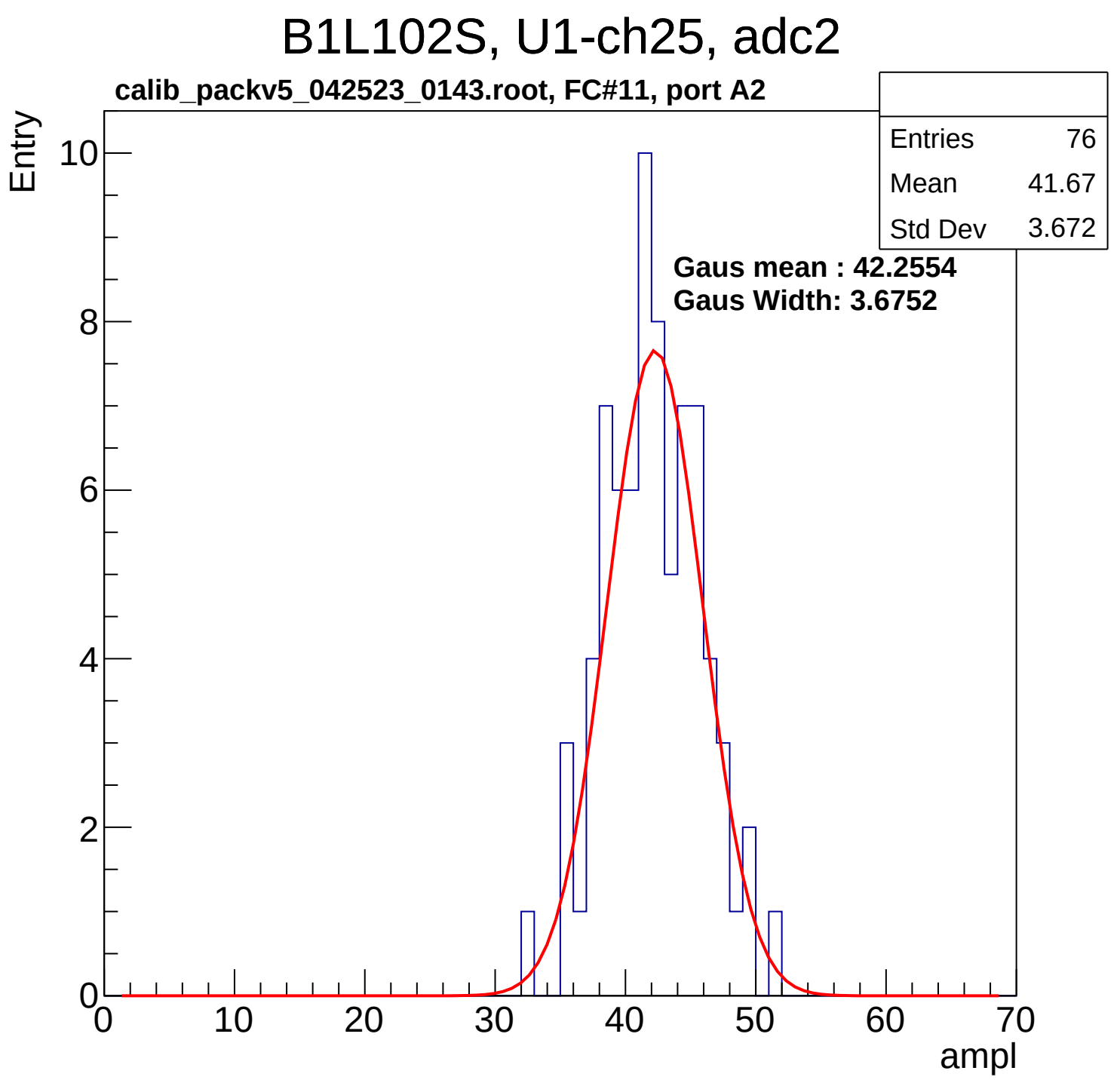
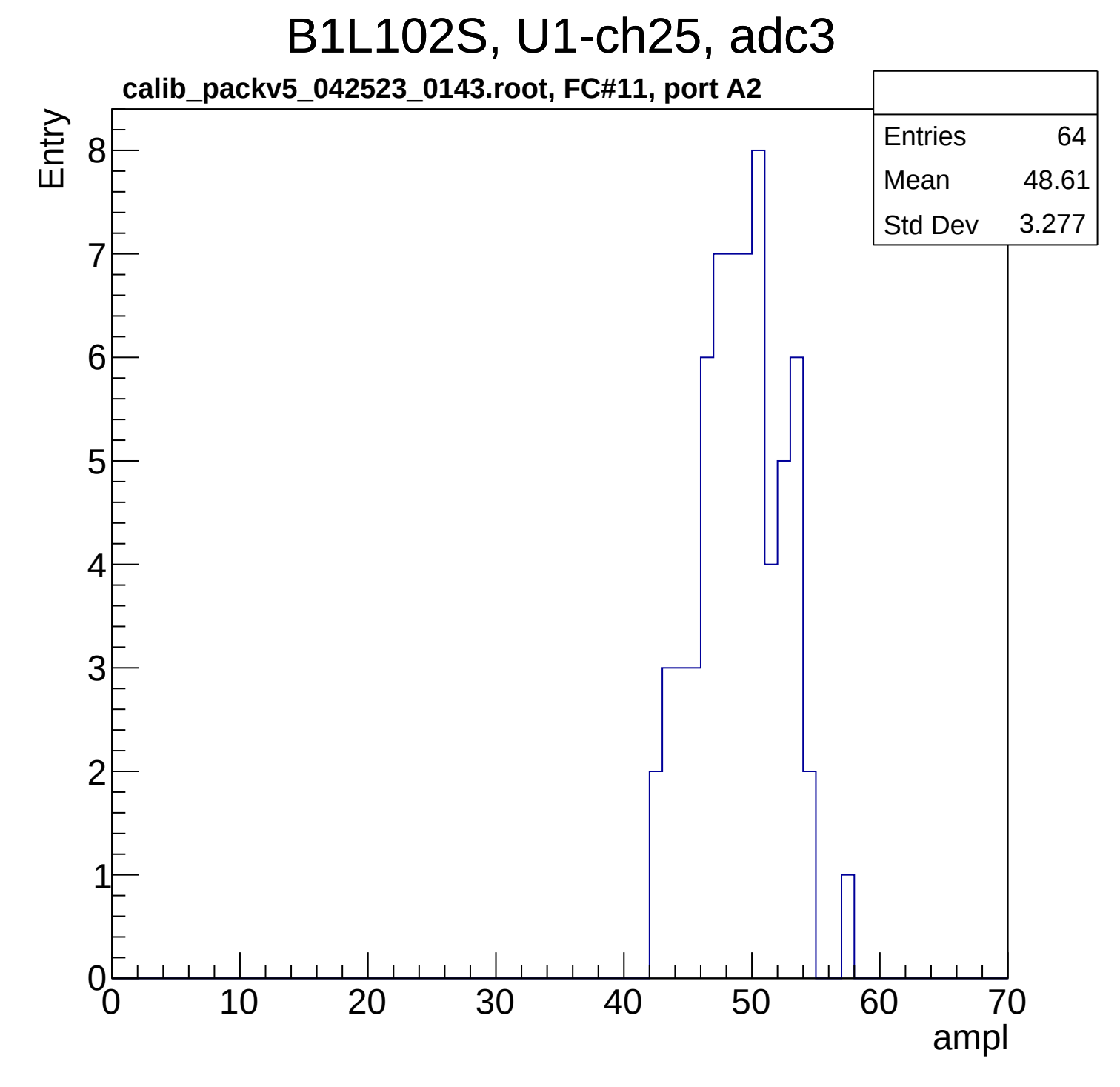
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 34.95
Std Dev 3.451
Gaus mean : 35.7870
Gaus Width: 5.8113
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 41.67
Std Dev 3.672
Gaus mean : 42.2554
Gaus Width: 3.6752
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 48.61
Std Dev 3.277
Entry
ampl
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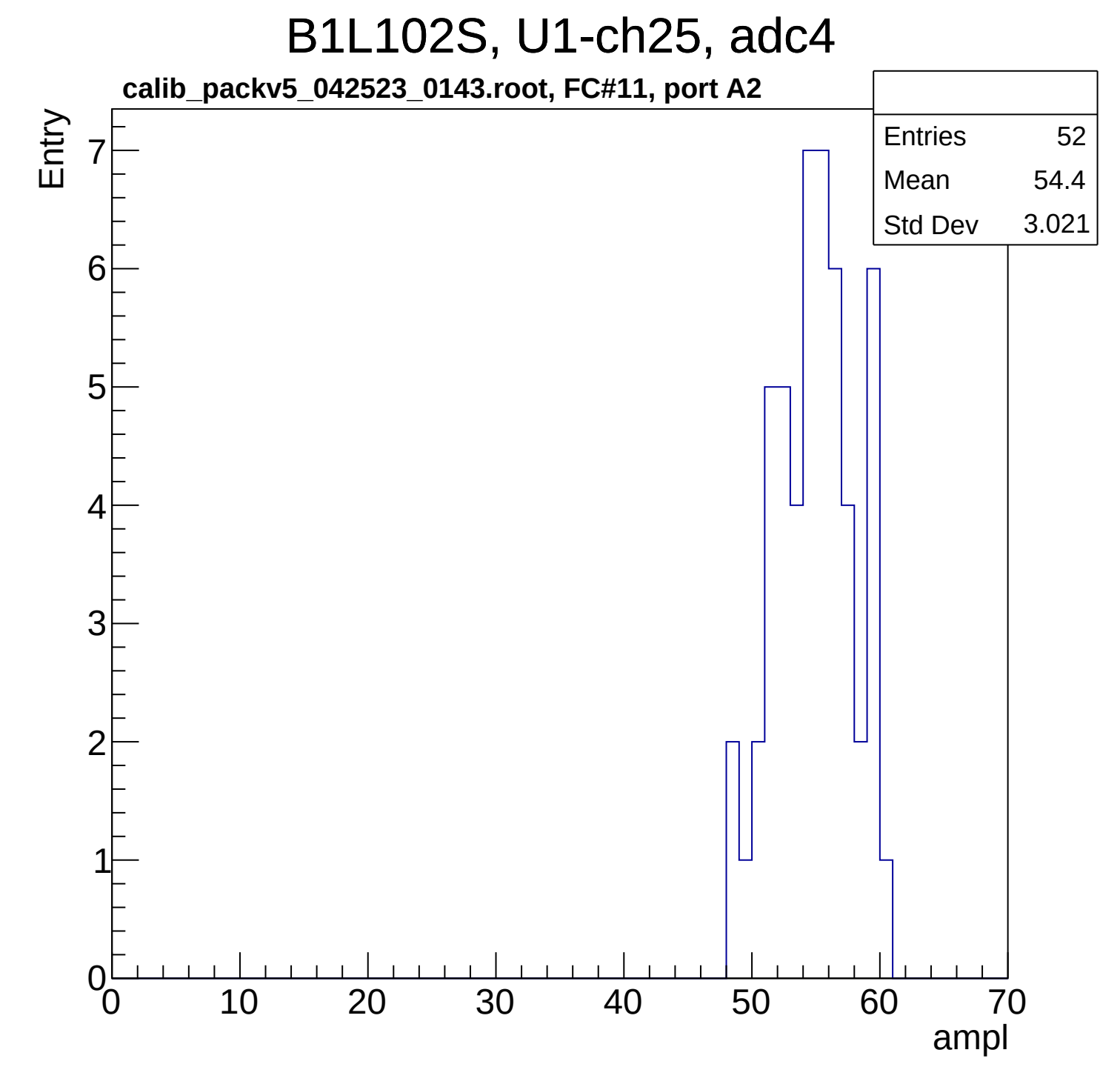
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 54.4
Std Dev 3.021
Entry
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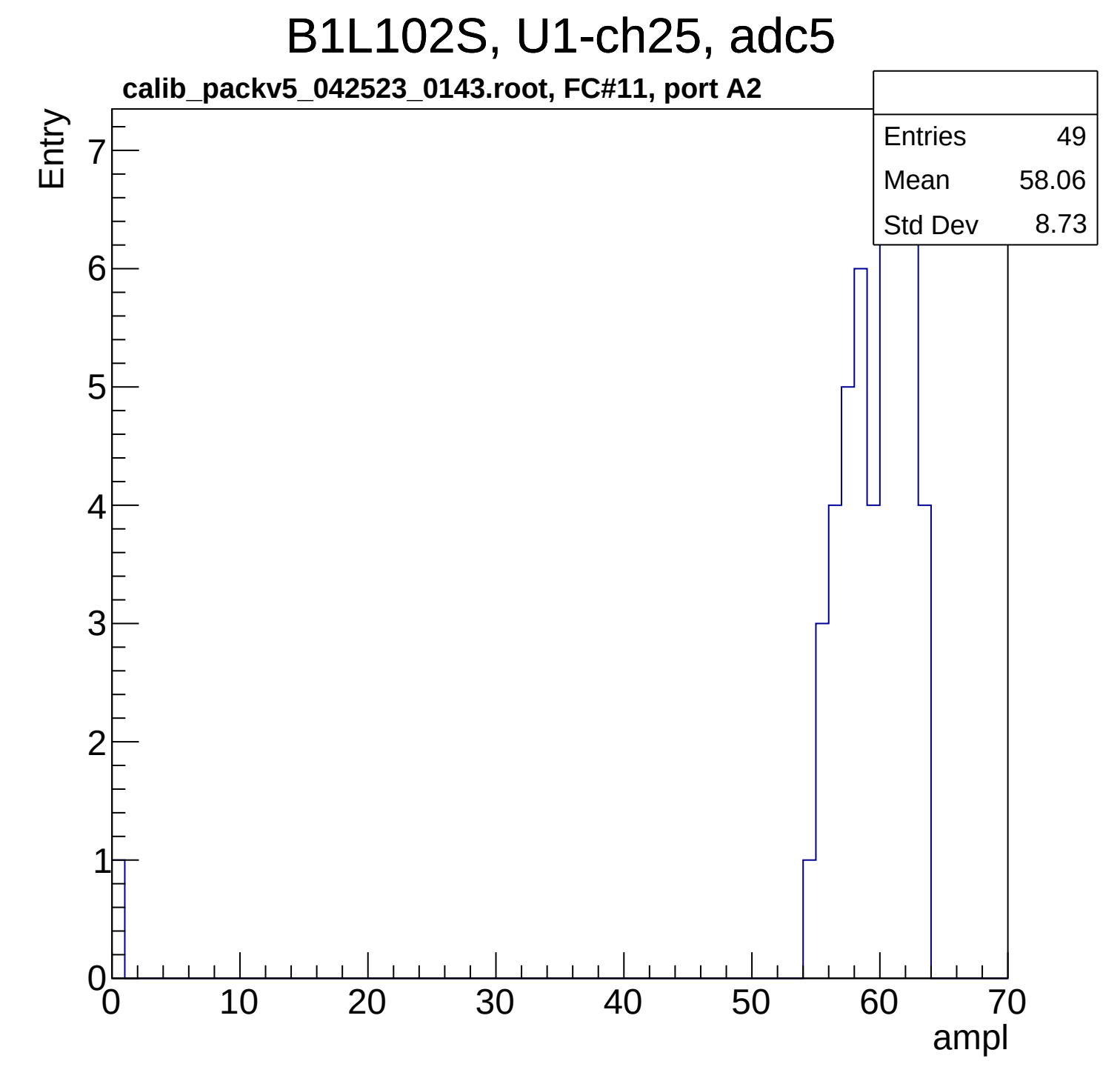
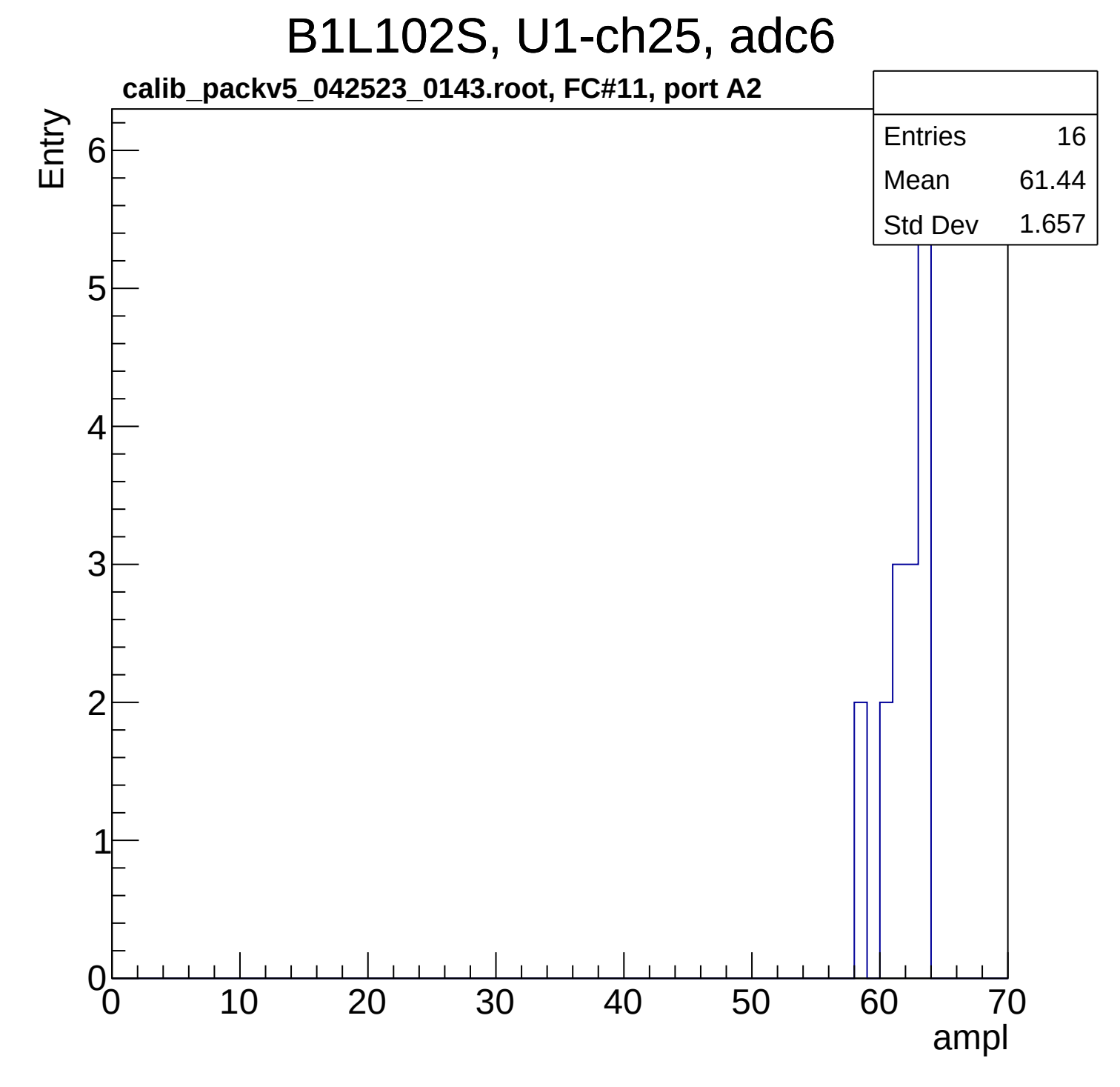
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.06
Std Dev 8.73
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 61.44
Std Dev 1.657
Entry
0
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4
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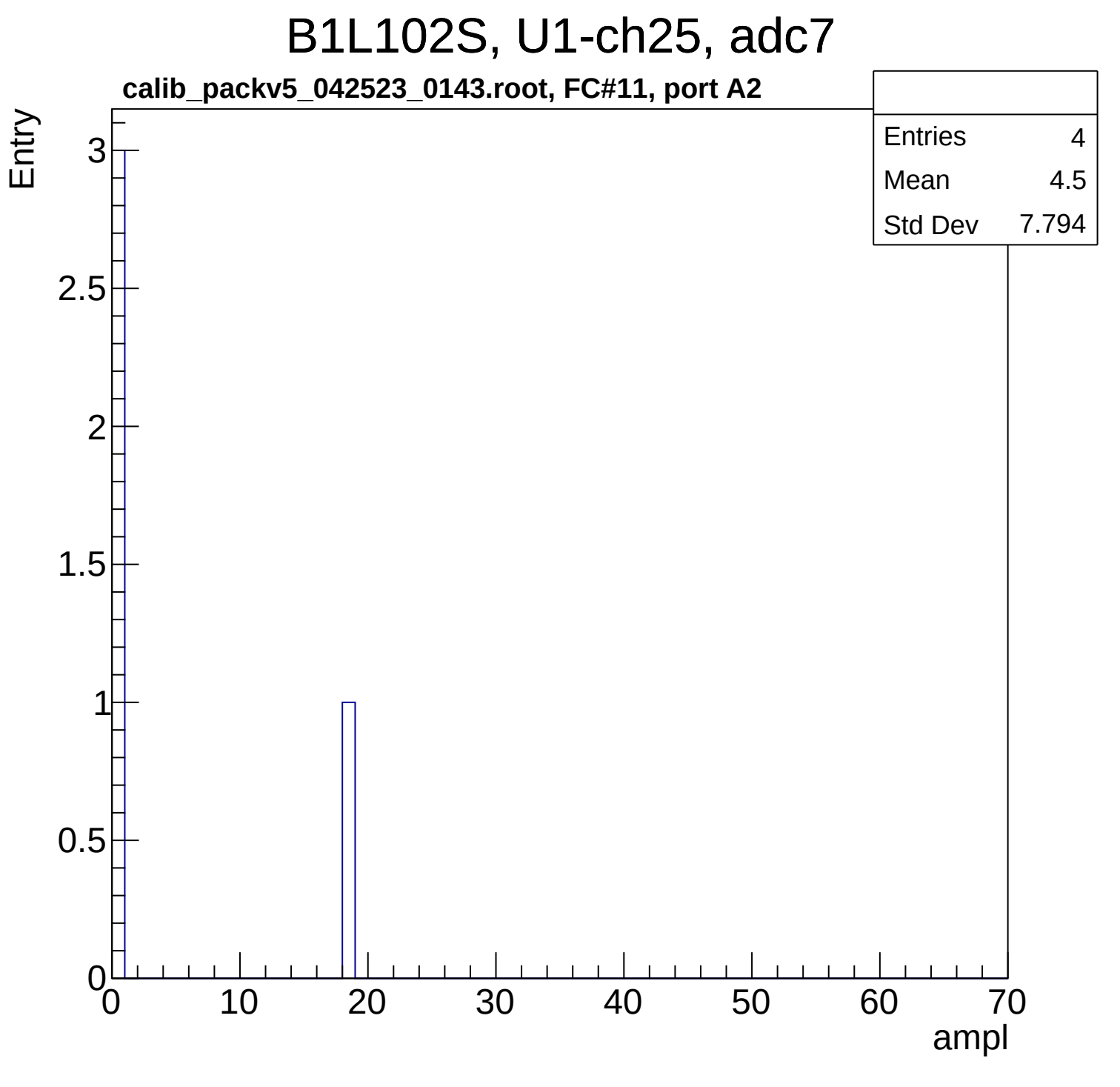
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 4.5
Std Dev 7.794
Entry
ampl
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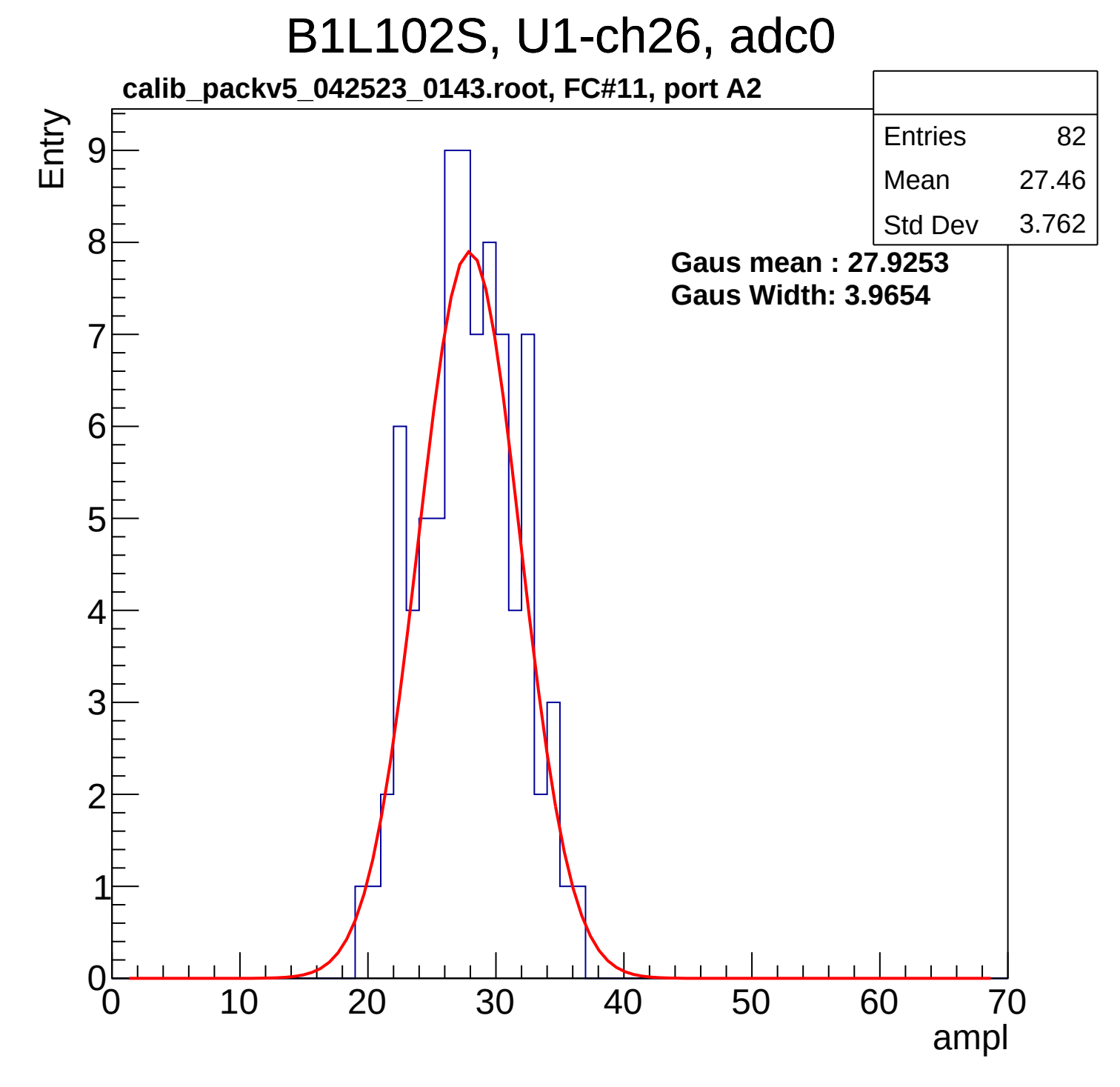
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 27.46
Std Dev 3.762
Gaus mean : 27.9253
Gaus Width: 3.9654
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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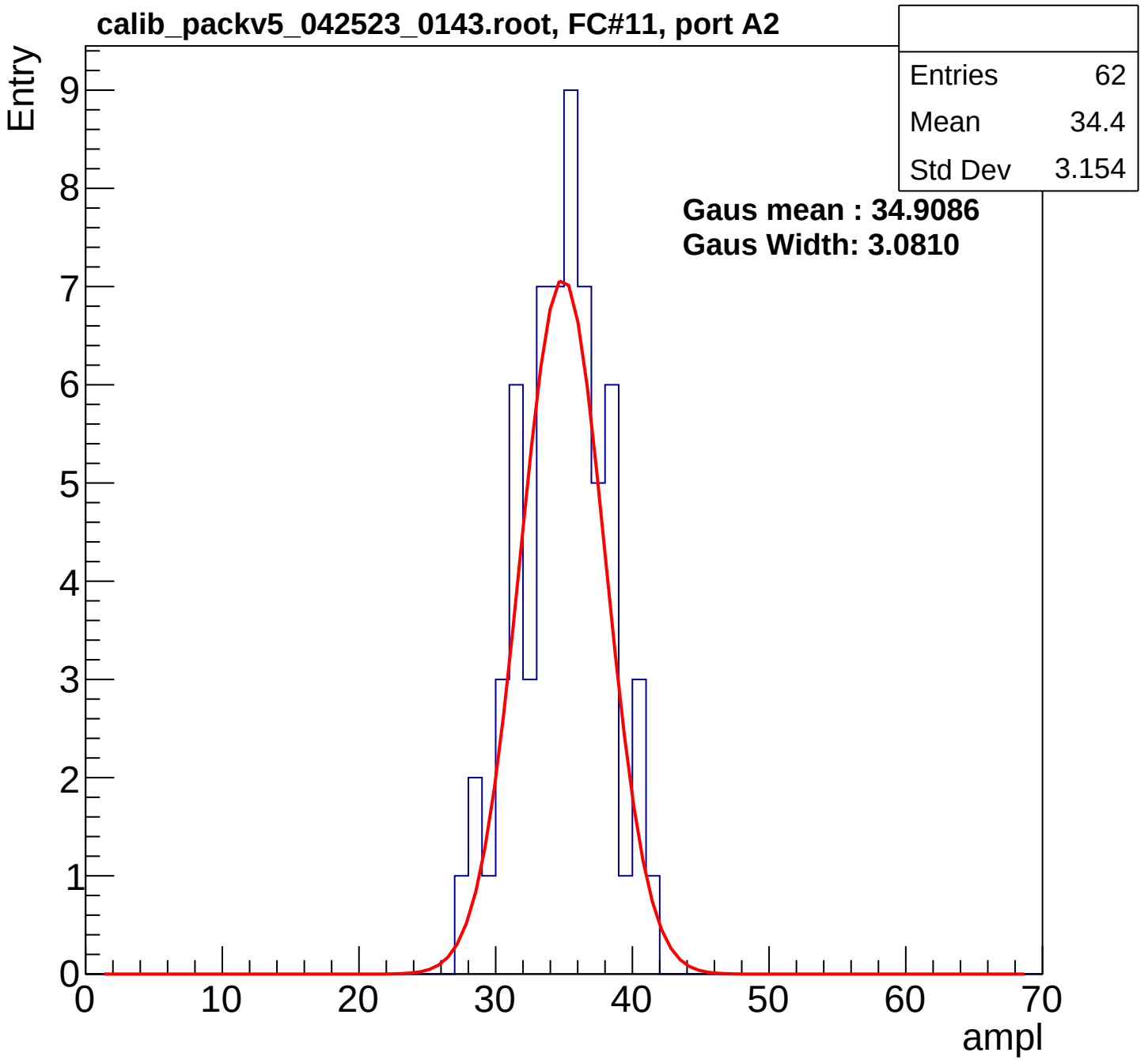
B1L102S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 34.4
Std Dev 3.154
Gaus mean : 34.9086
Gaus Width: 3.0810
Entry
ampl
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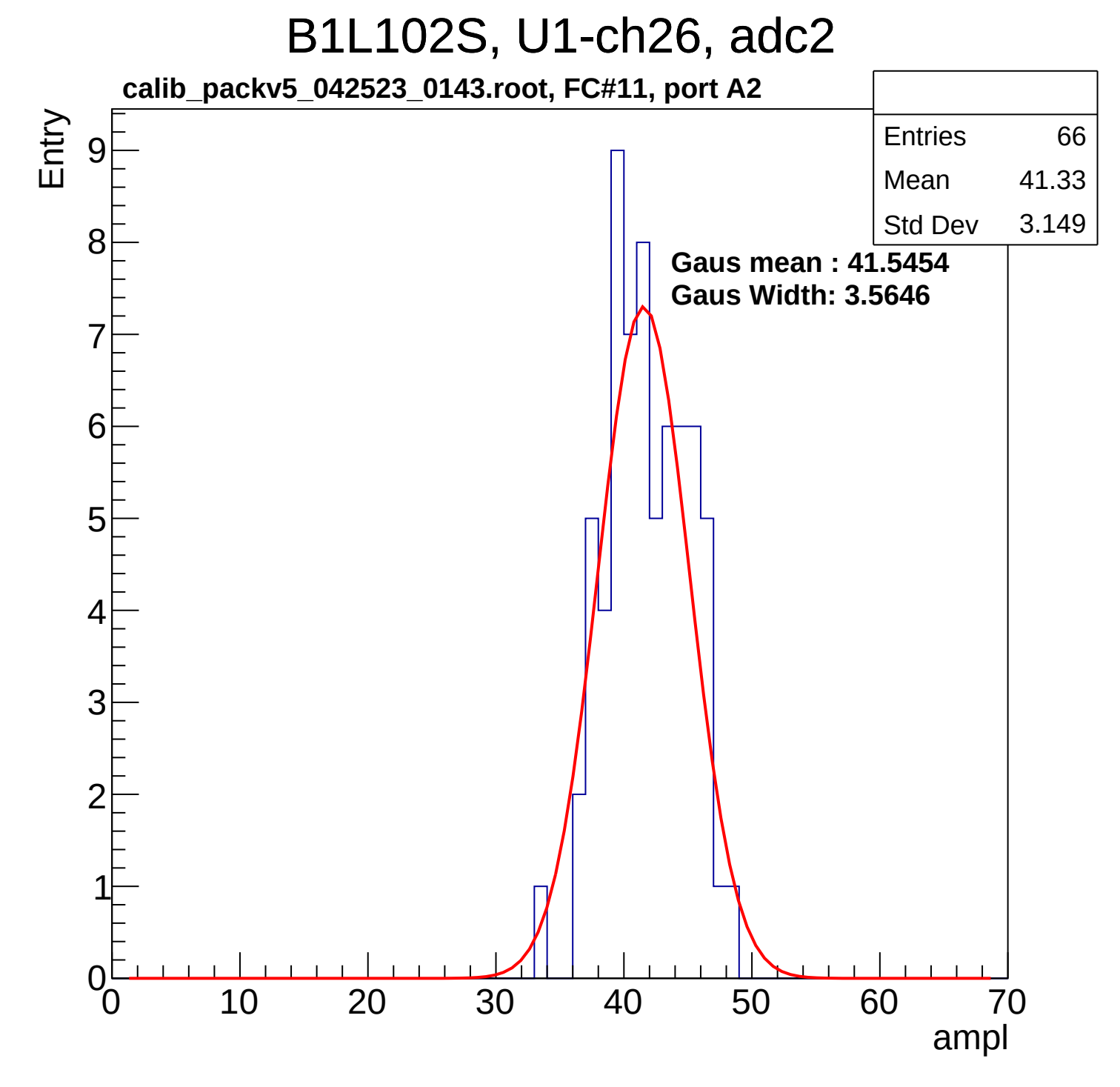
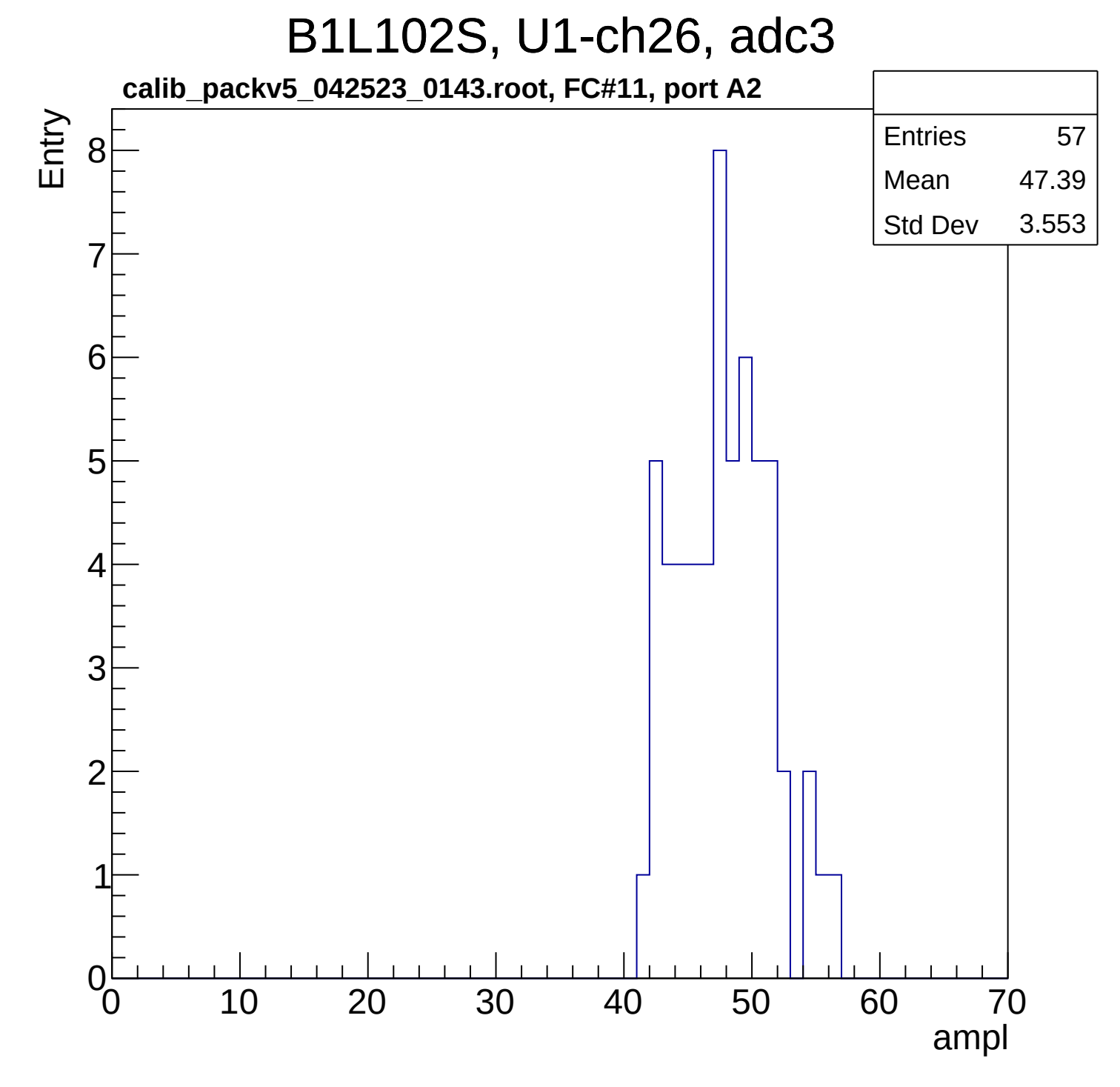
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 41.33
Std Dev 3.149
Gaus mean : 41.5454
Gaus Width: 3.5646
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 47.39
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
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0
10
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40
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60
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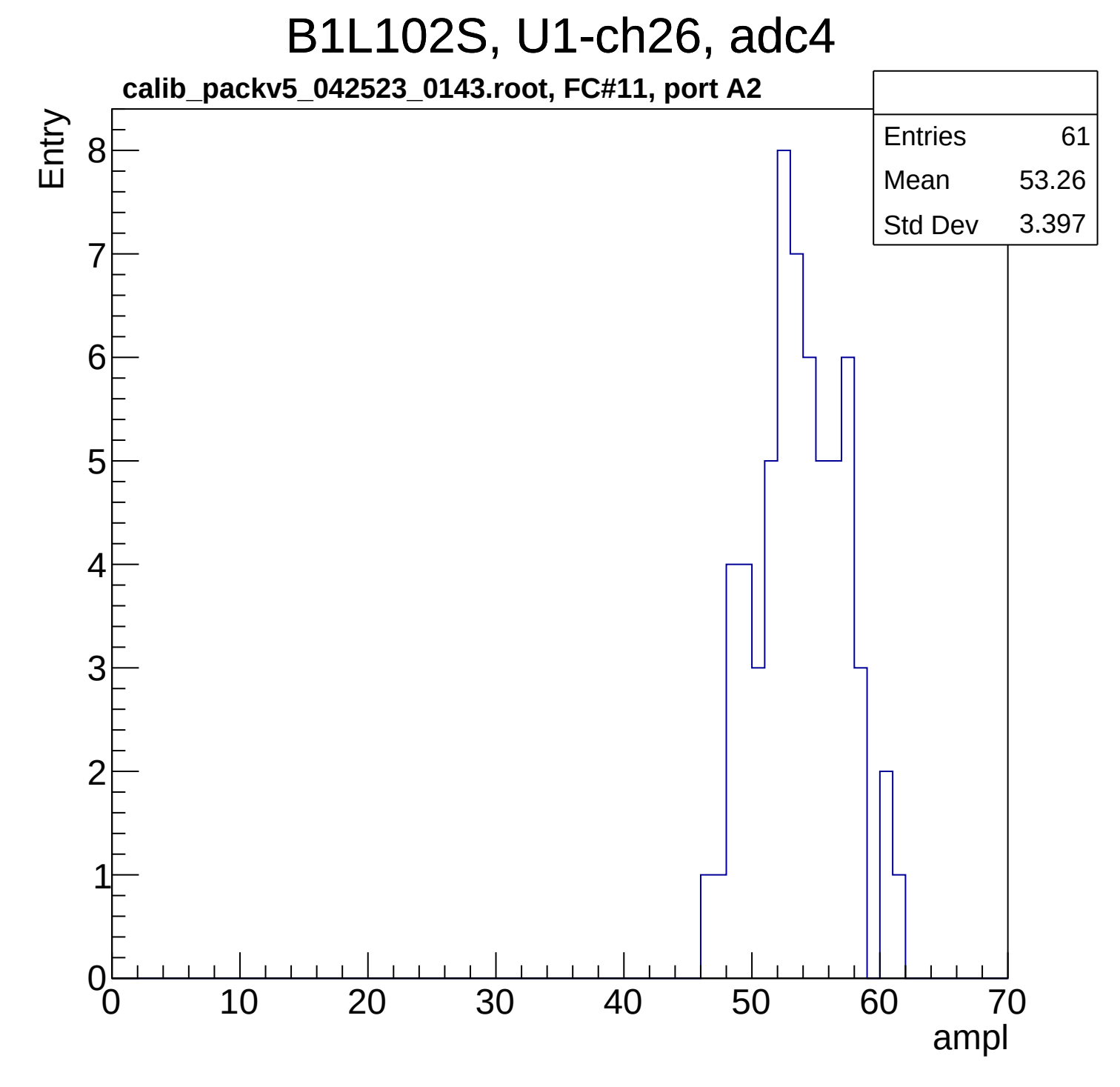
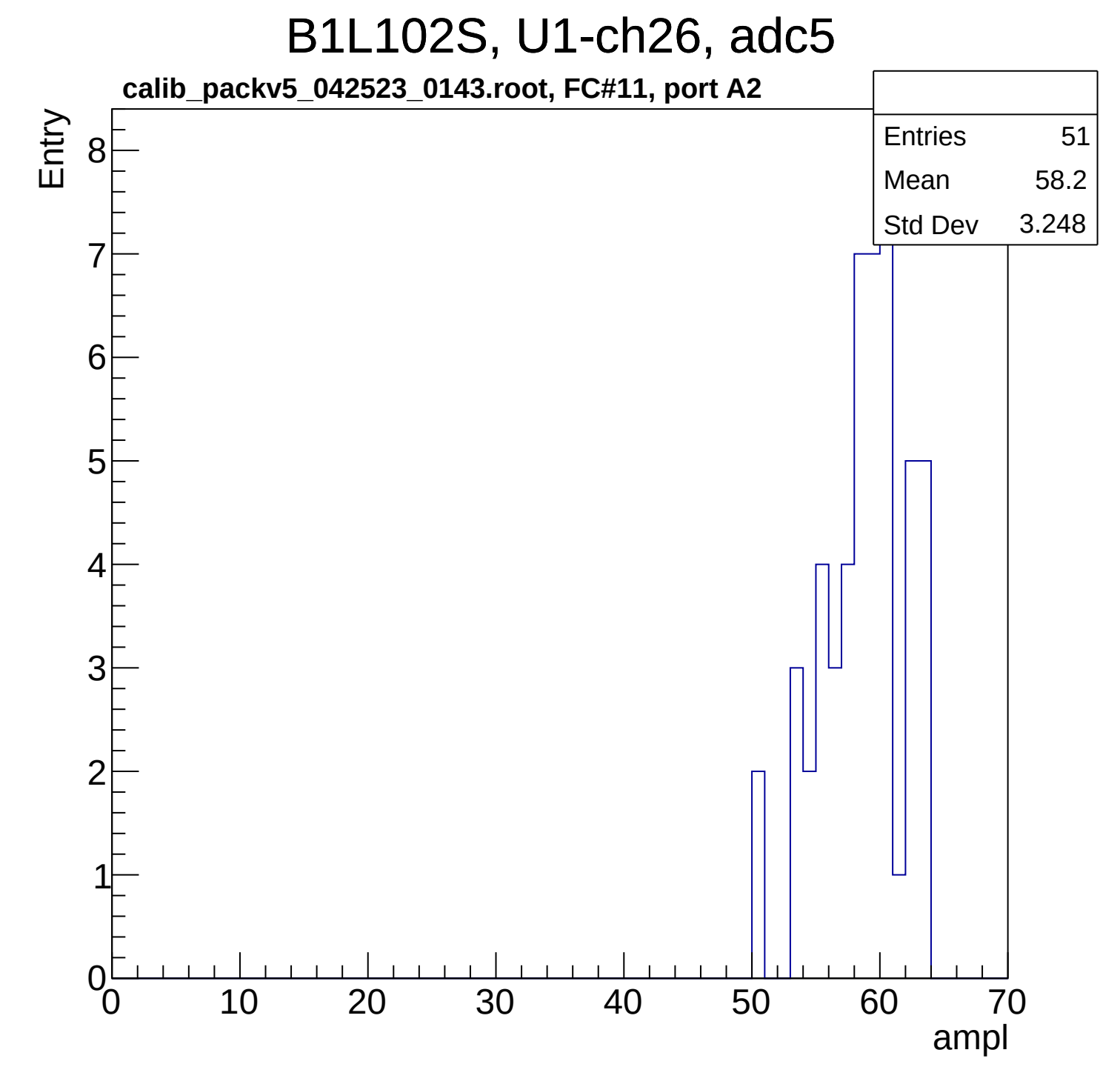

B1L102S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 53.26
Std Dev 3.397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58.2
Std Dev 3.248
Entry
ampl
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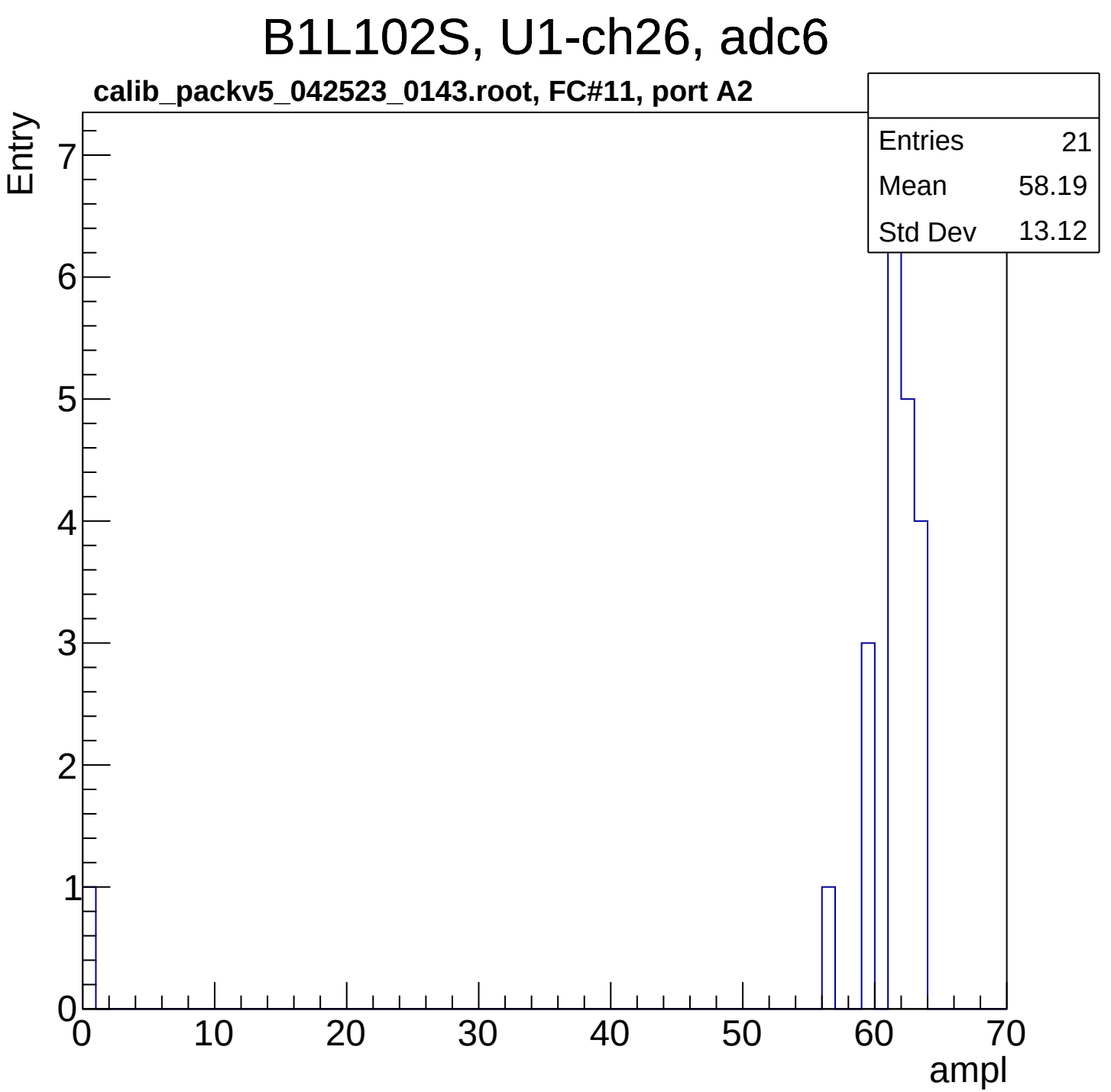
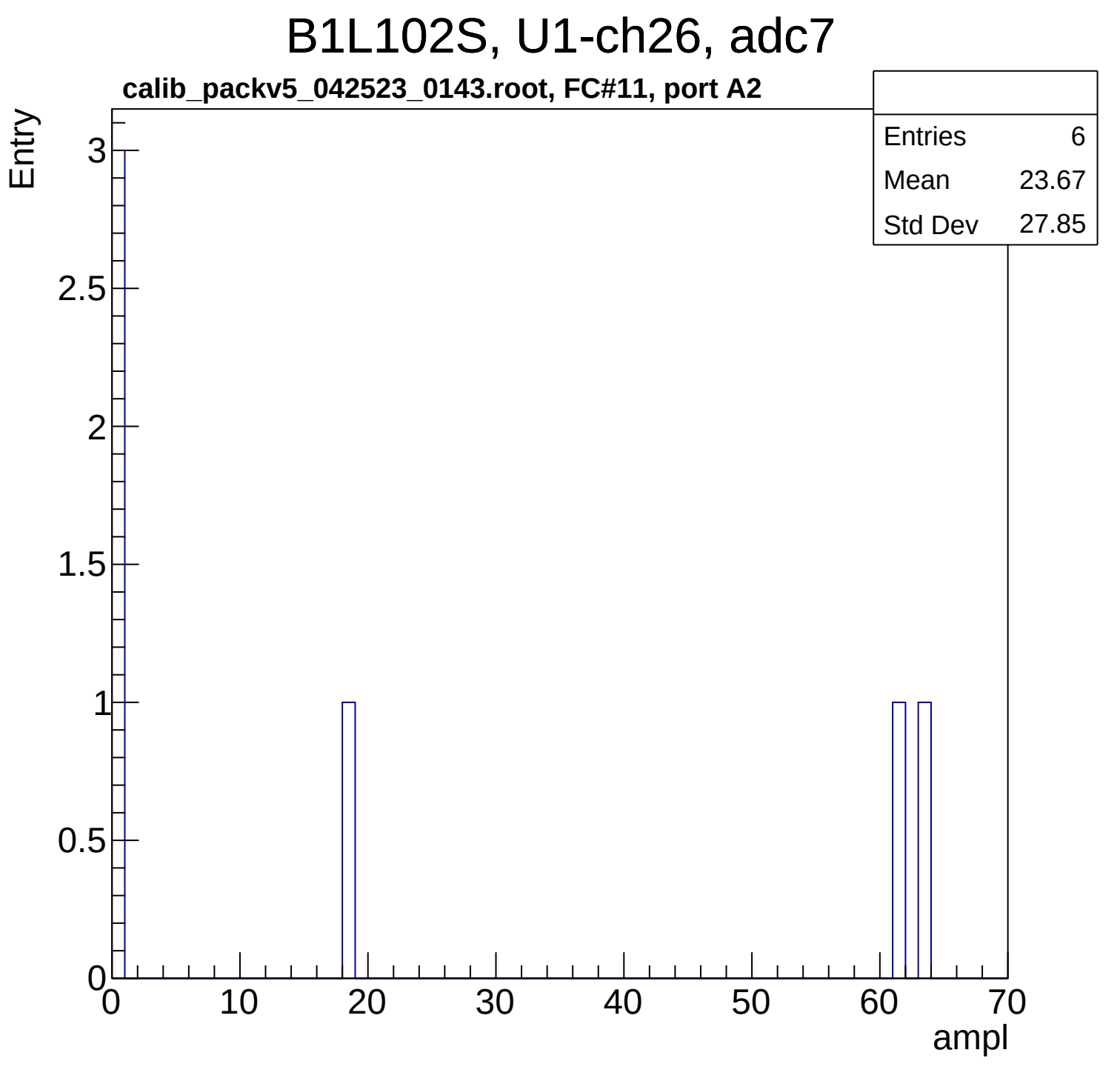
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 21
Mean 58.19
Std Dev 13.12
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 23.67
Std Dev 27.85
Entry
ampl
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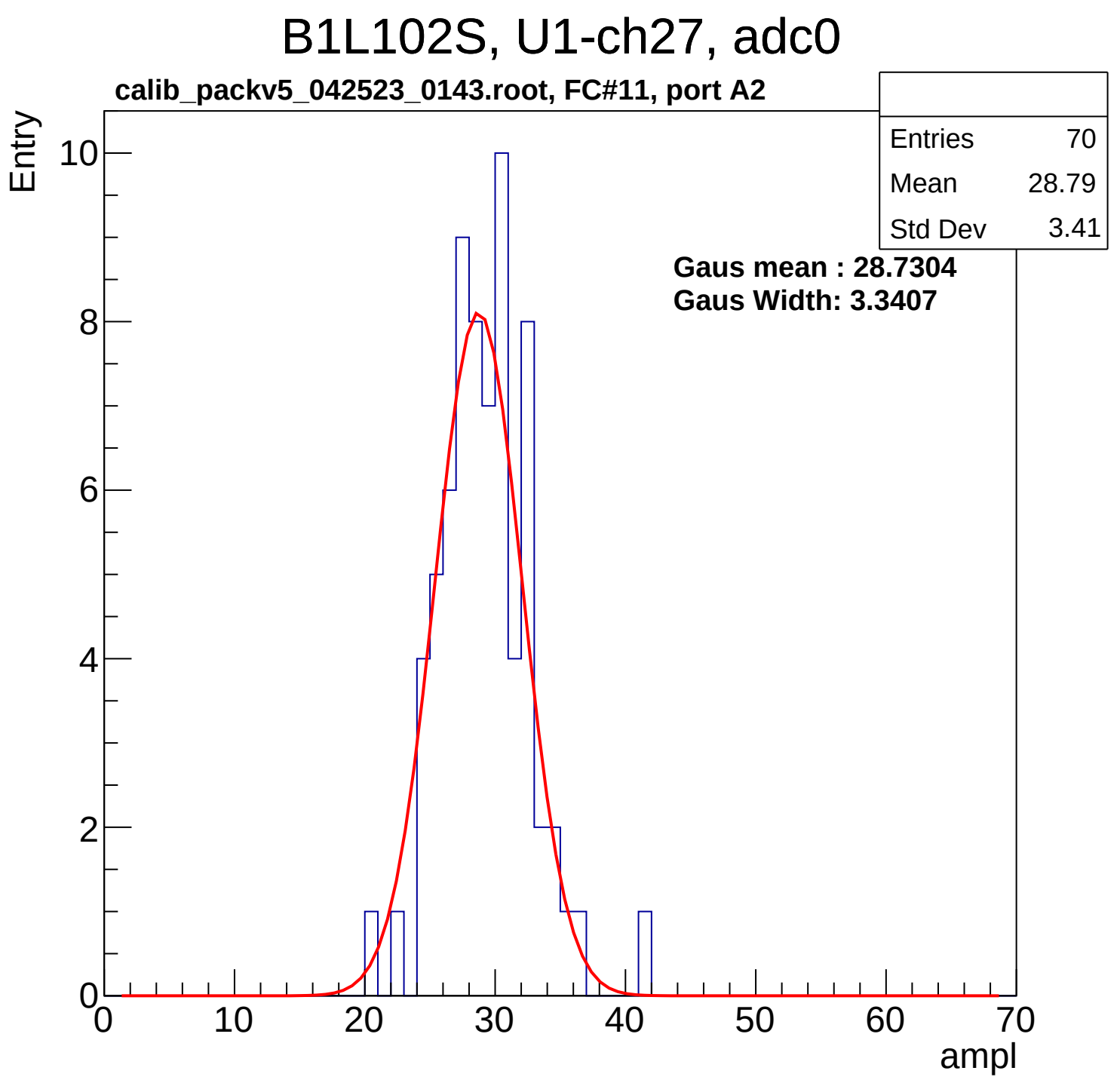
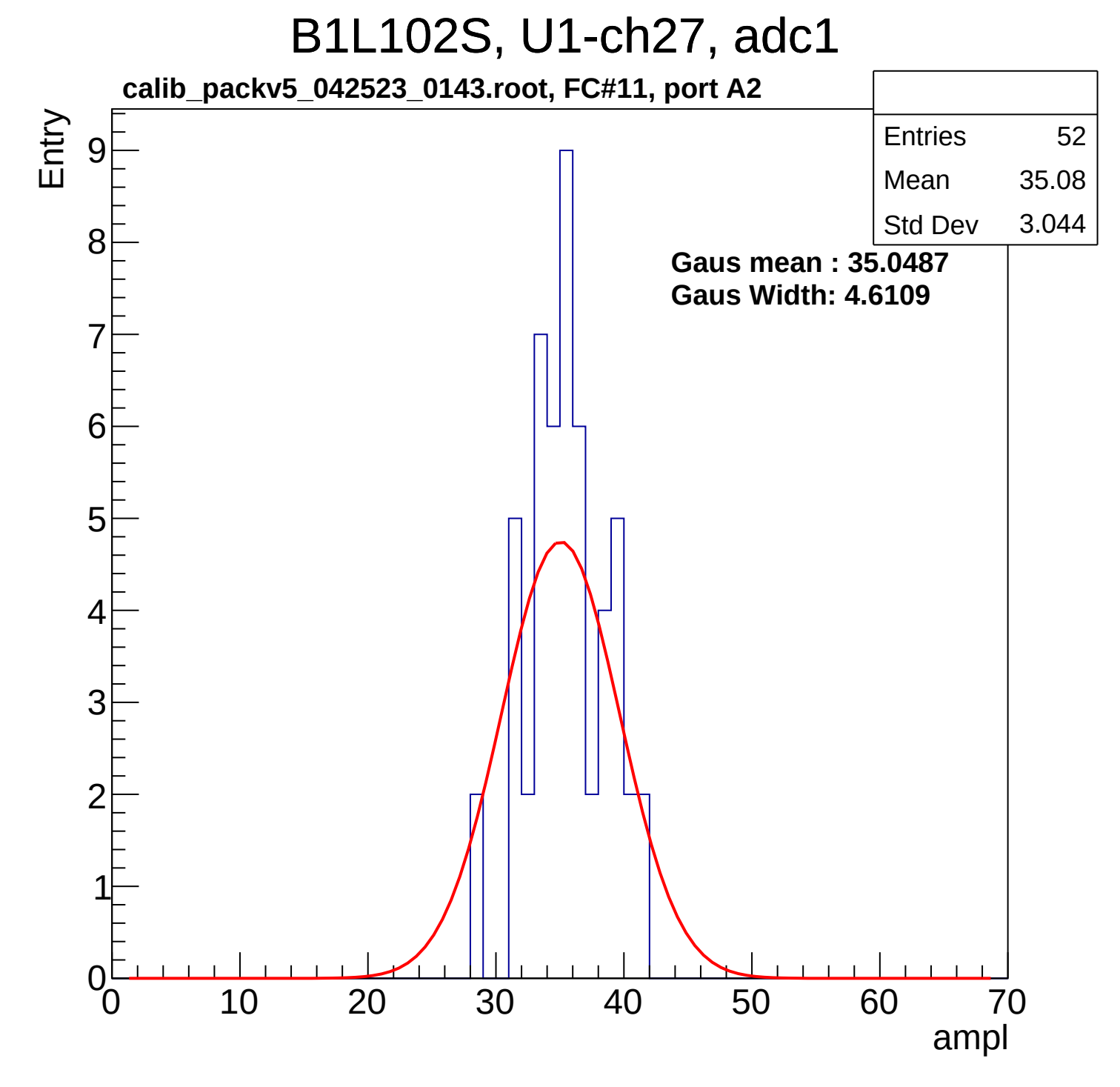
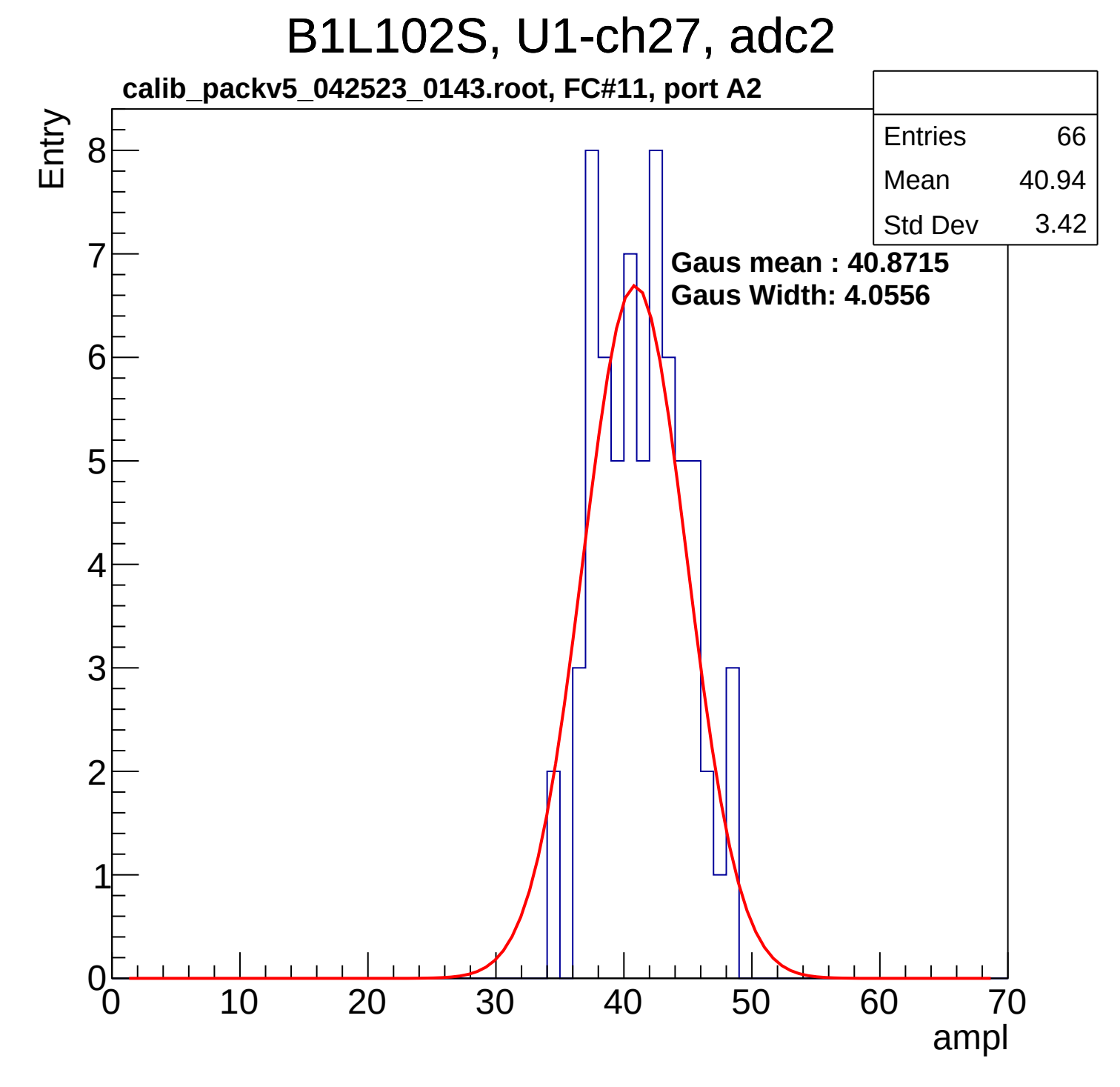
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.79
Std Dev 3.41
Gaus mean : 28.7304
Gaus Width: 3.3407
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 35.08
Std Dev 3.044
Gaus mean : 35.0487
Gaus Width: 4.6109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 40.94
Std Dev 3.42
Gaus mean : 40.8715
Gaus Width: 4.0556
Entry
ampl
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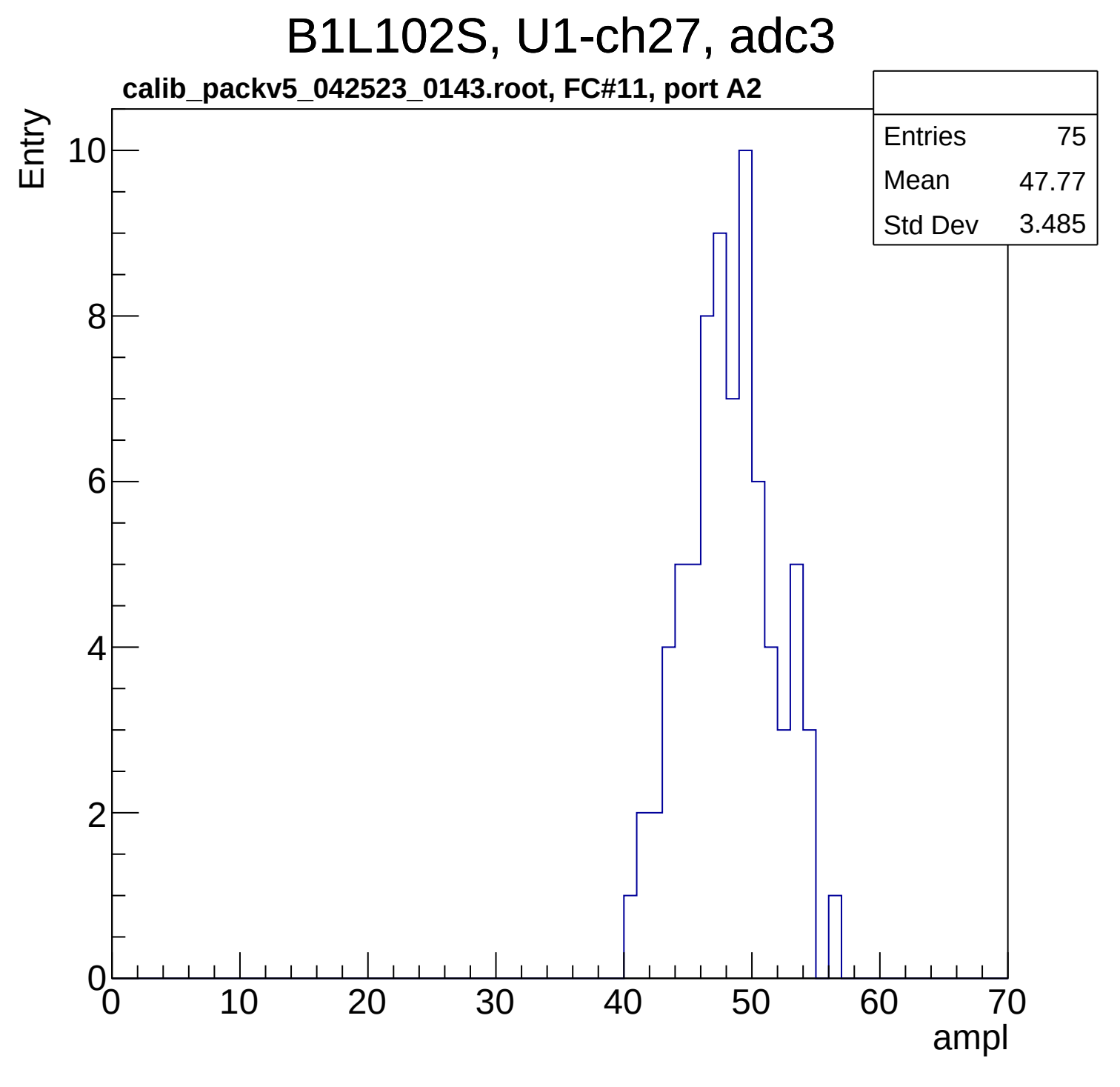
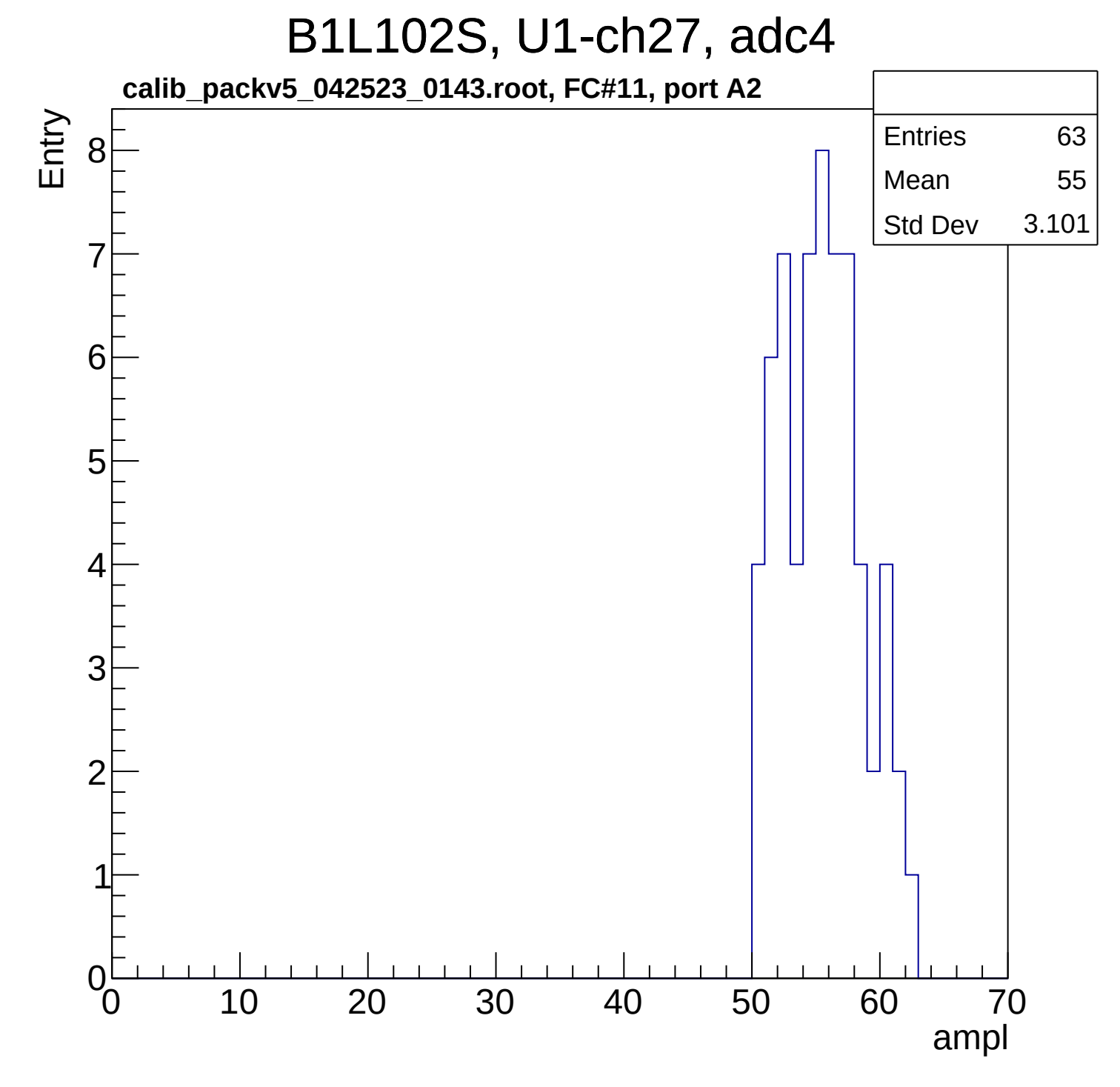
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 47.77
Std Dev 3.485
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 55
Std Dev 3.101
Entry
ampl
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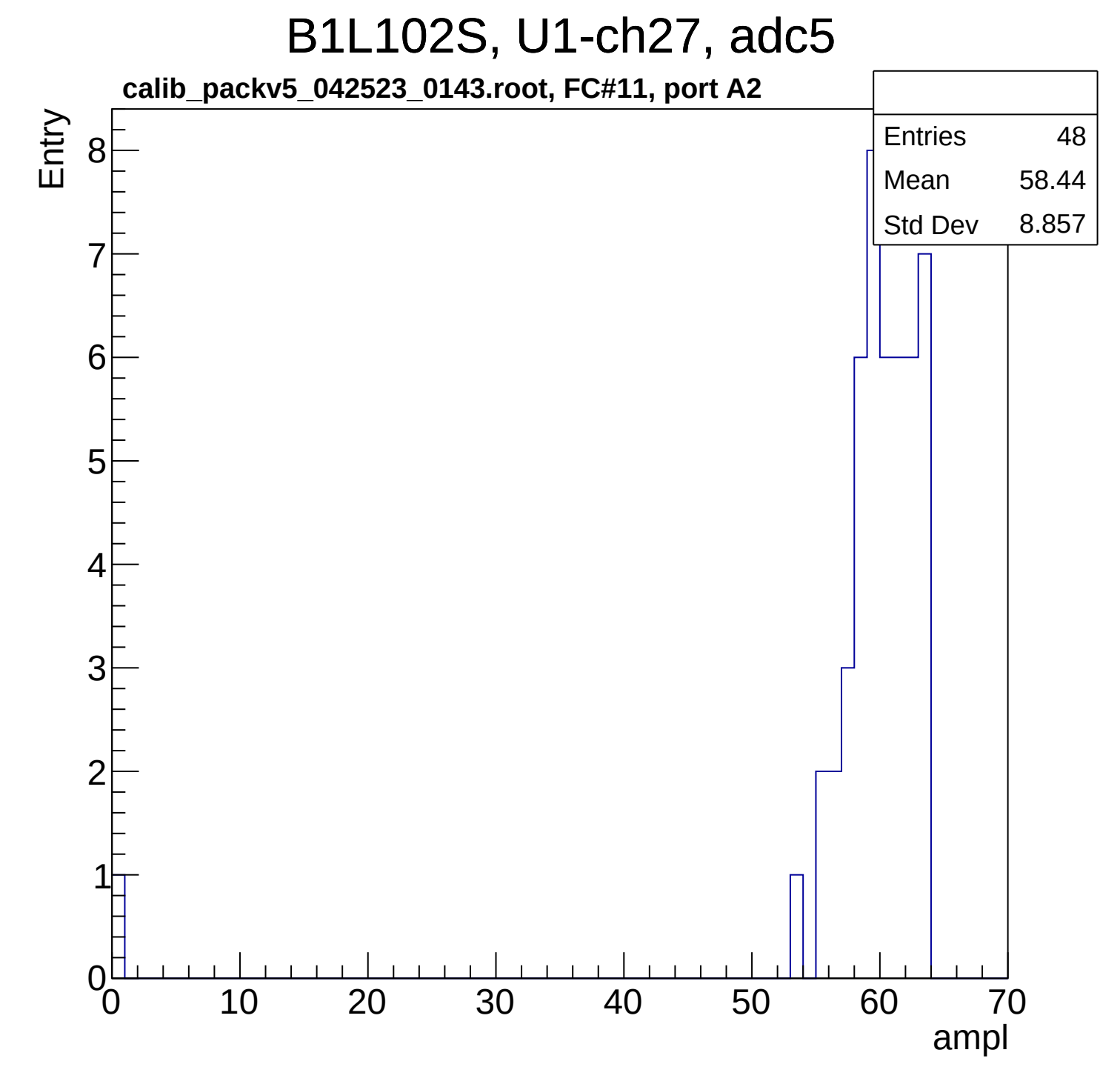
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 58.44
Std Dev 8.857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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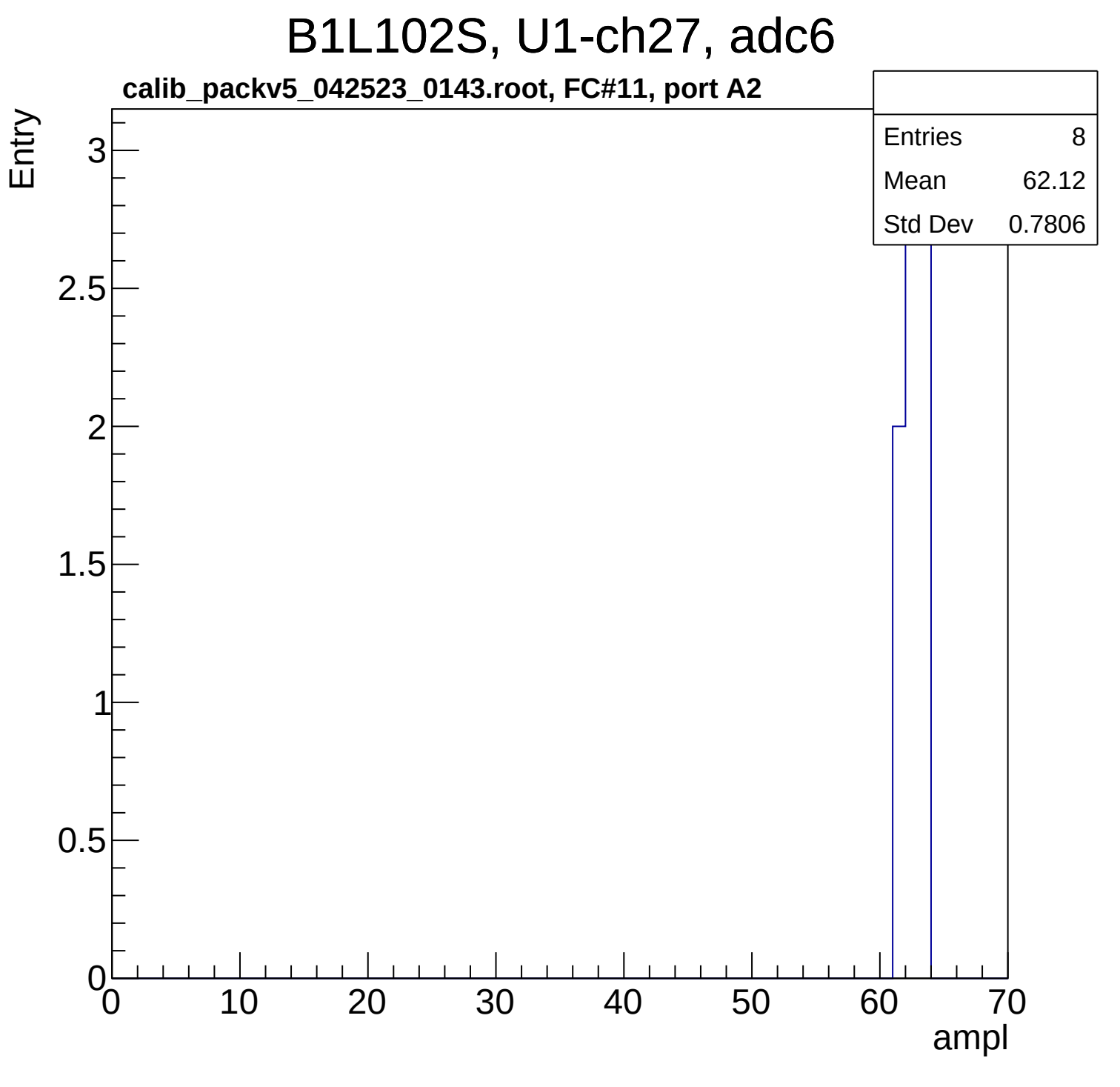
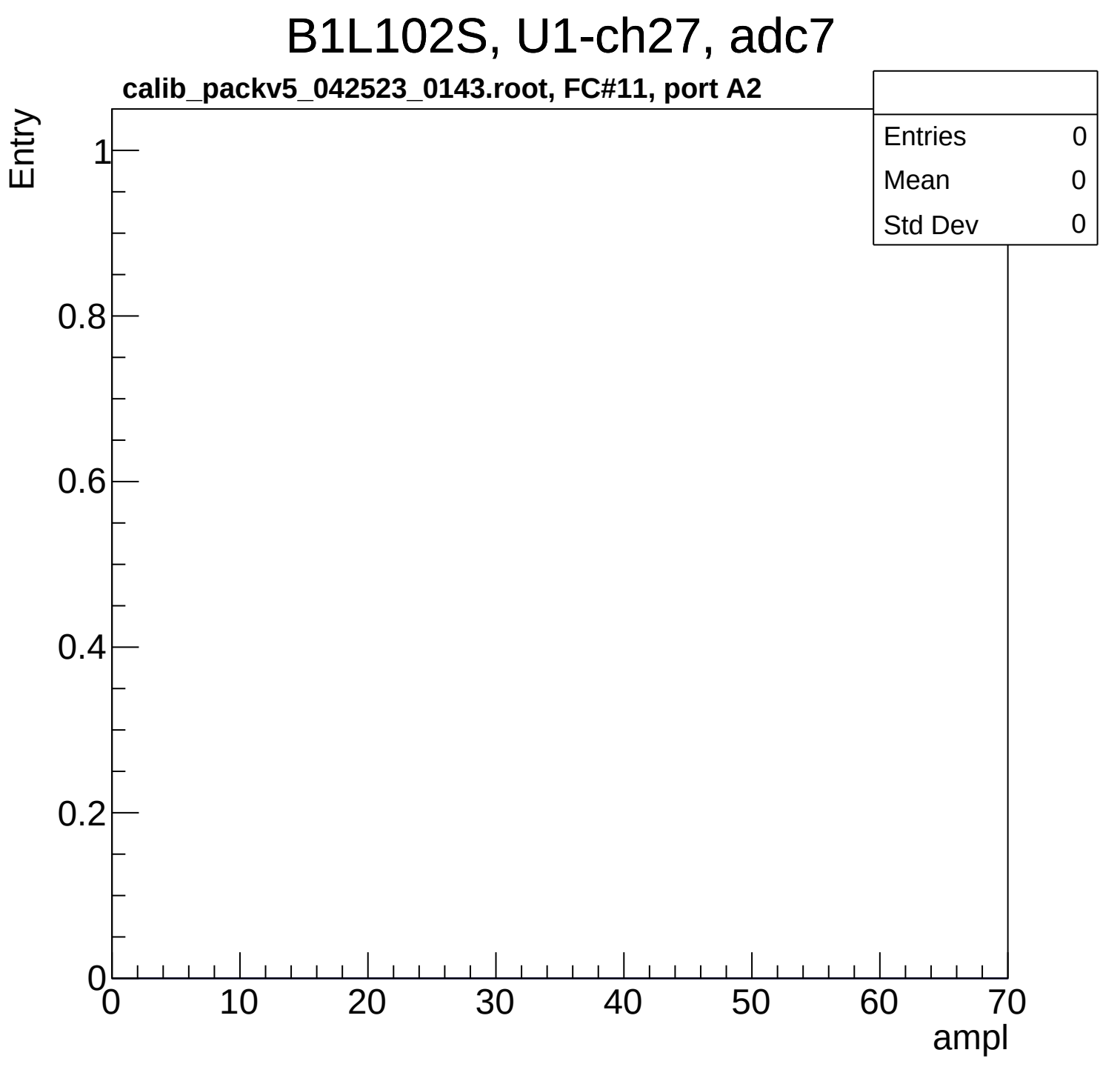
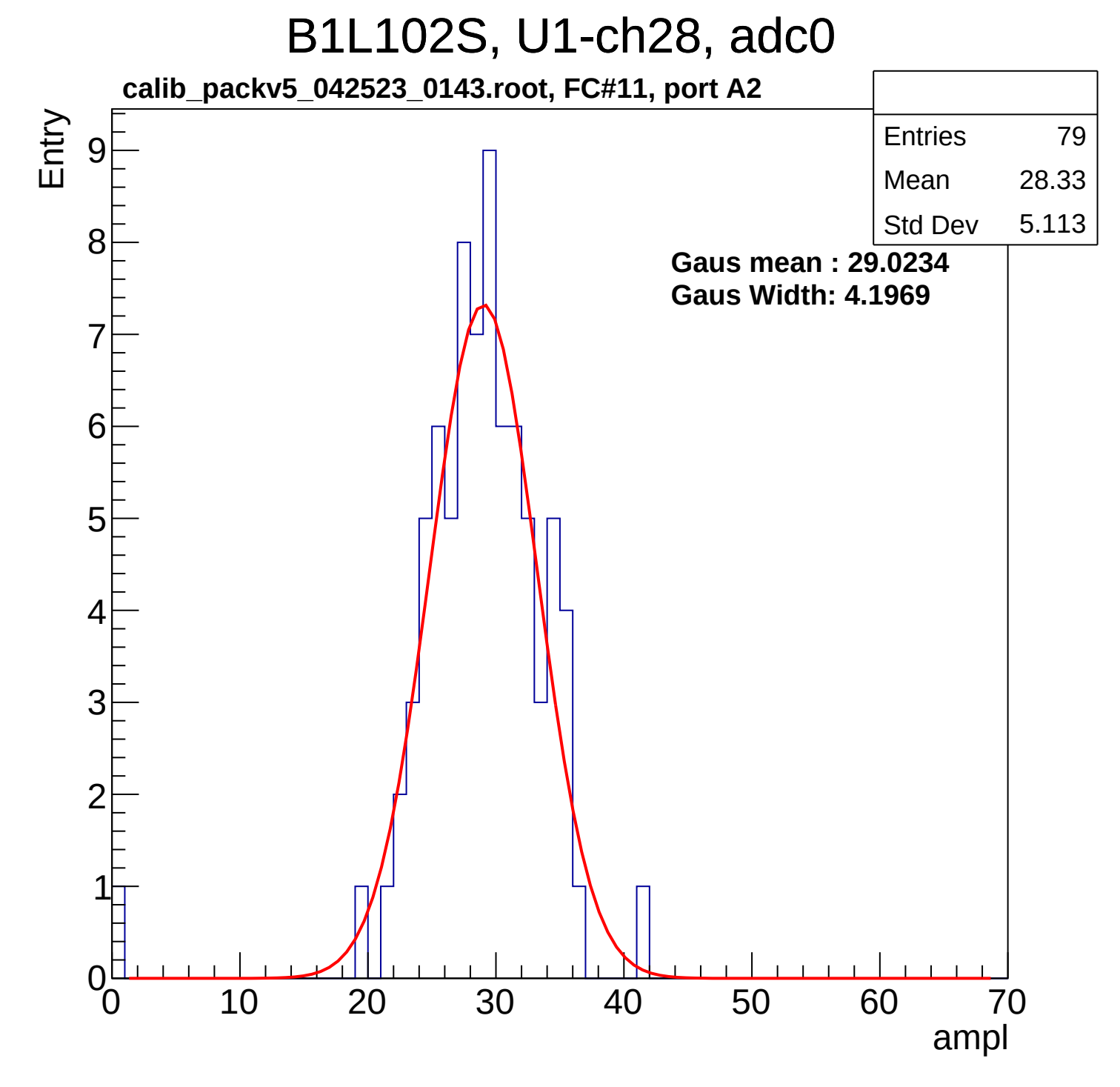
60
70


B1L102S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 62.12
Std Dev 0.7806
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 28.33
Std Dev 5.113
Gaus mean : 29.0234
Gaus Width: 4.1969
Entry
ampl
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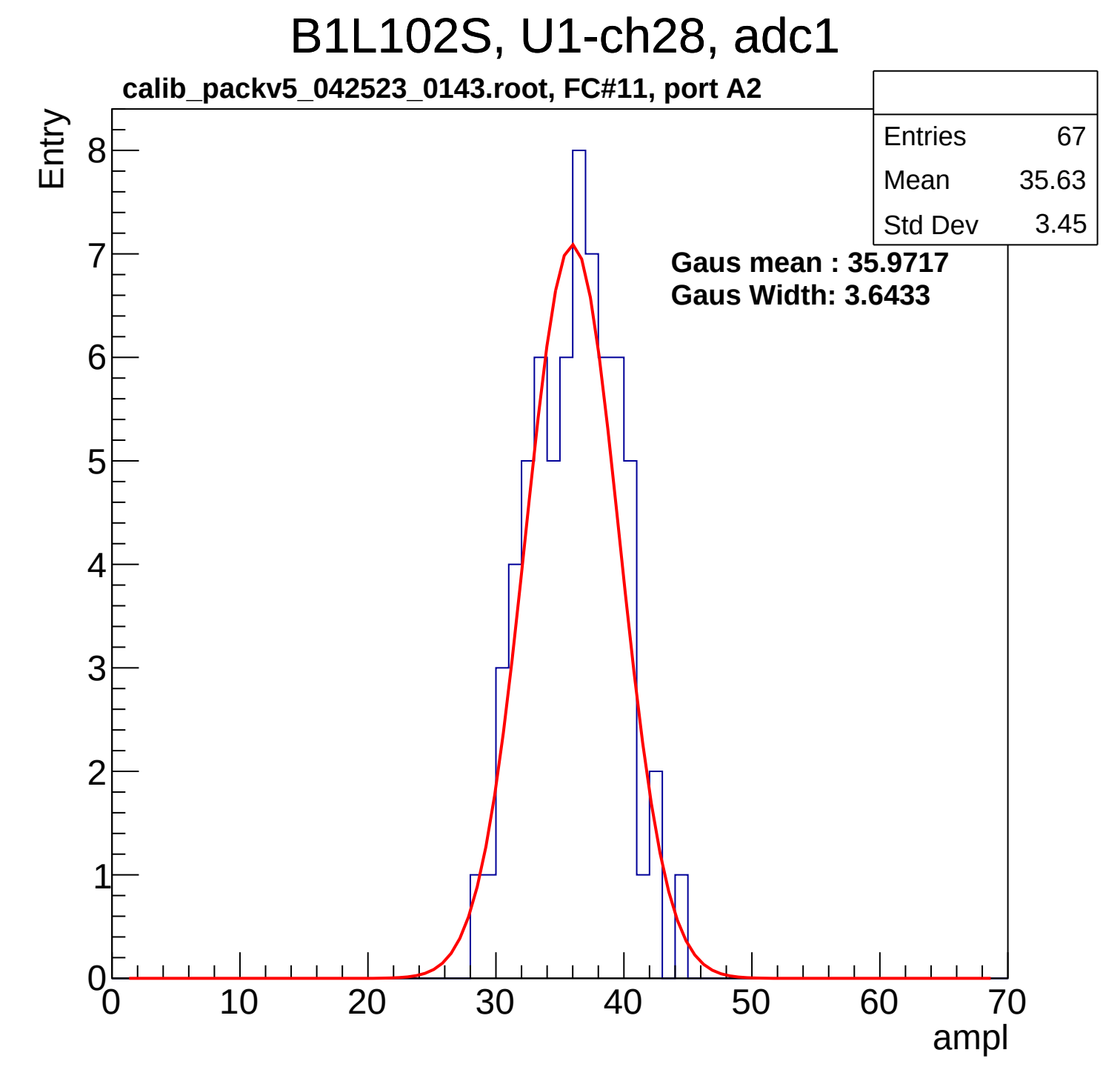
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.63
Std Dev 3.45
Gaus mean : 35.9717
Gaus Width: 3.6433
Entry
ampl
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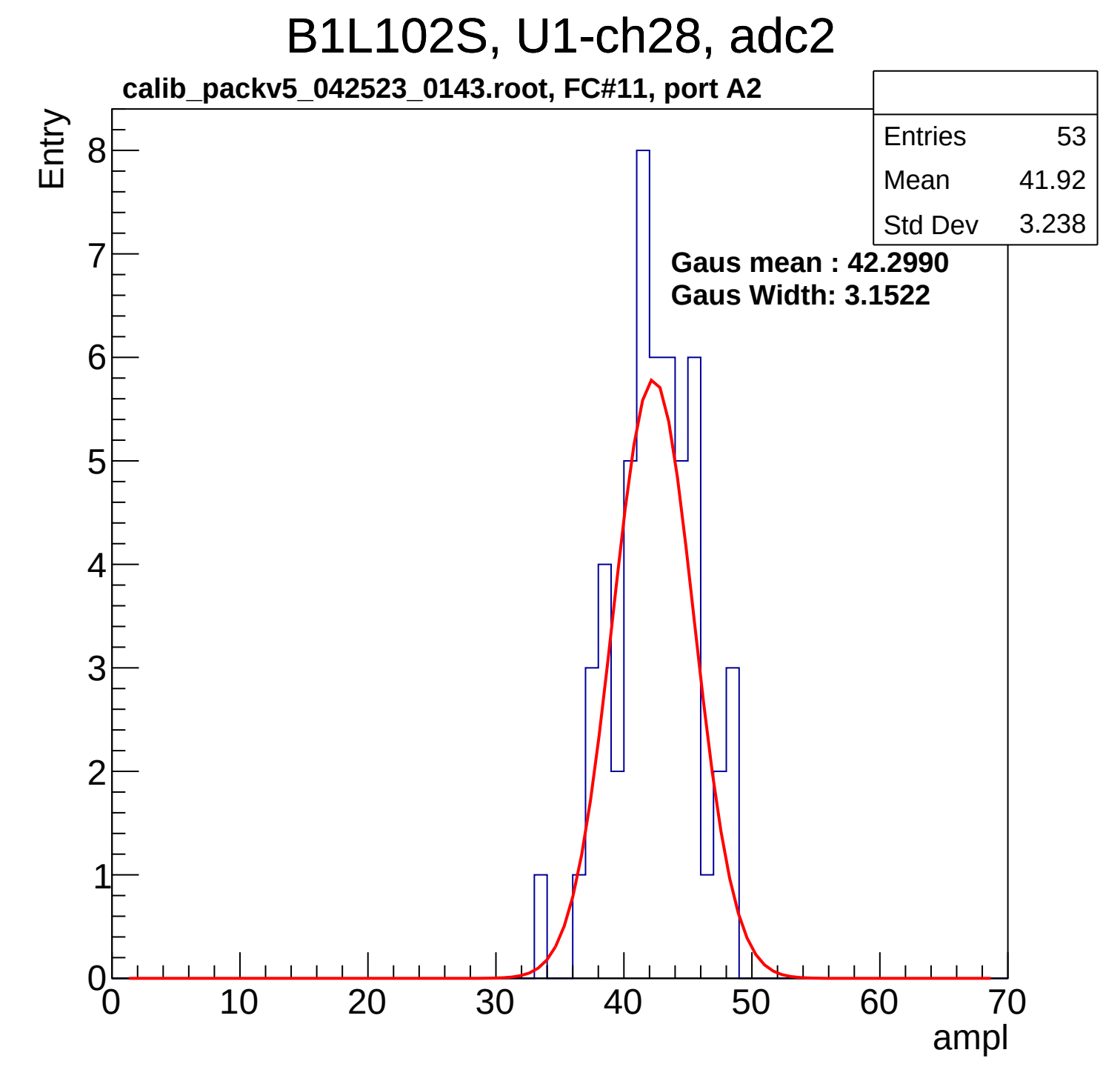
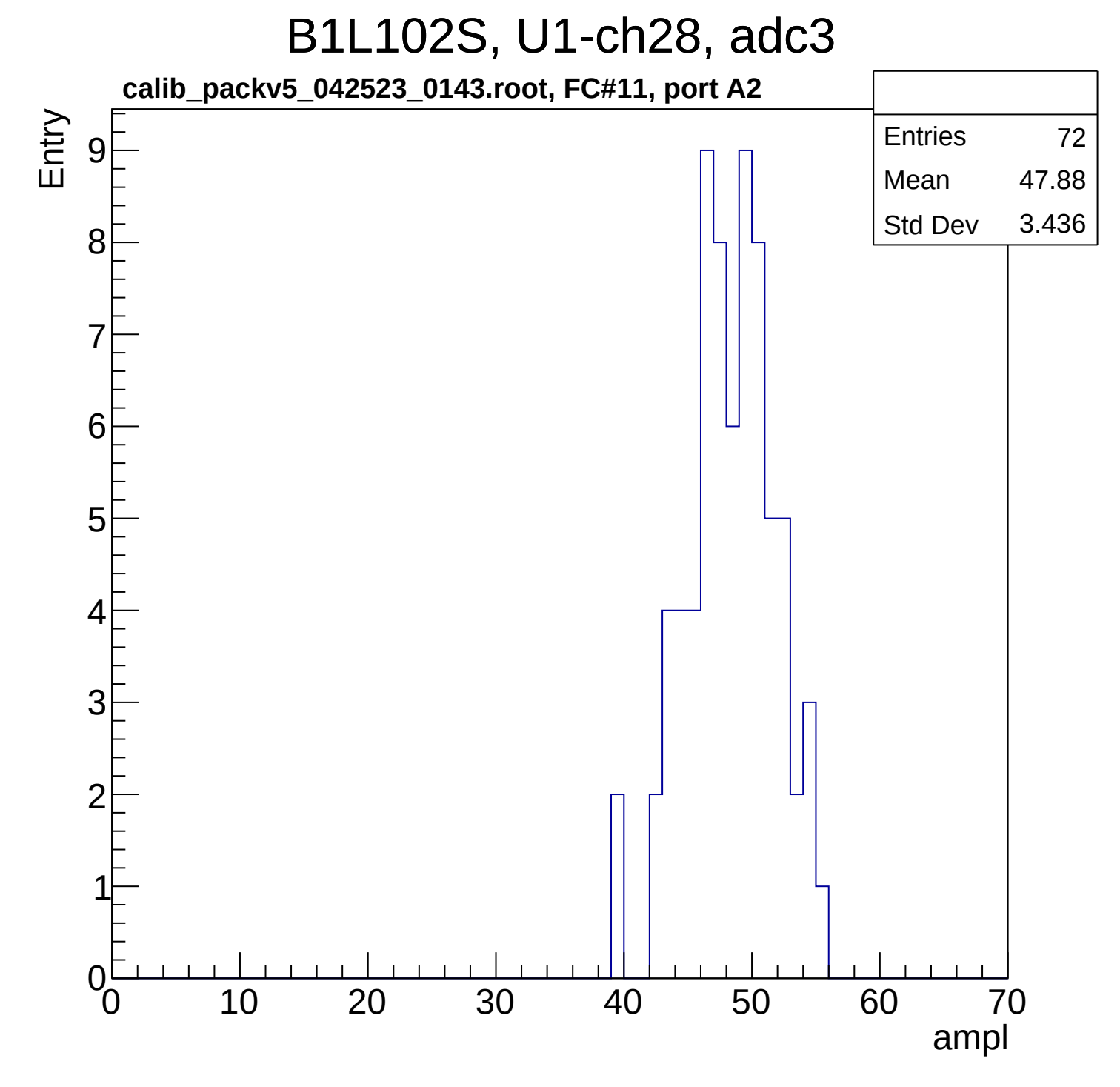
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 41.92
Std Dev 3.238
Gaus mean : 42.2990
Gaus Width: 3.1522
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 47.88
Std Dev 3.436
Entry
ampl
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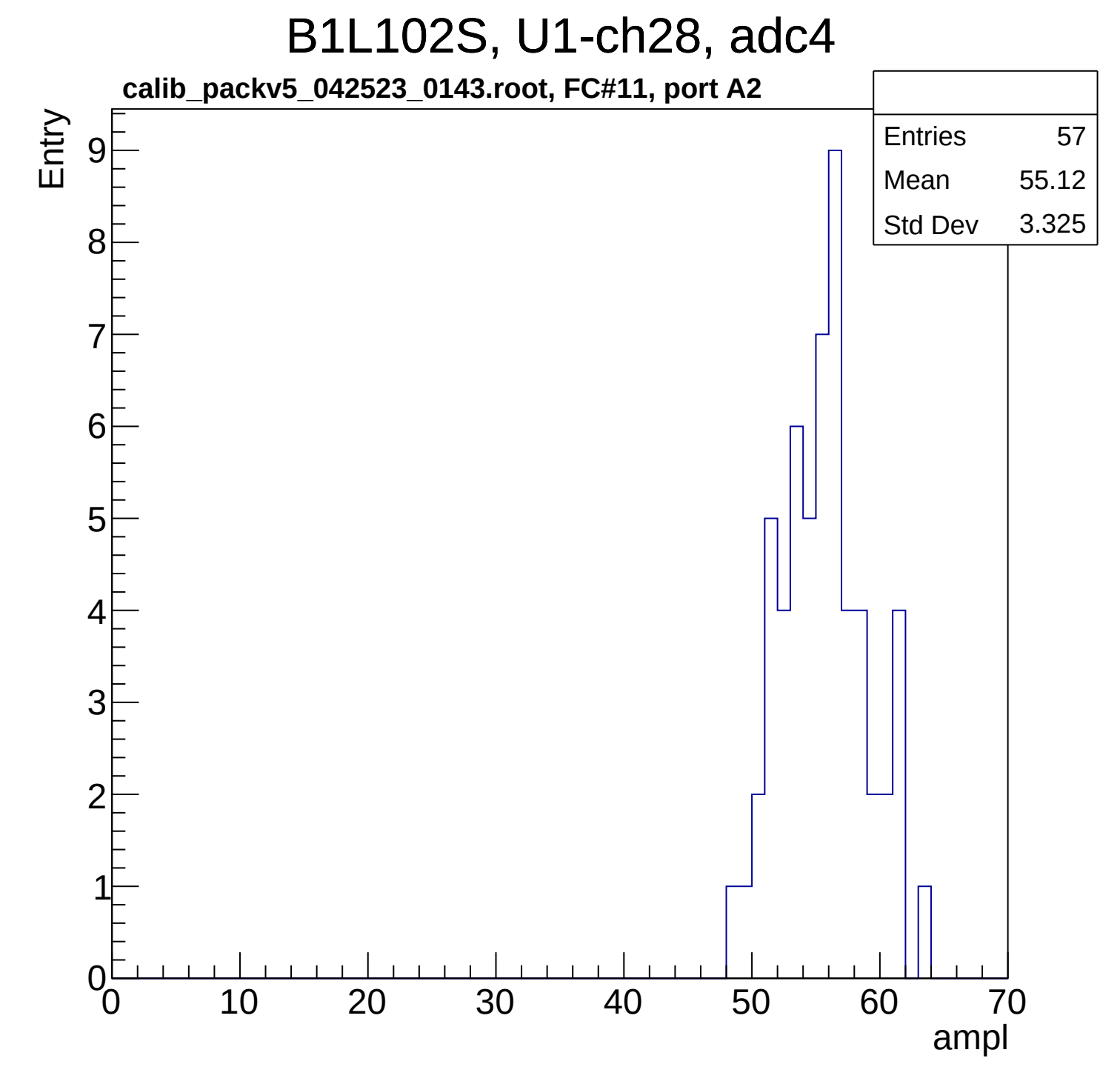
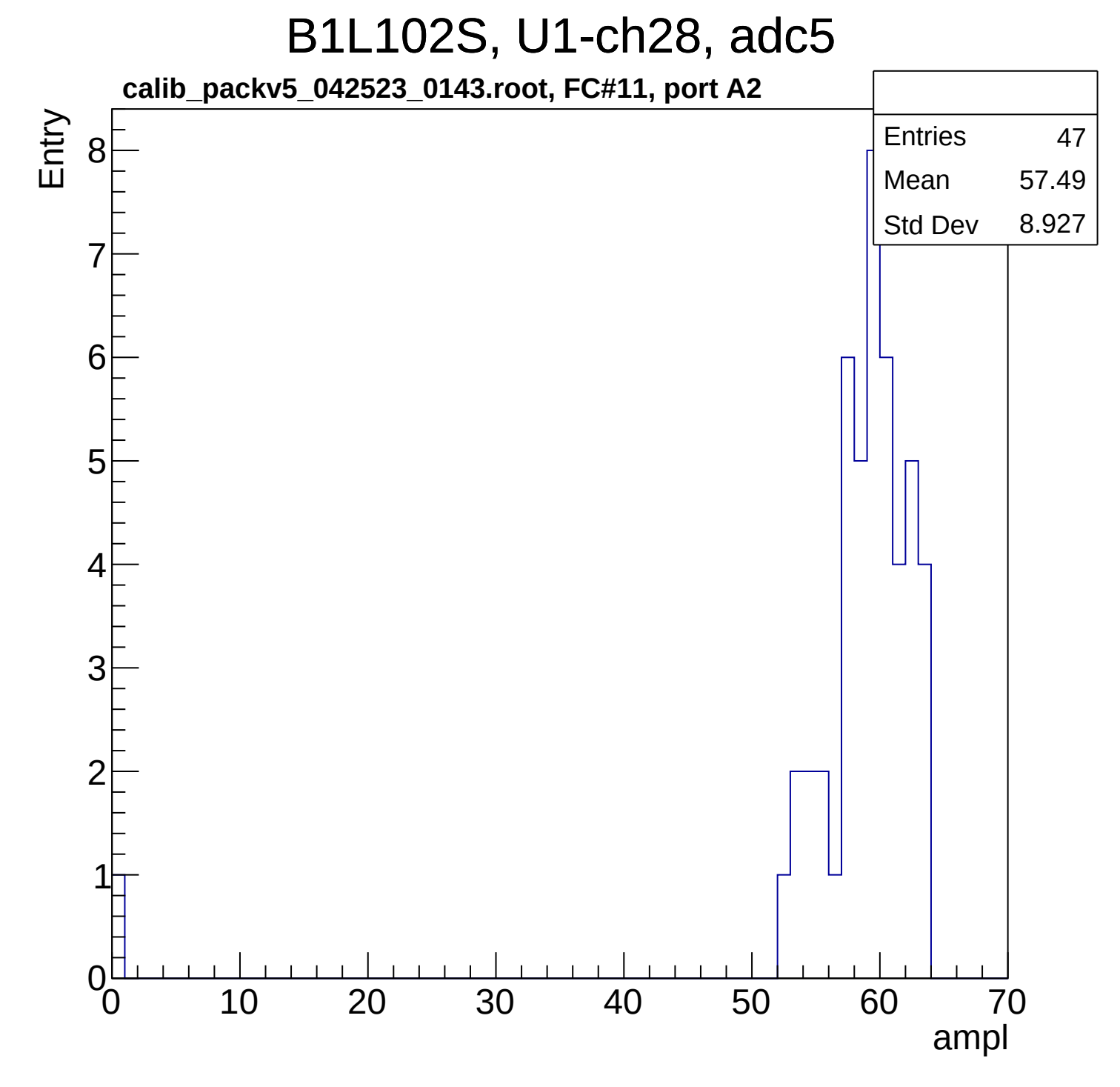
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.12
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 57.49
Std Dev 8.927
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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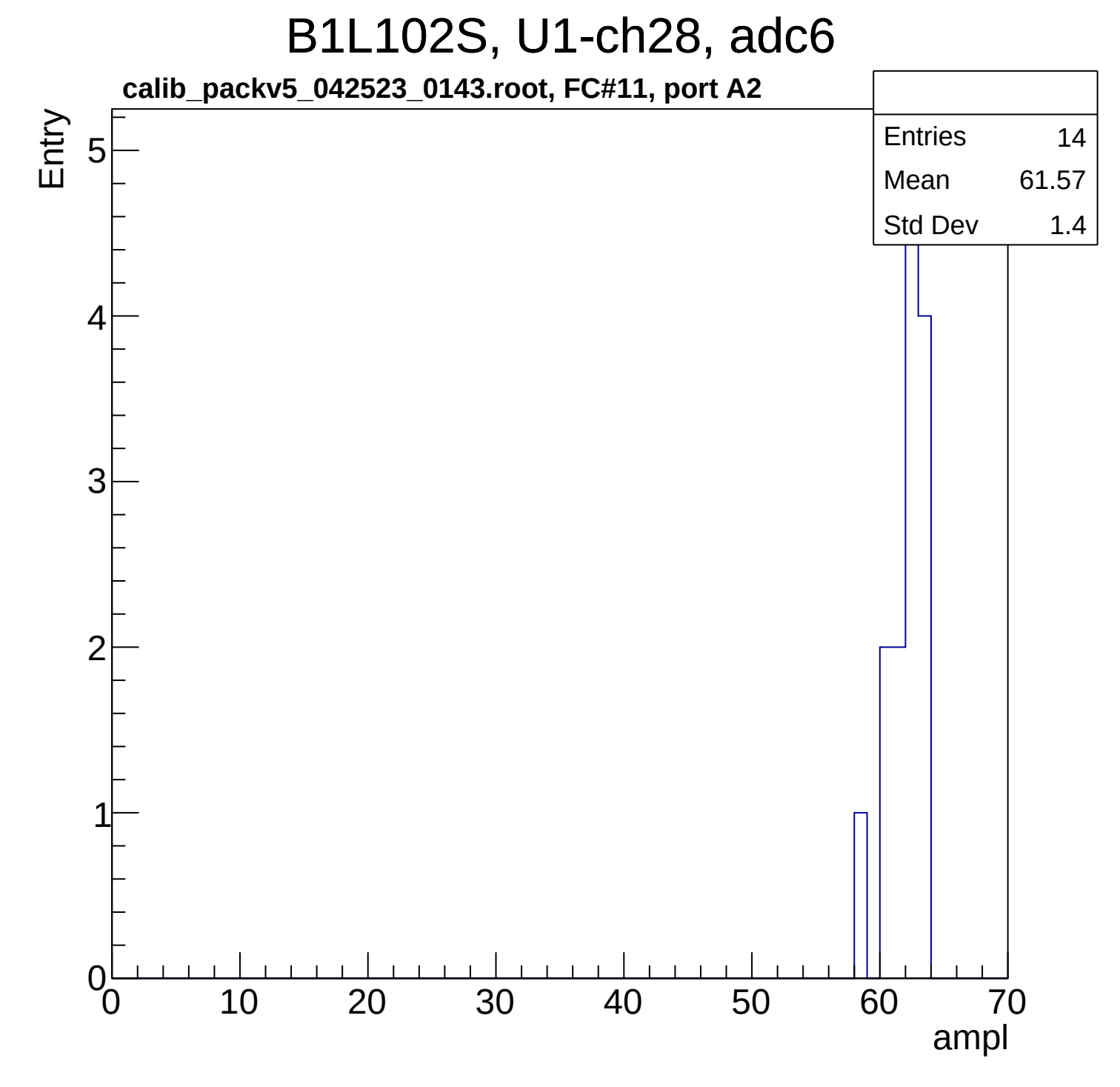
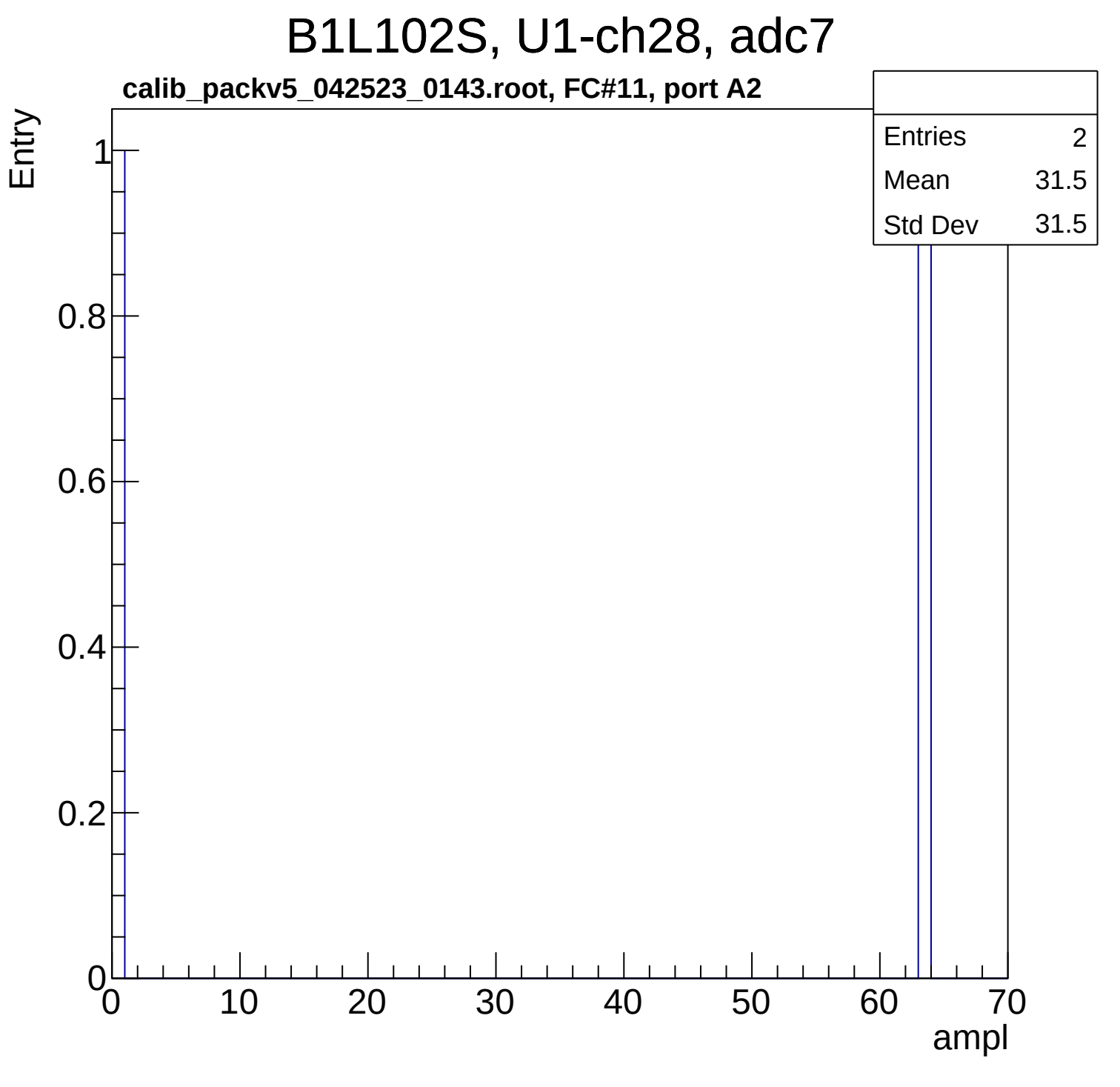

B1L102S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61.57
Std Dev 1.4
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L102S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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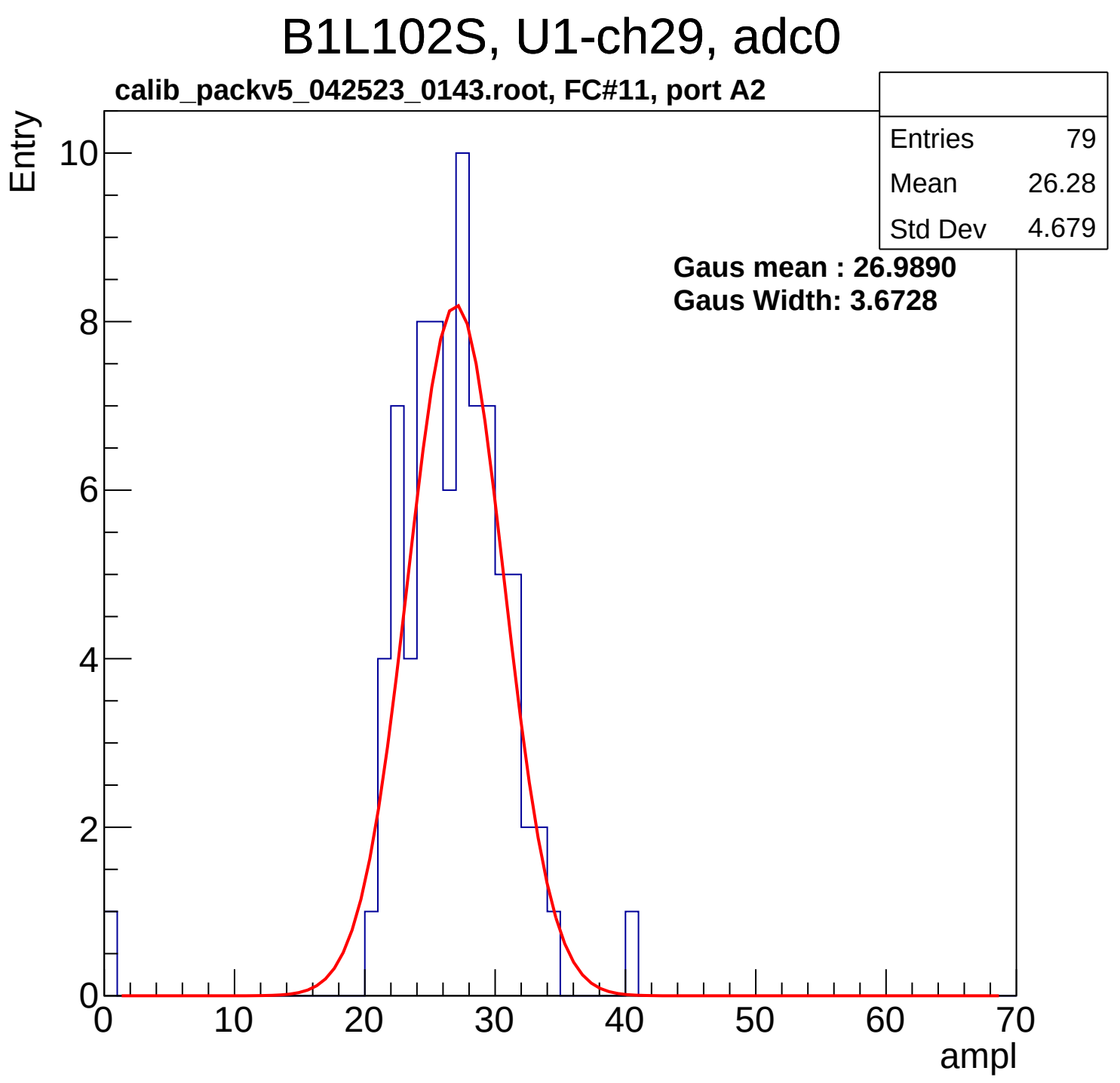
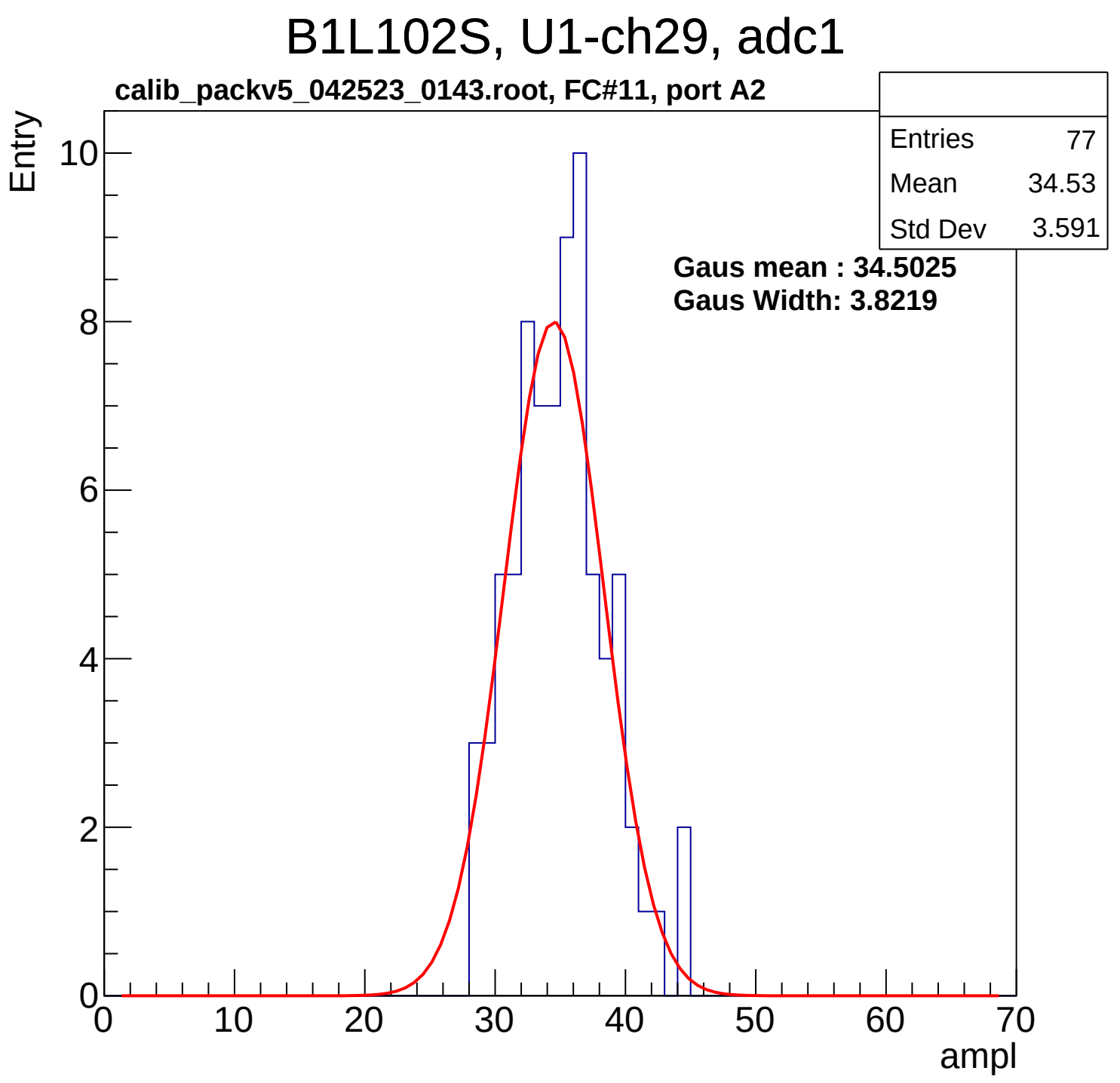
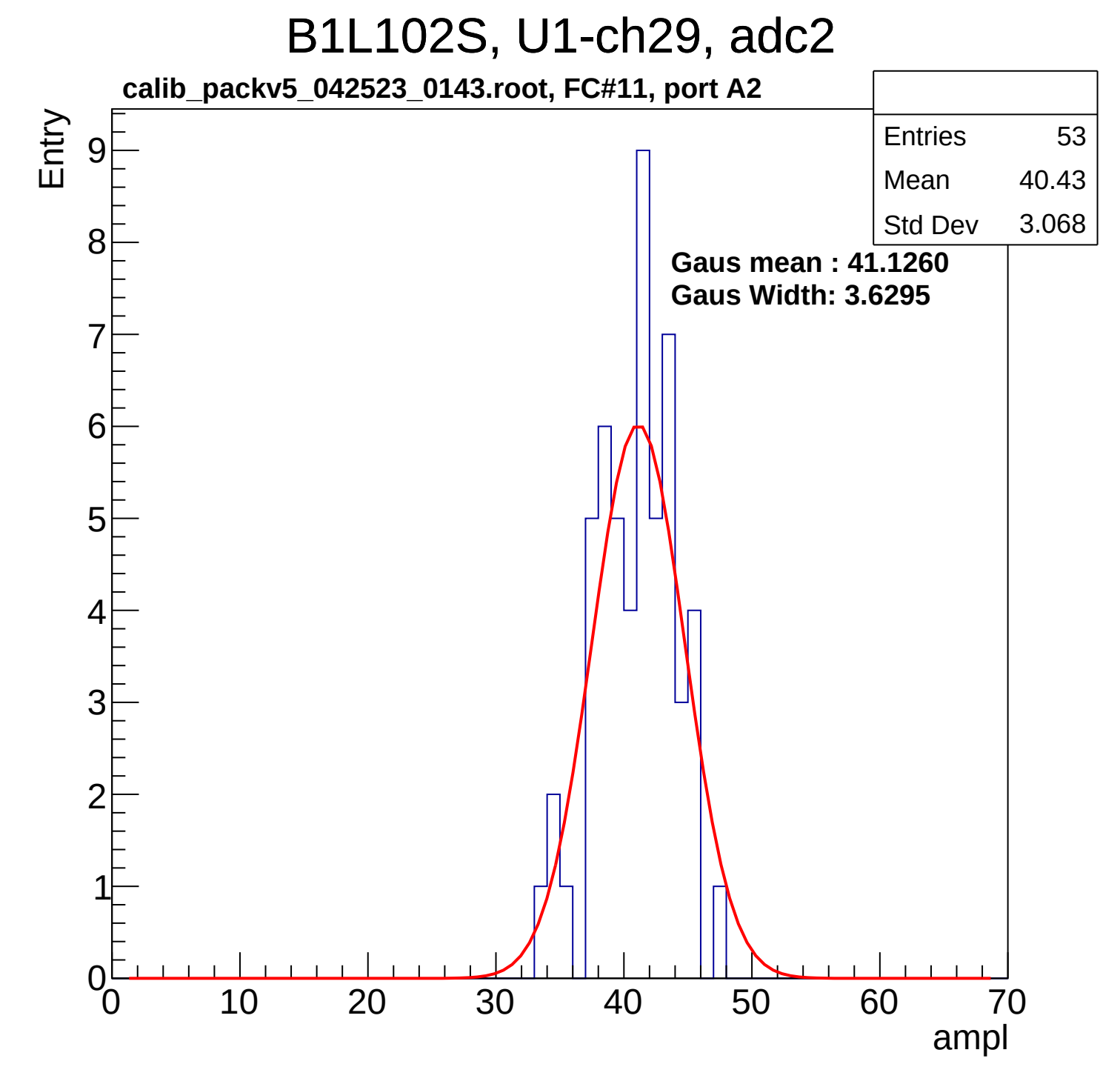
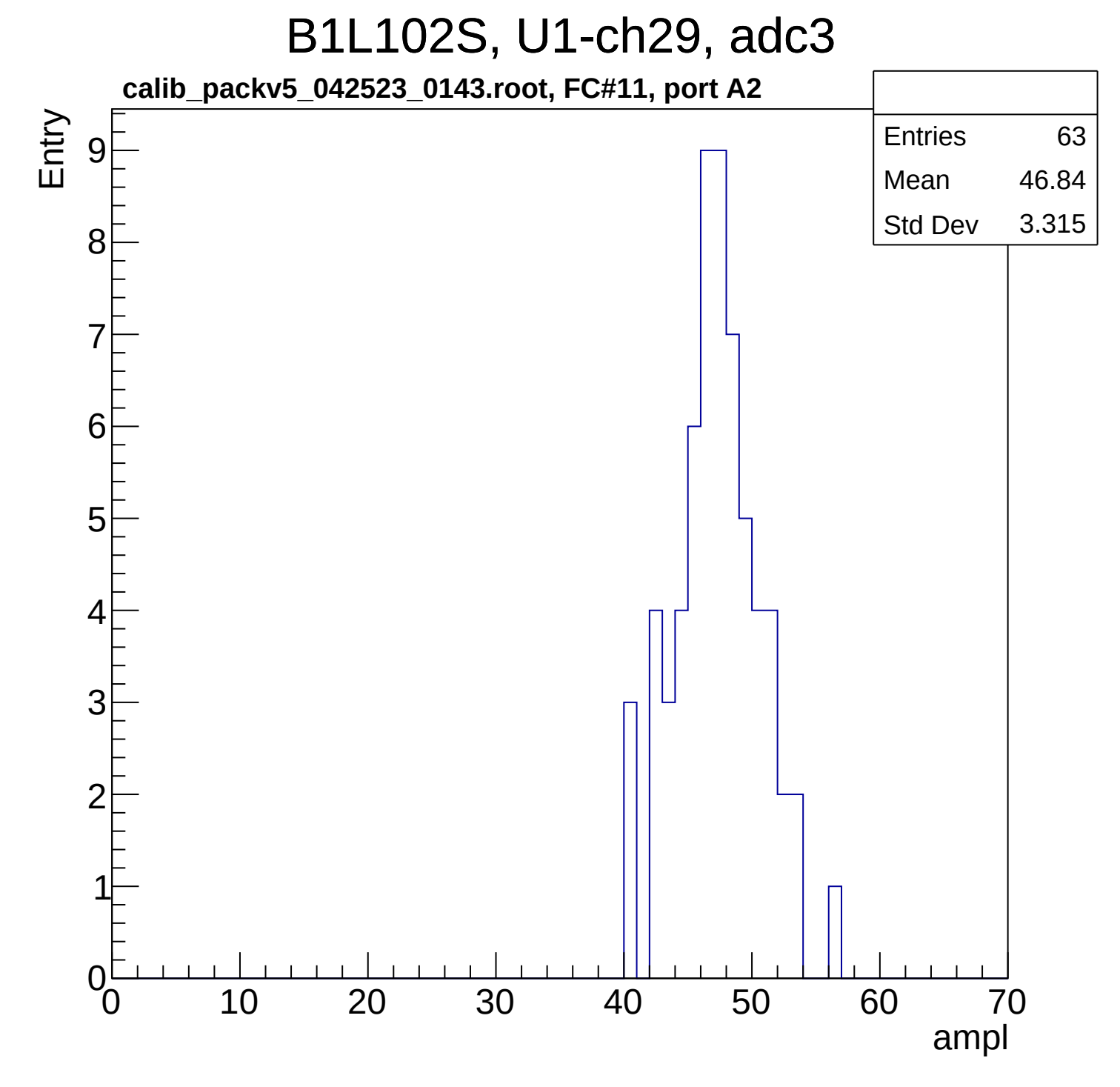
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 26.28
Std Dev 4.679
Gaus mean : 26.9890
Gaus Width: 3.6728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 34.53
Std Dev 3.591
Gaus mean : 34.5025
Gaus Width: 3.8219
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 40.43
Std Dev 3.068
Gaus mean : 41.1260
Gaus Width: 3.6295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 46.84
Std Dev 3.315
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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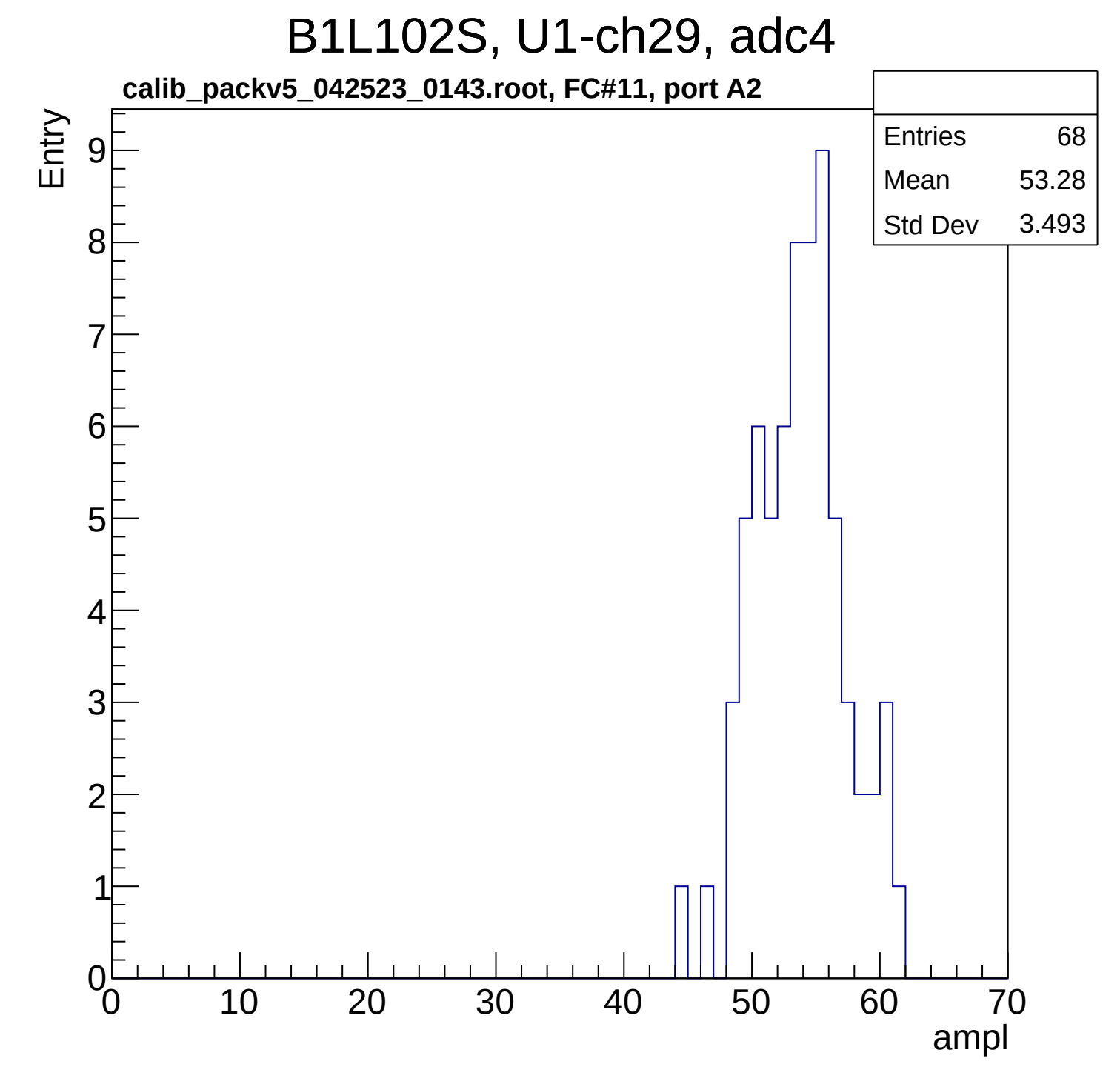
B1L102S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 53.28
Std Dev 3.493
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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50
60
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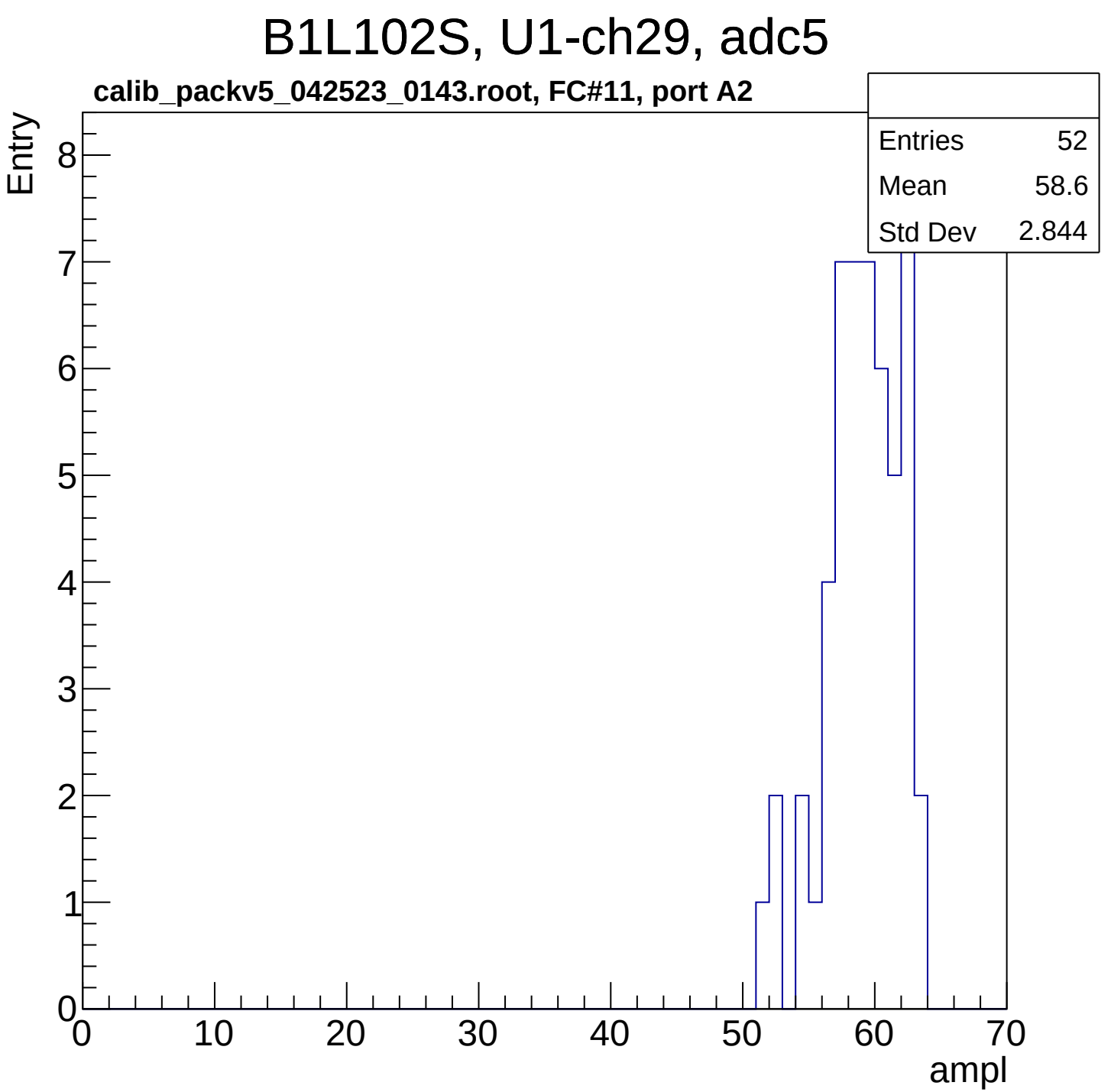
B1L102S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.6
Std Dev 2.844
Entry
ampl
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2
3
4
5
6
7
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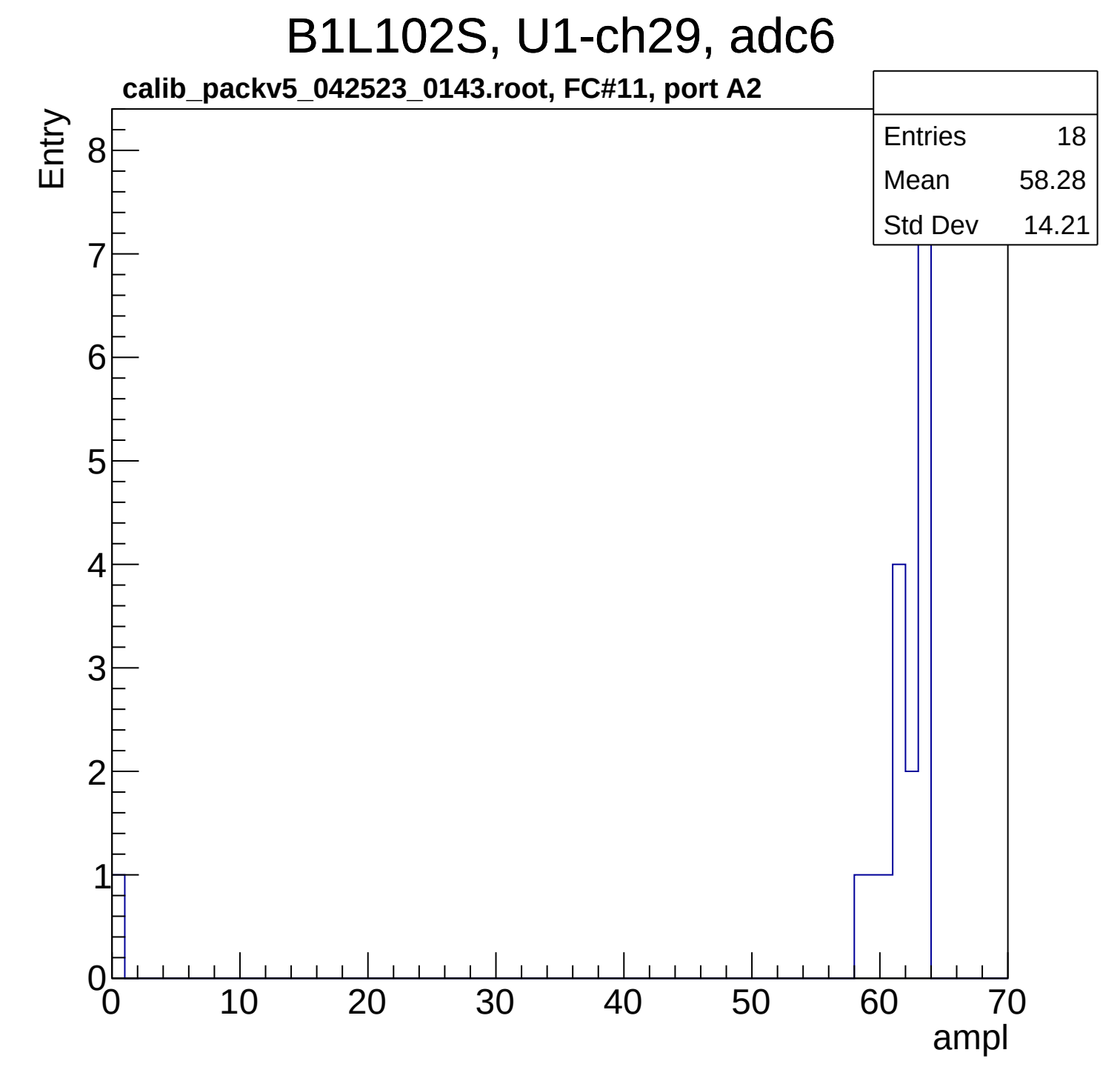
B1L102S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 58.28
Std Dev 14.21
Entry
ampl
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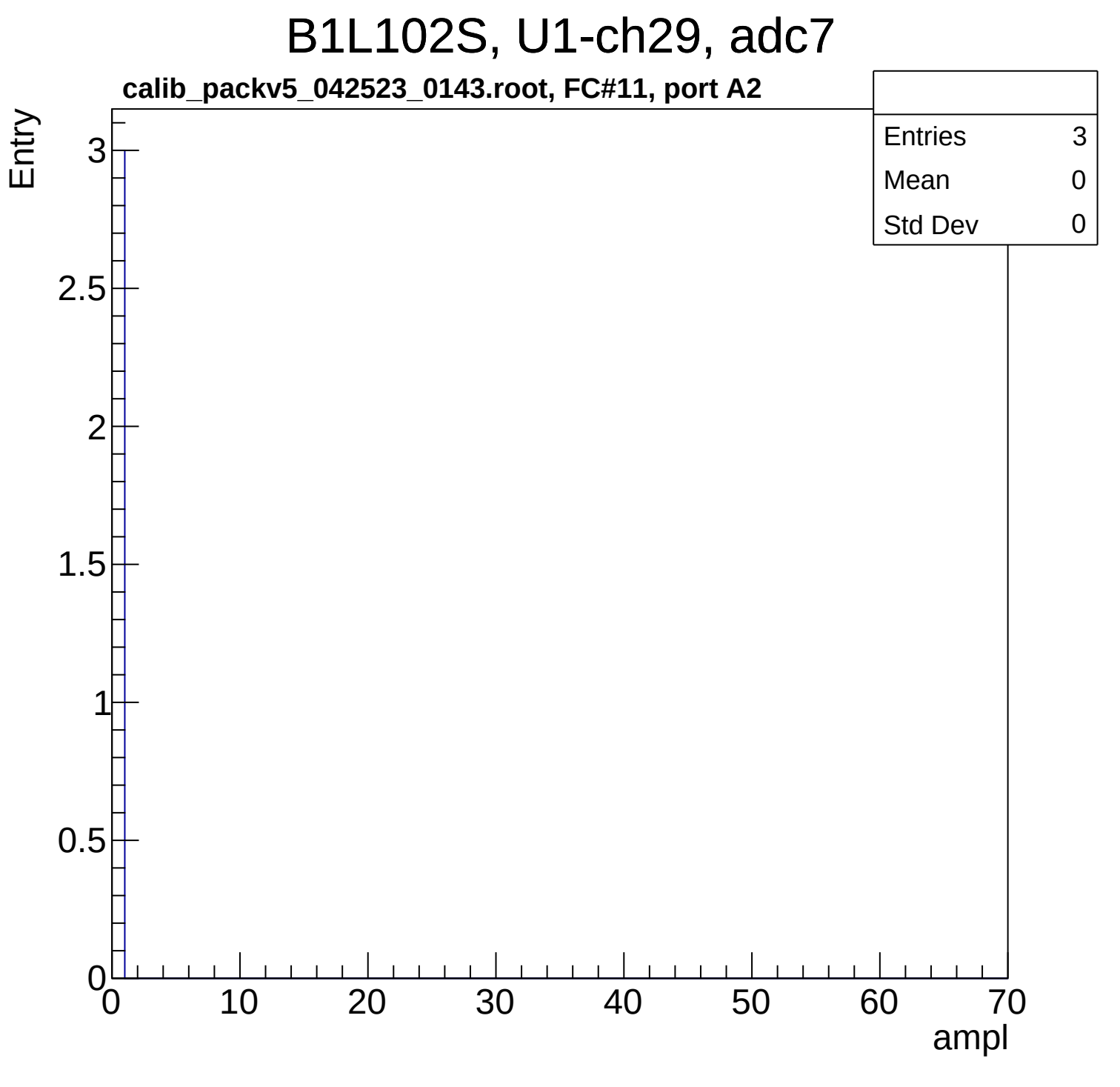
B1L102S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
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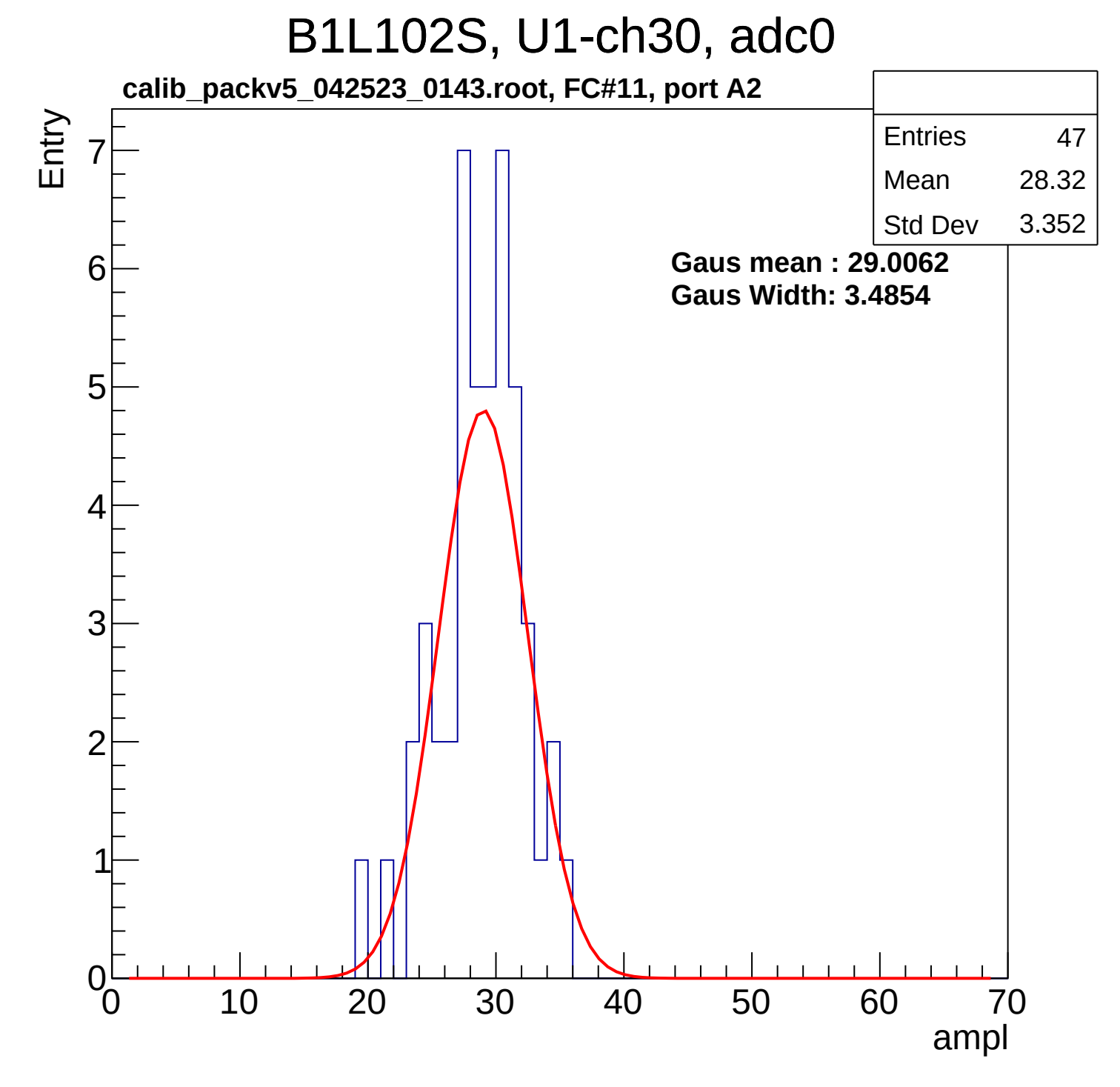
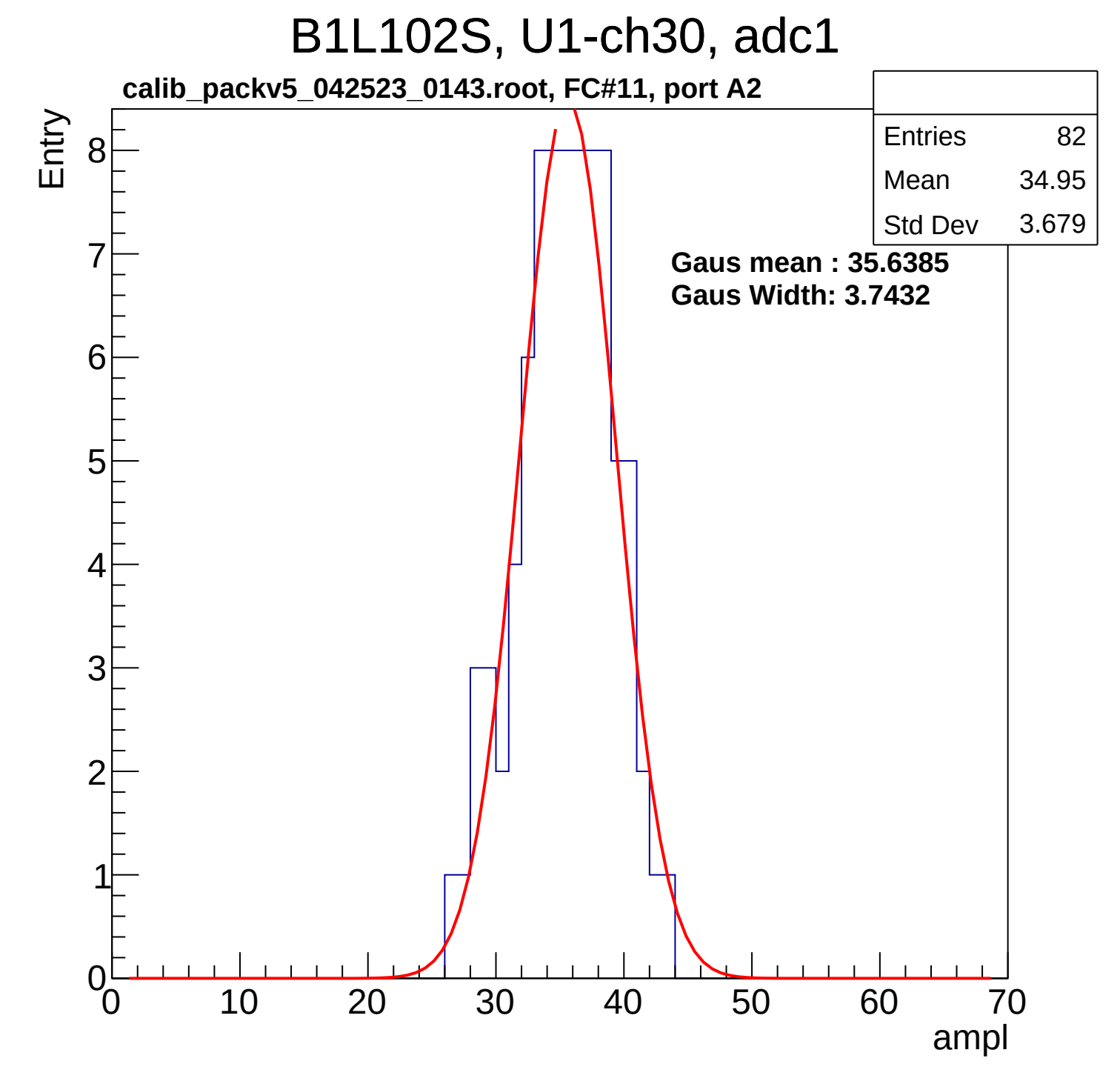
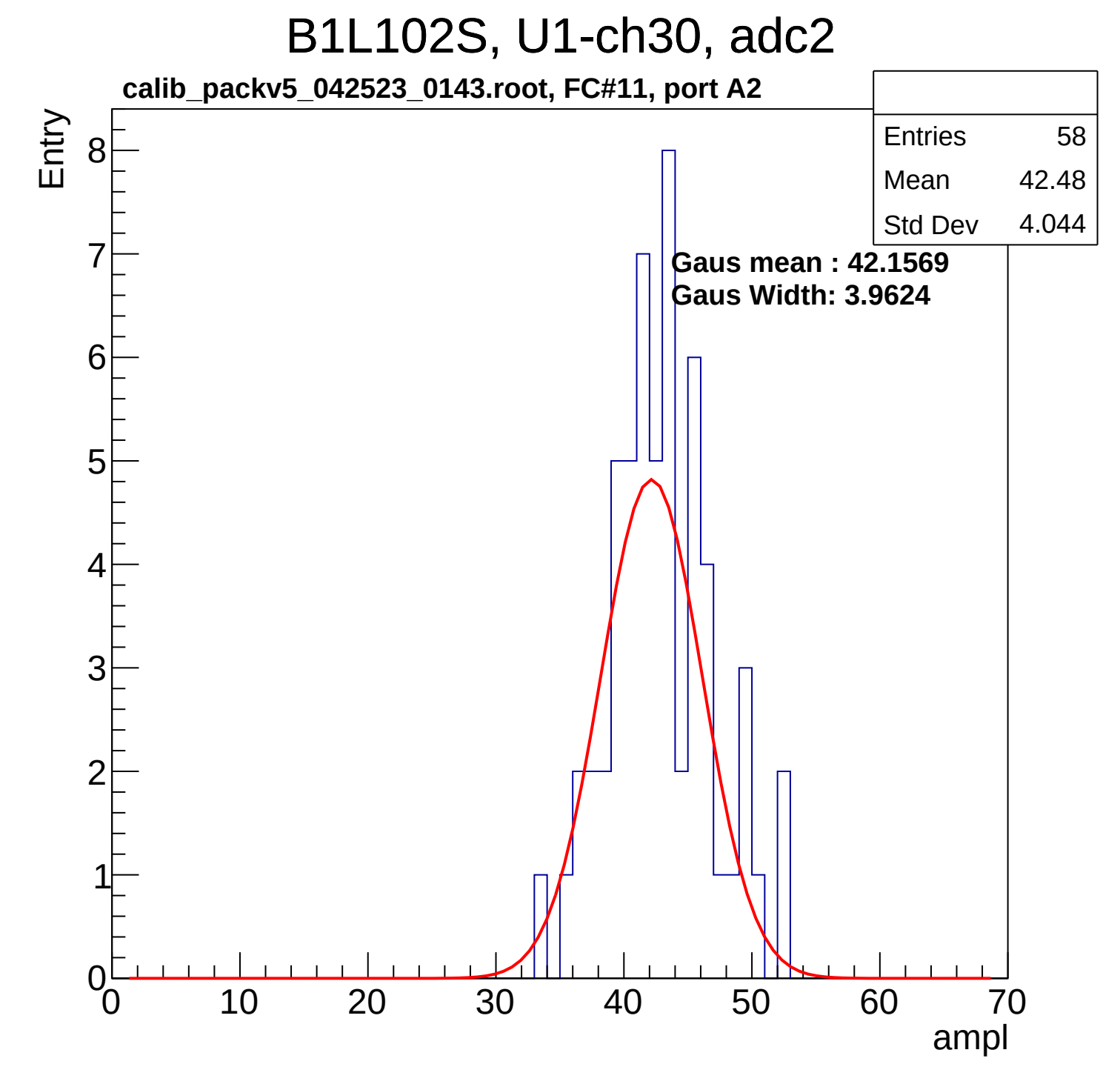
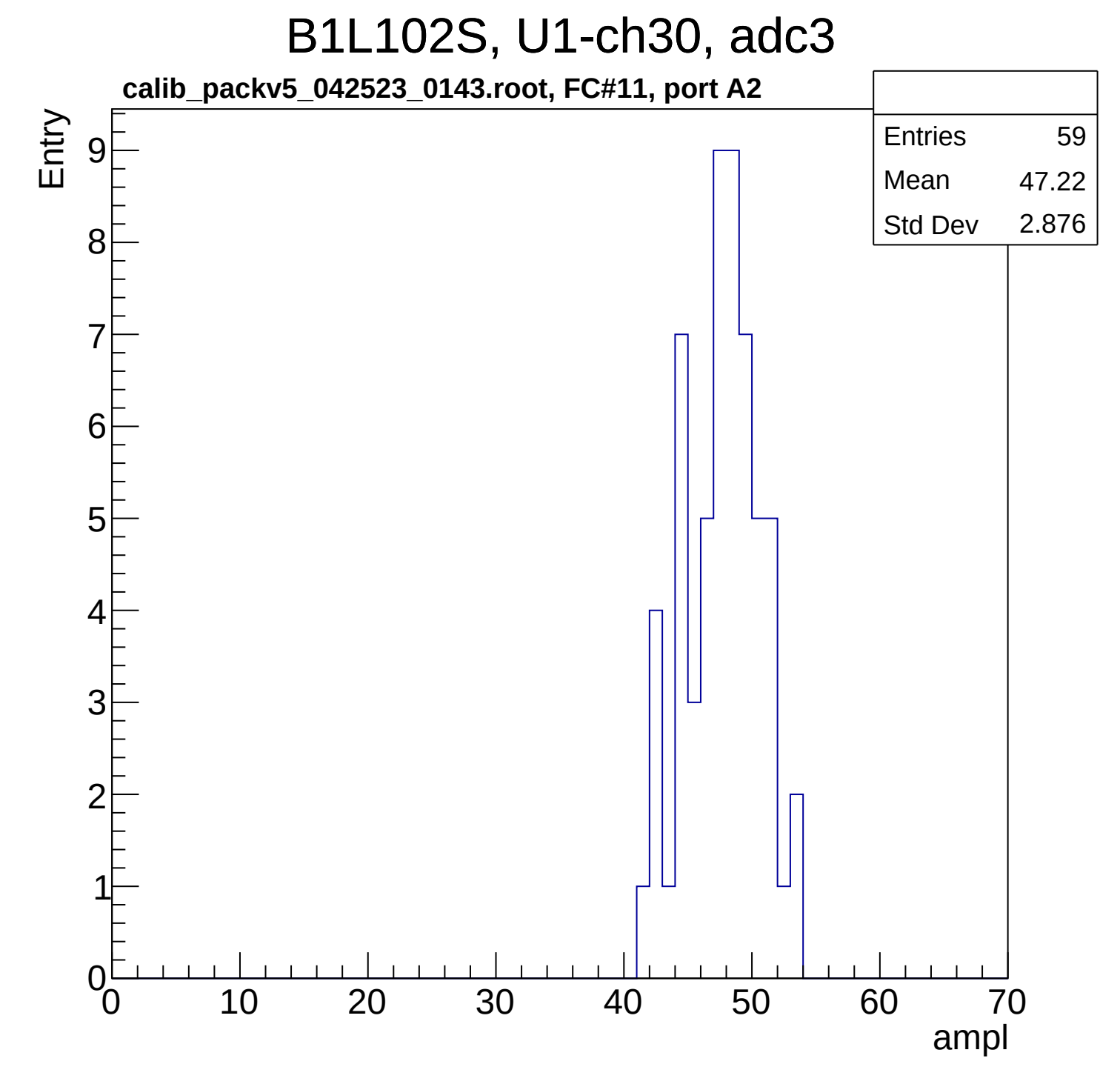
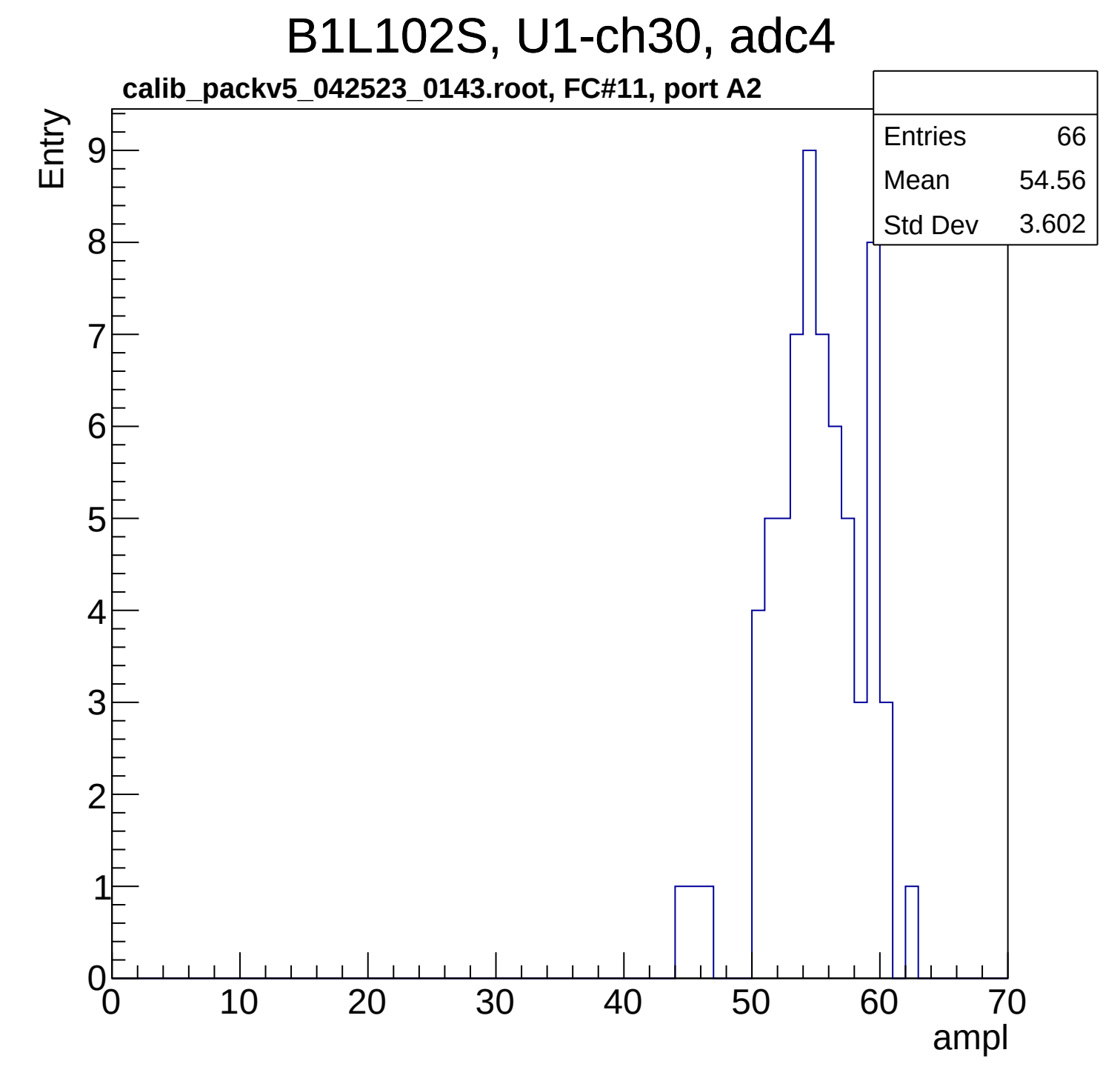
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

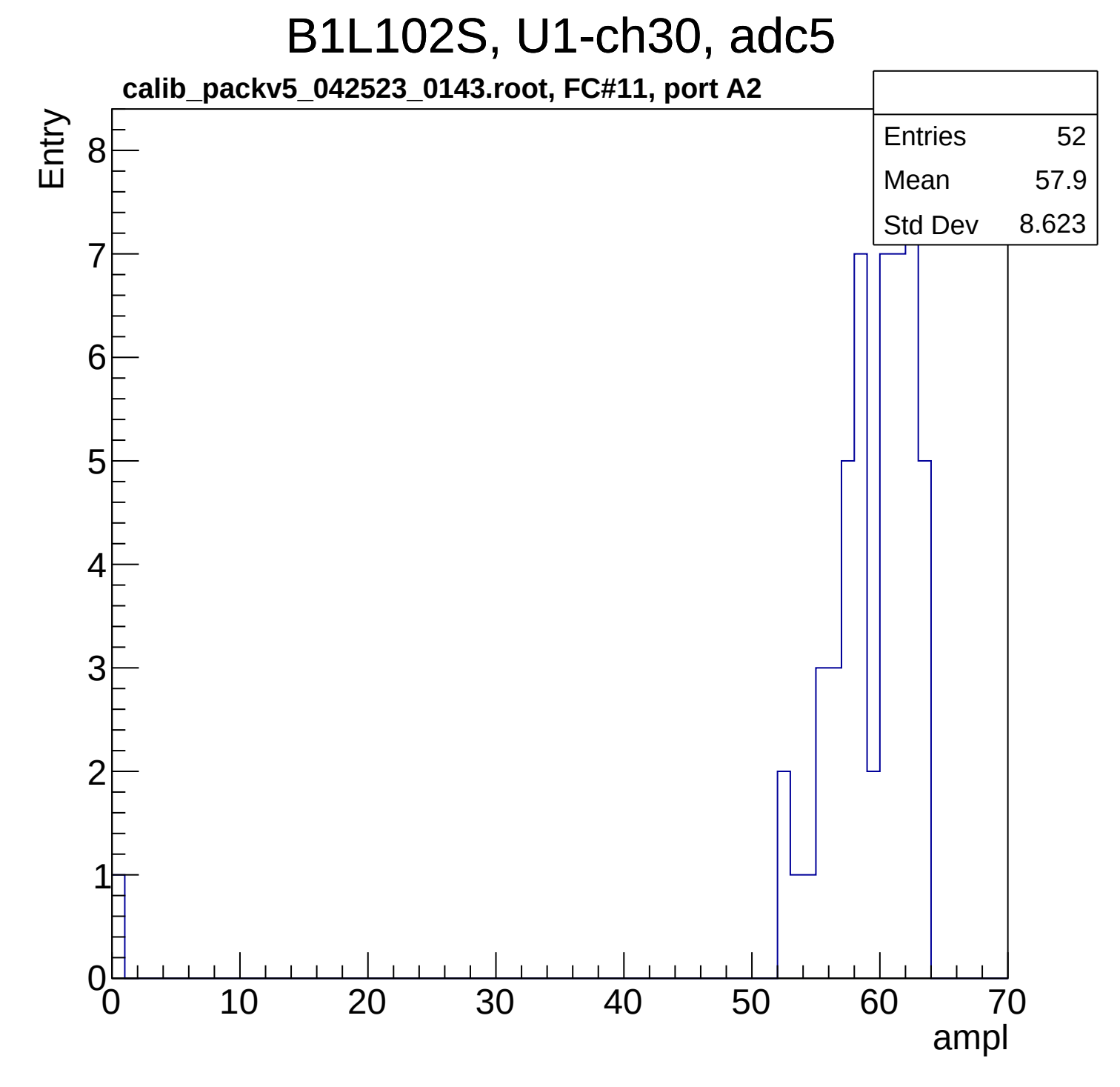

B1L102S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 28.32
Std Dev 3.352
Gaus mean : 29.0062
Gaus Width: 3.4854
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

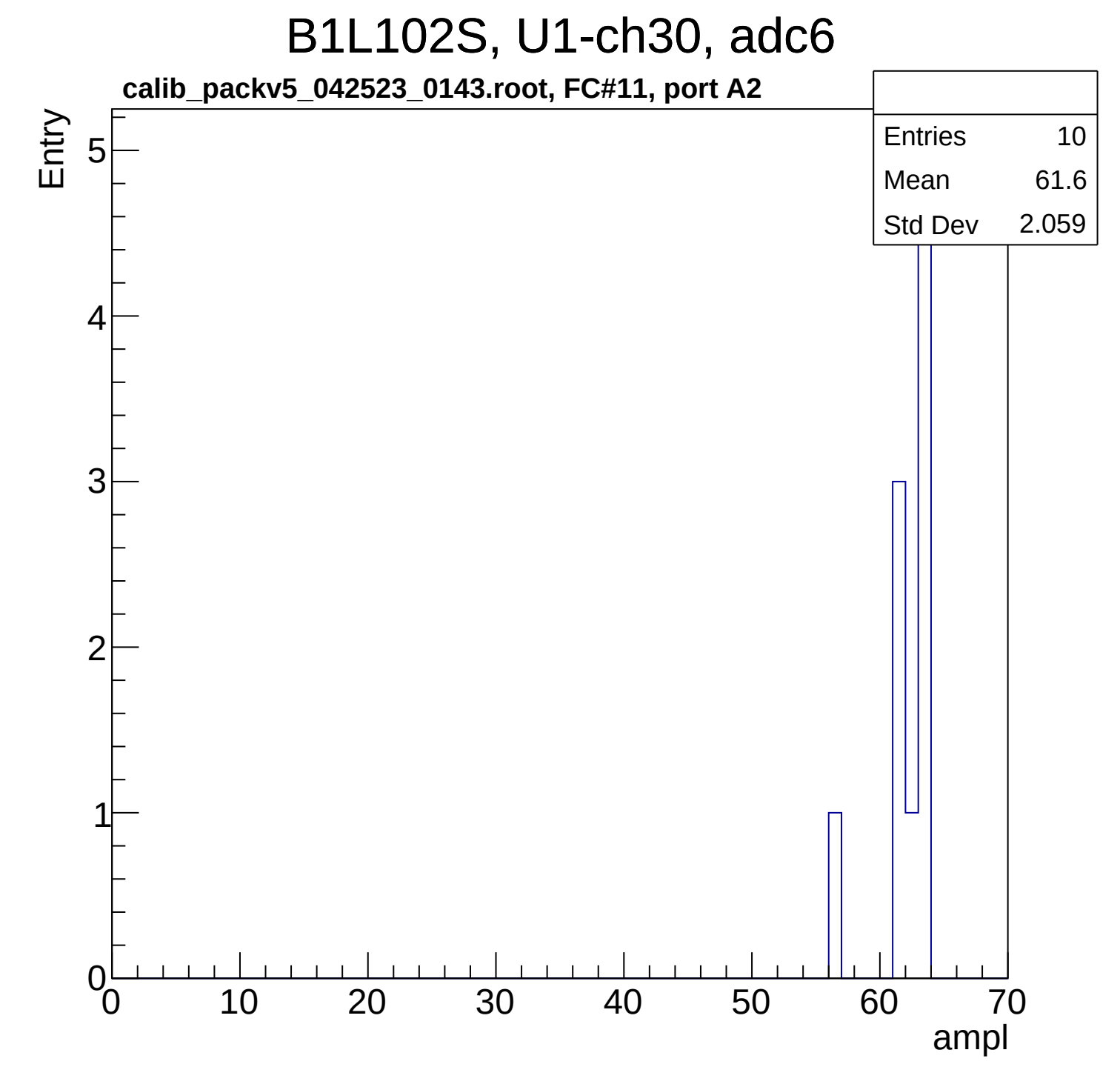

B1L102S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 34.95
Std Dev 3.679
Gaus mean : 35.6385
Gaus Width: 3.7432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

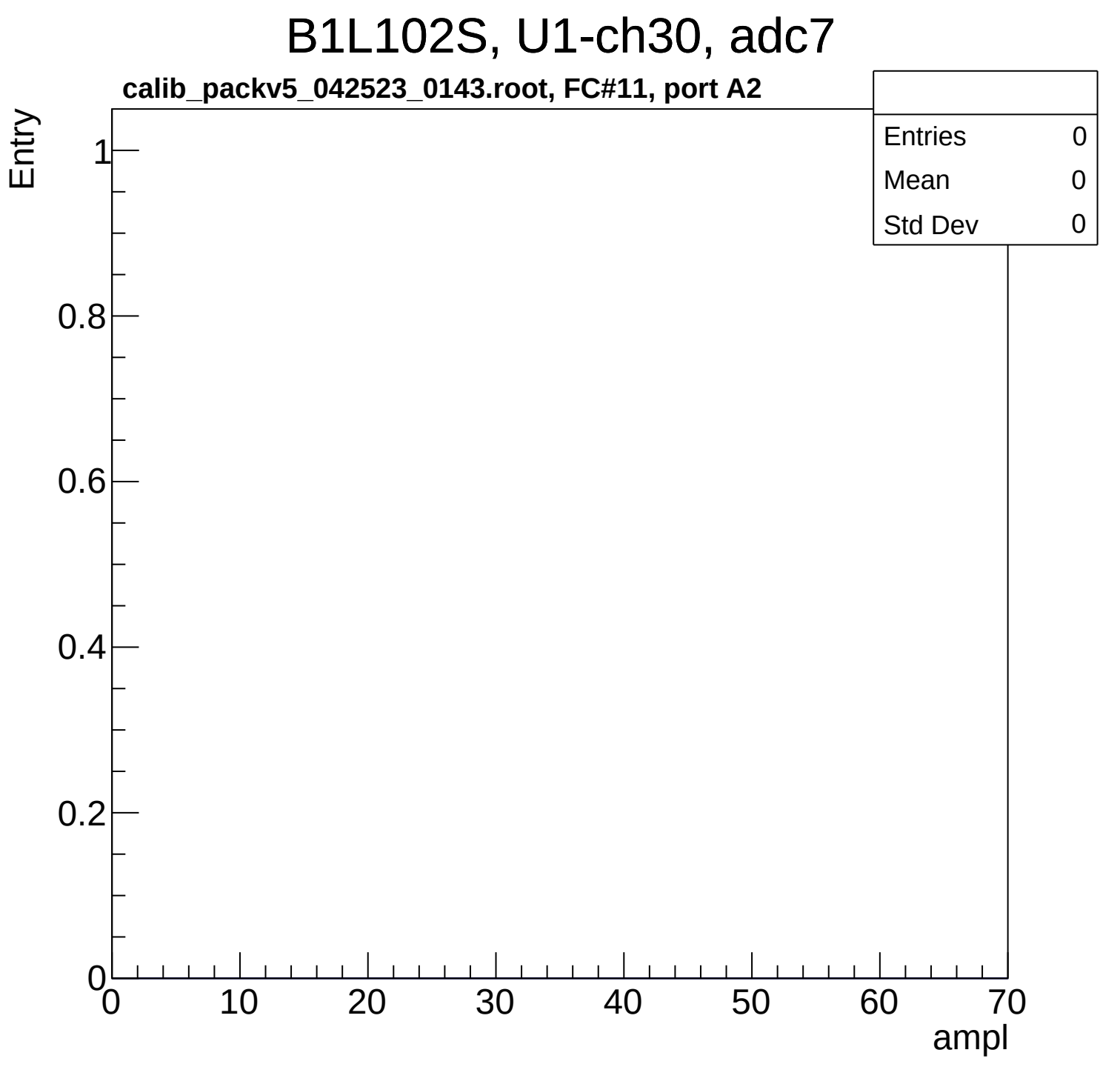

B1L102S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 42.48
Std Dev 4.044
Gaus mean : 42.1569
Gaus Width: 3.9624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

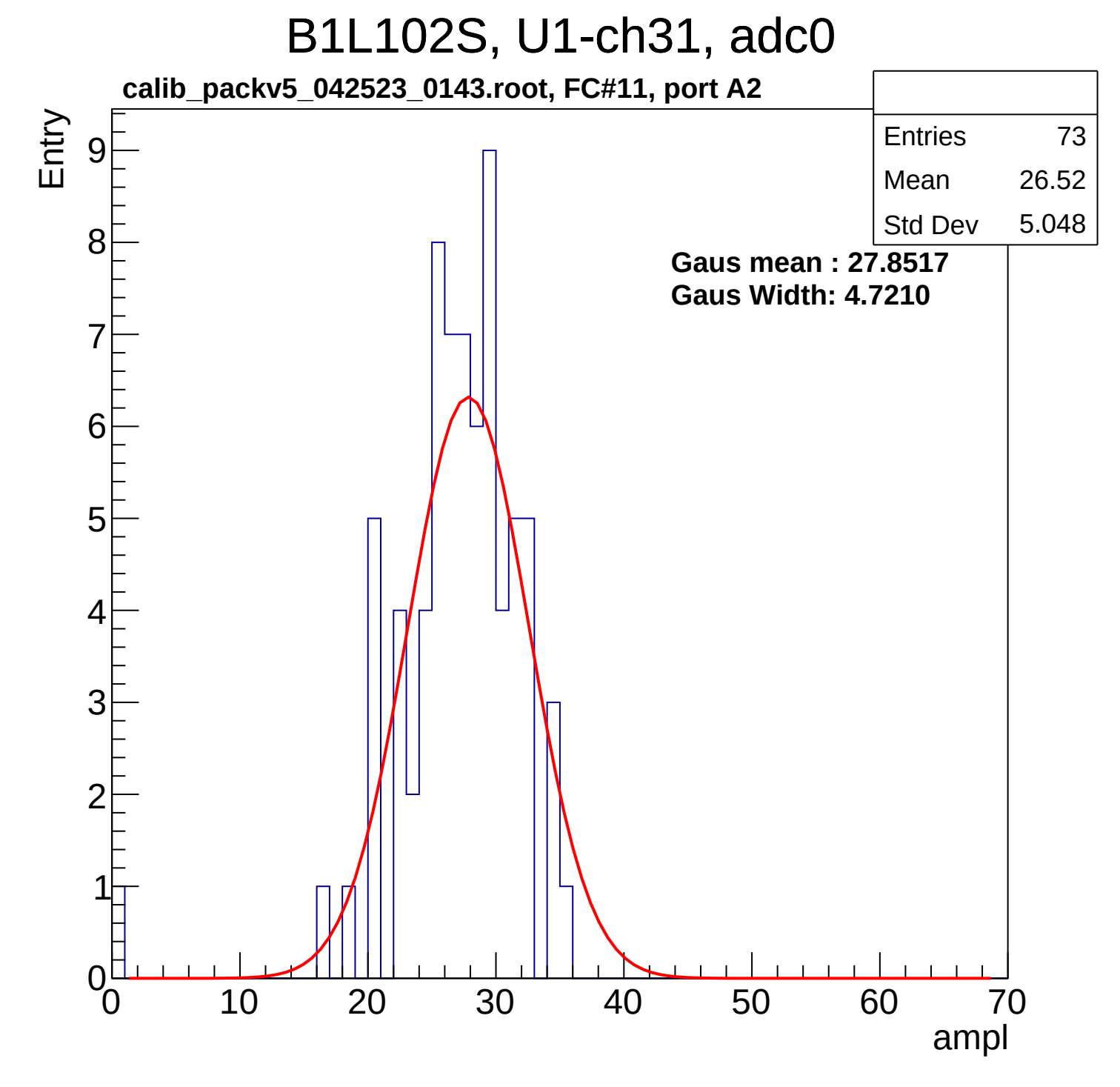

B1L102S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 47.22
Std Dev 2.876
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

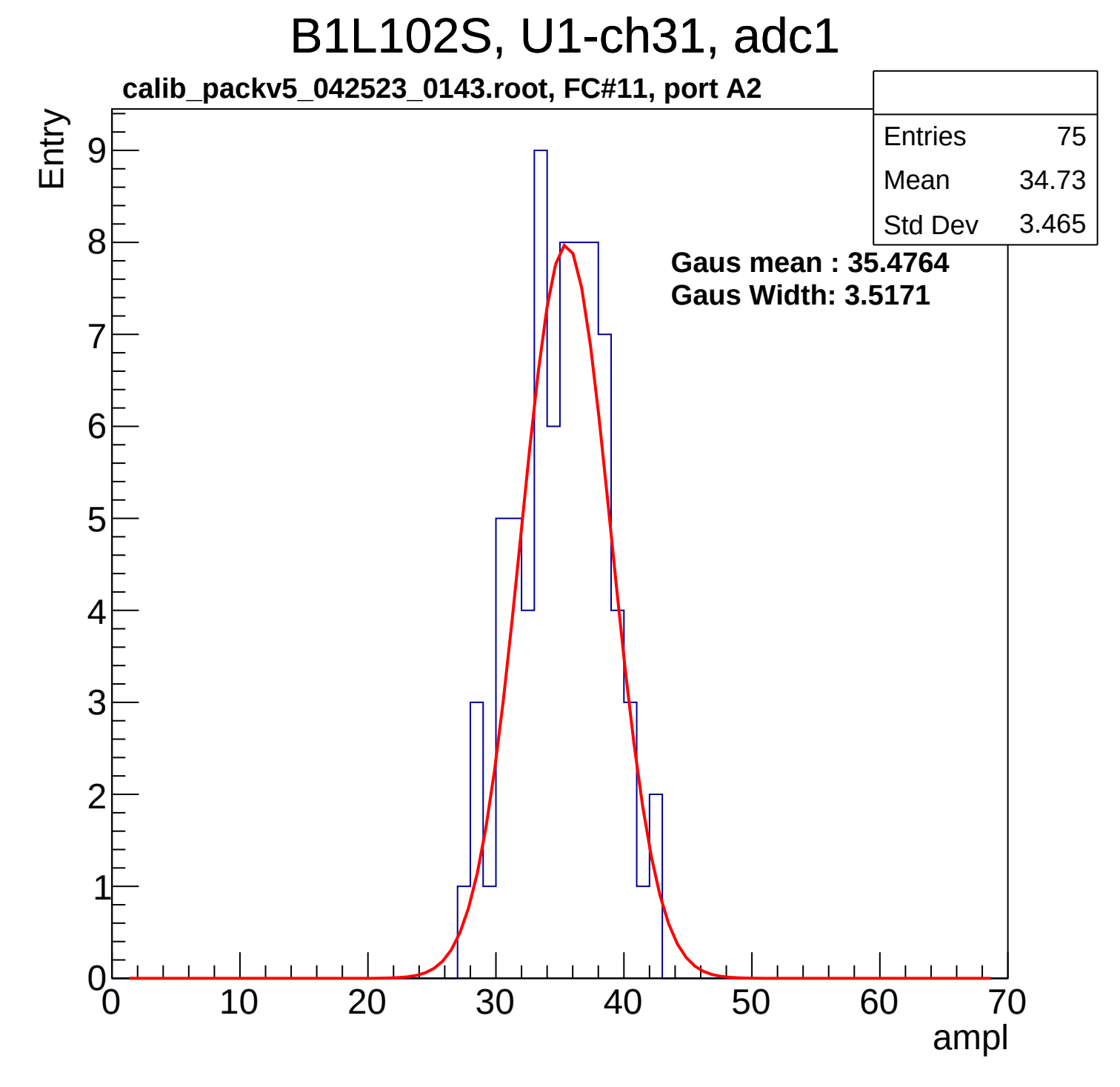

B1L102S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 54.56
Std Dev 3.602
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

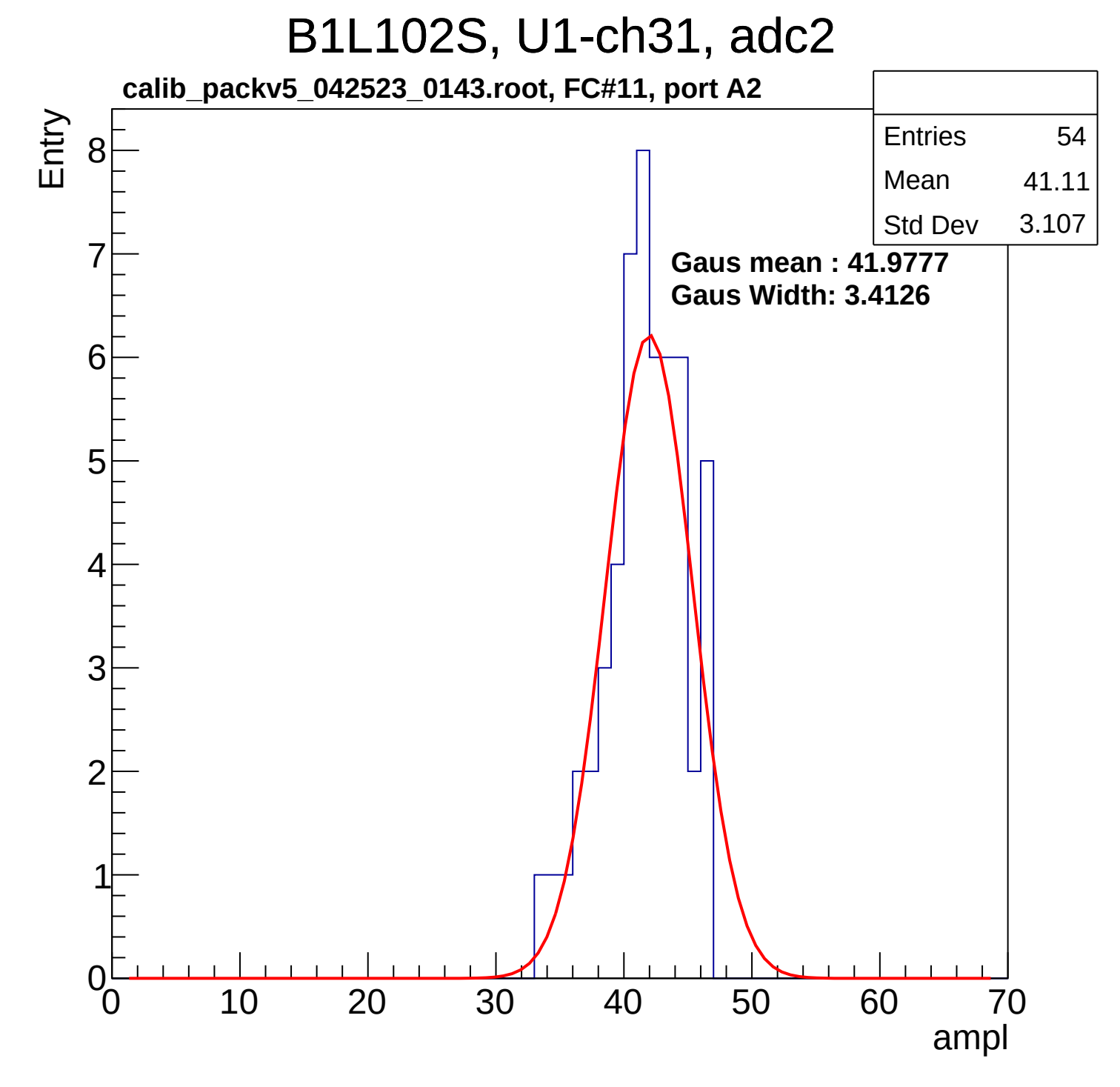

B1L102S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 57.9
Std Dev 8.623
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

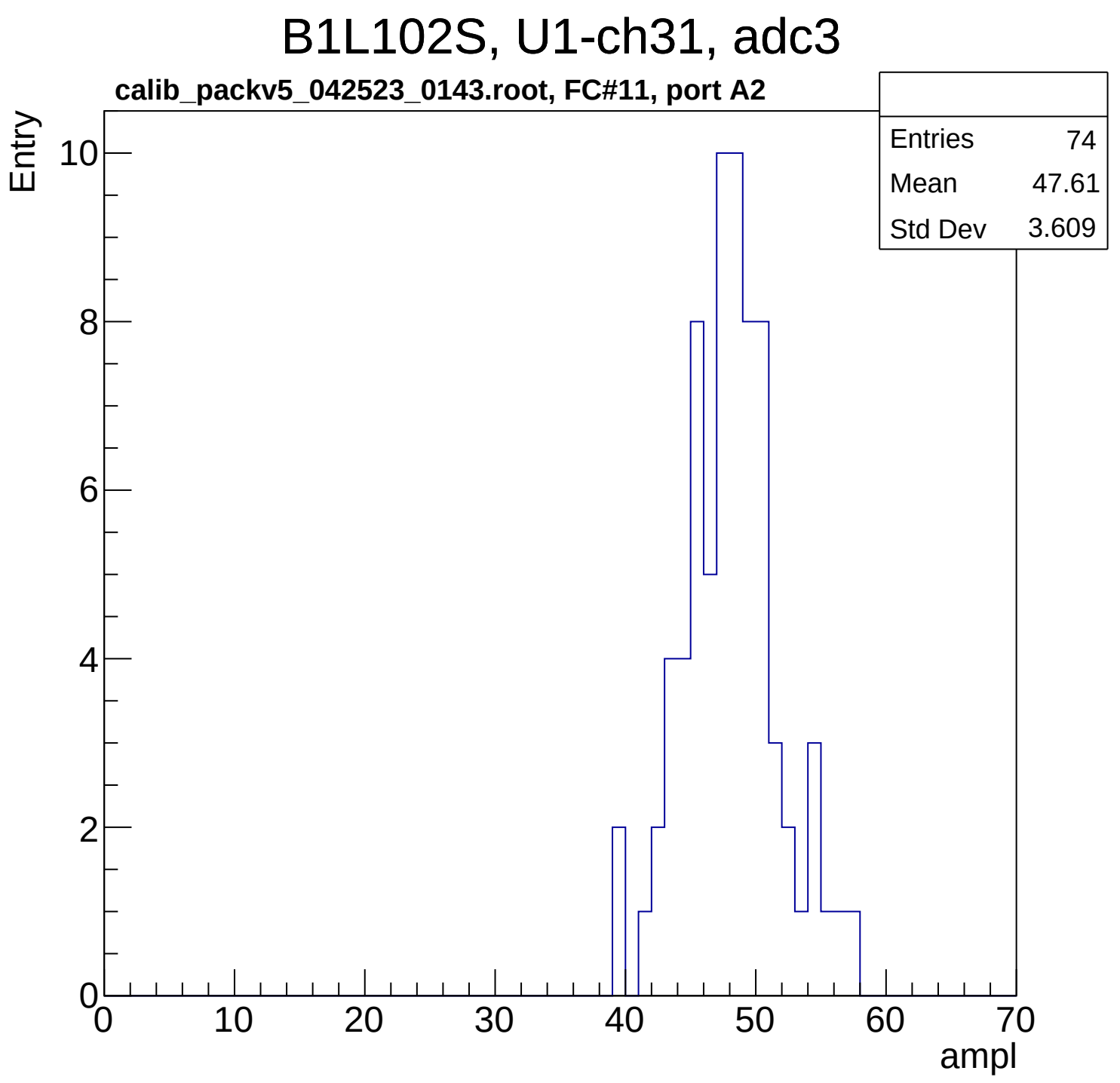

B1L102S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 61.6
Std Dev 2.059
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

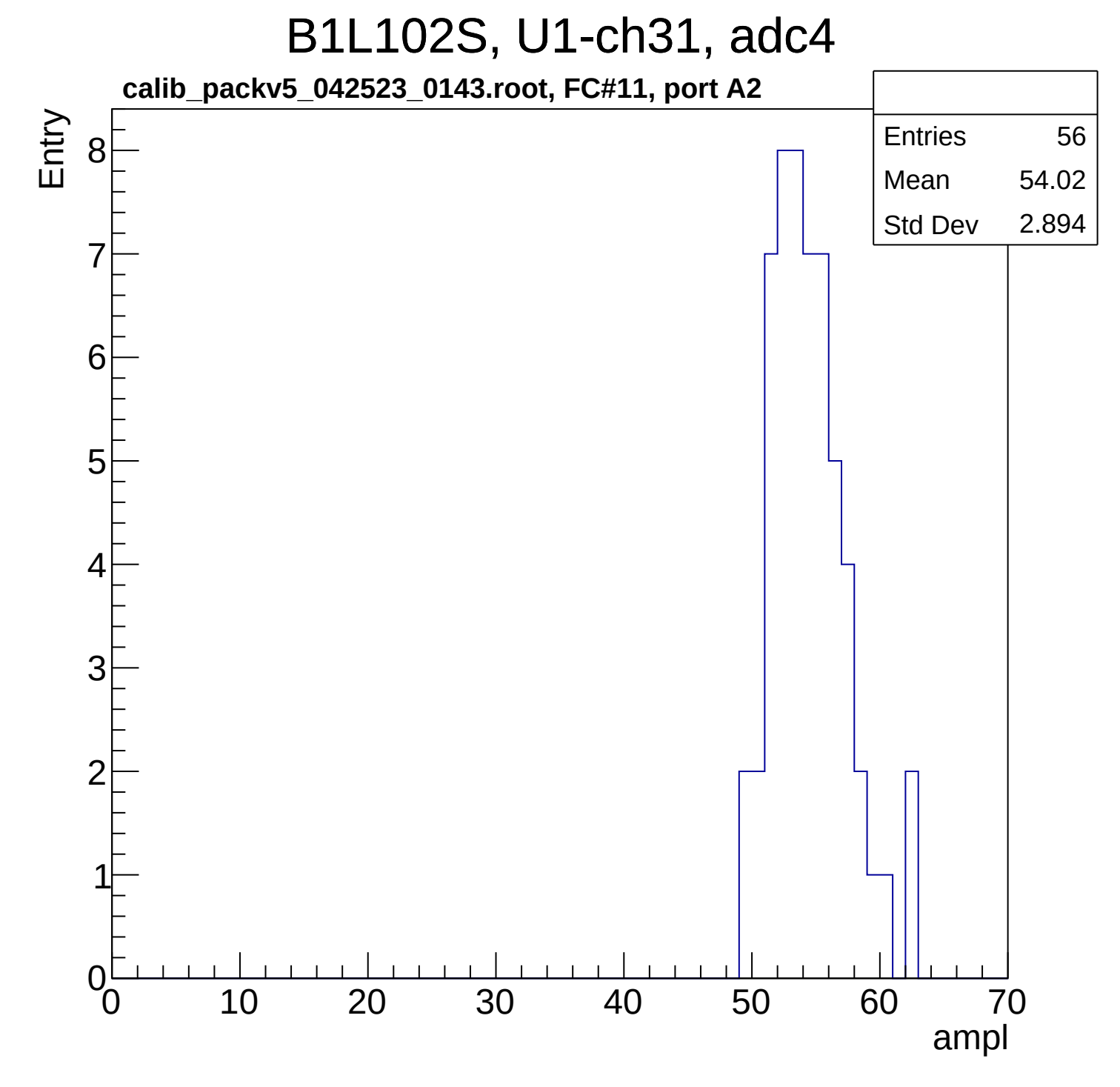

B1L102S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

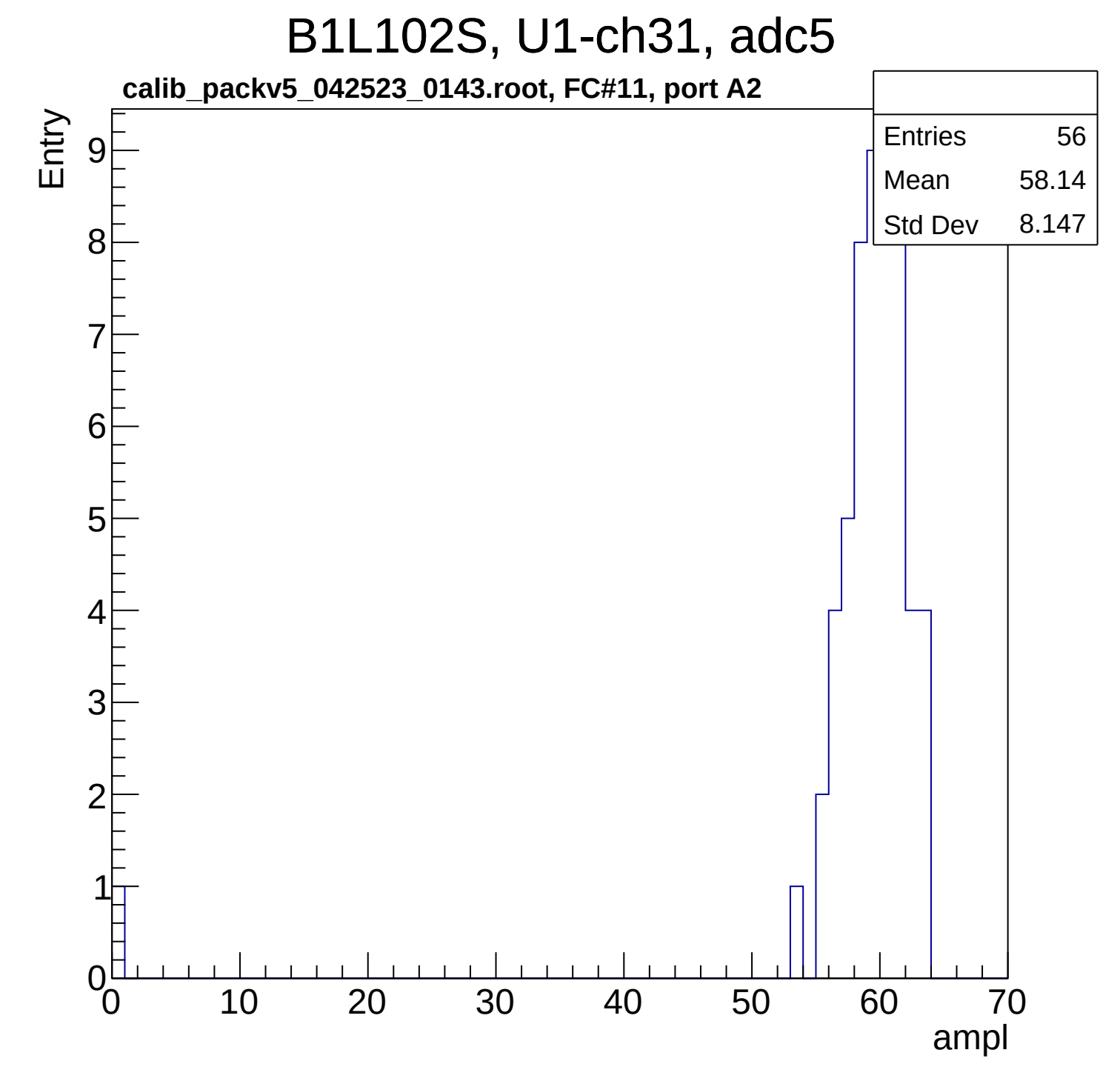

B1L102S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 26.52
Std Dev 5.048
Gaus mean : 27.8517
Gaus Width: 4.7210
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

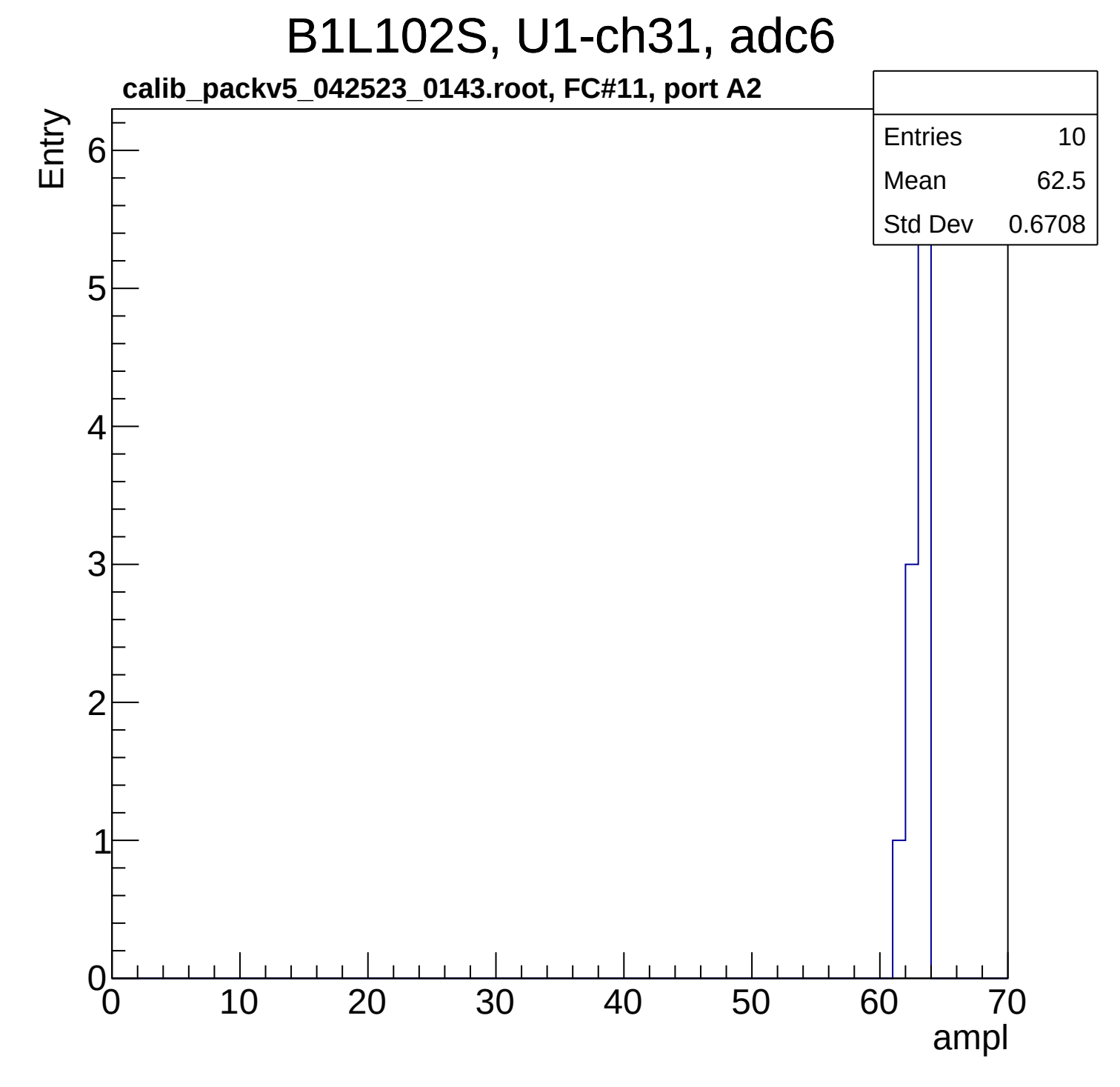

B1L102S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 34.73
Std Dev 3.465
Gaus mean : 35.4764
Gaus Width: 3.5171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

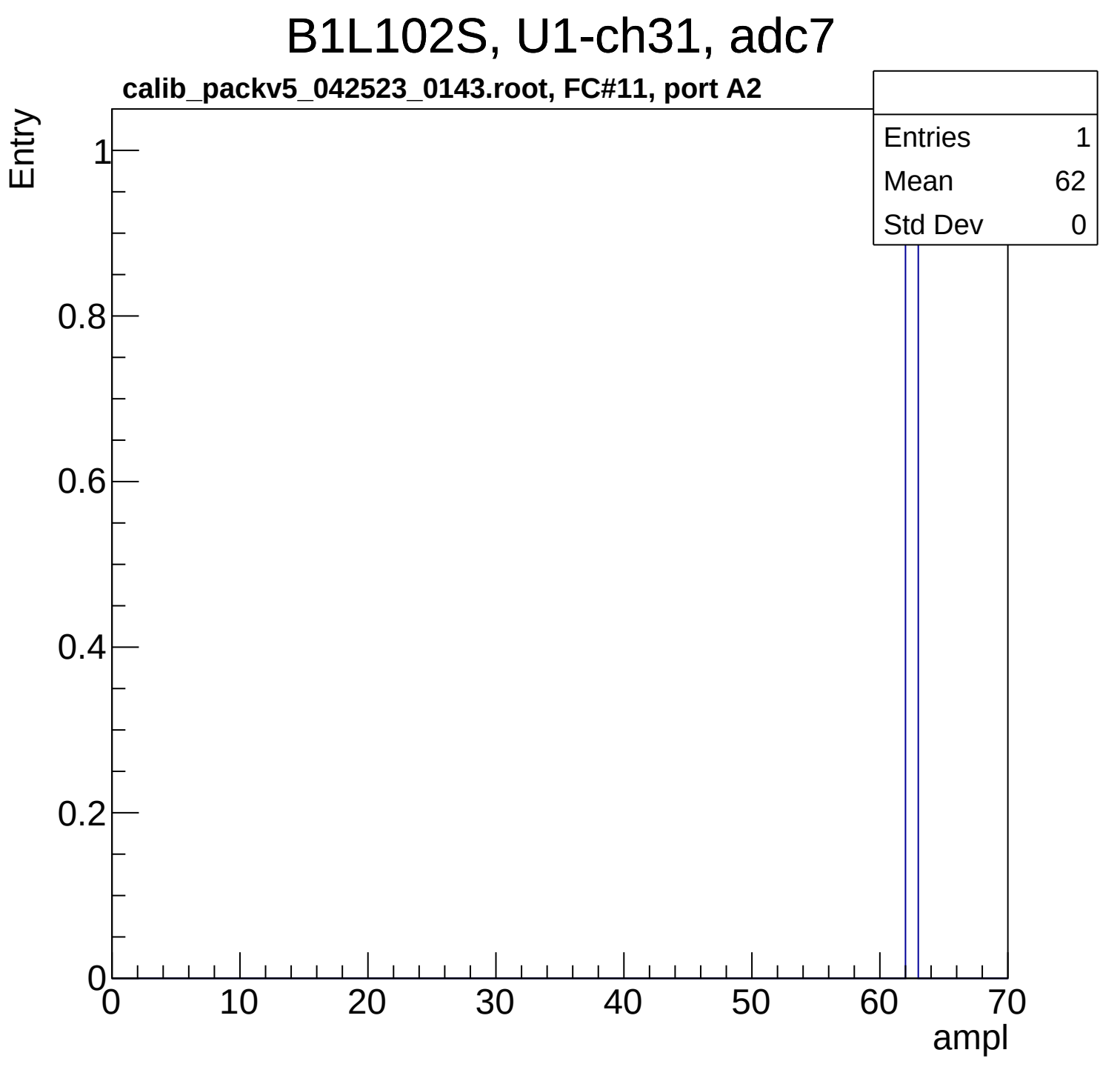

B1L102S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 41.11
Std Dev 3.107
Gaus mean : 41.9777
Gaus Width: 3.4126
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

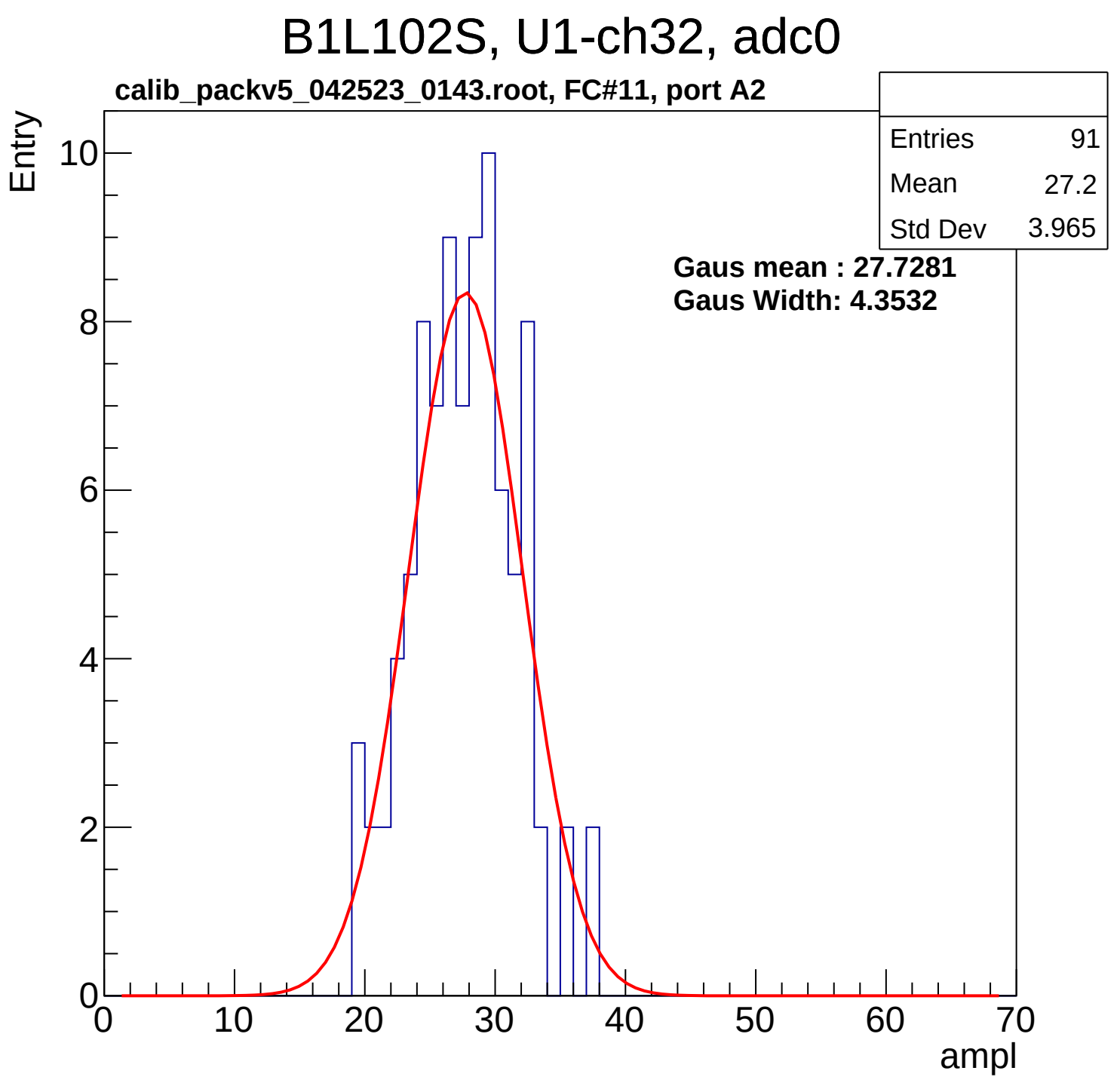

B1L102S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 47.61
Std Dev 3.609
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

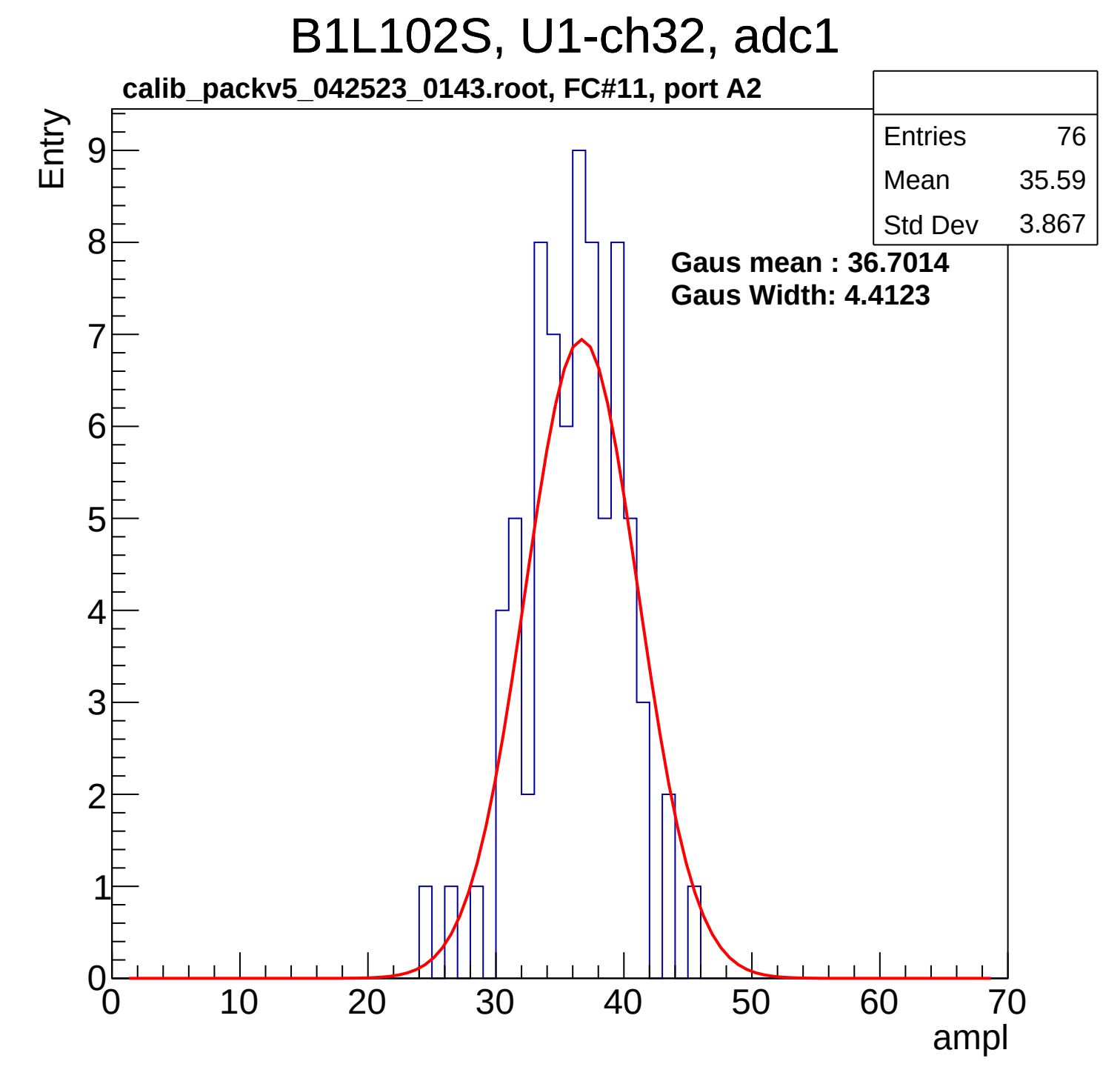

B1L102S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 54.02
Std Dev 2.894
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

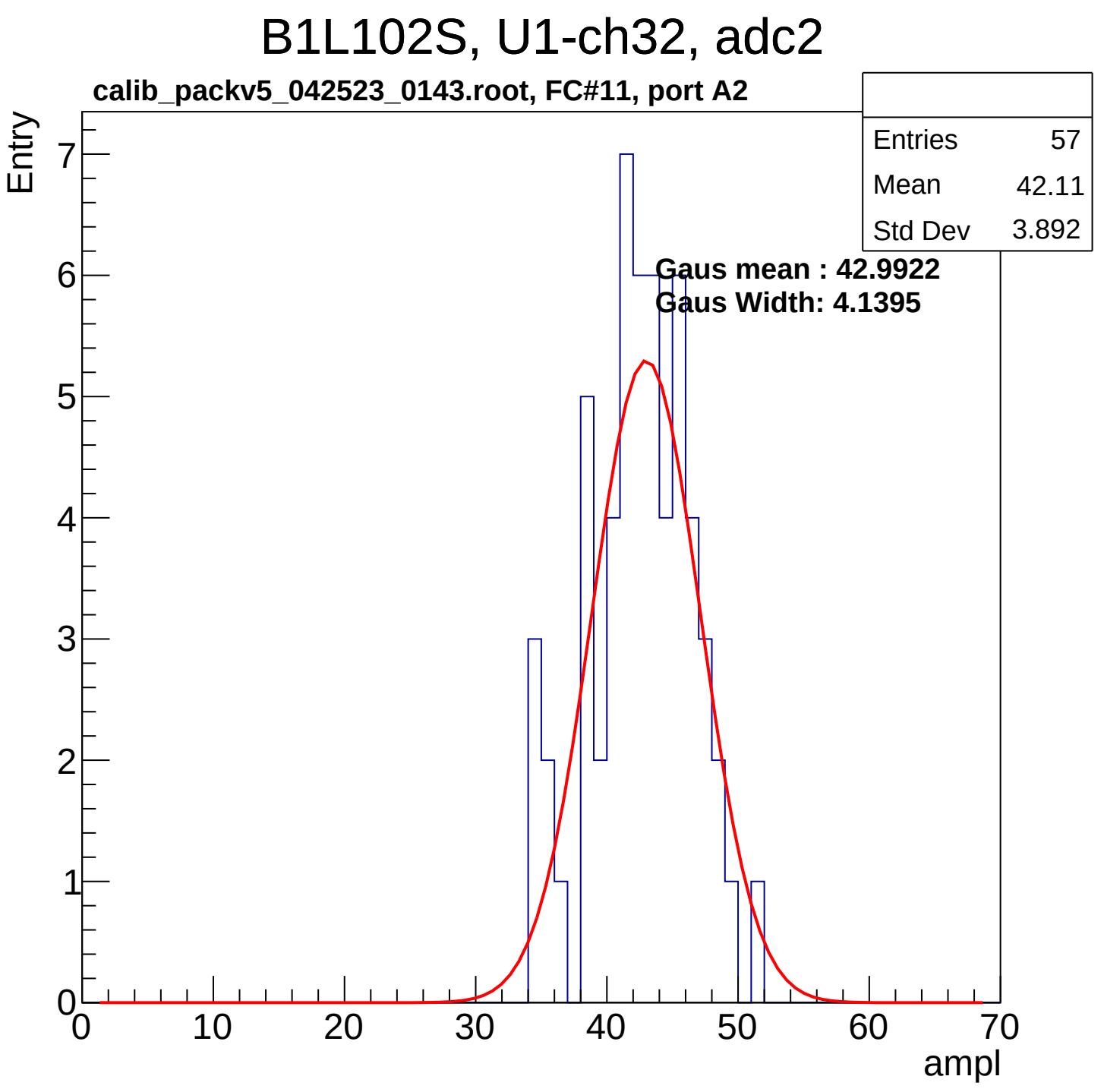

B1L102S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 58.14
Std Dev 8.147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

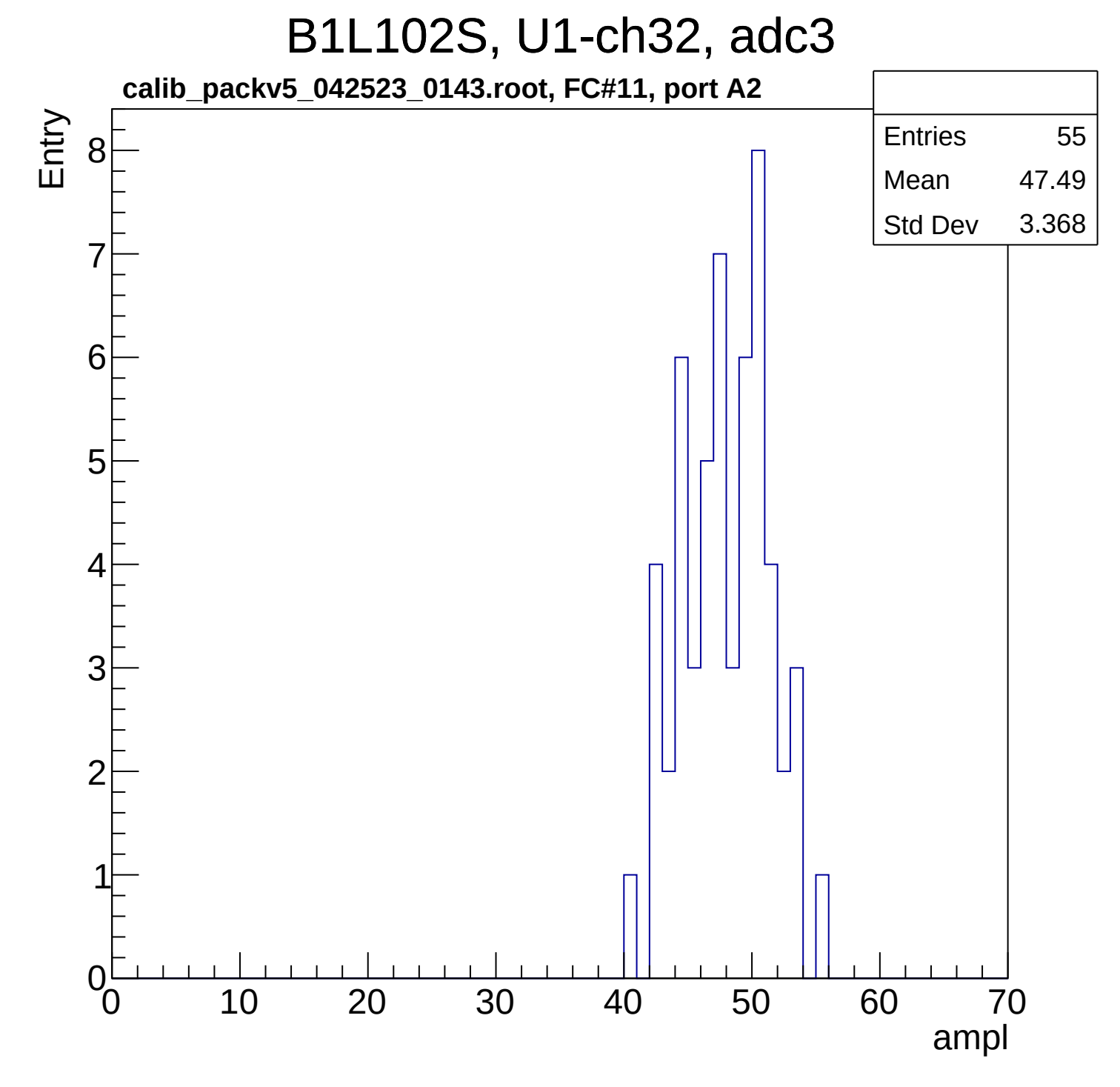

B1L102S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 62.5
Std Dev 0.6708
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

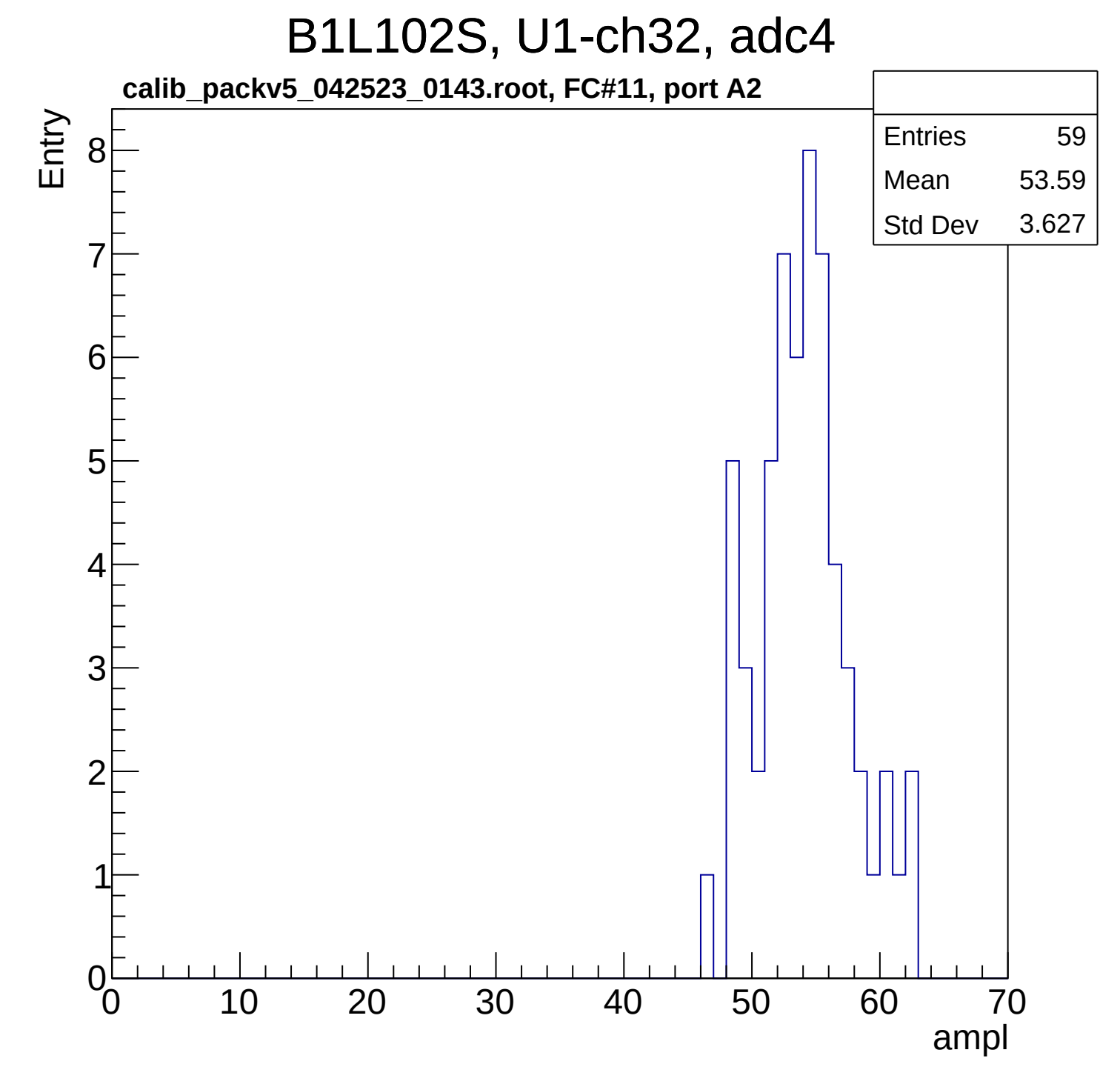

B1L102S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 91
Mean 27.2
Std Dev 3.965
Gaus mean : 27.7281
Gaus Width: 4.3532
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 35.59
Std Dev 3.867
Gaus mean : 36.7014
Gaus Width: 4.4123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 42.11
Std Dev 3.892
Gaus mean : 42.9922
Gaus Width: 4.1395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 47.49
Std Dev 3.368
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 53.59
Std Dev 3.627
Entry
ampl
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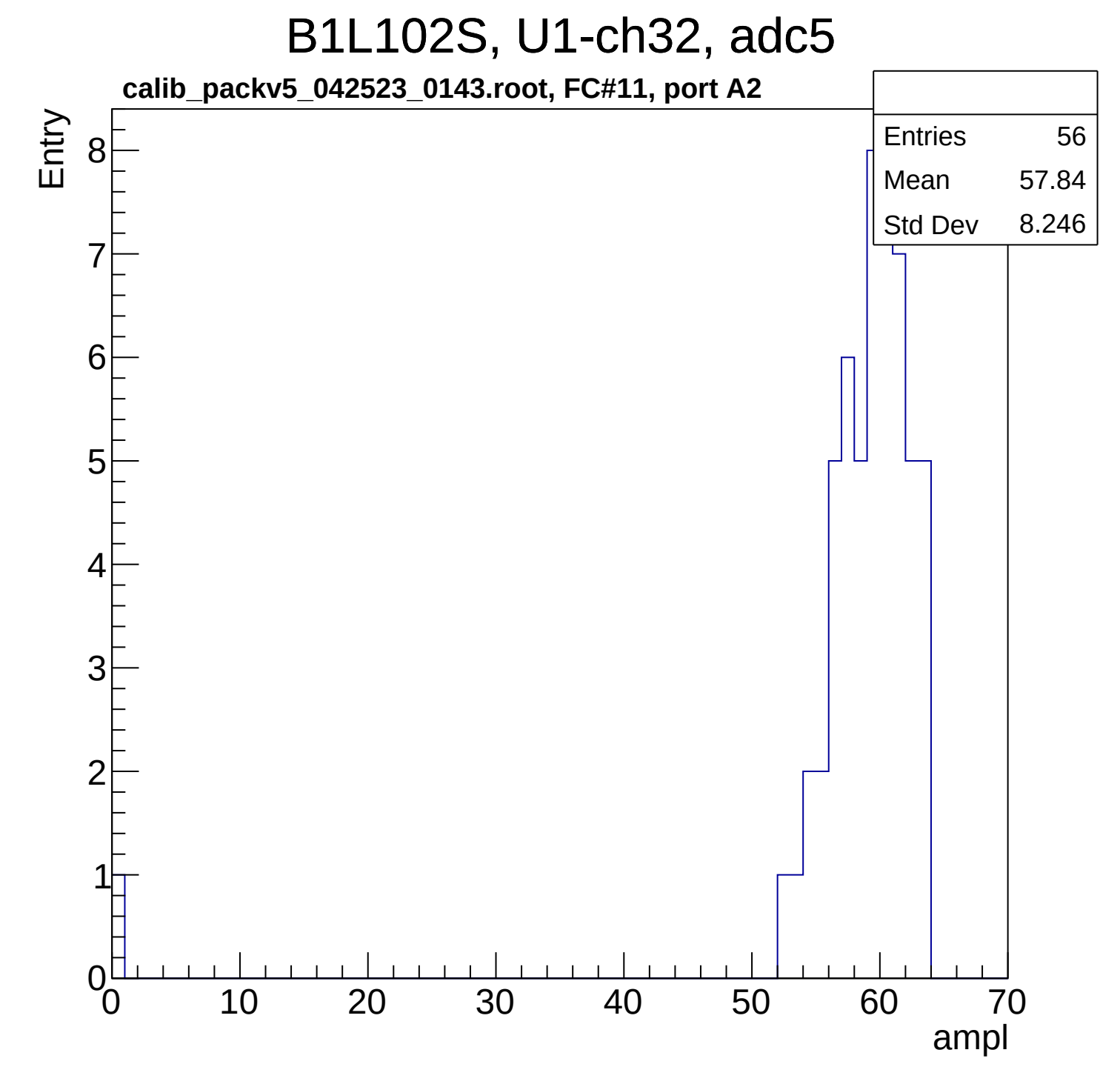
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 57.84
Std Dev 8.246
Entry
ampl
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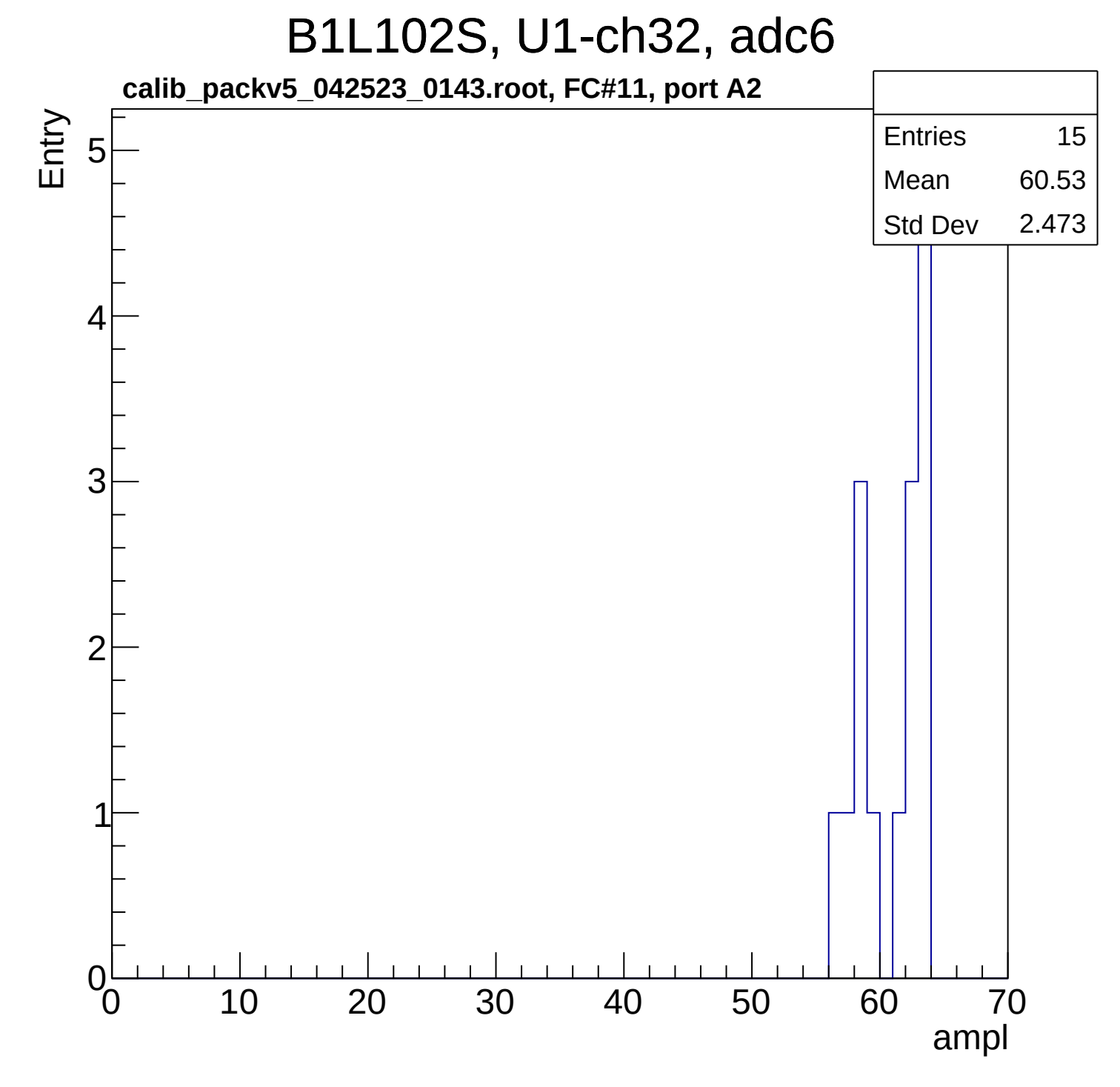
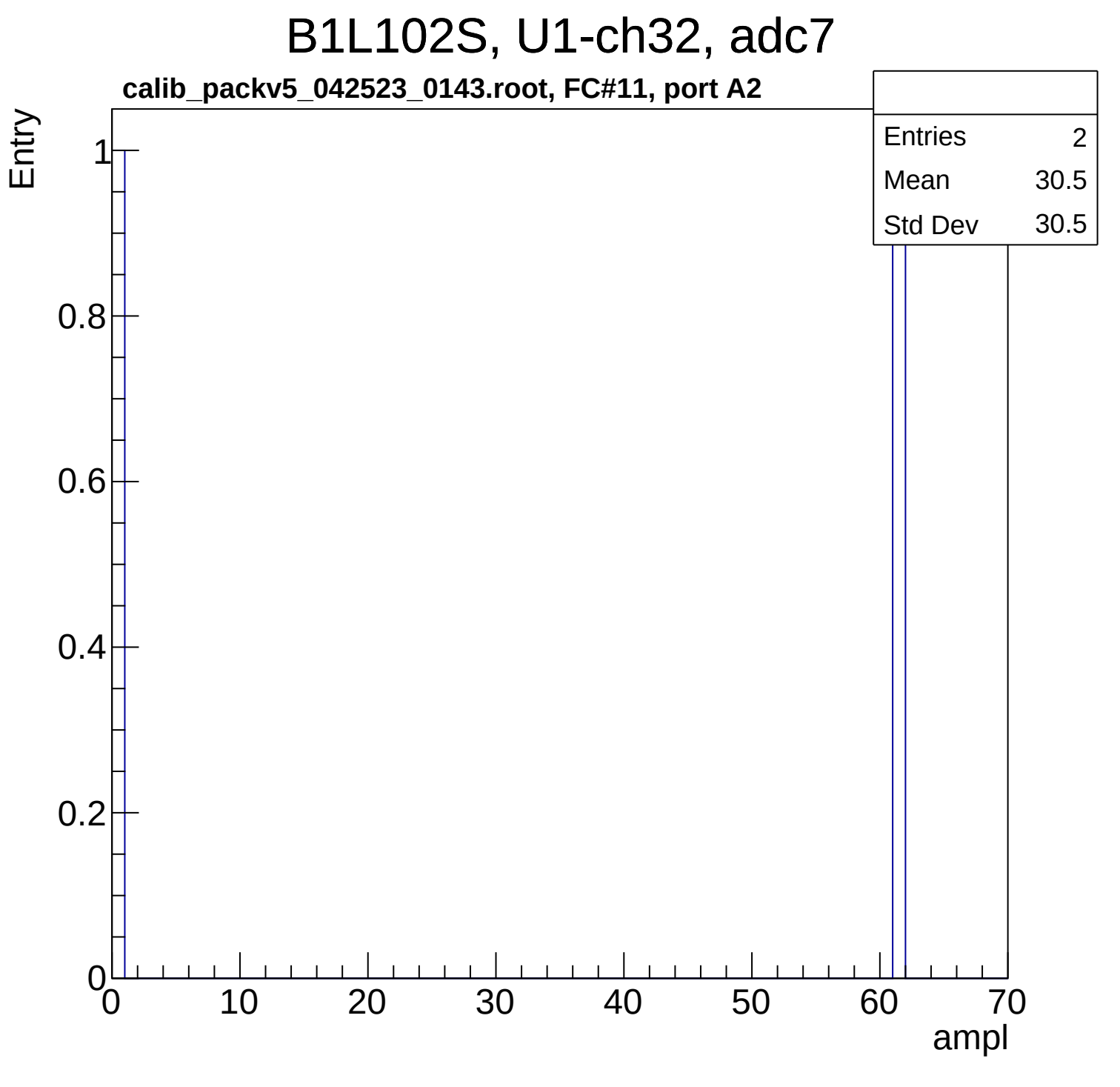
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 60.53
Std Dev 2.473
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L102S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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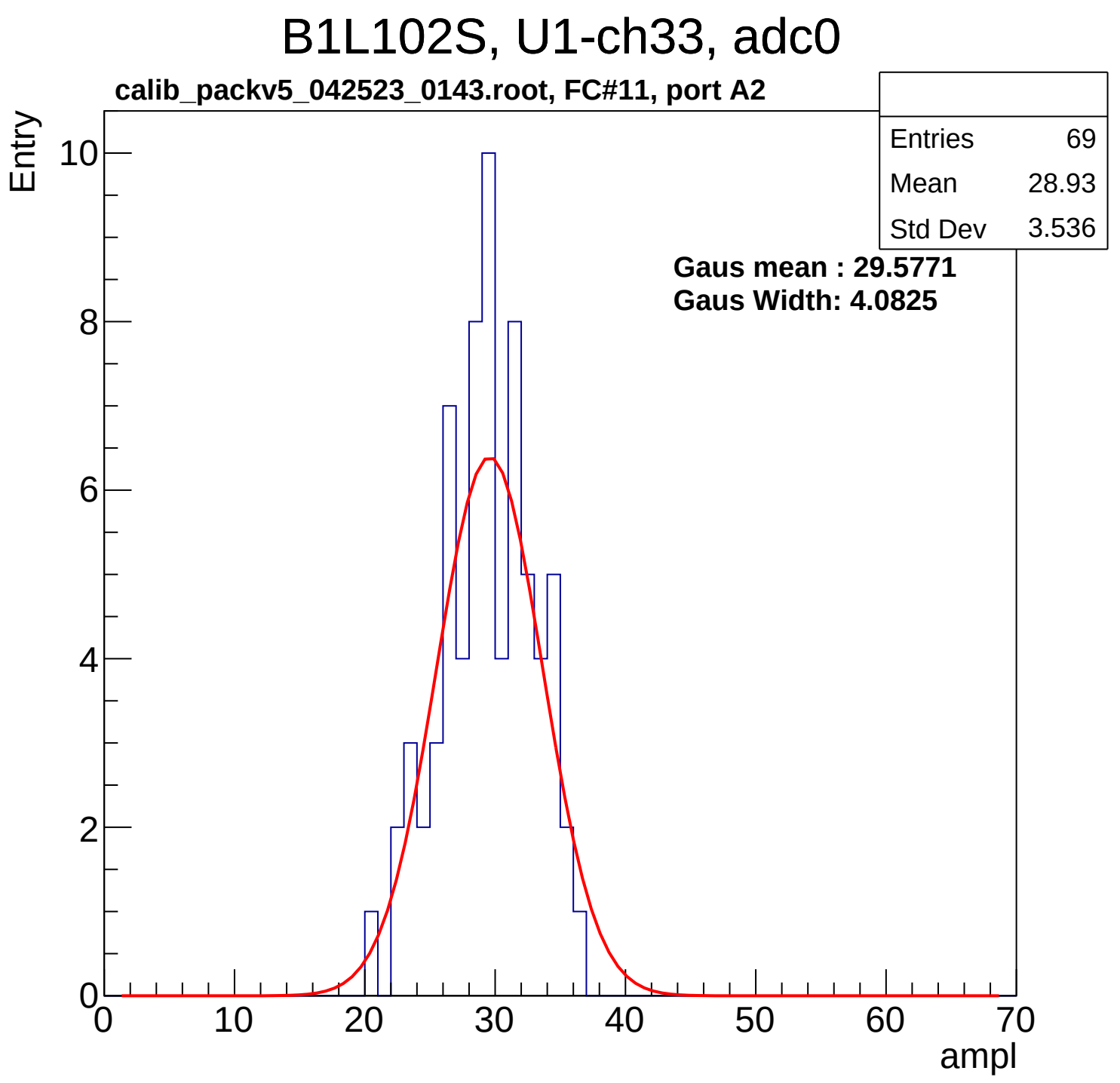
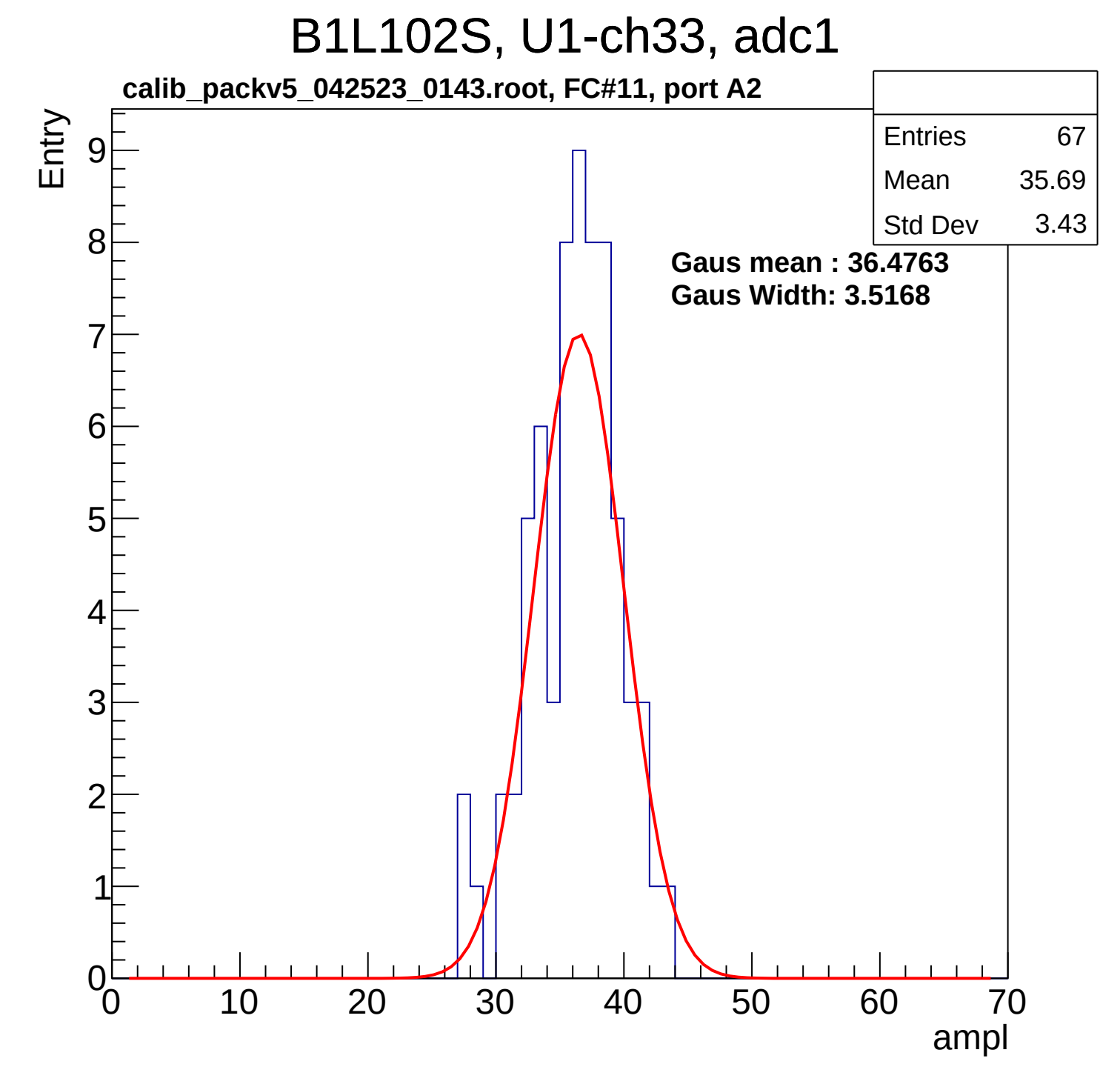
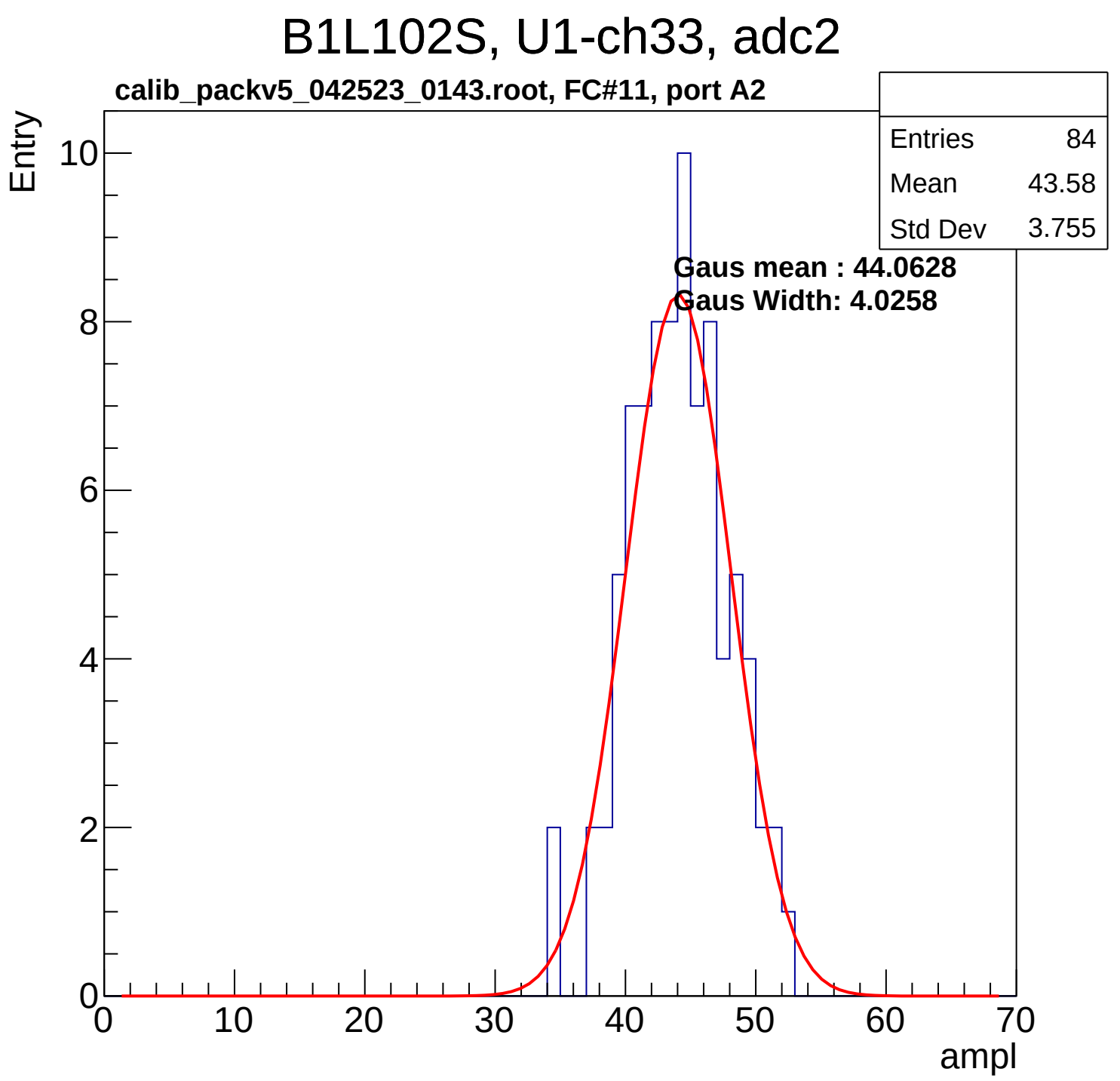
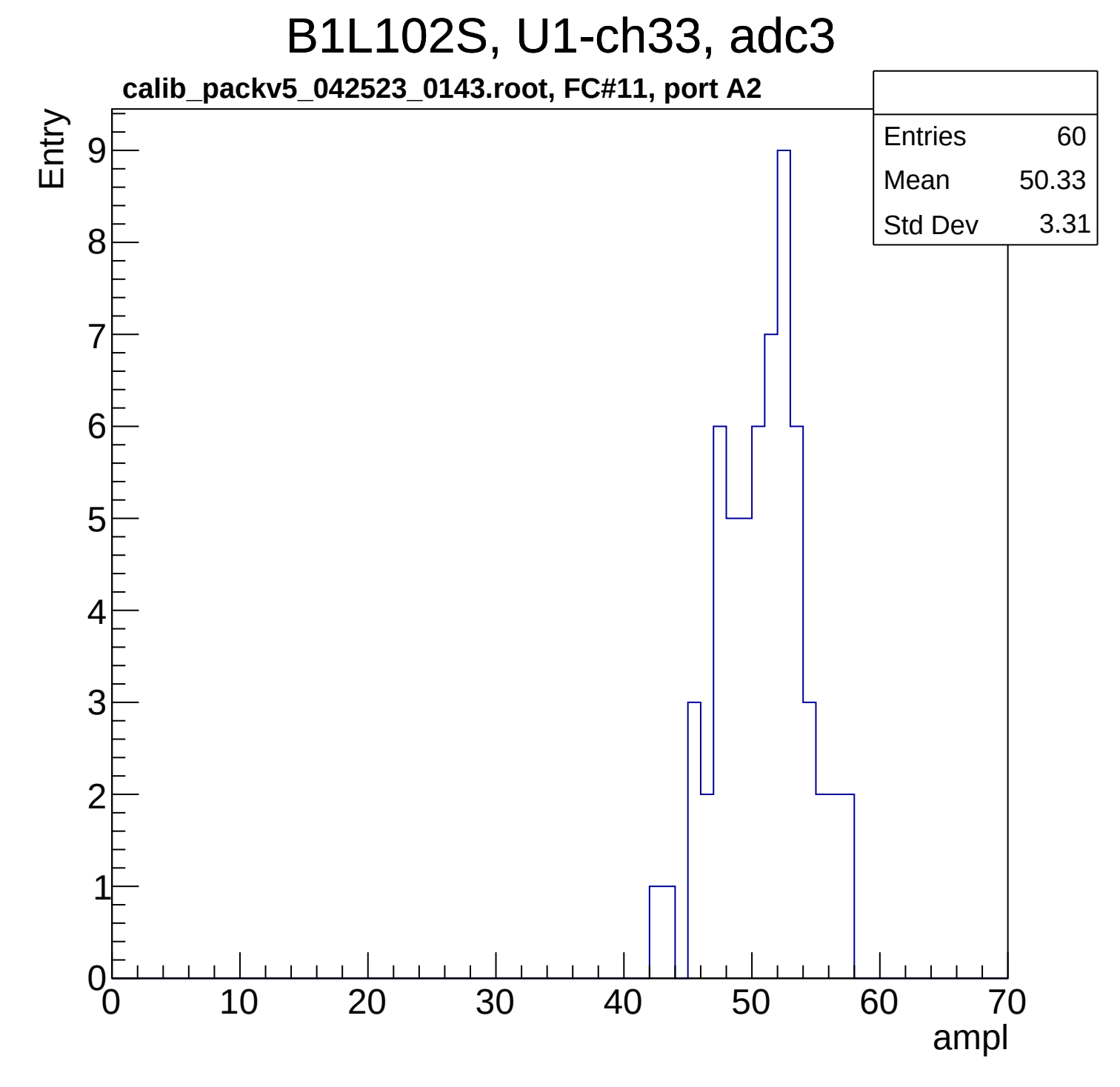
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.93
Std Dev 3.536
Gaus mean : 29.5771
Gaus Width: 4.0825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.69
Std Dev 3.43
Gaus mean : 36.4763
Gaus Width: 3.5168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 43.58
Std Dev 3.755
Gaus mean : 44.0628
Gaus Width: 4.0258
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 50.33
Std Dev 3.31
Entry
ampl
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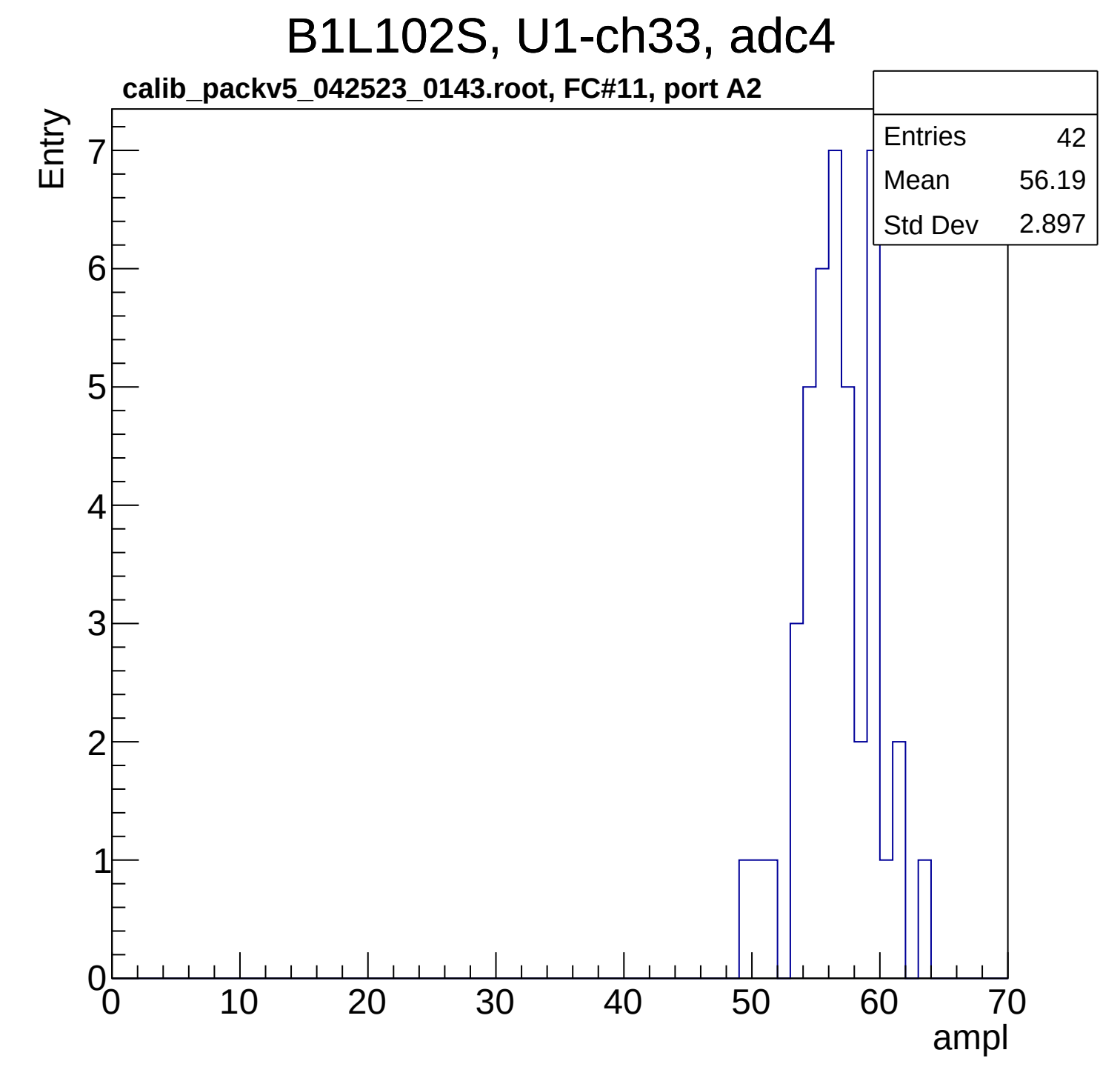
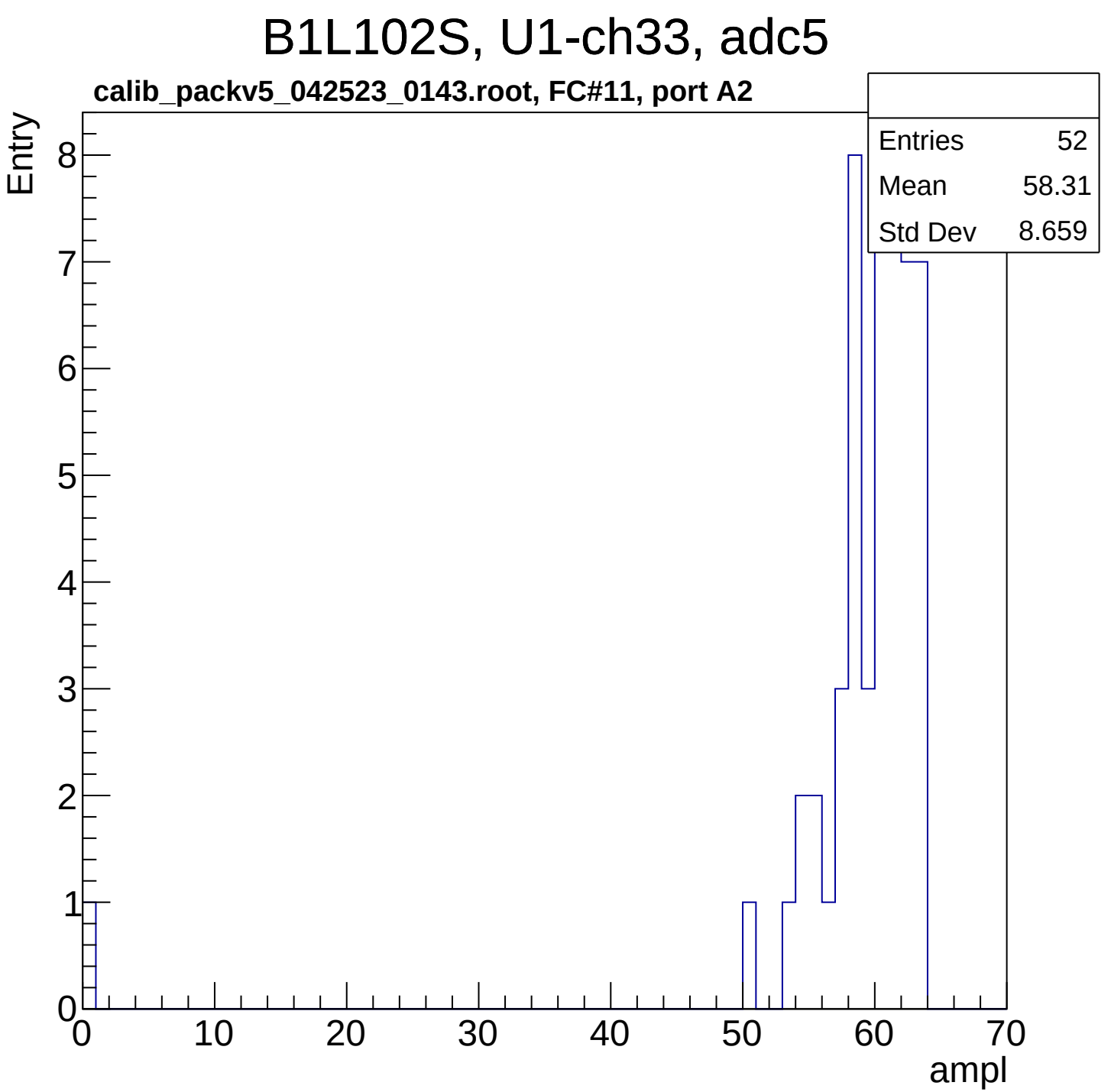
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 56.19
Std Dev 2.897
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.31
Std Dev 8.659
Entry
ampl
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1
2
3
4
5
6
7
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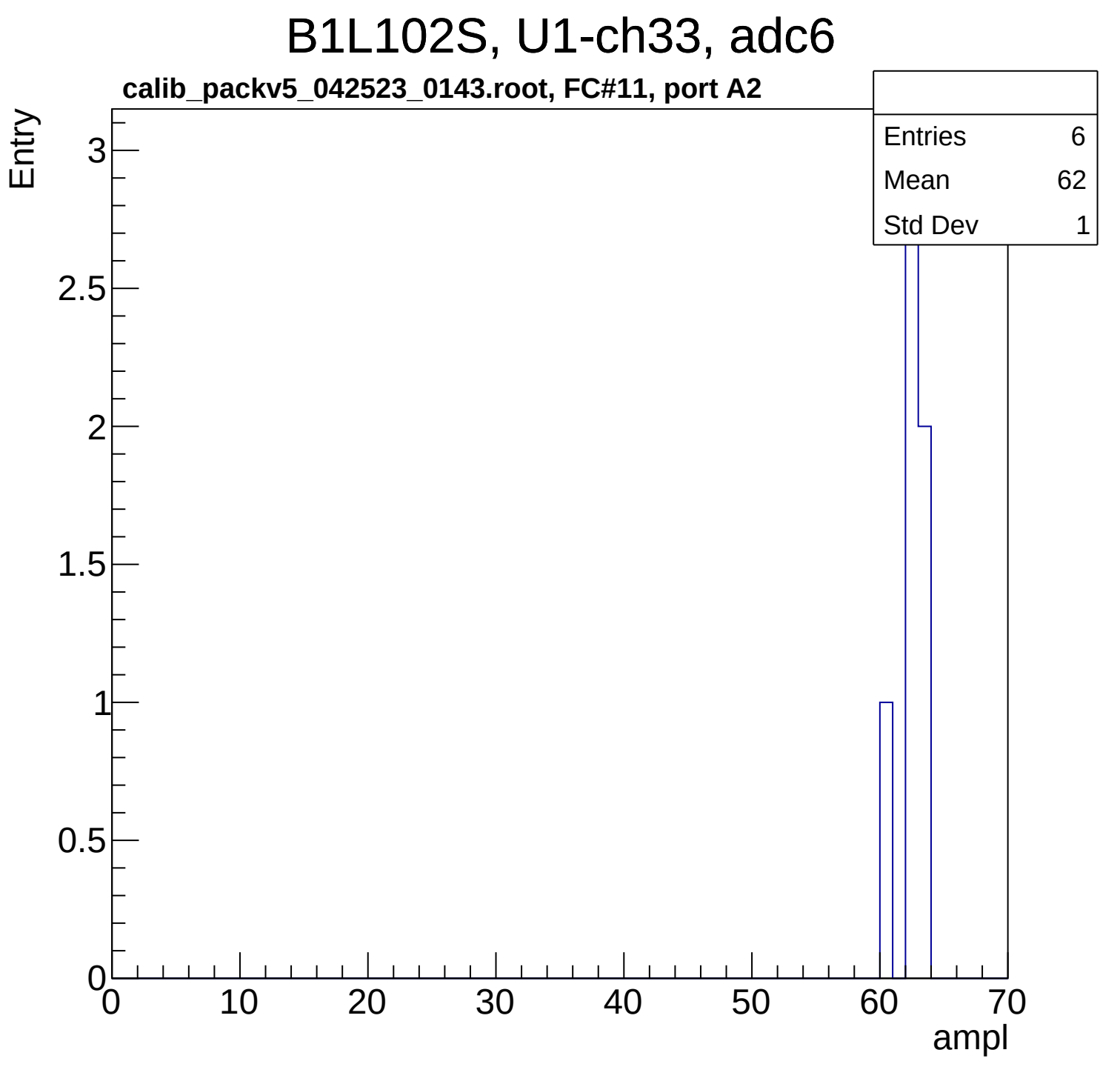
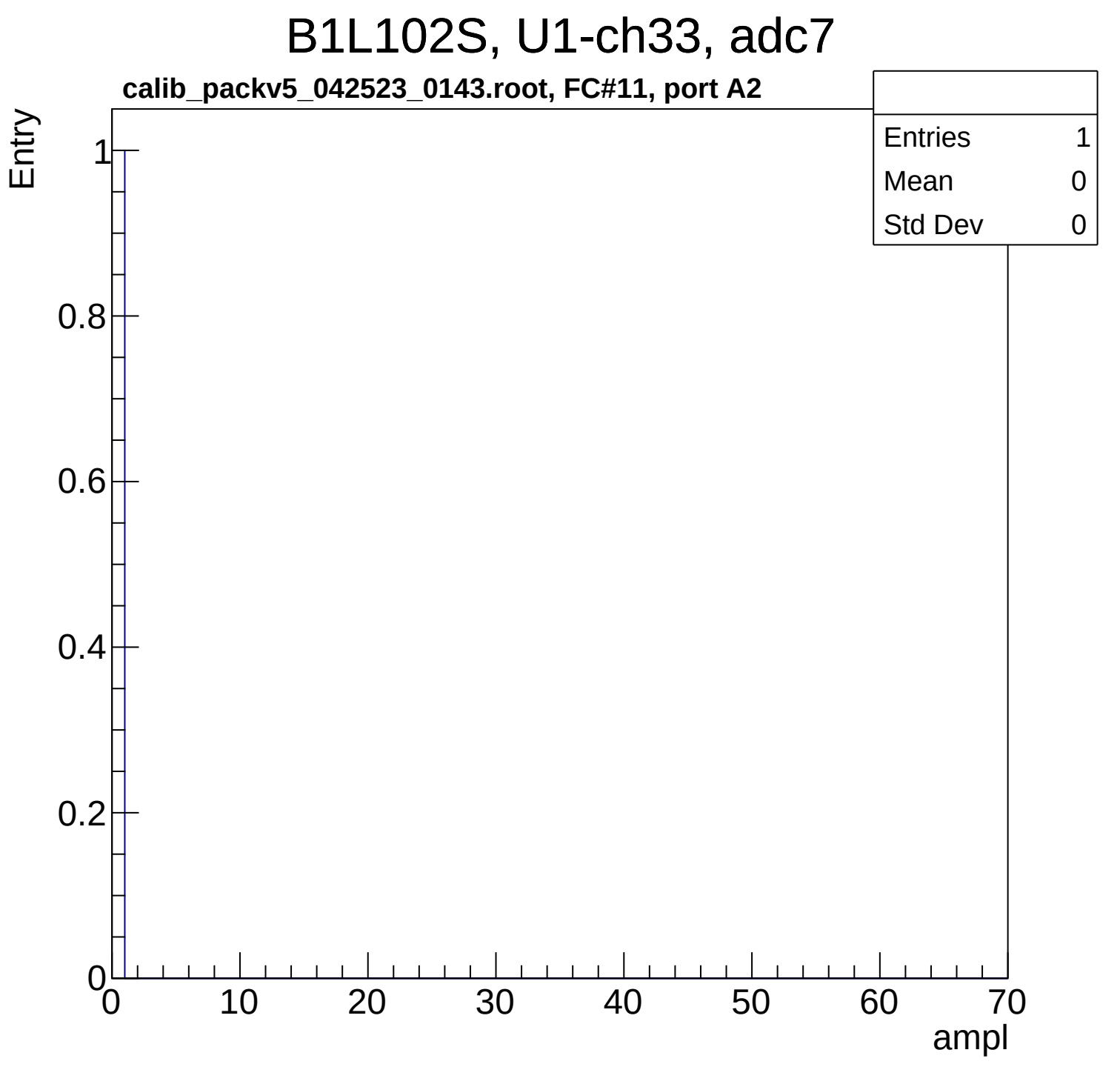
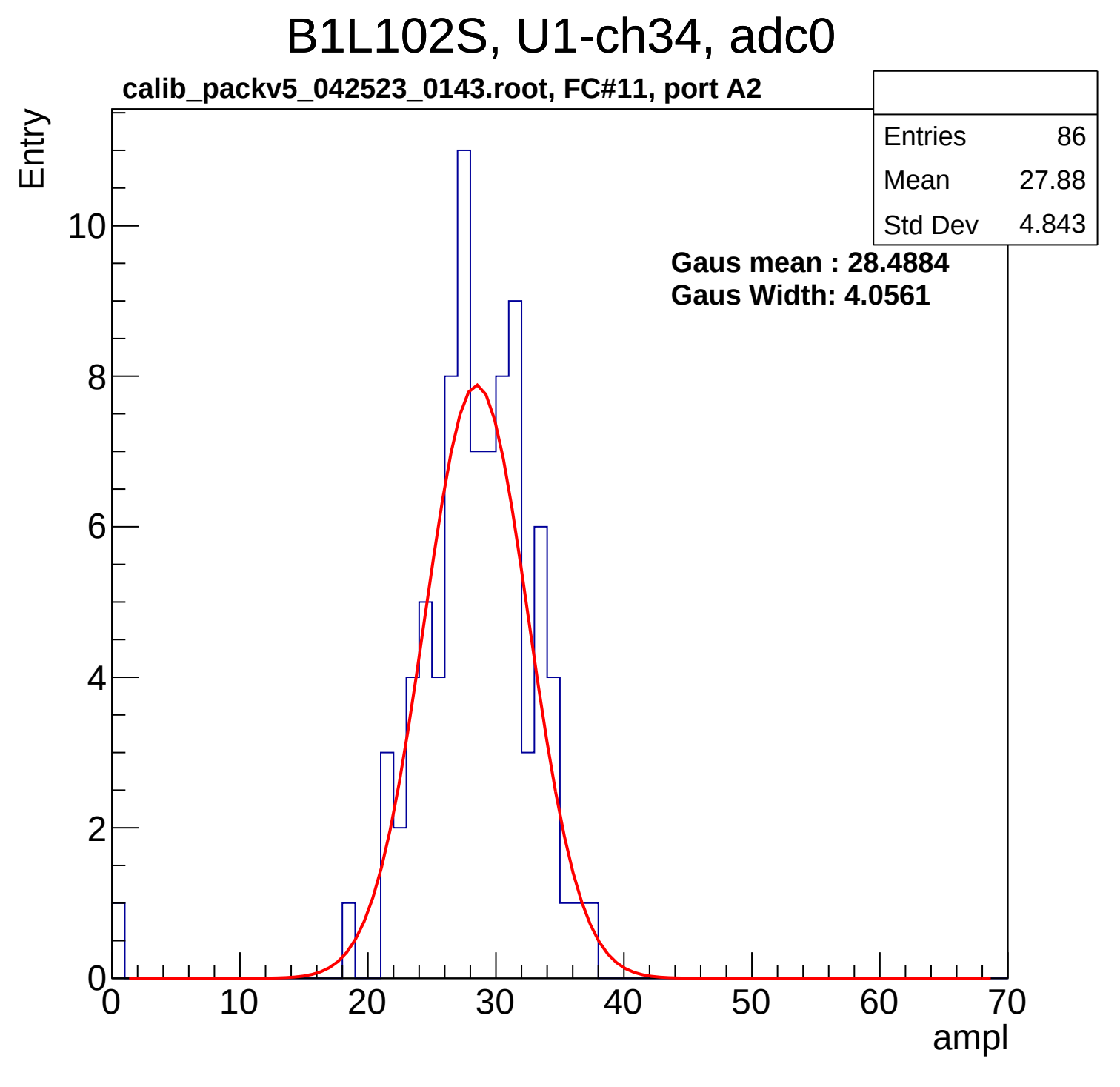
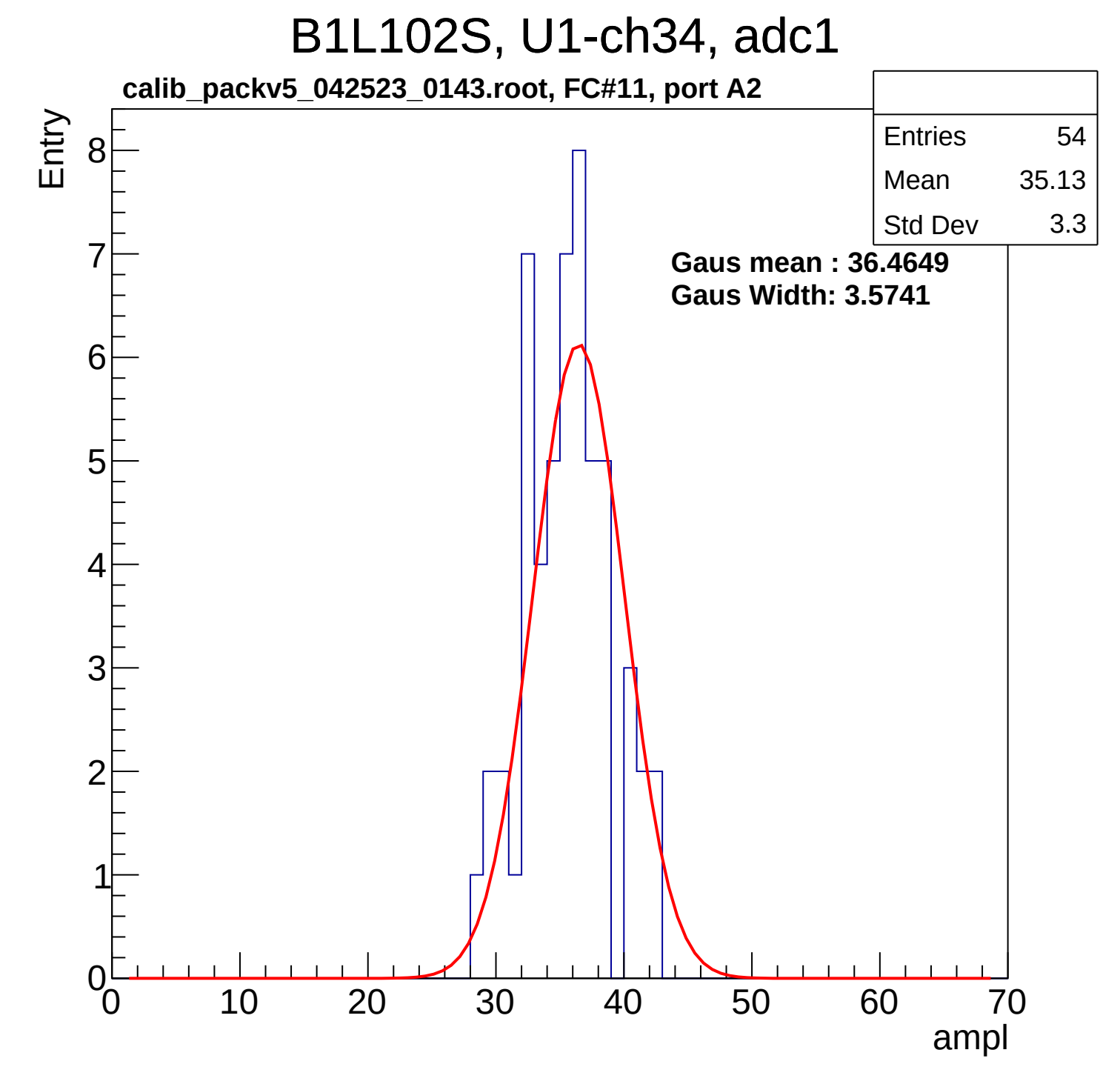
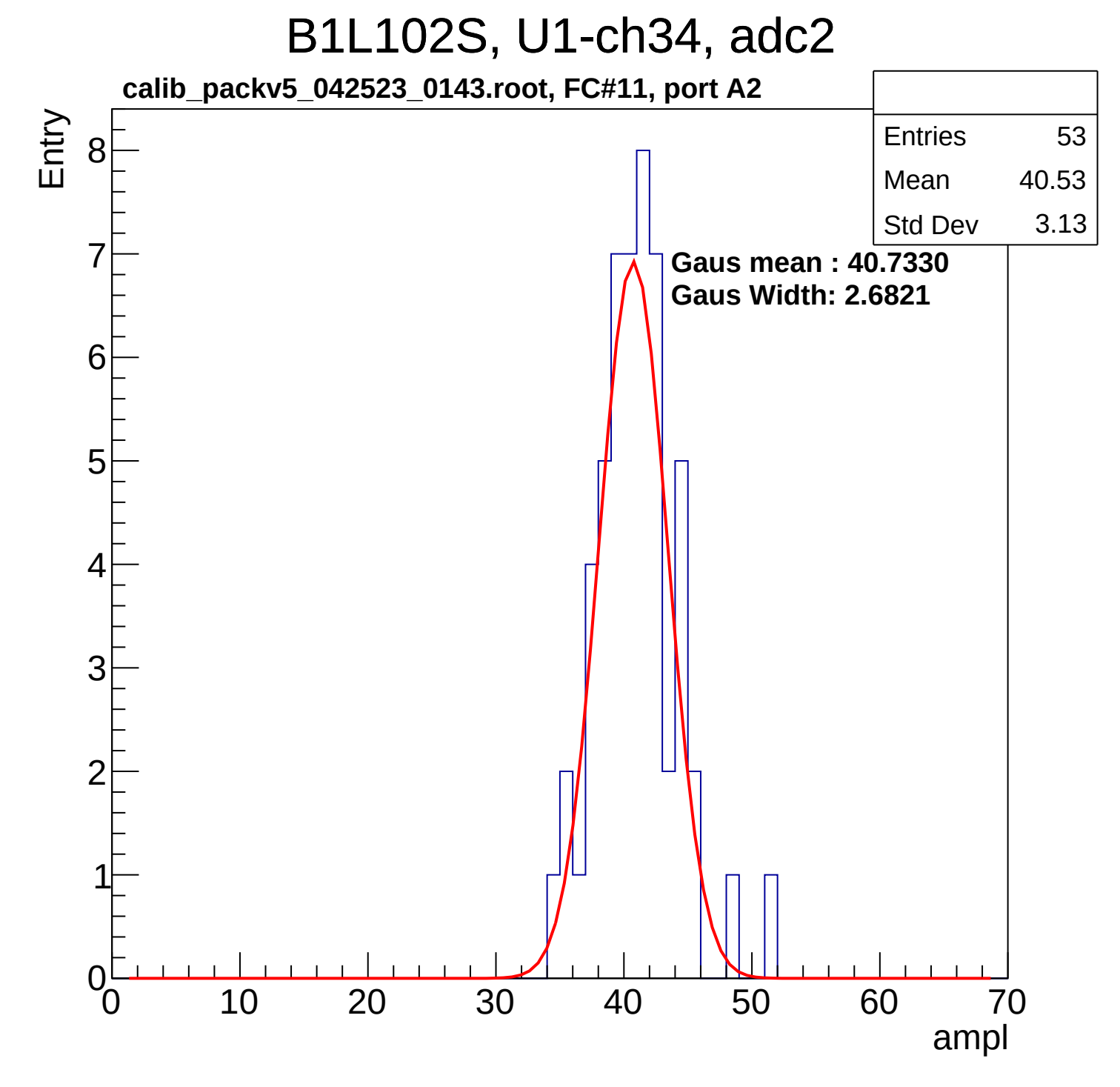
0
10
20
30
40
50
60
70

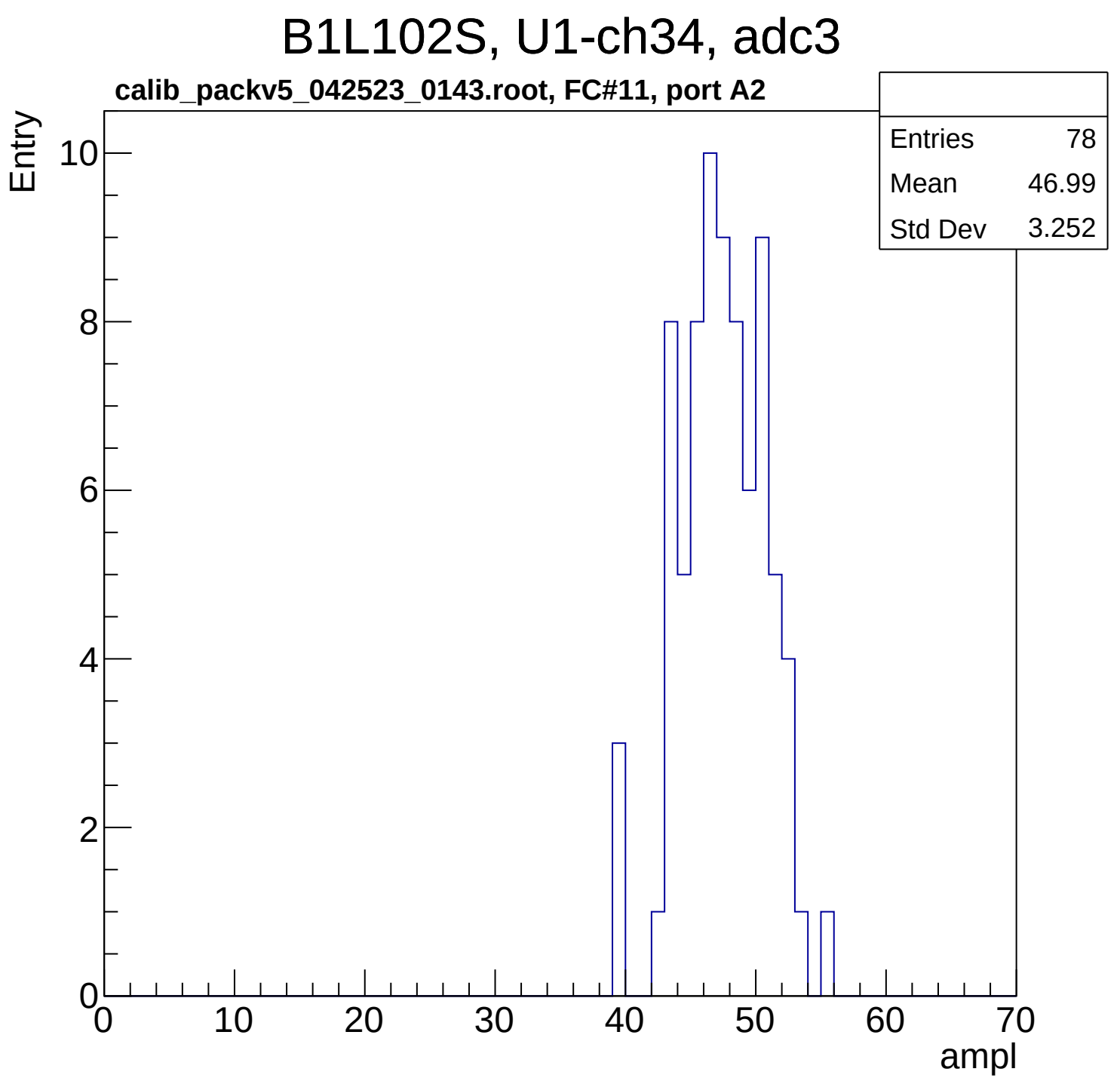

B1L102S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

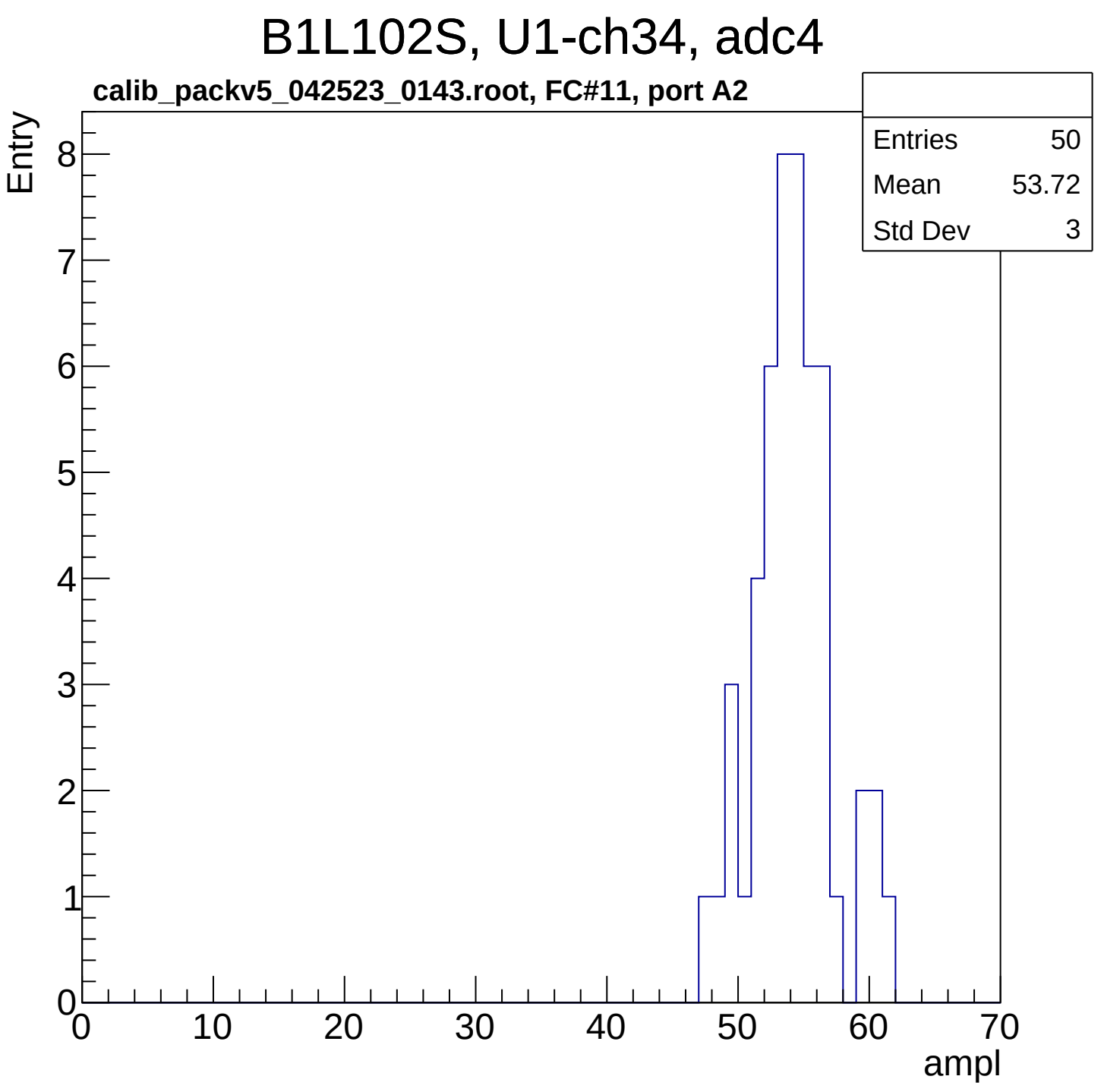

B1L102S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

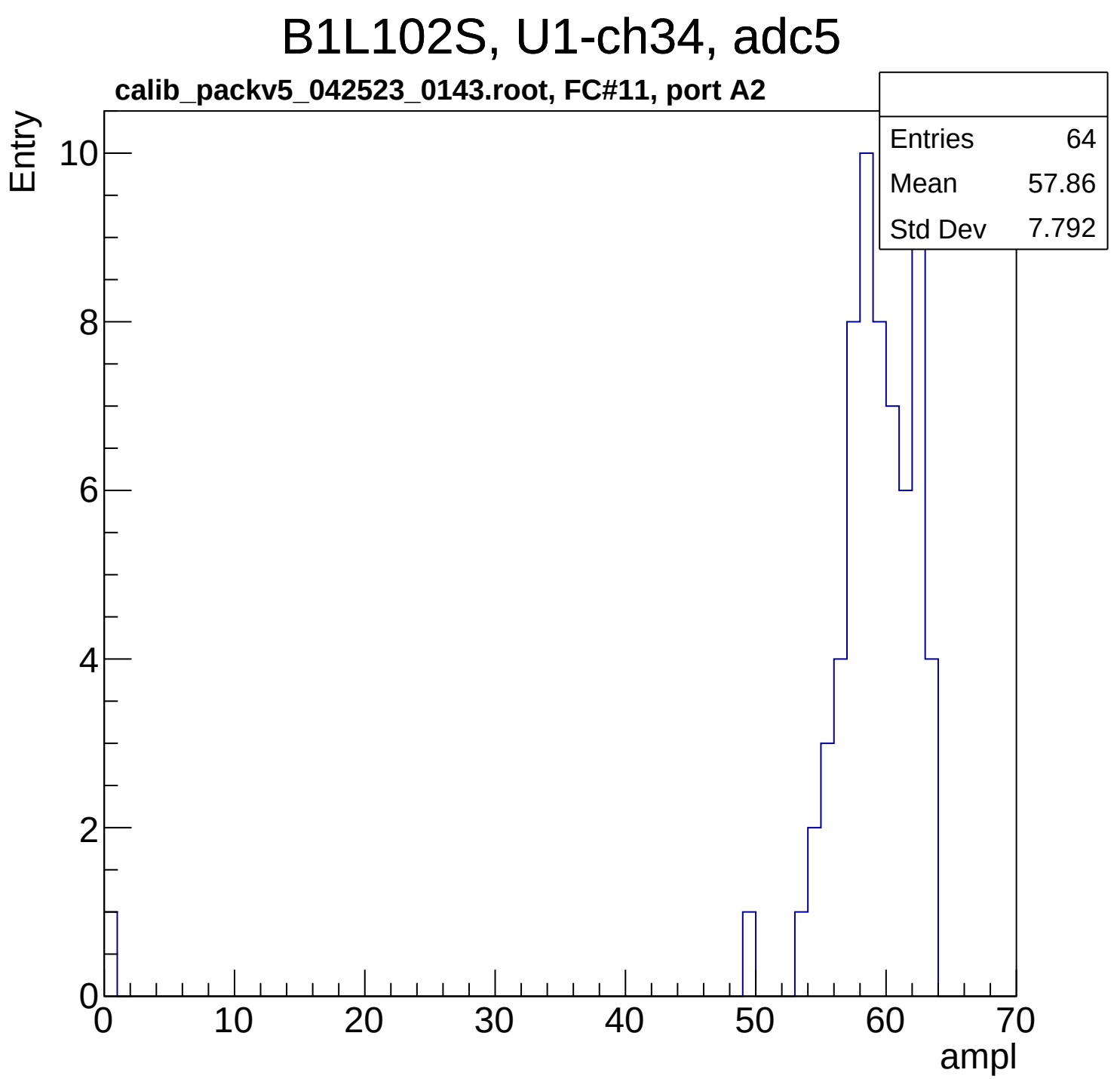

B1L102S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 27.88
Std Dev 4.843
Gaus mean : 28.4884
Gaus Width: 4.0561
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

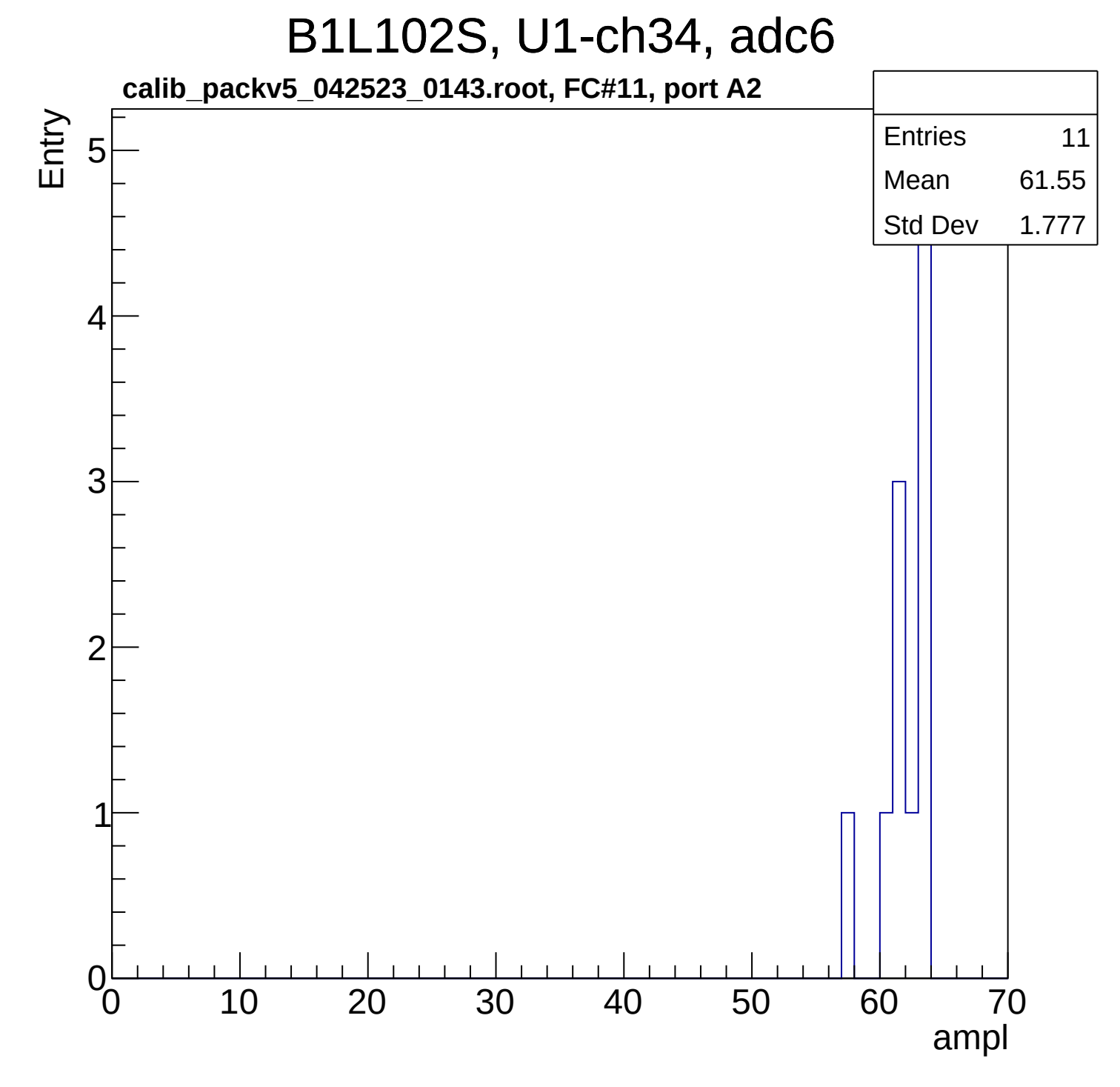

B1L102S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 35.13
Std Dev 3.3
Gaus mean : 36.4649
Gaus Width: 3.5741
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

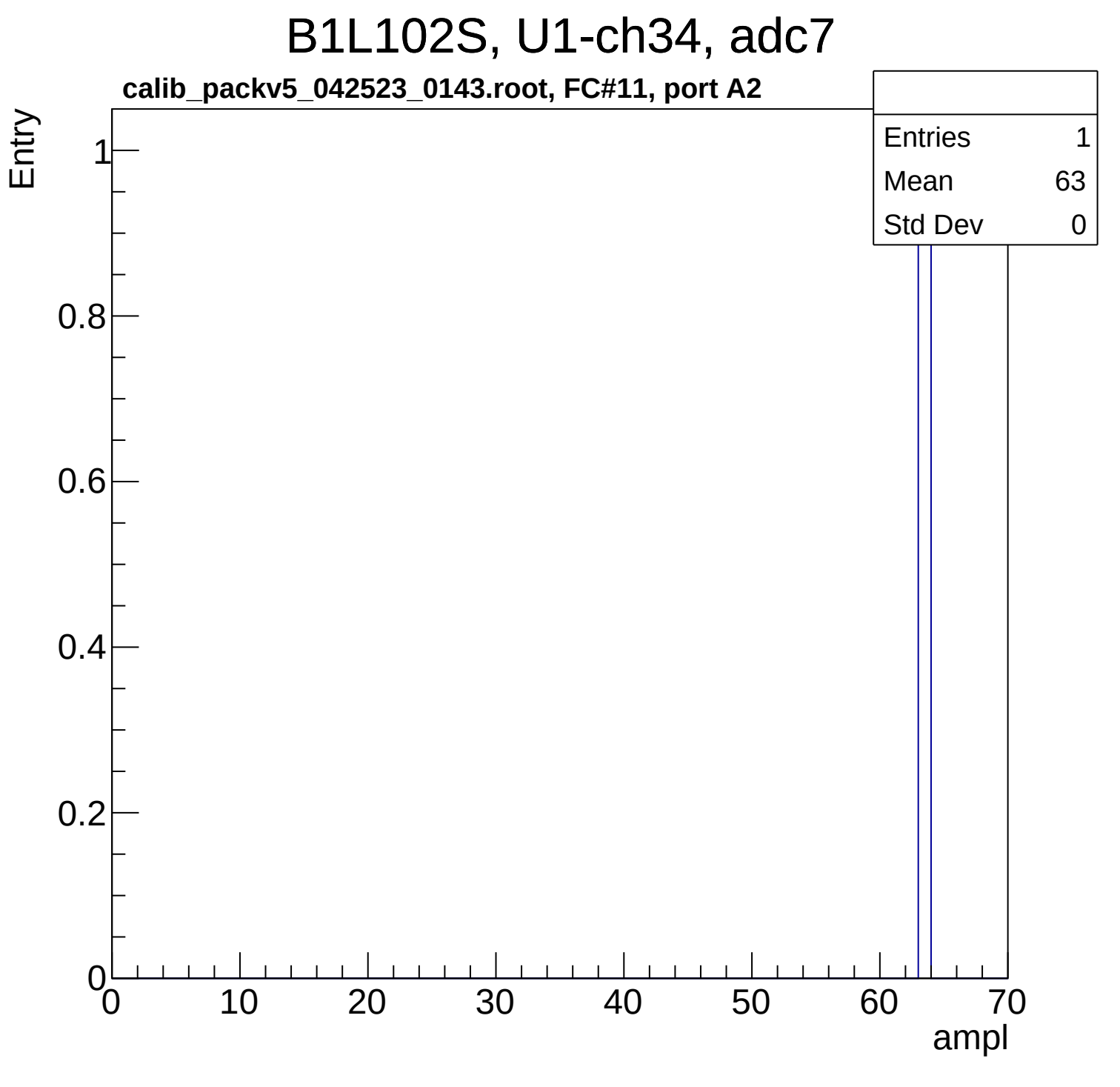

B1L102S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 40.53
Std Dev 3.13
Gaus mean : 40.7330
Gaus Width: 2.6821
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

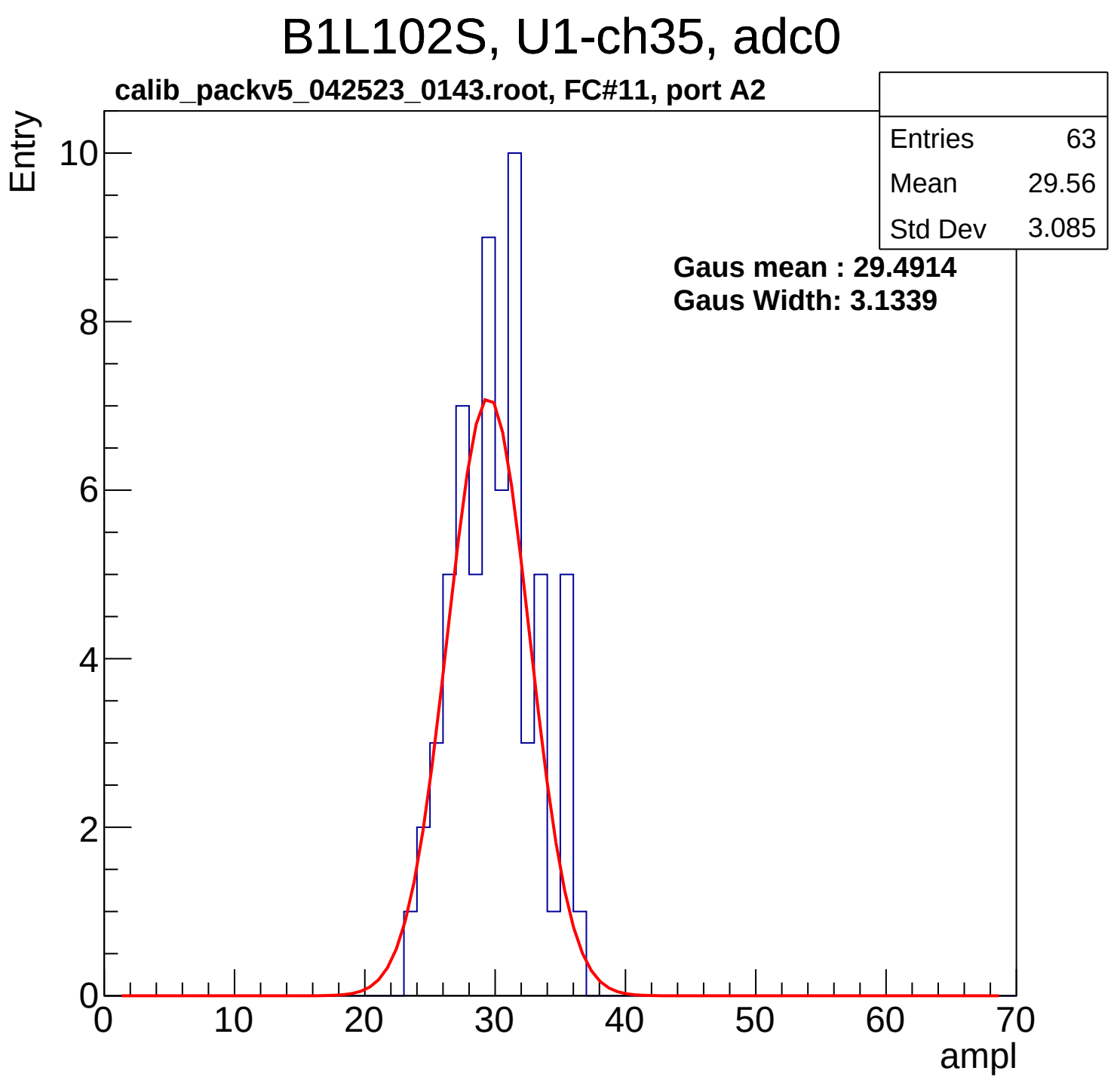

B1L102S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 46.99
Std Dev 3.252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

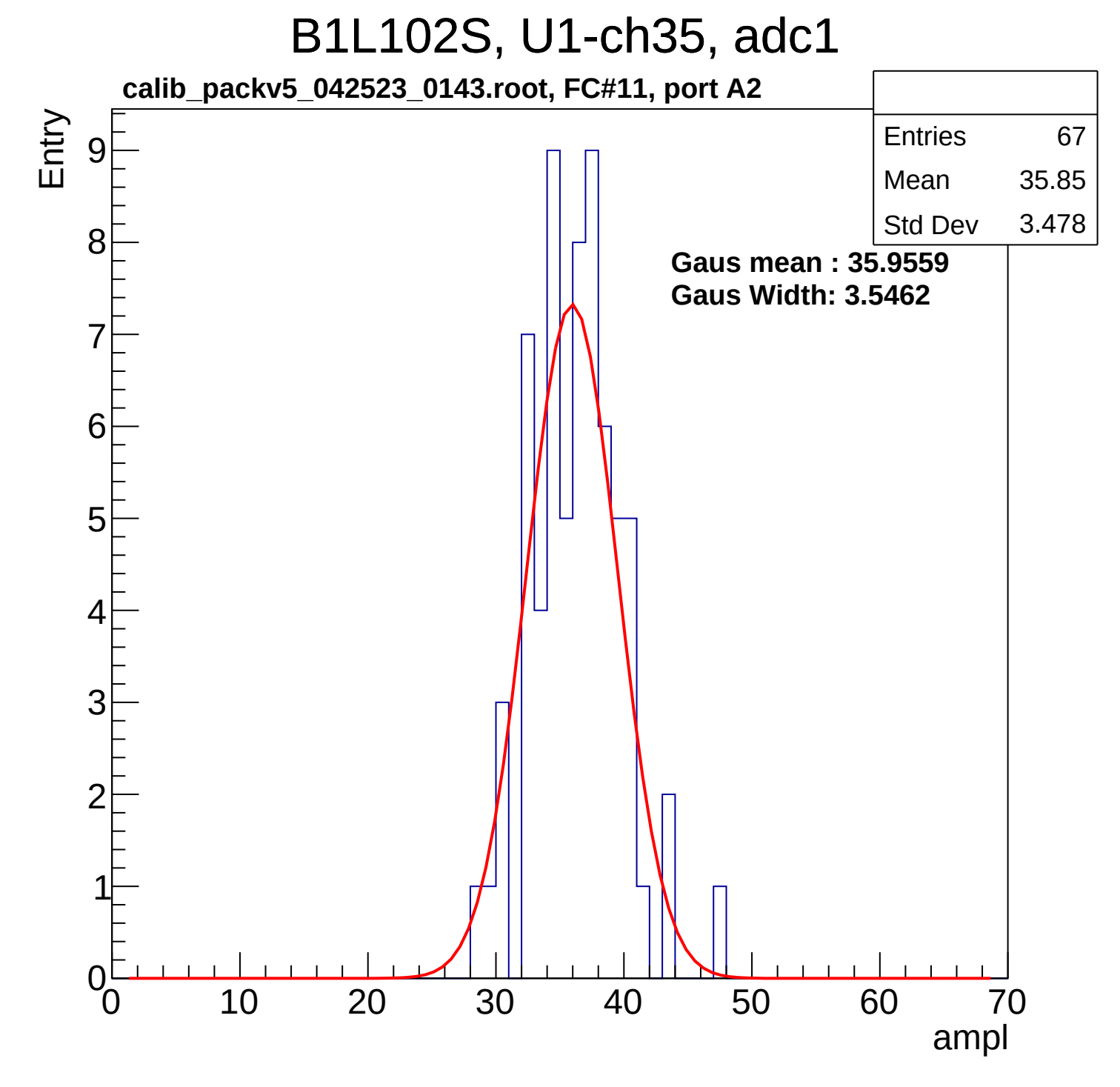

B1L102S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 53.72
Std Dev 3
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

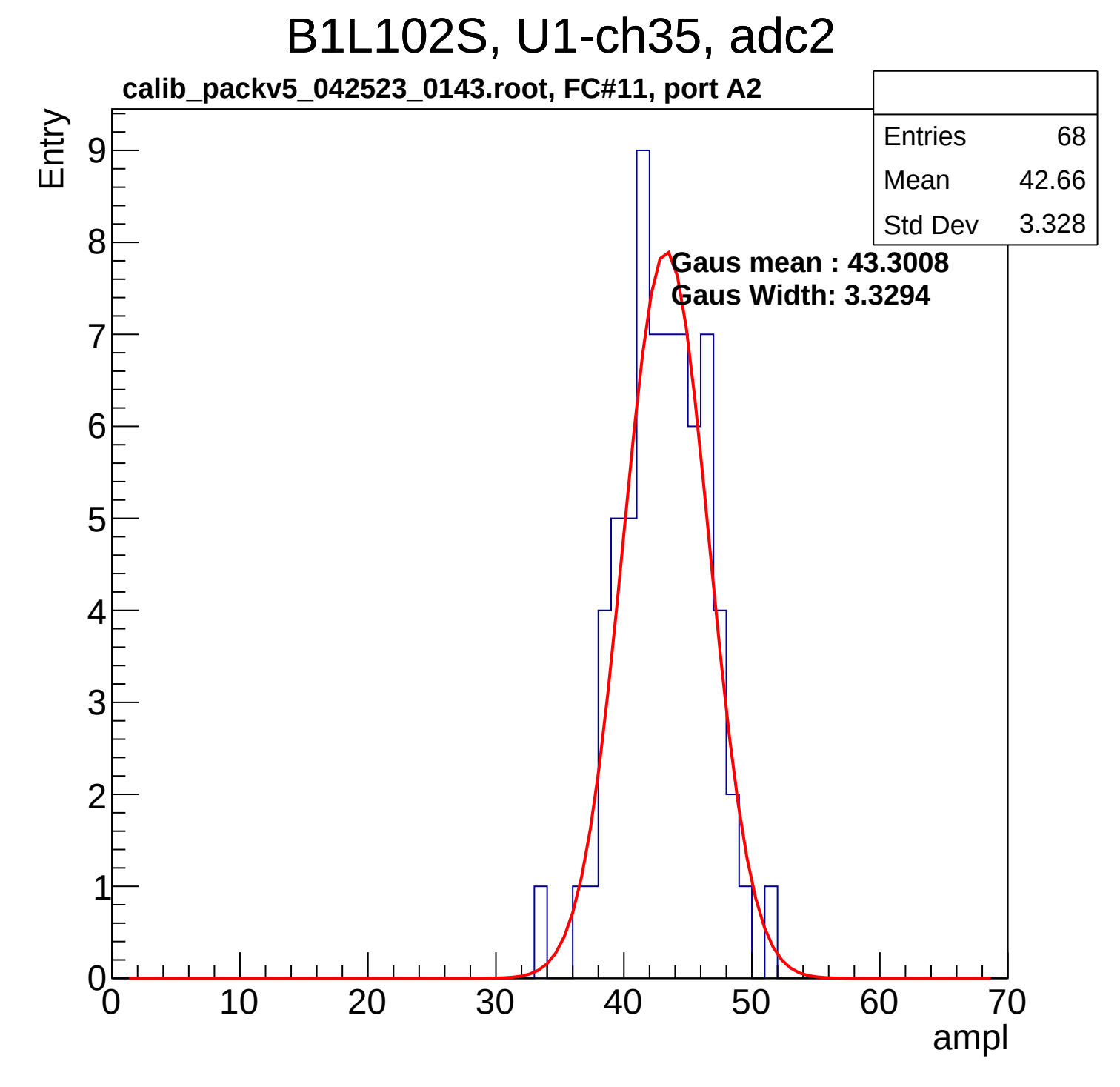

B1L102S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 57.86
Std Dev 7.792
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

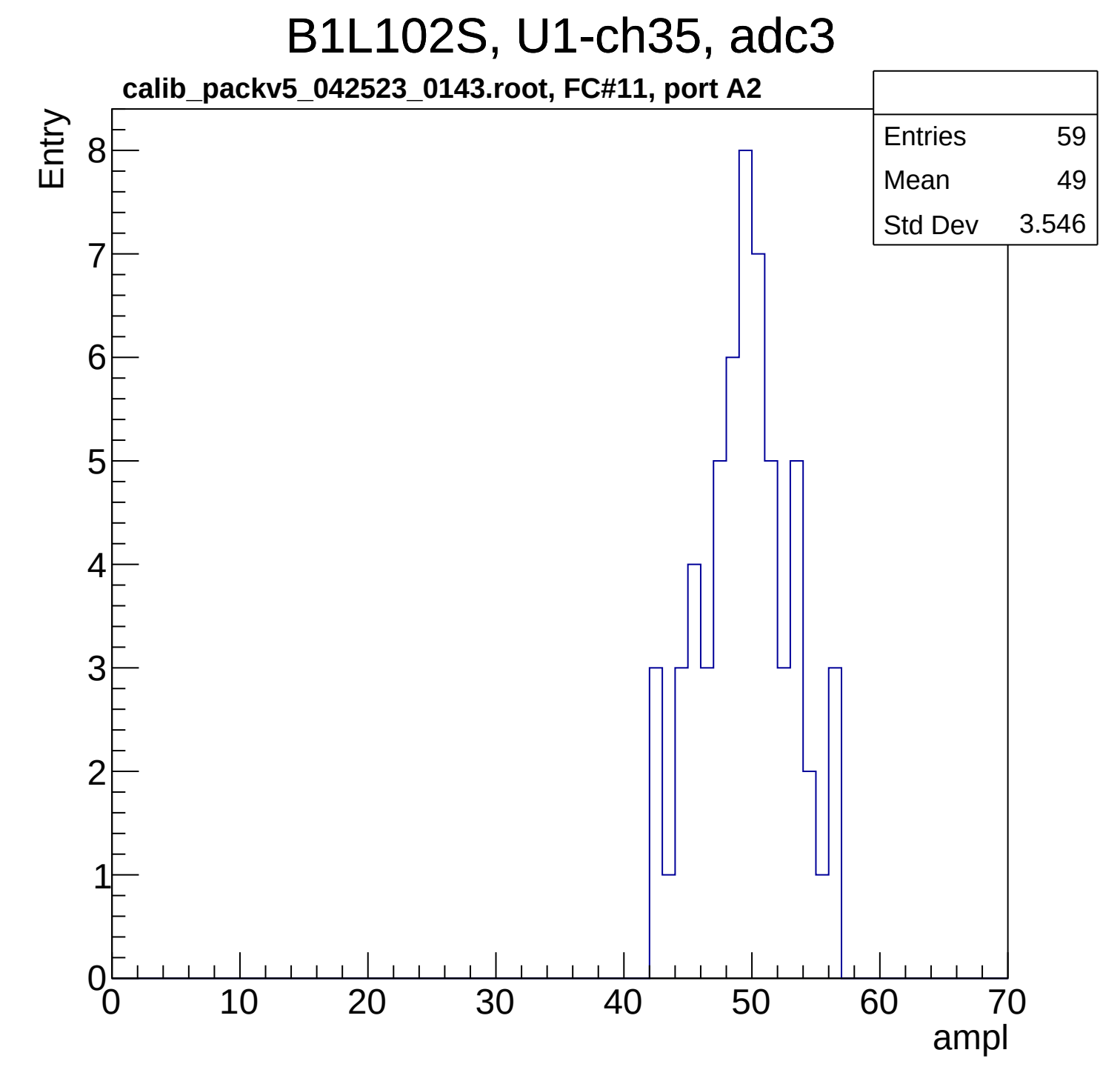

B1L102S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.55
Std Dev 1.777
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

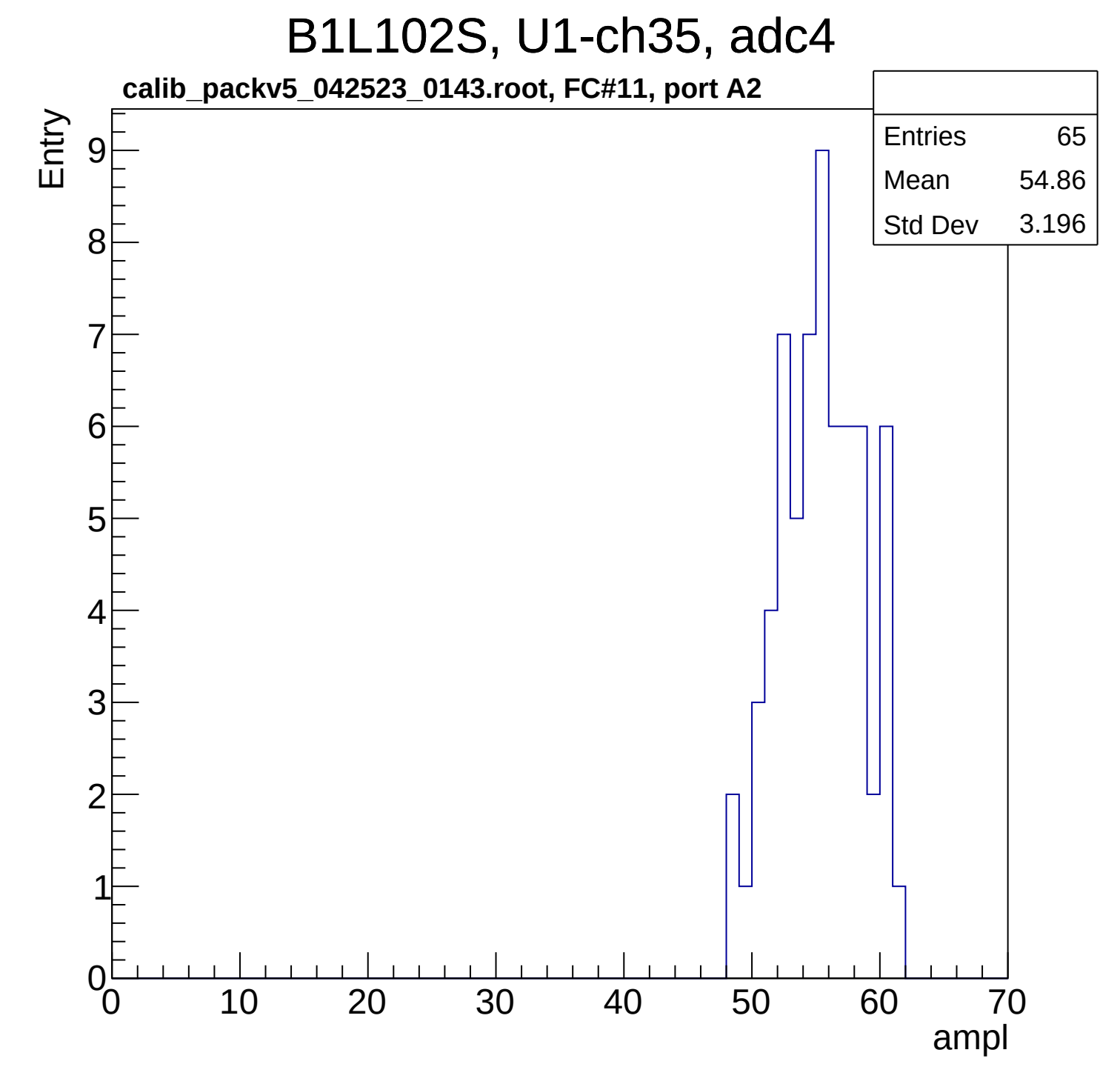

B1L102S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 29.56
Std Dev 3.085
Gaus mean : 29.4914
Gaus Width: 3.1339
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.85
Std Dev 3.478
Gaus mean : 35.9559
Gaus Width: 3.5462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 42.66
Std Dev 3.328
Gaus mean : 43.3008
Gaus Width: 3.3294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 49
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 54.86
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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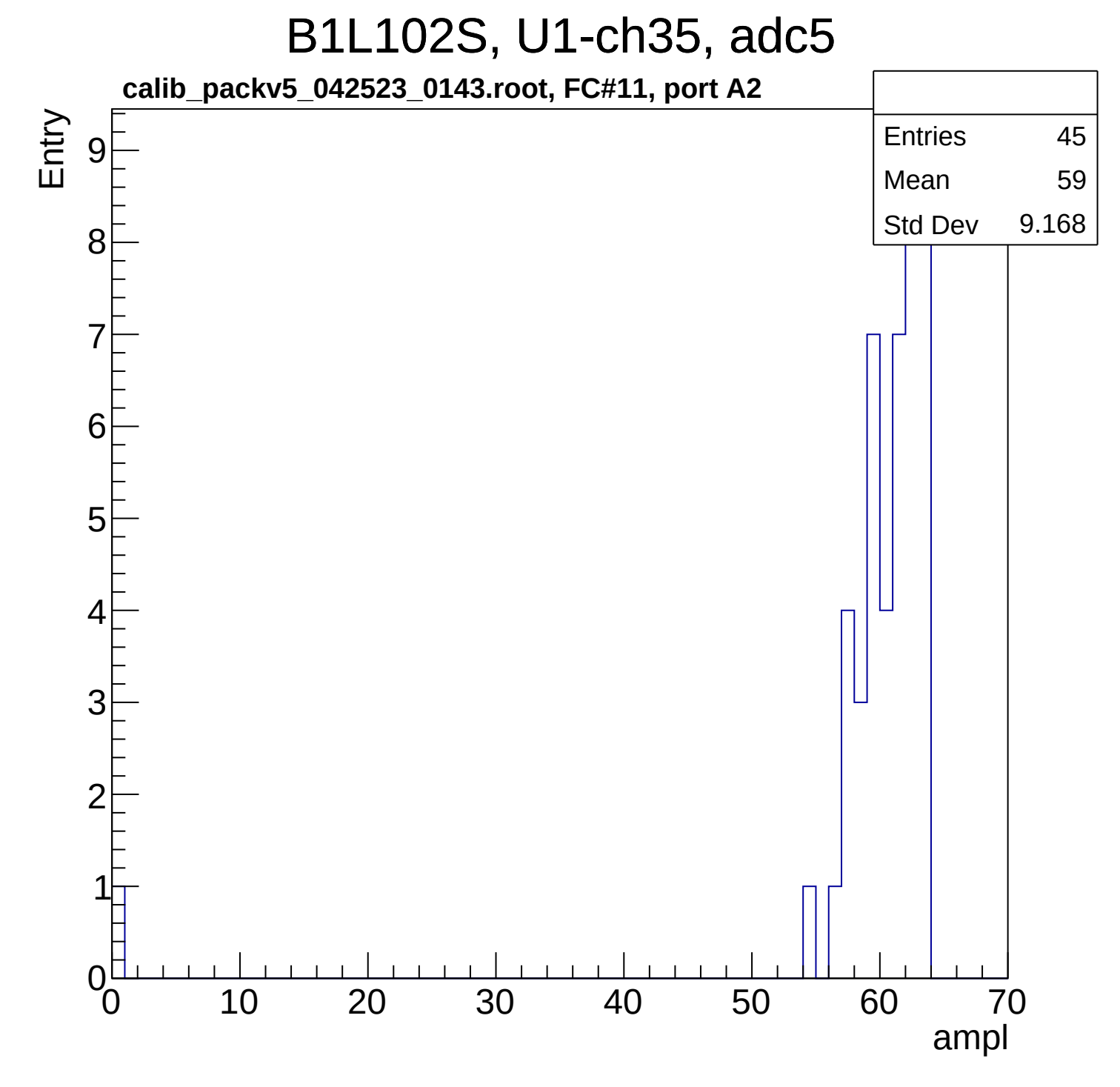
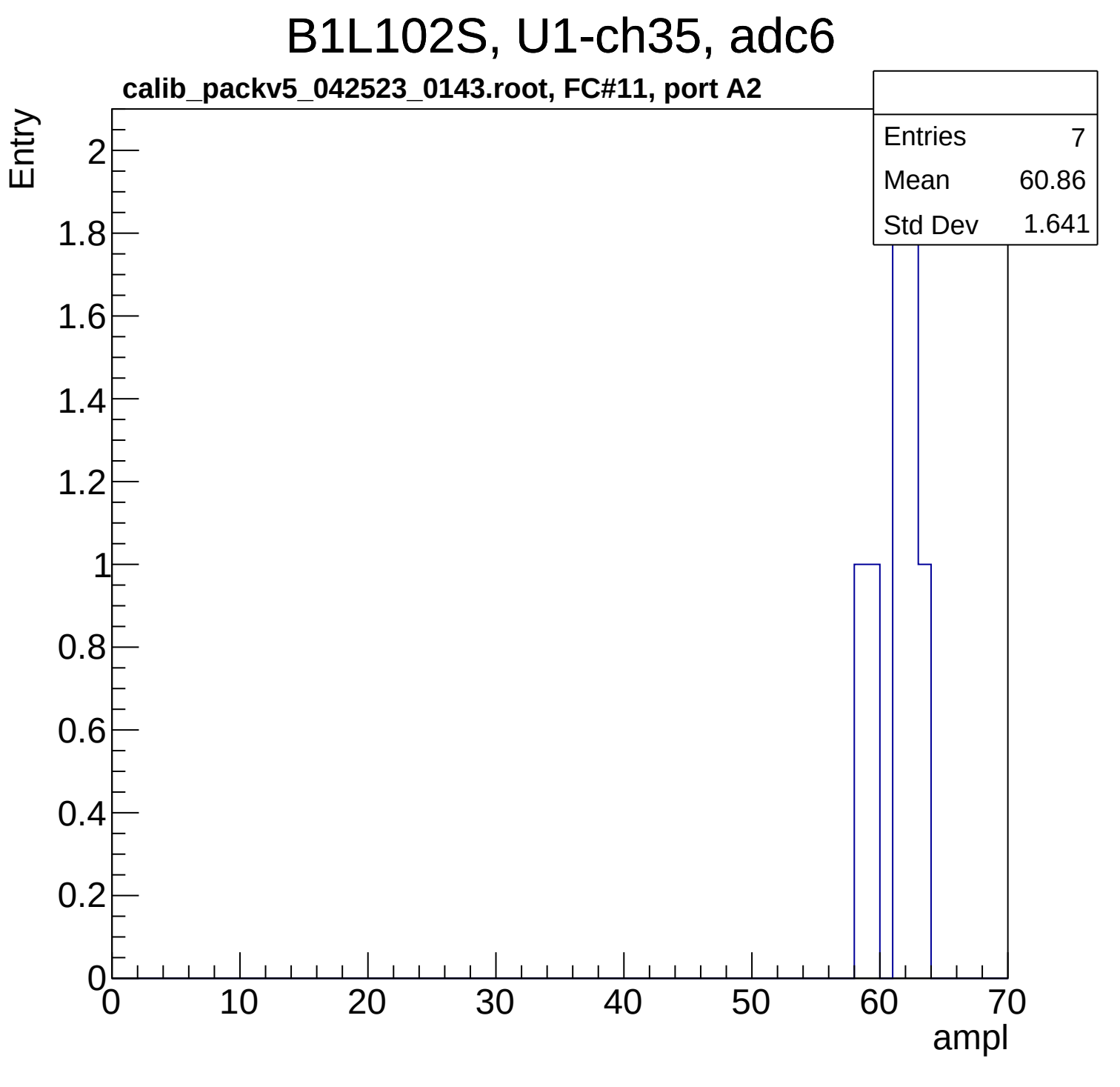
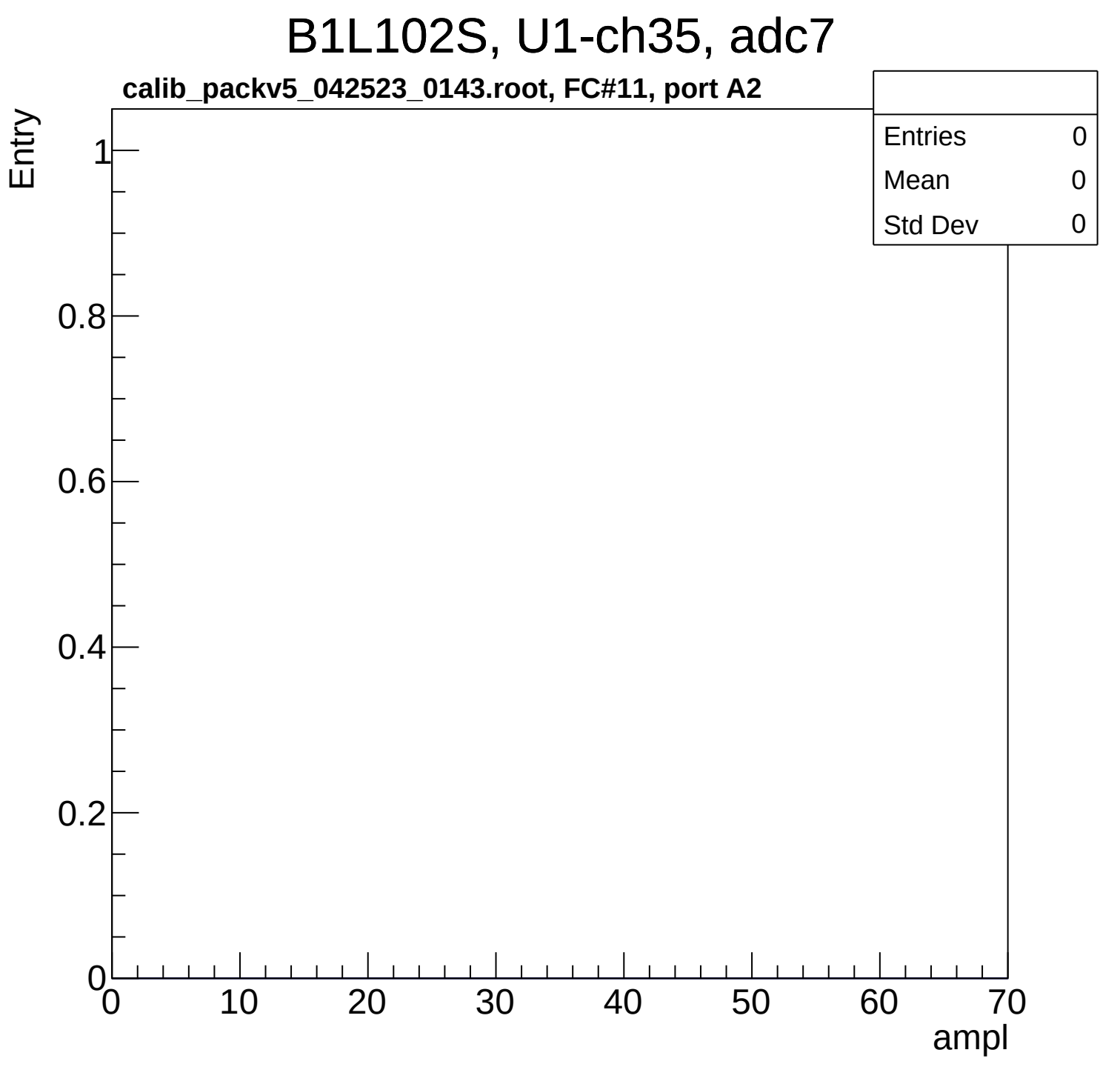

B1L102S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 59
Std Dev 9.168
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 60.86
Std Dev 1.641
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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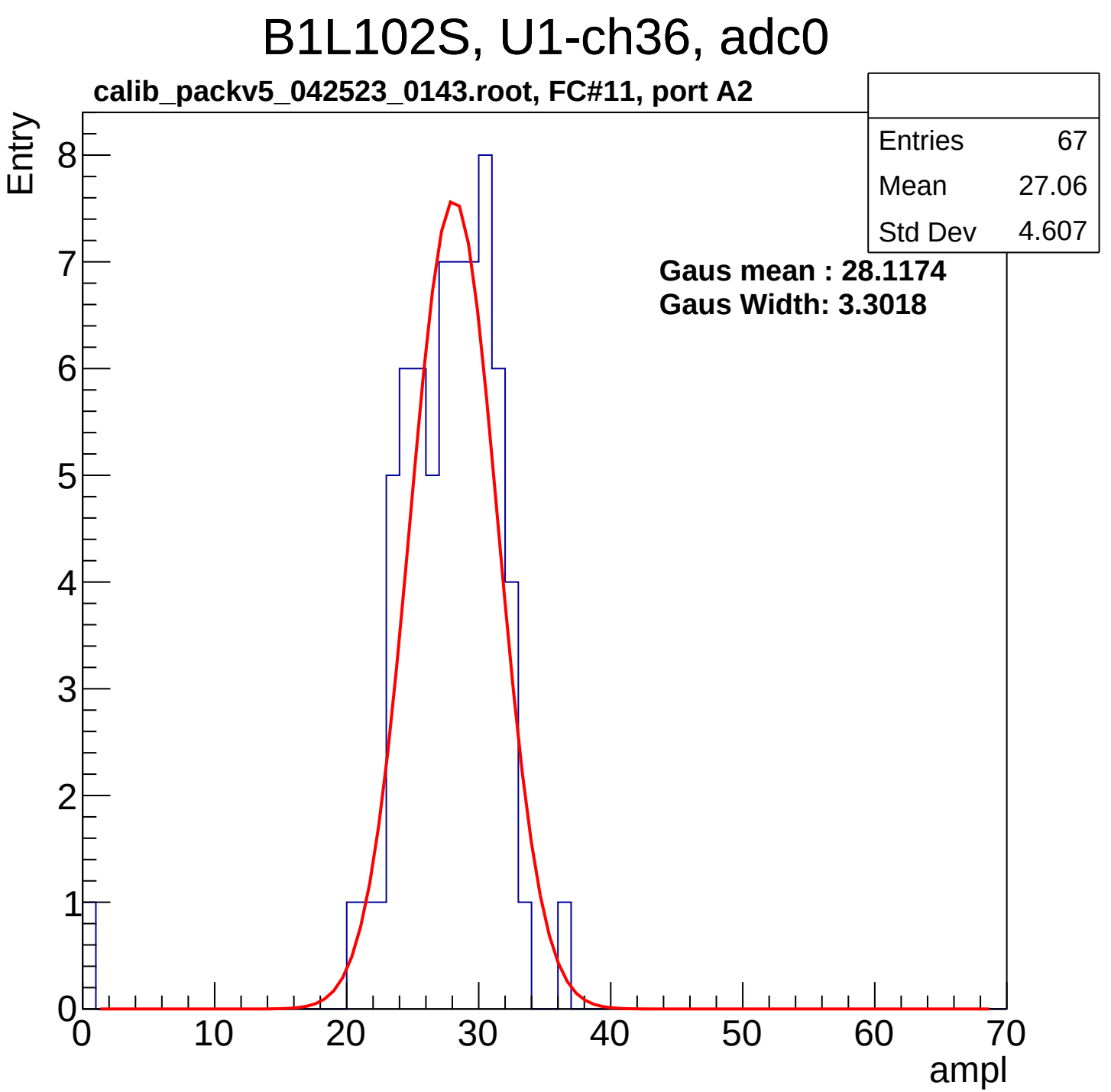
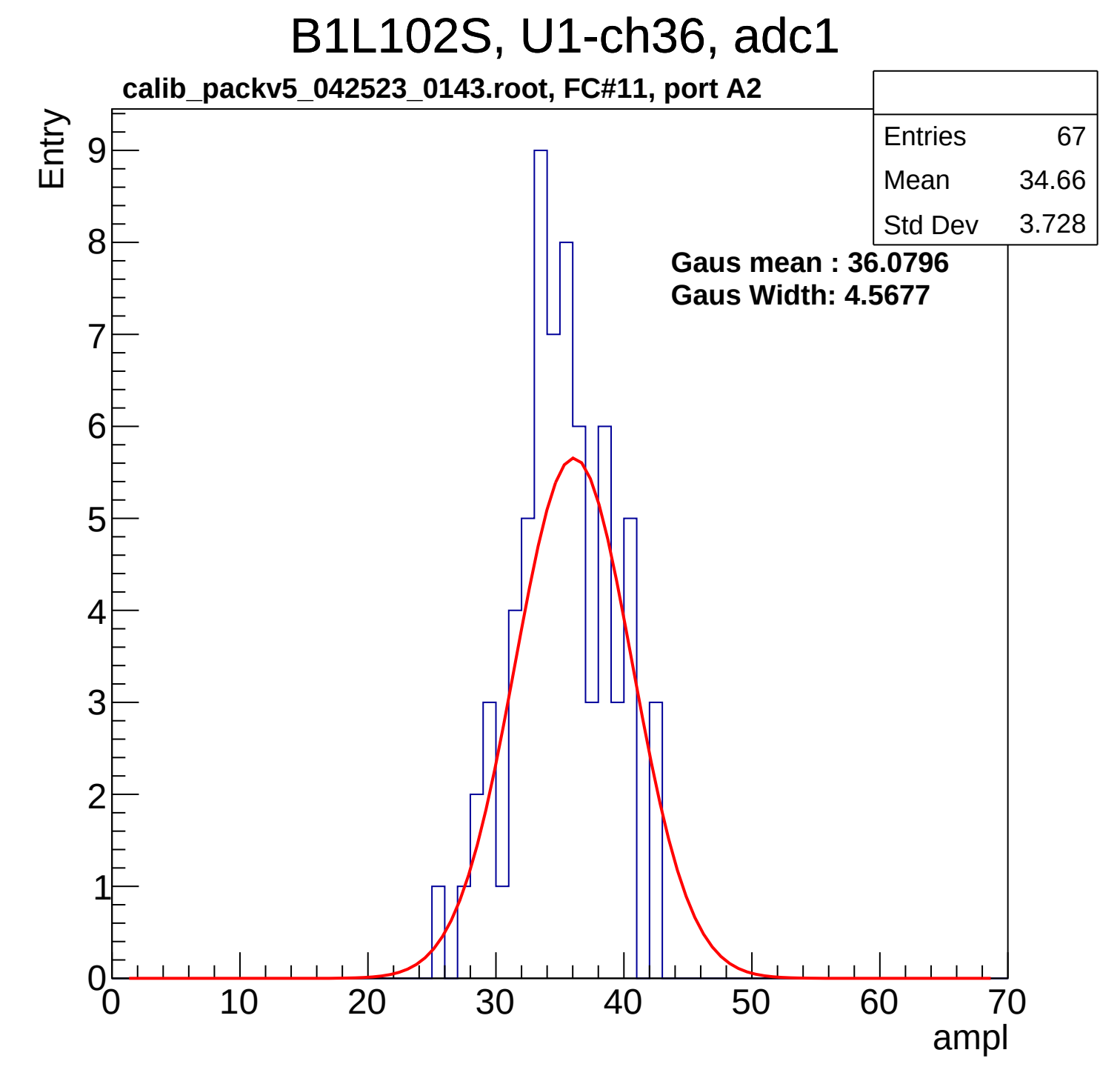
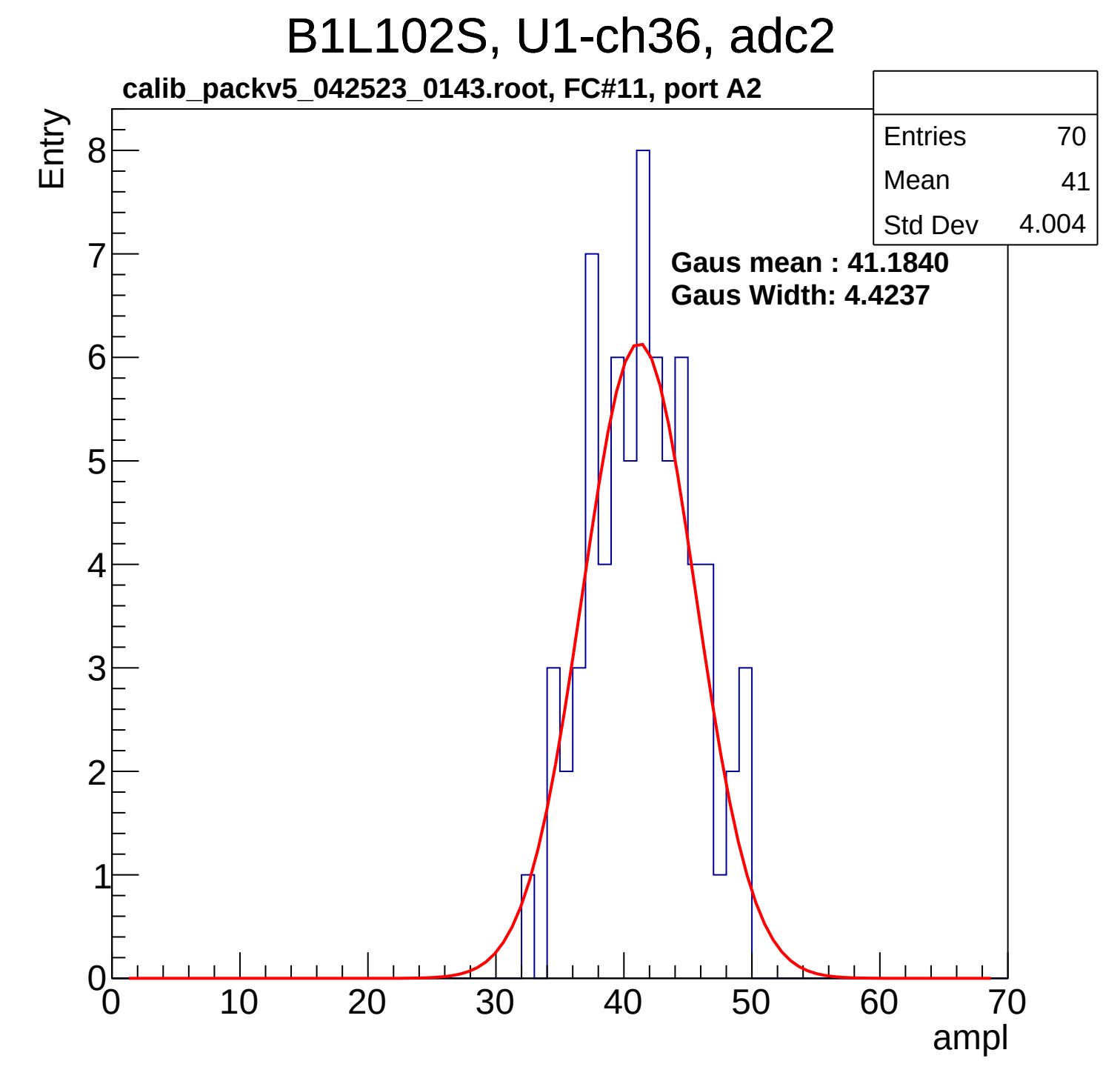
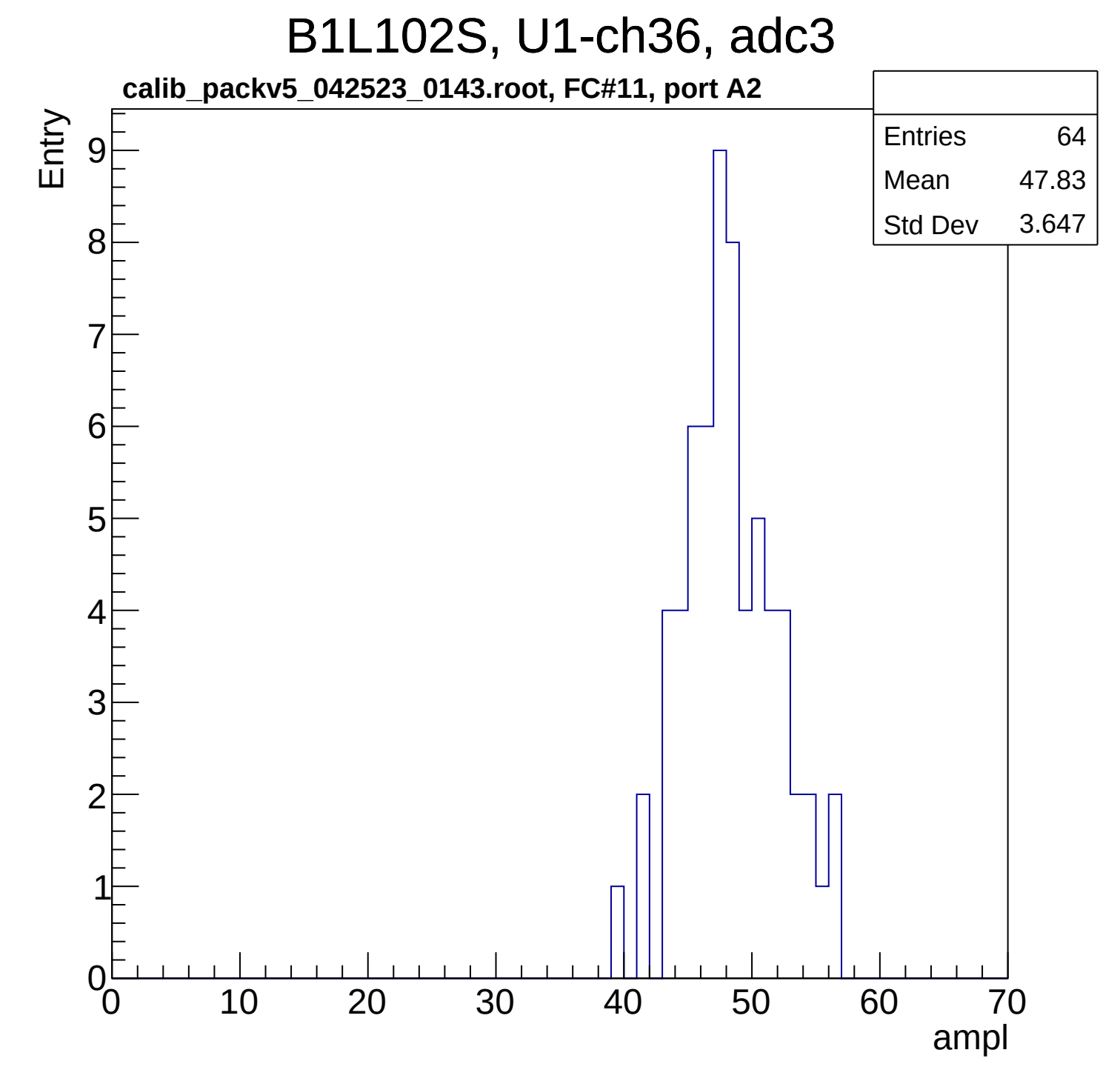
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 27.06
Std Dev 4.607
Gaus mean : 28.1174
Gaus Width: 3.3018
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 34.66
Std Dev 3.728
Gaus mean : 36.0796
Gaus Width: 4.5677
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 41
Std Dev 4.004
Gaus mean : 41.1840
Gaus Width: 4.4237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 47.83
Std Dev 3.647
Entry
ampl
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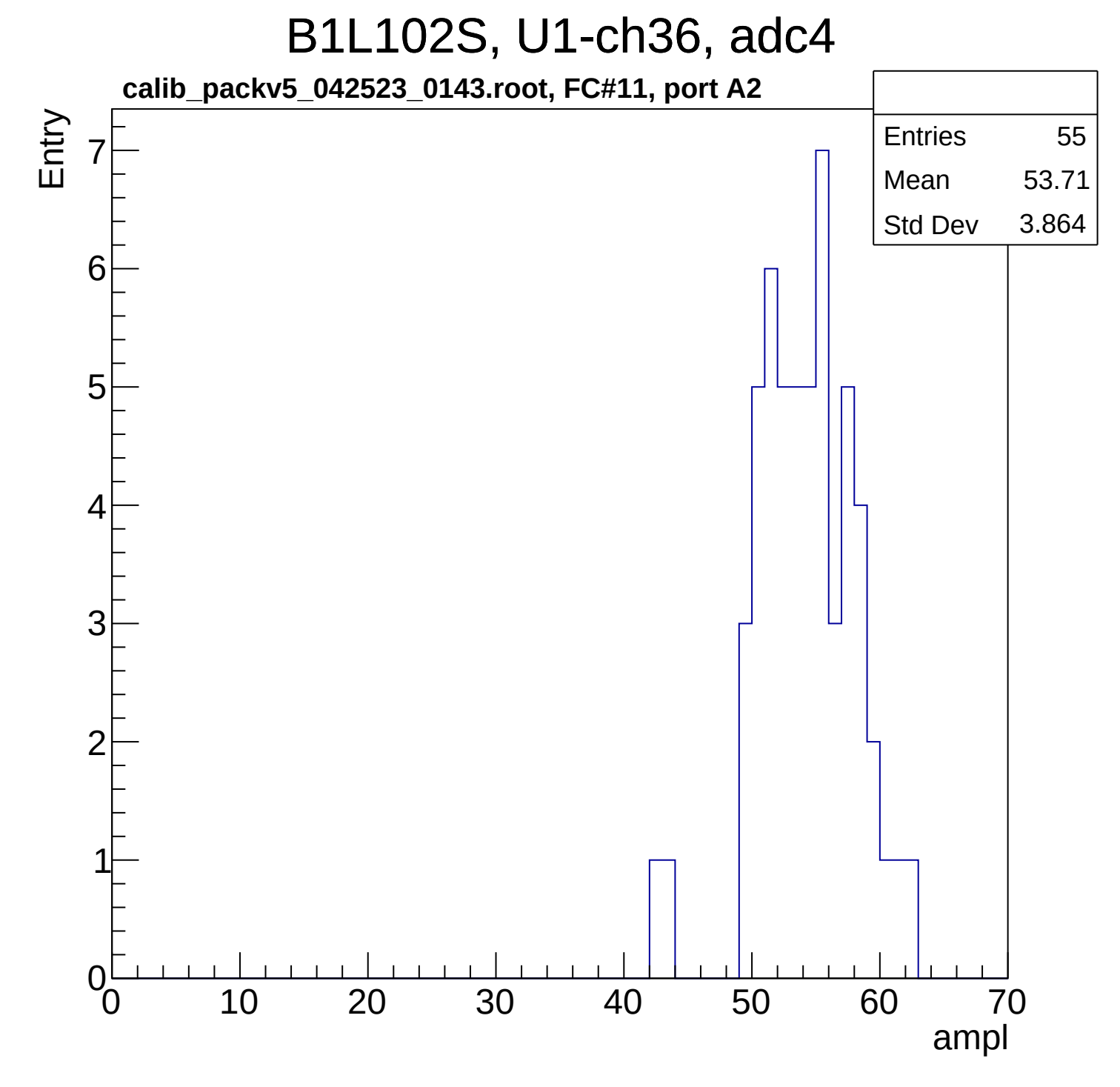
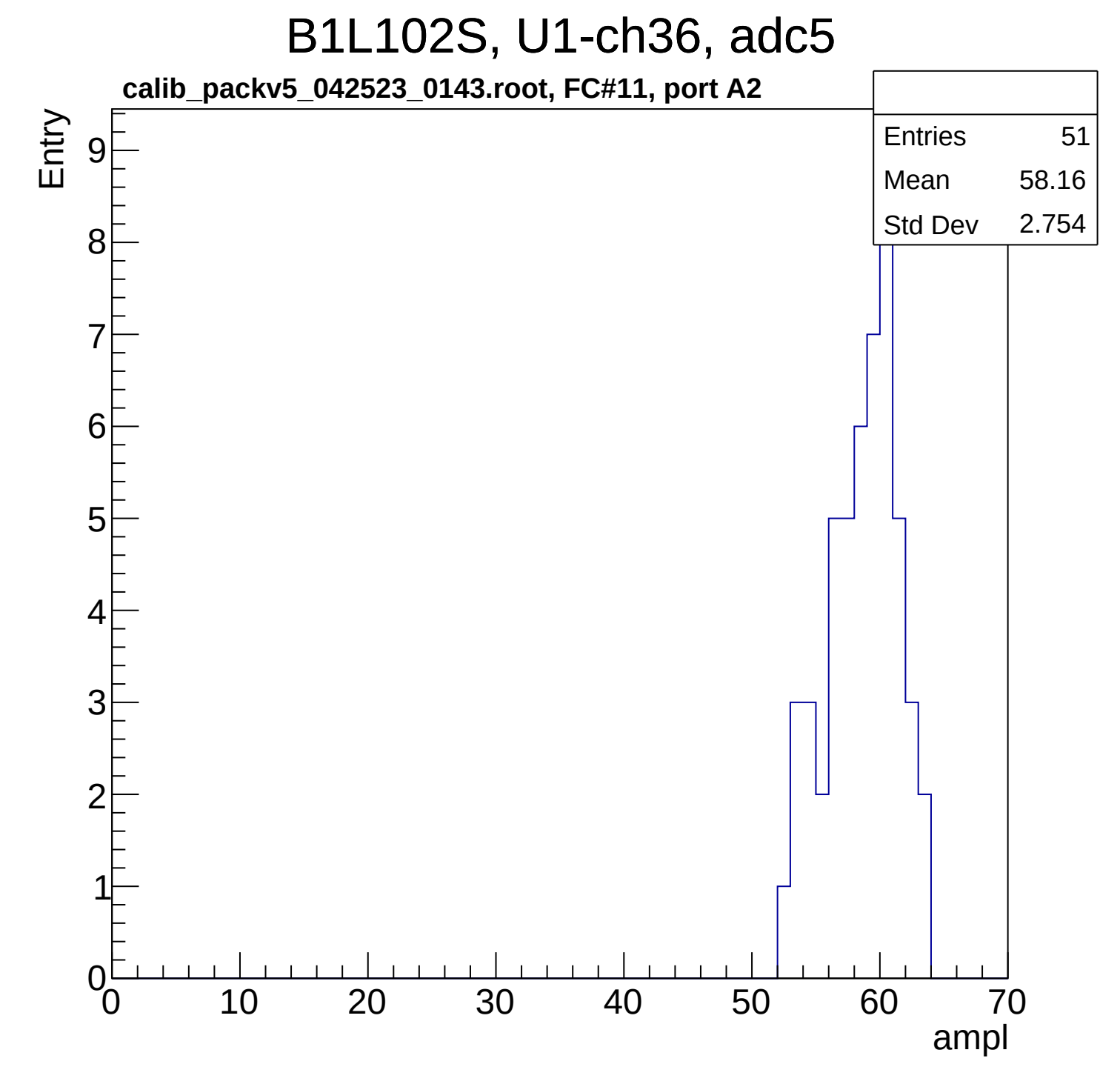
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.71
Std Dev 3.864
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58.16
Std Dev 2.754
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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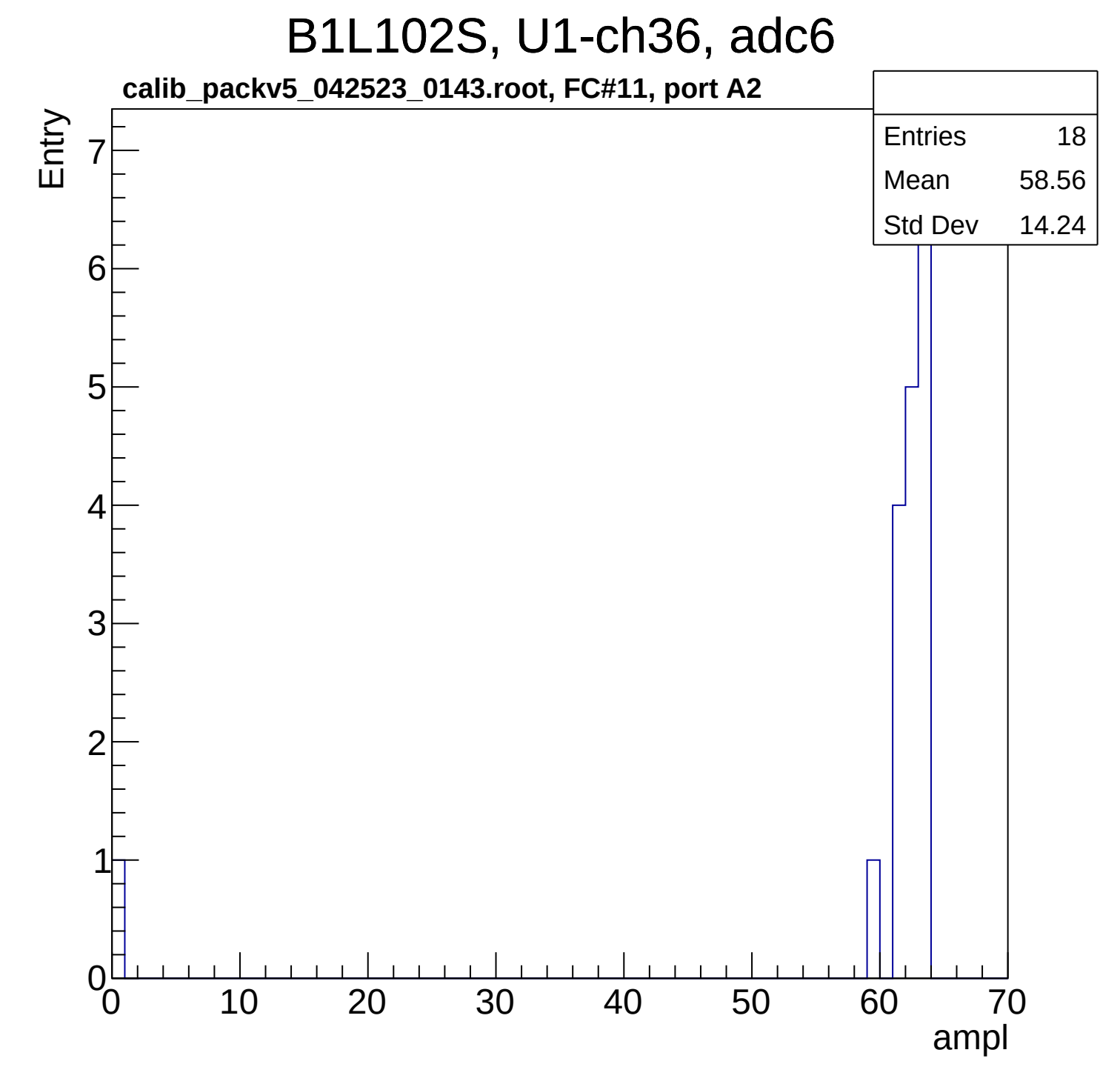
40
50
60
70


B1L102S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 58.56
Std Dev 14.24
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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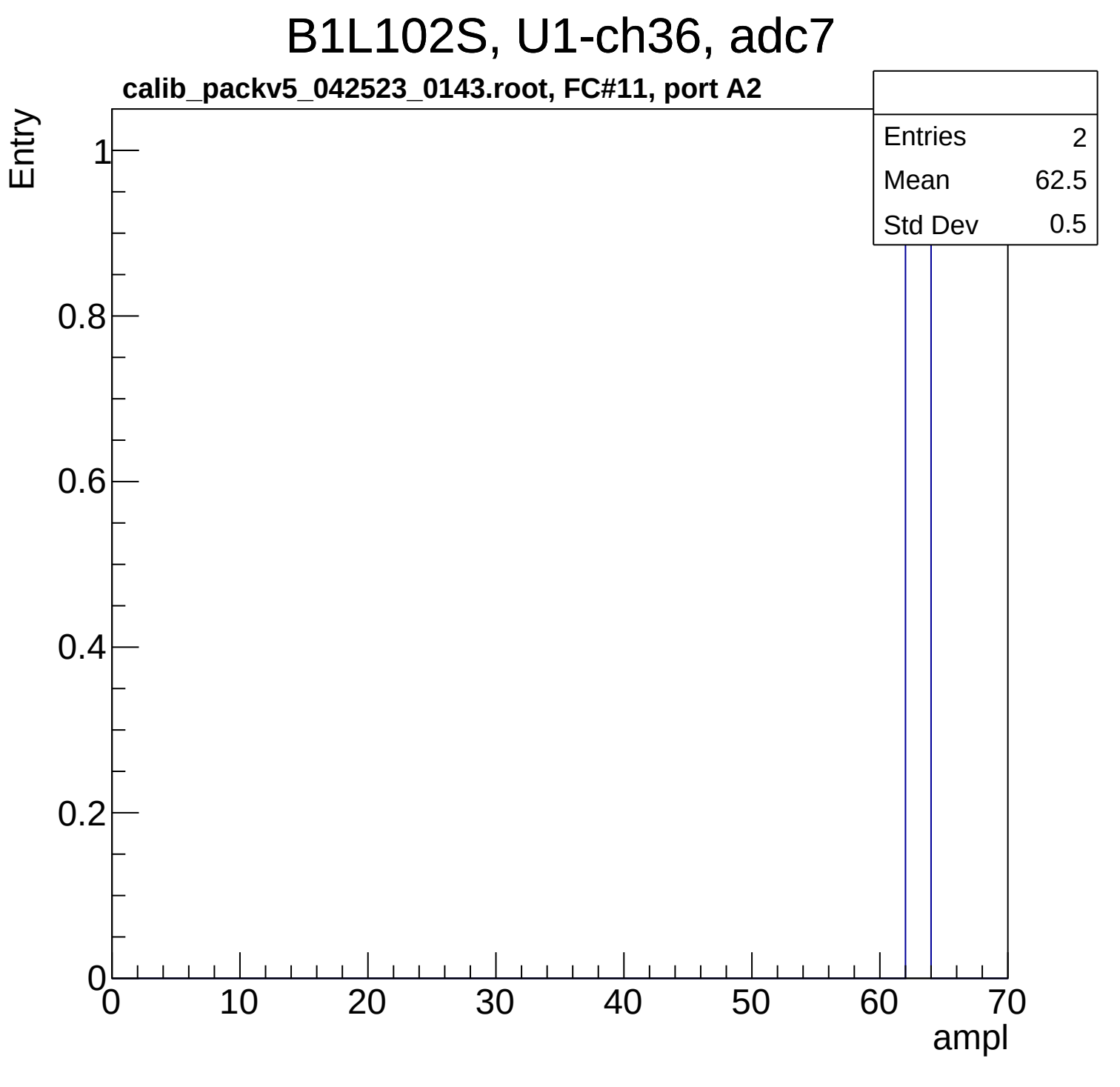
60
70


B1L102S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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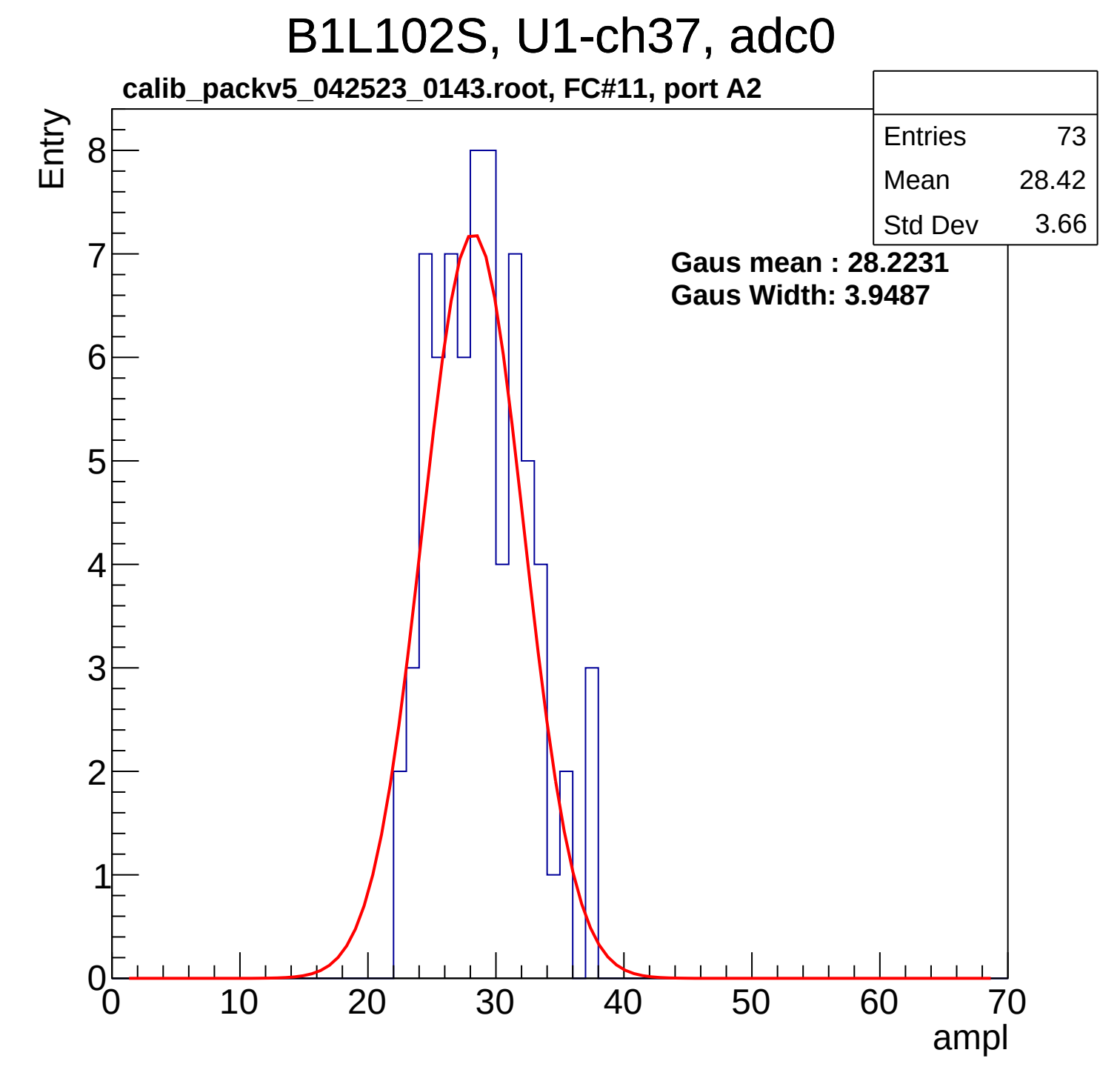
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 28.42
Std Dev 3.66
Gaus mean : 28.2231
Gaus Width: 3.9487
Entry
ampl
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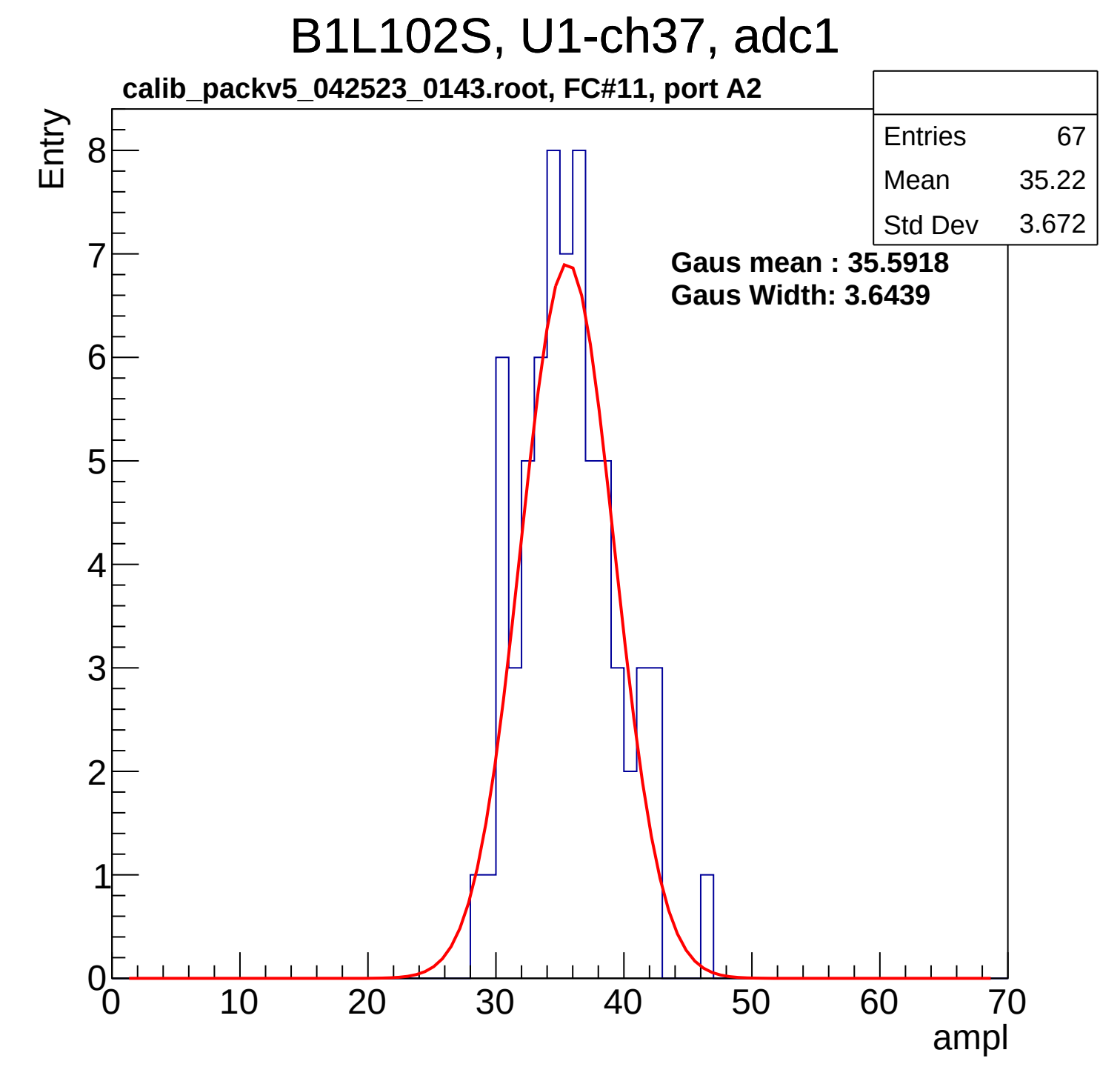
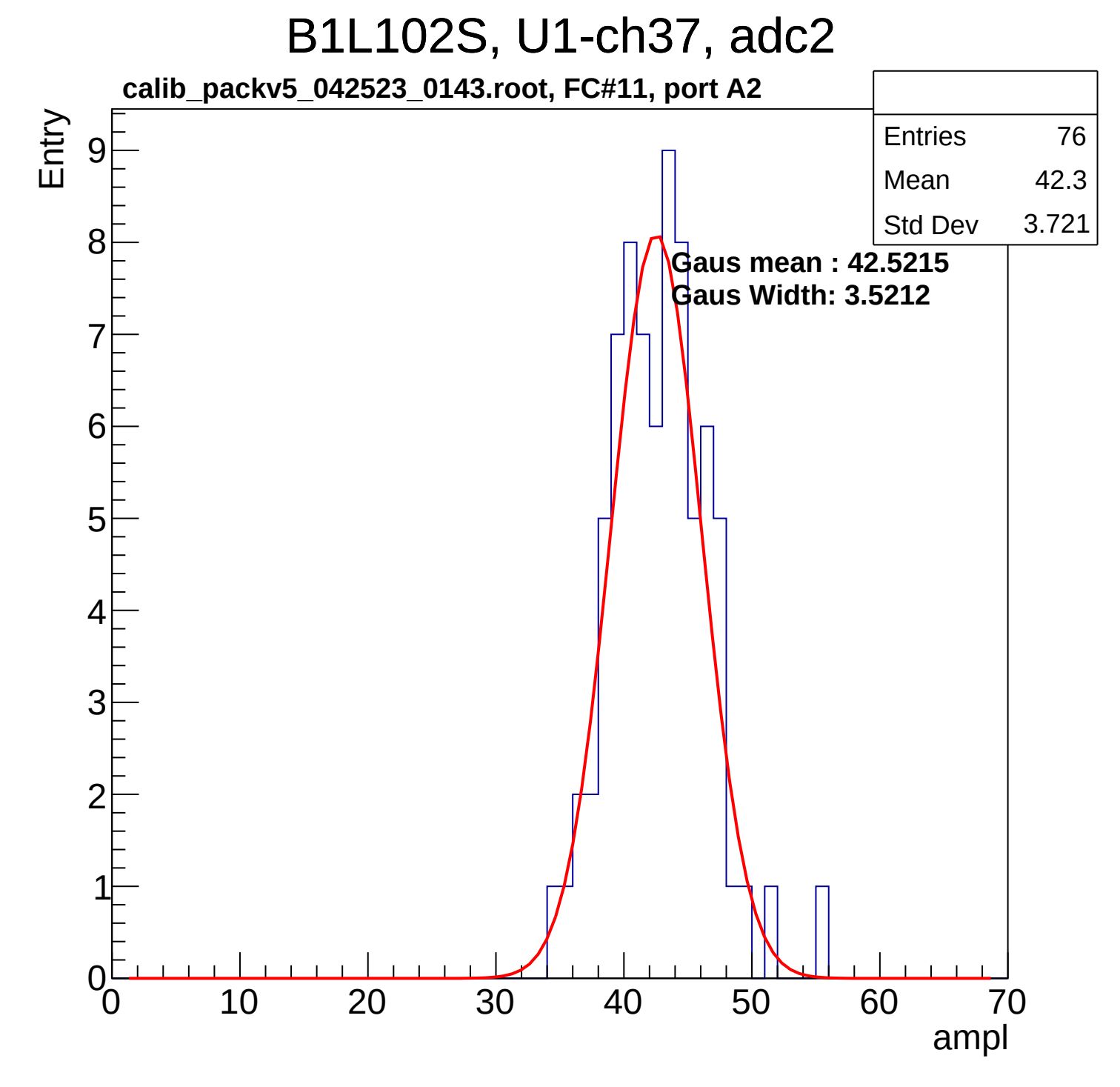
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.22
Std Dev 3.672
Gaus mean : 35.5918
Gaus Width: 3.6439
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 42.3
Std Dev 3.721
Gaus mean : 42.5215
Gaus Width: 3.5212
Entry
ampl
0
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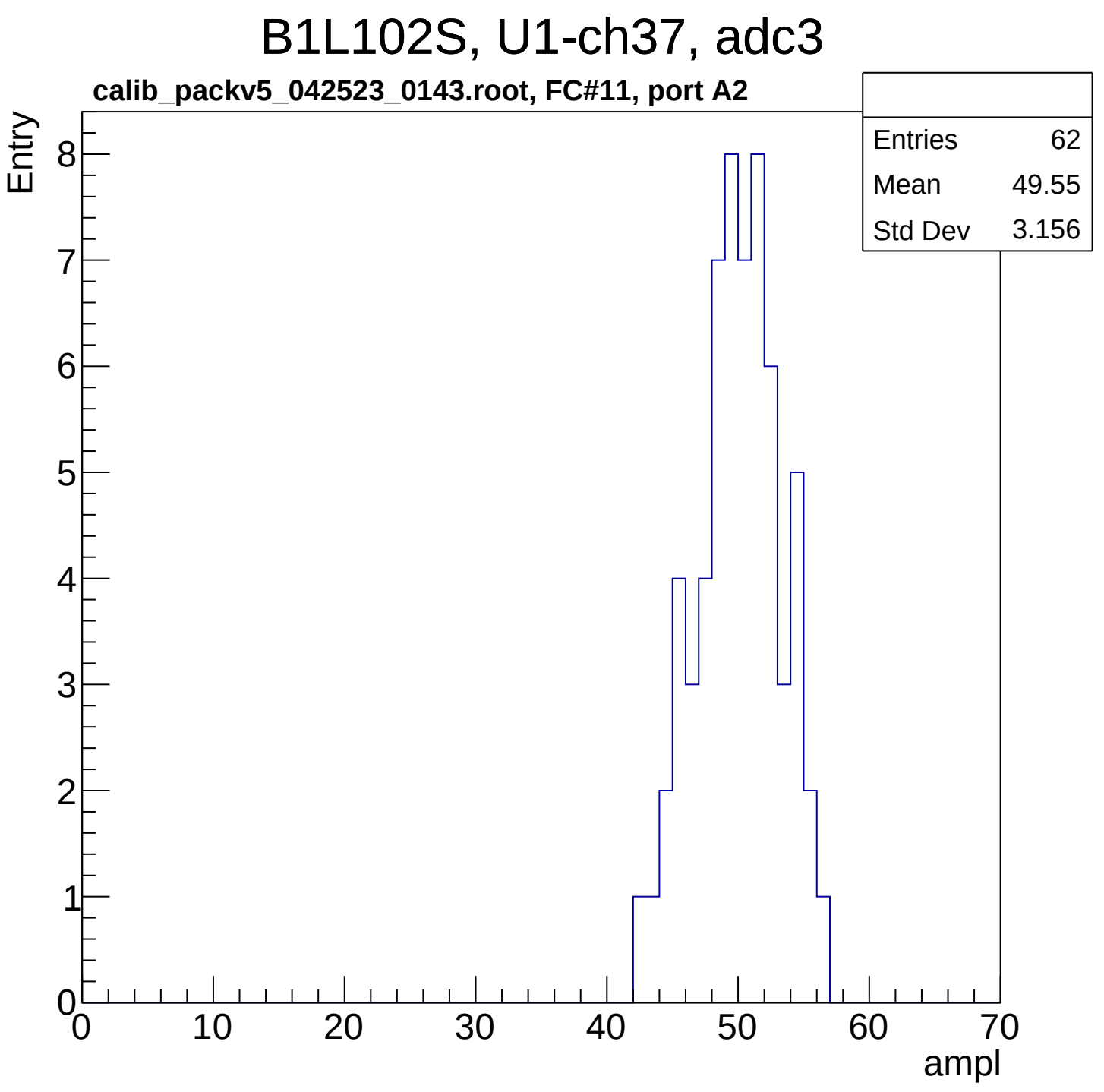
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 49.55
Std Dev 3.156
Entry
ampl
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2
3
4
5
6
7
8
0
10
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40
50
60
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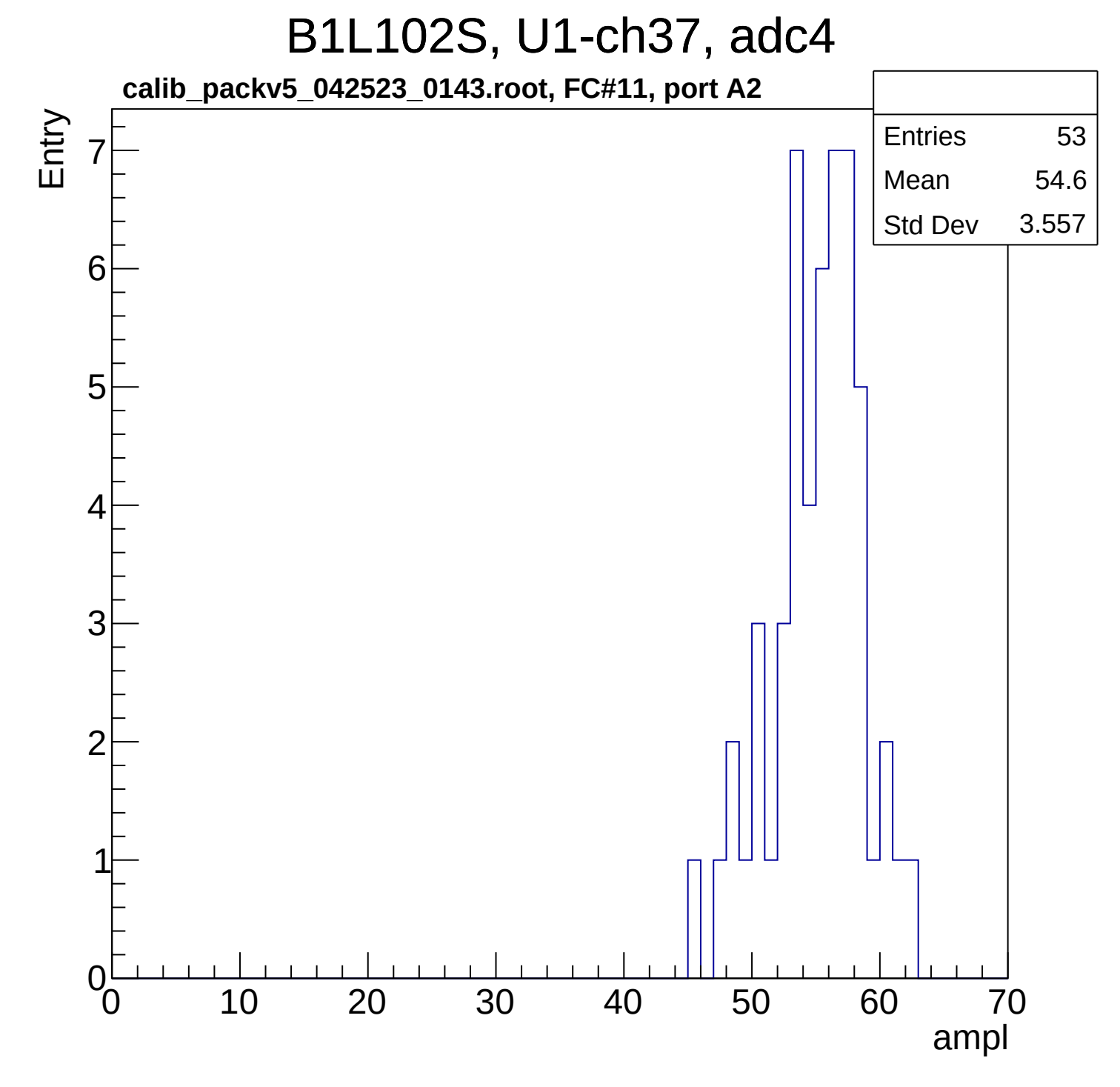
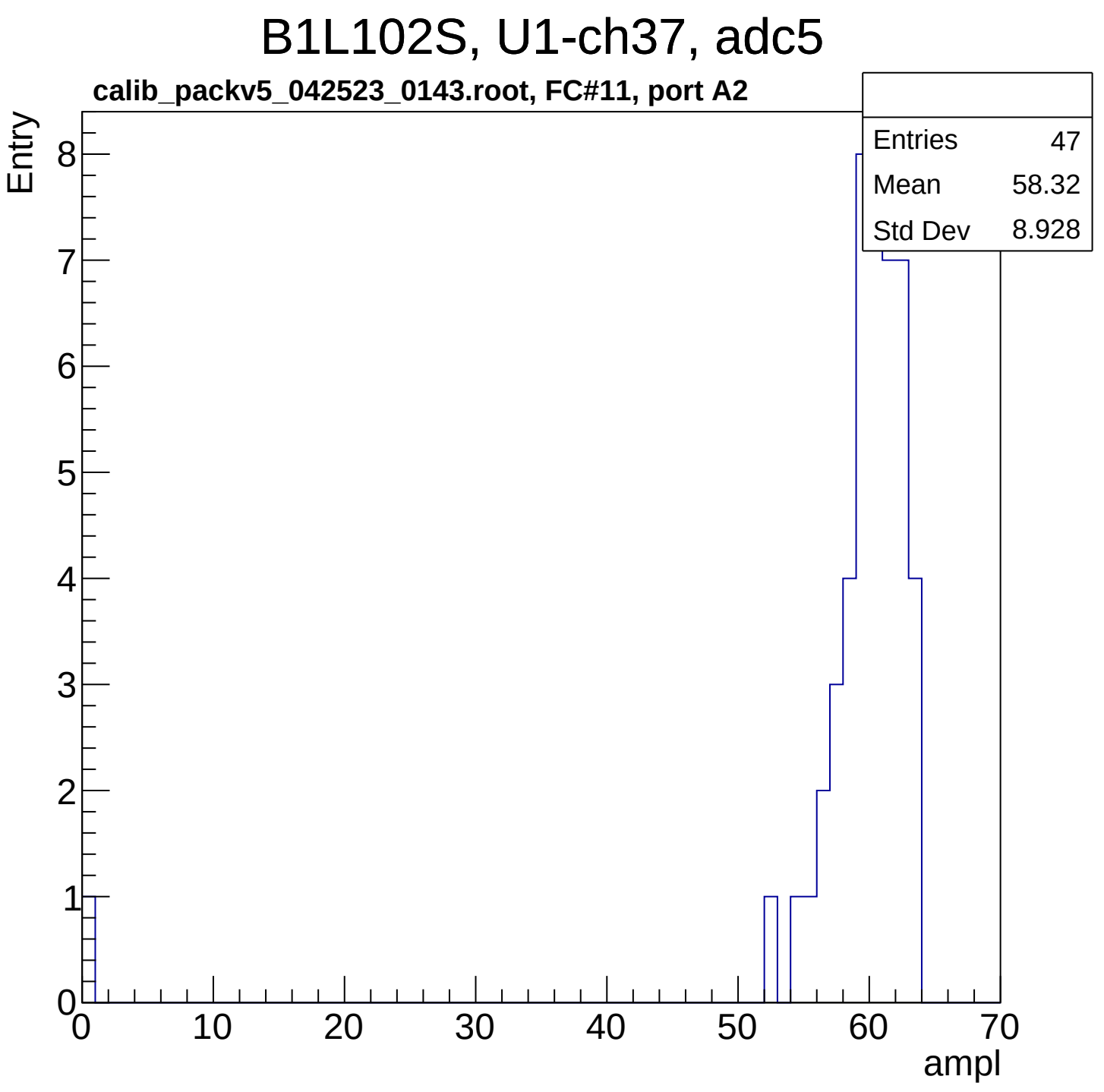

B1L102S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 54.6
Std Dev 3.557
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.32
Std Dev 8.928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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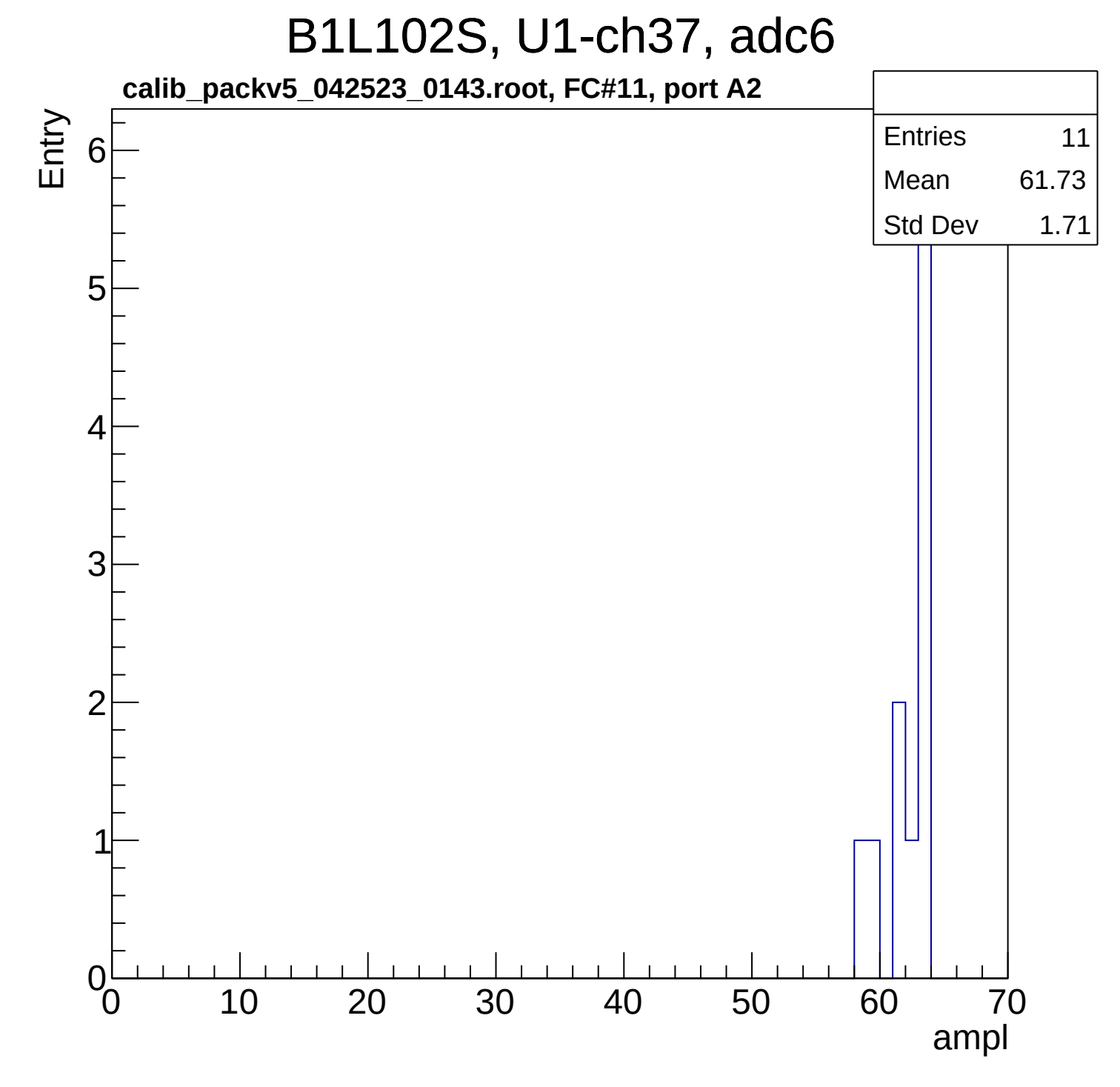
B1L102S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 61.73
Std Dev 1.71
Entry
0
1
2
3
4
5
6
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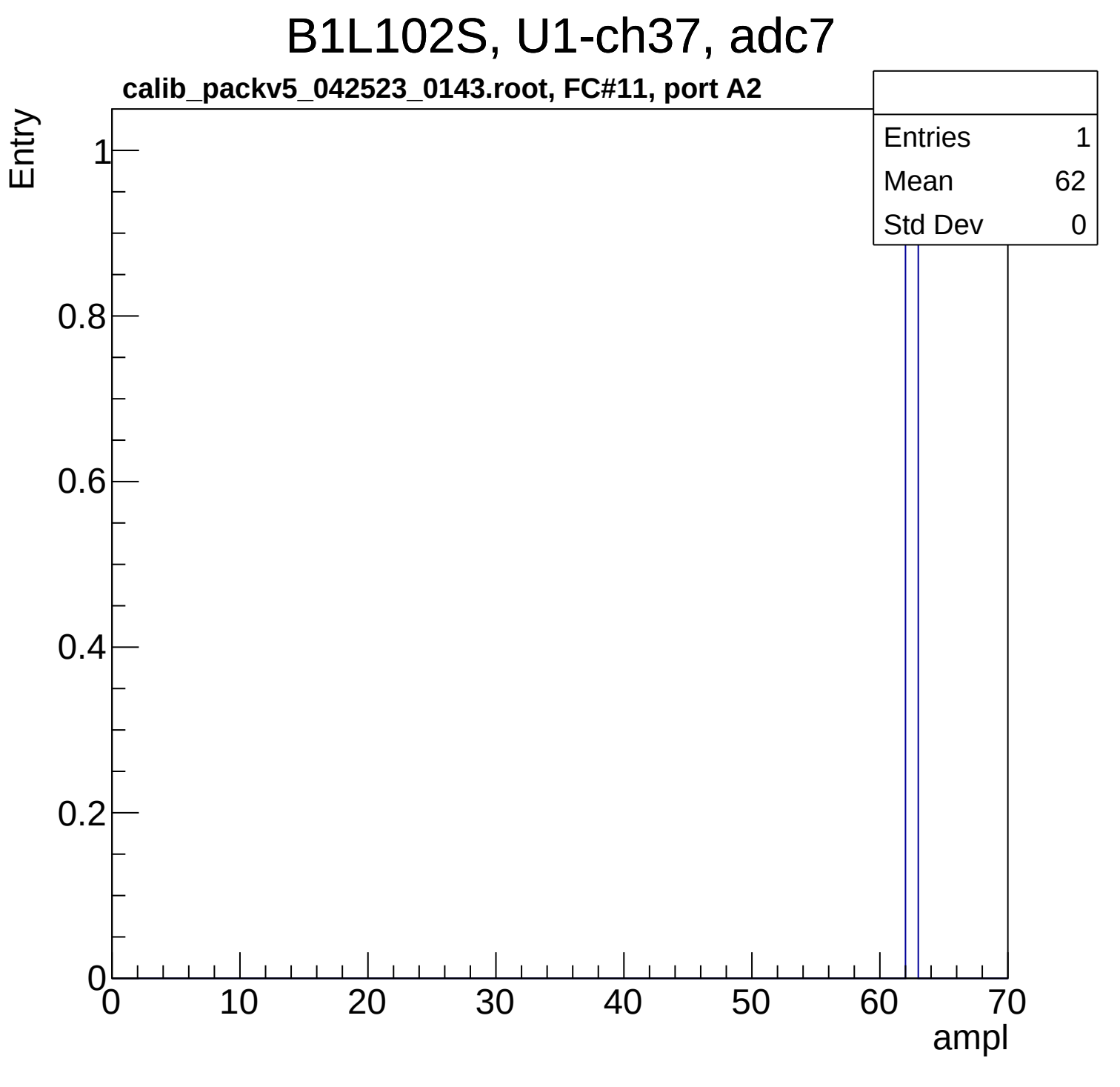
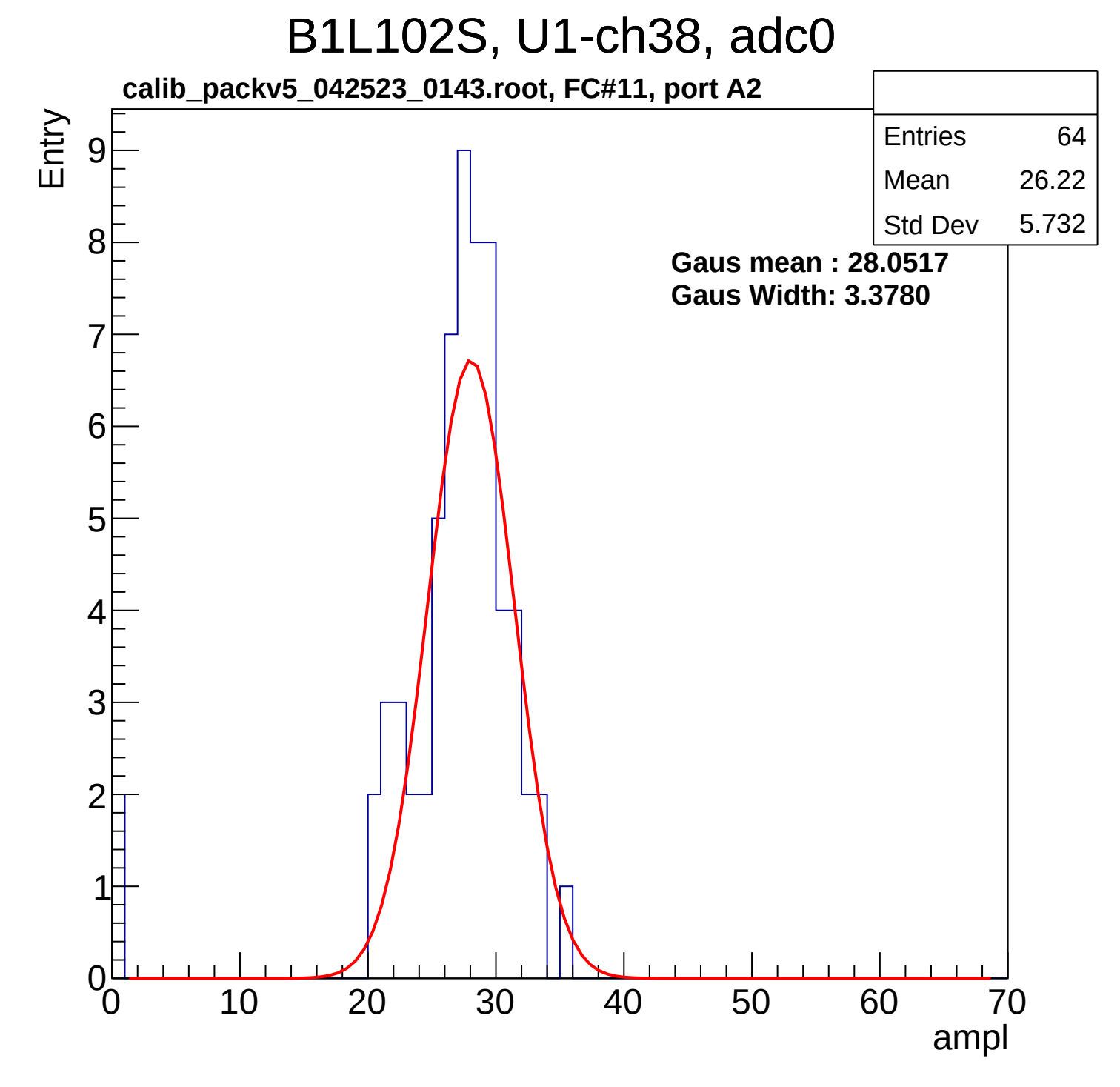
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 26.22
Std Dev 5.732
Gaus mean : 28.0517
Gaus Width: 3.3780
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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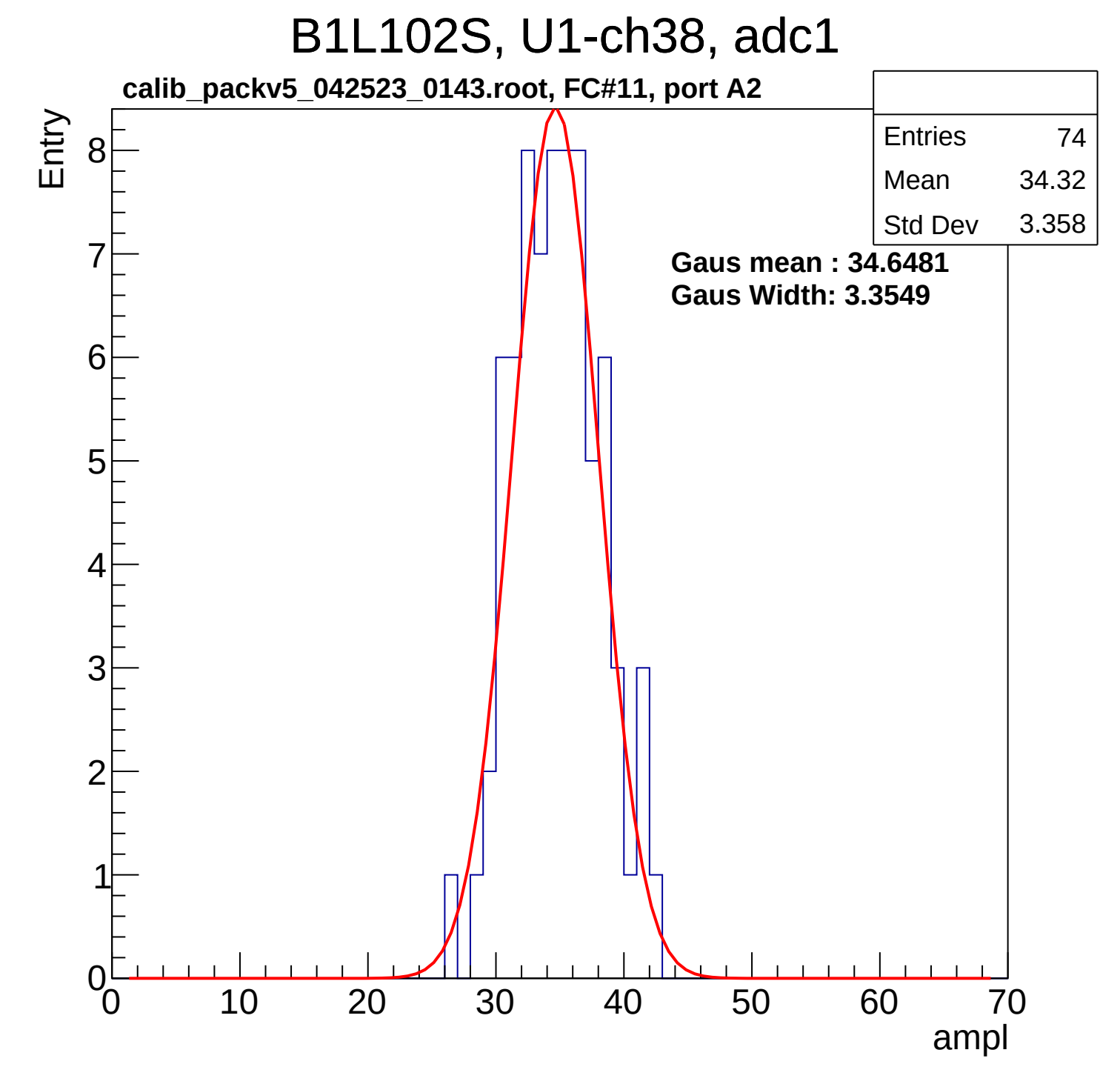
B1L102S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 34.32
Std Dev 3.358
Gaus mean : 34.6481
Gaus Width: 3.3549
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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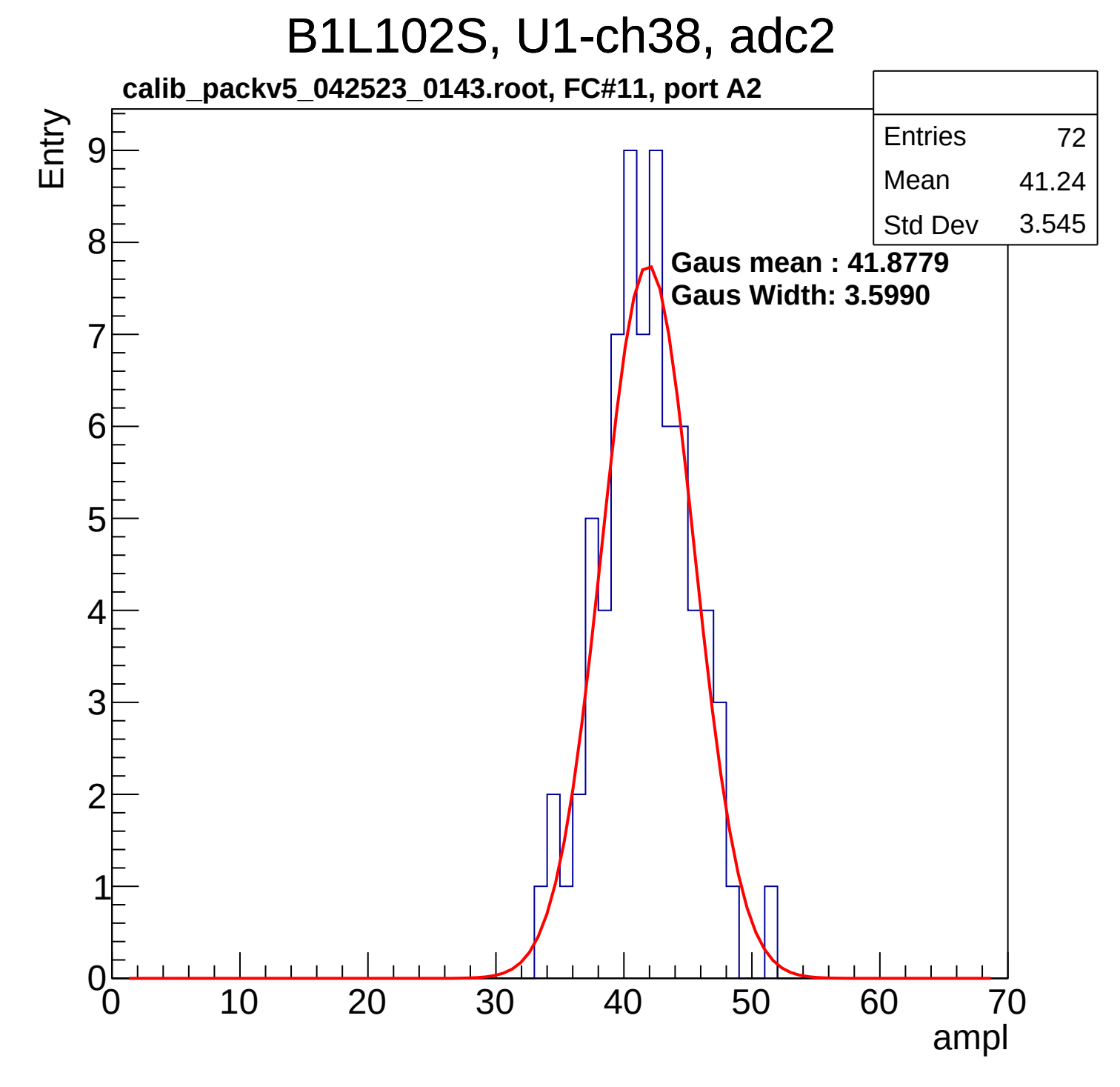
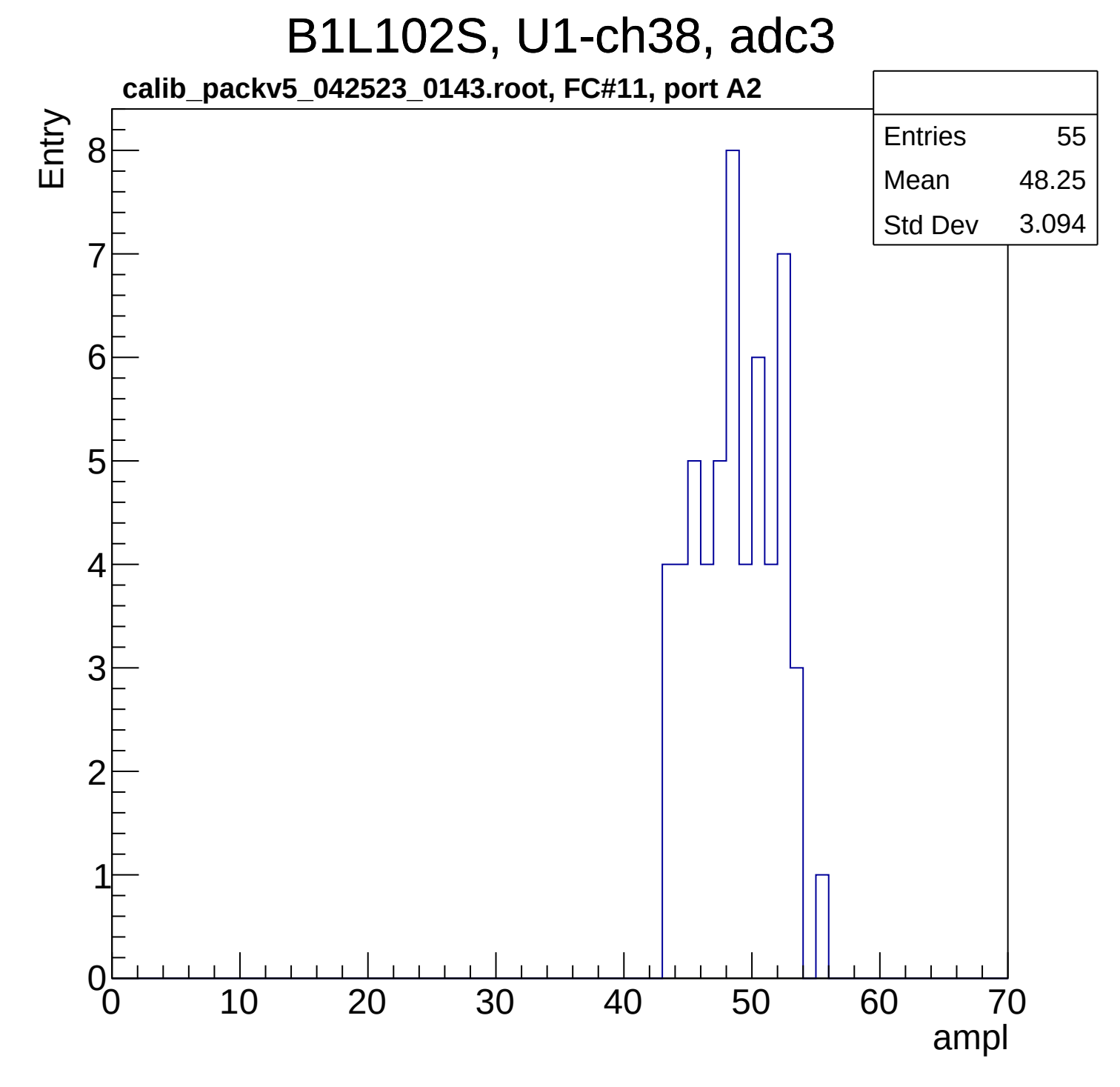

B1L102S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 41.24
Std Dev 3.545
Gaus mean : 41.8779
Gaus Width: 3.5990
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 48.25
Std Dev 3.094
Entry
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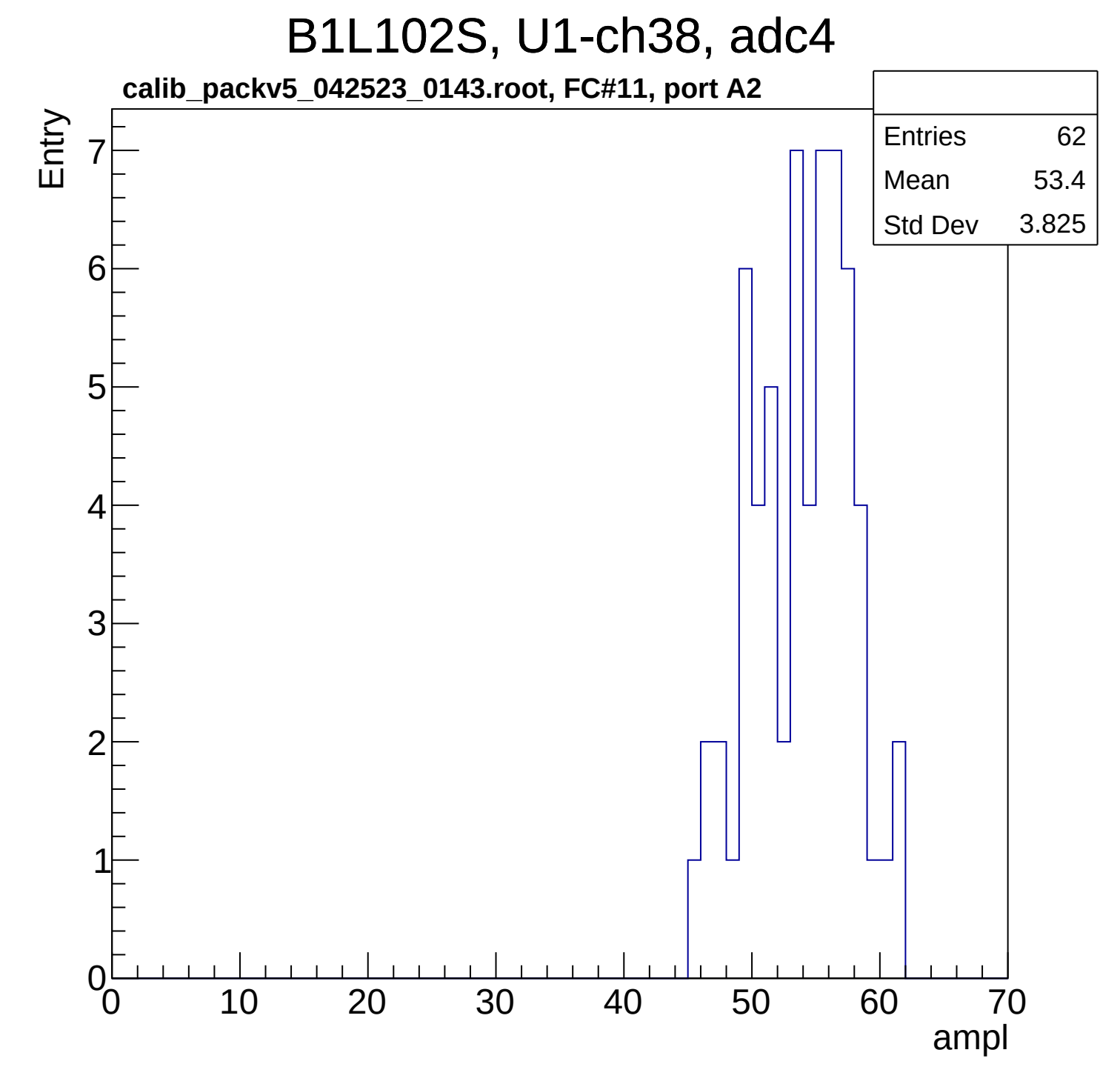
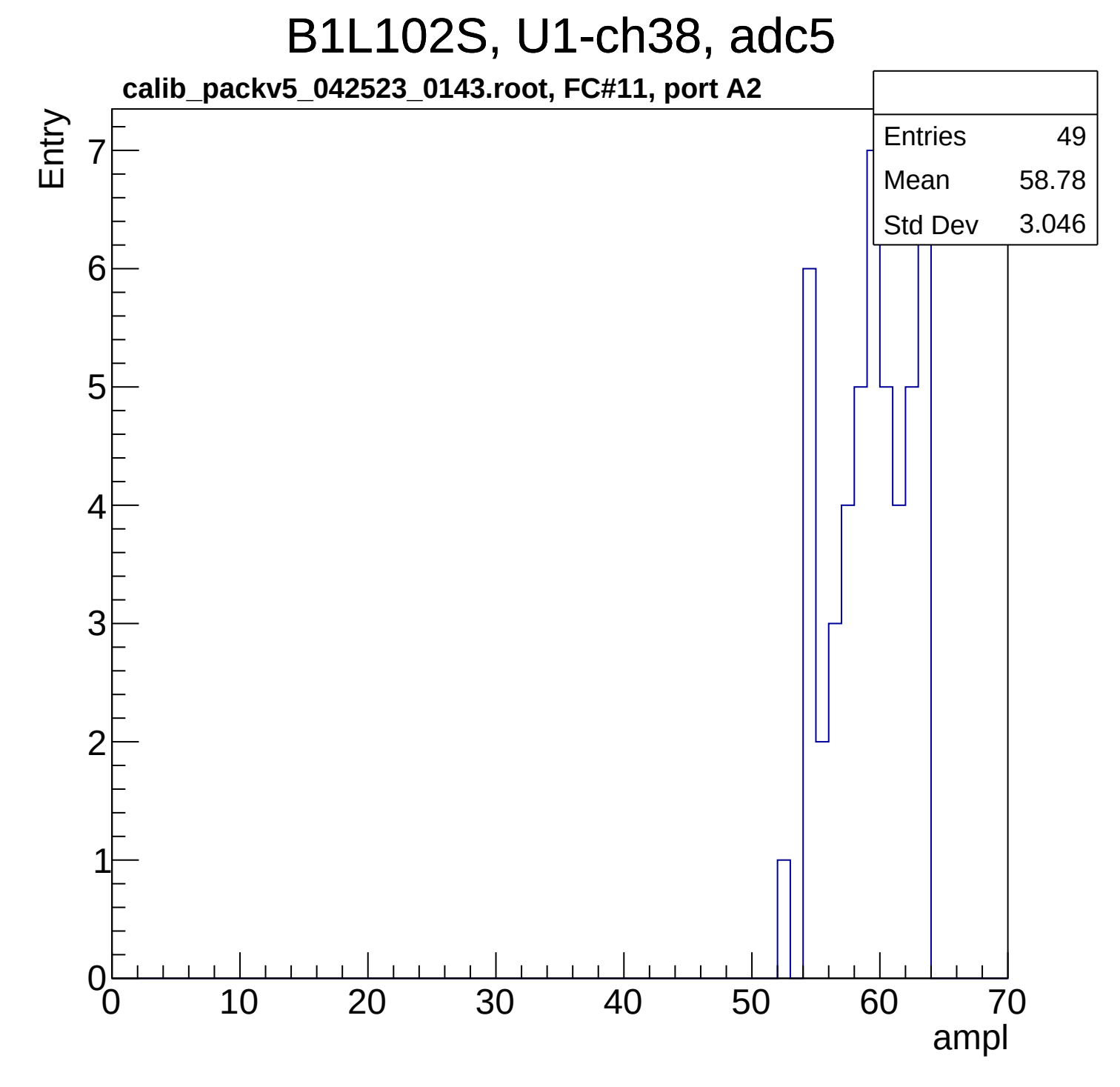
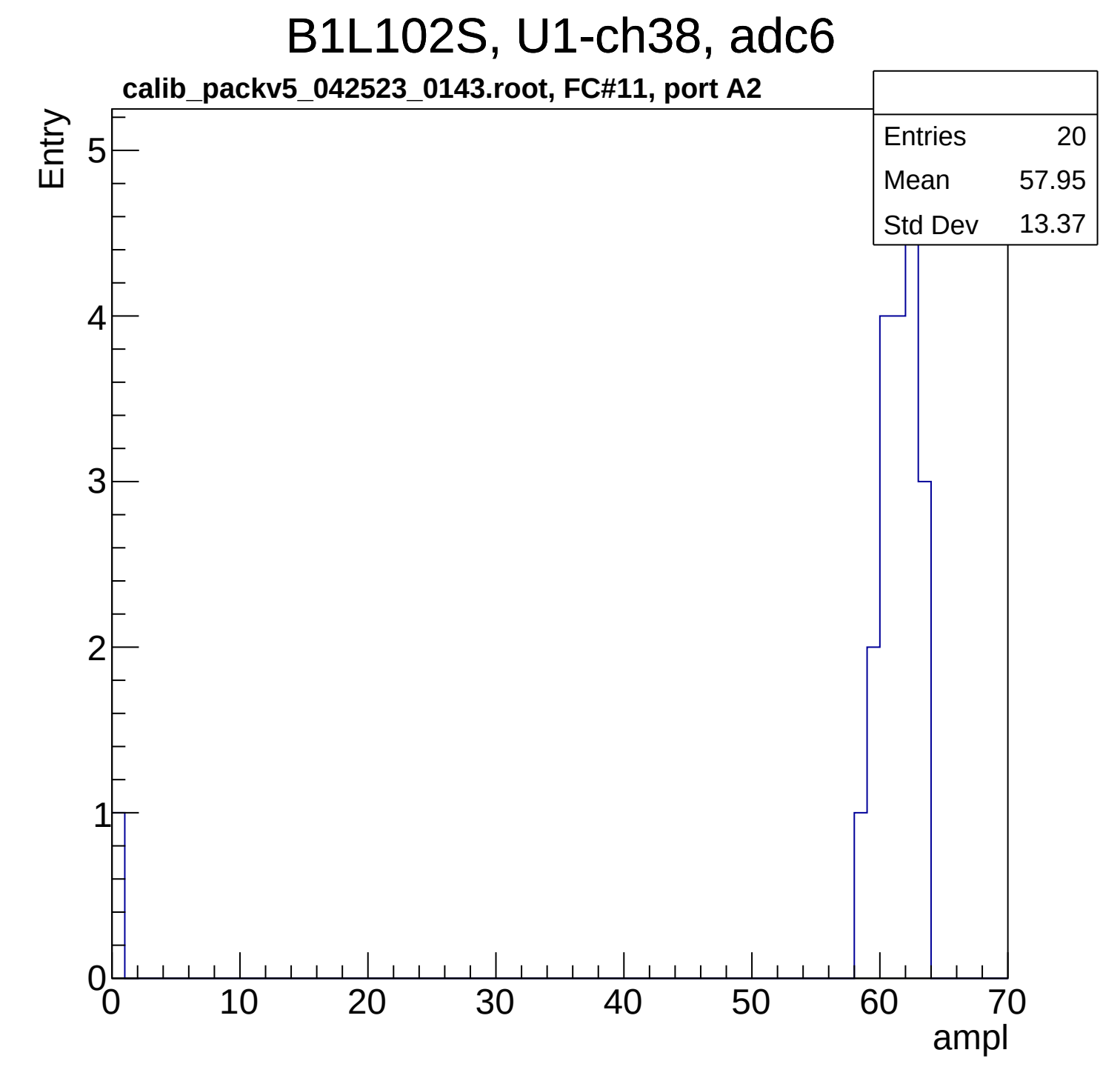
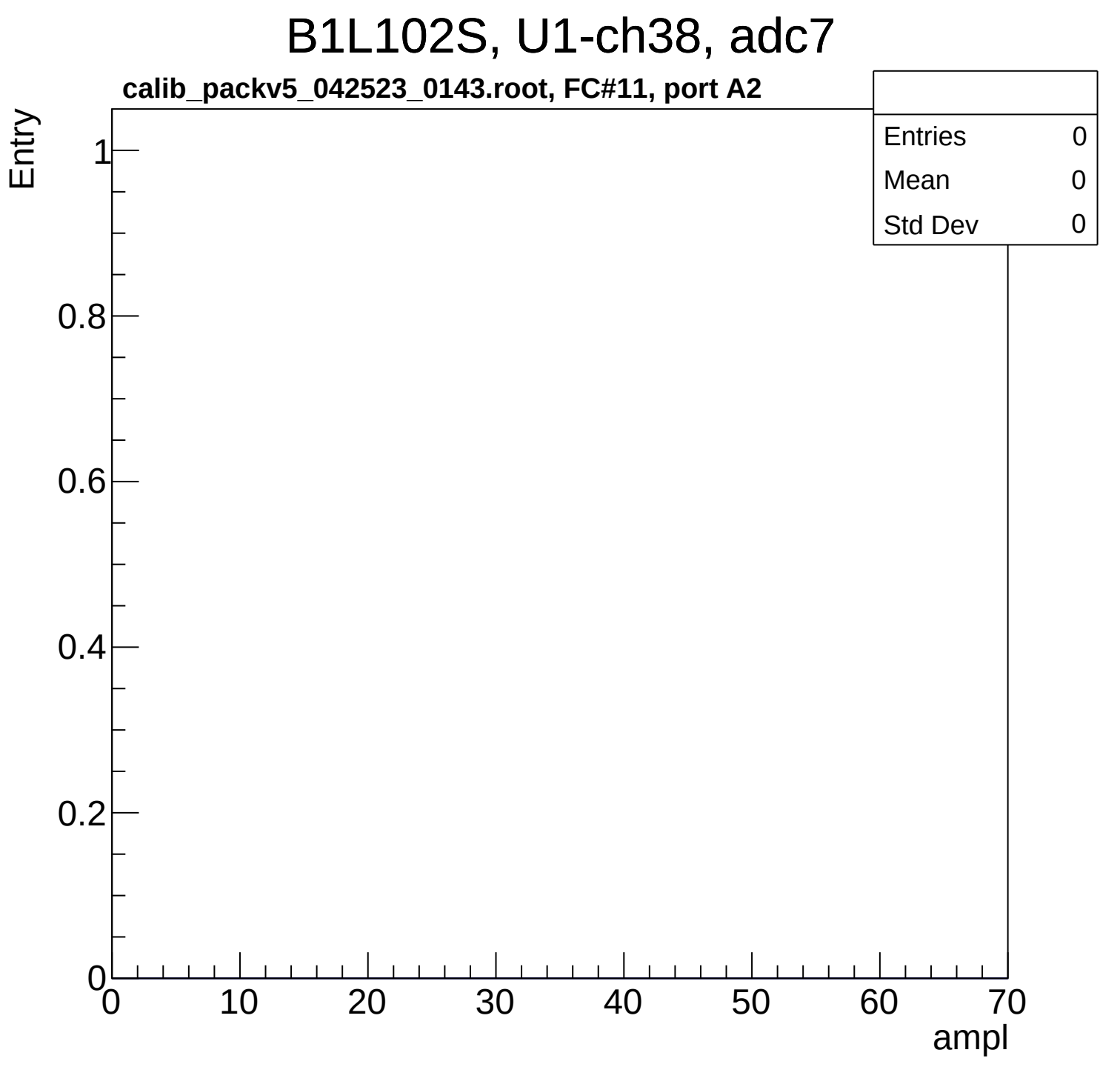
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 53.4
Std Dev 3.825
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.78
Std Dev 3.046
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 20
Mean 57.95
Std Dev 13.37
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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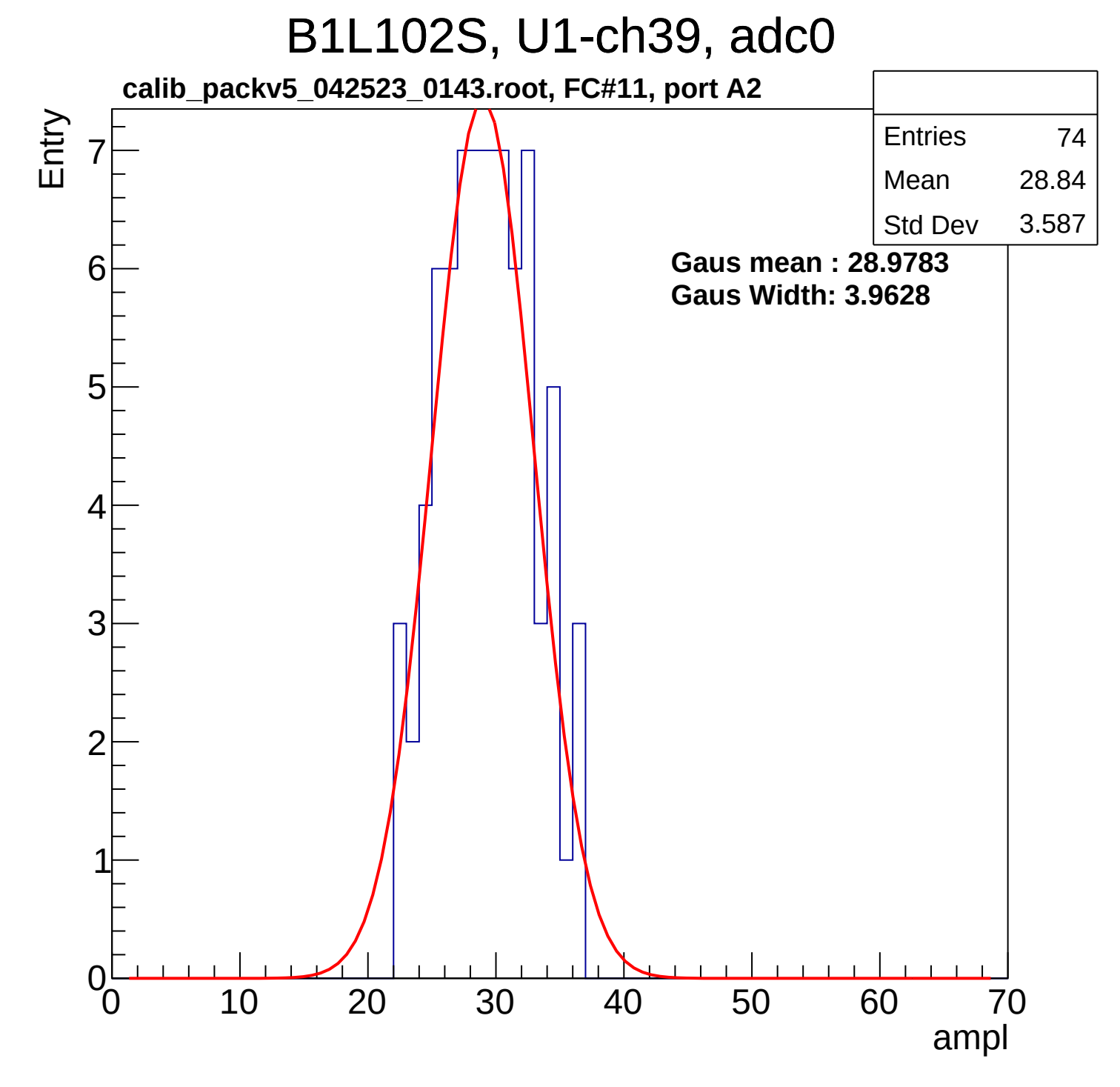
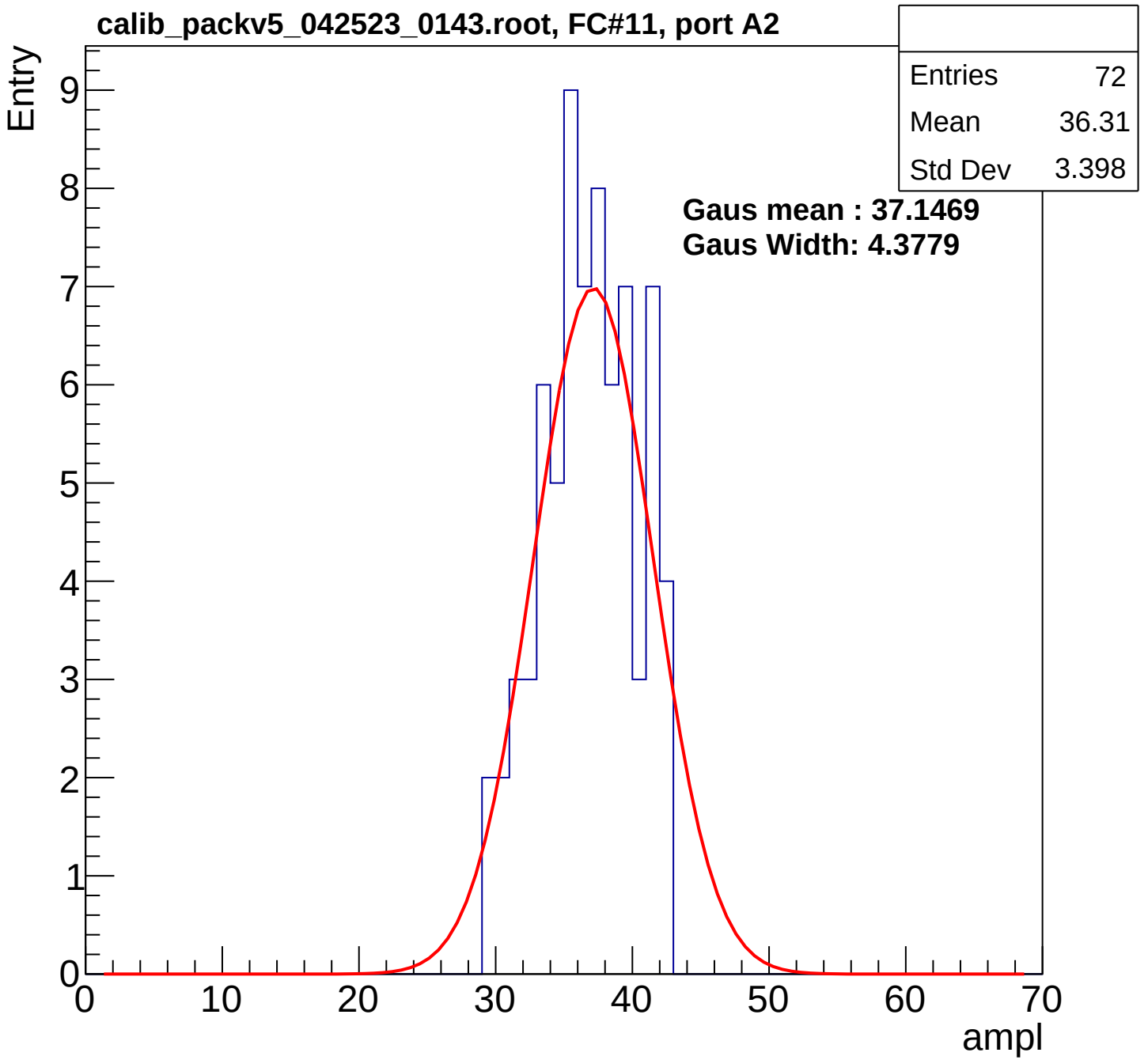
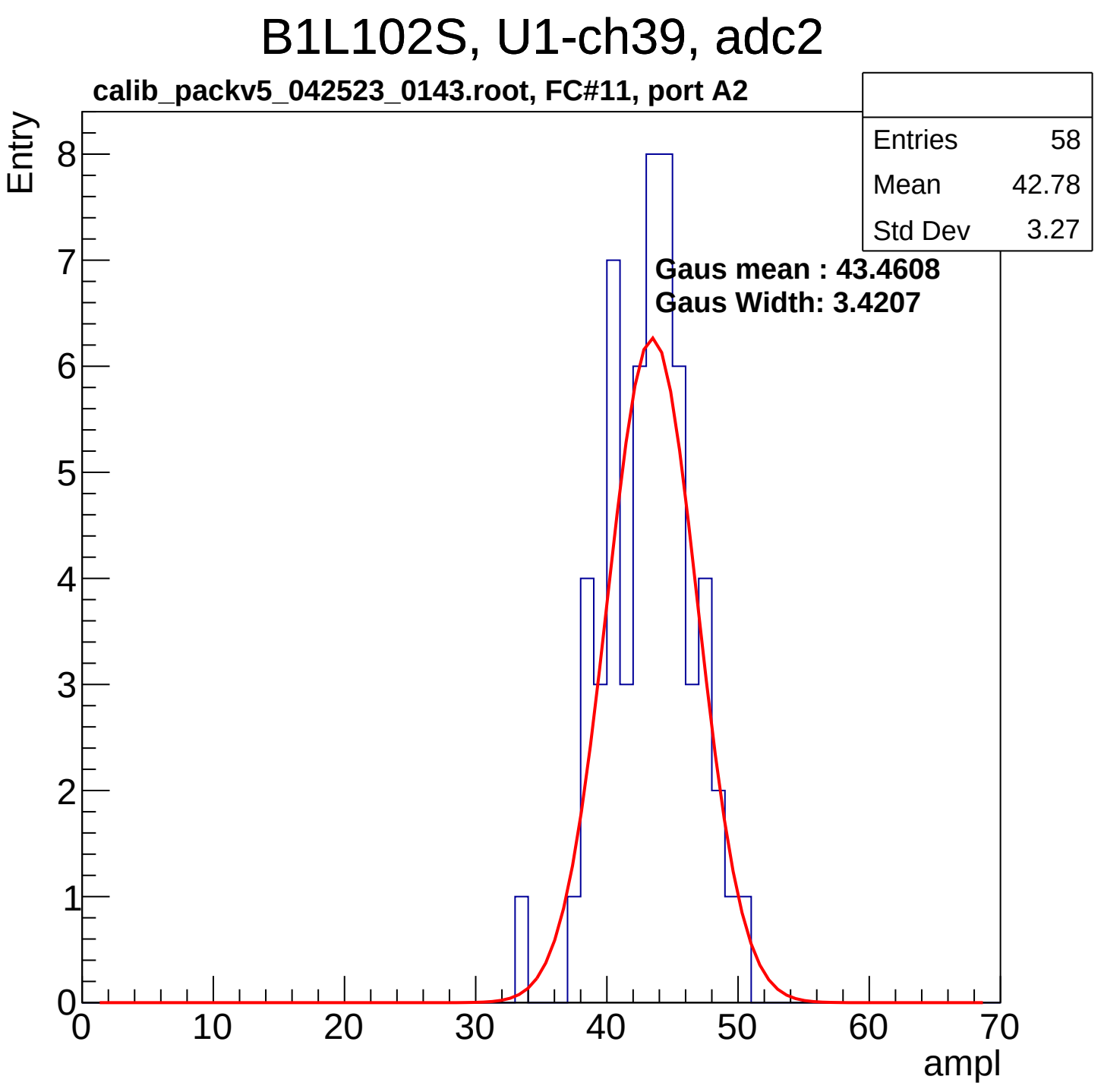
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 28.84
Std Dev 3.587
Gaus mean : 28.9783
Gaus Width: 3.9628
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 36.31
Std Dev 3.398
Gaus mean : 37.1469
Gaus Width: 4.3779
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 42.78
Std Dev 3.27
Gaus mean : 43.4608
Gaus Width: 3.4207
Entry
ampl
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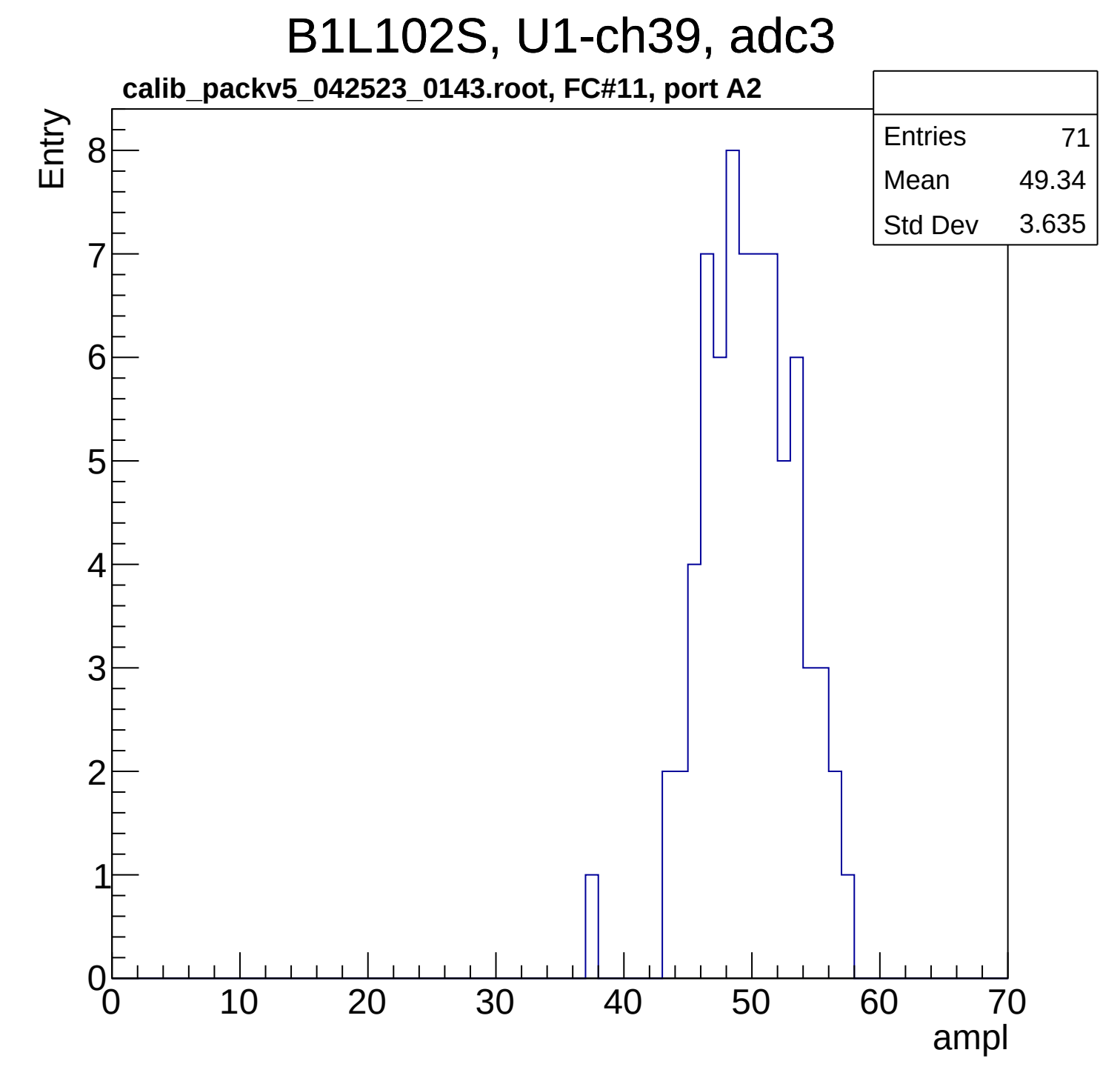
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 49.34
Std Dev 3.635
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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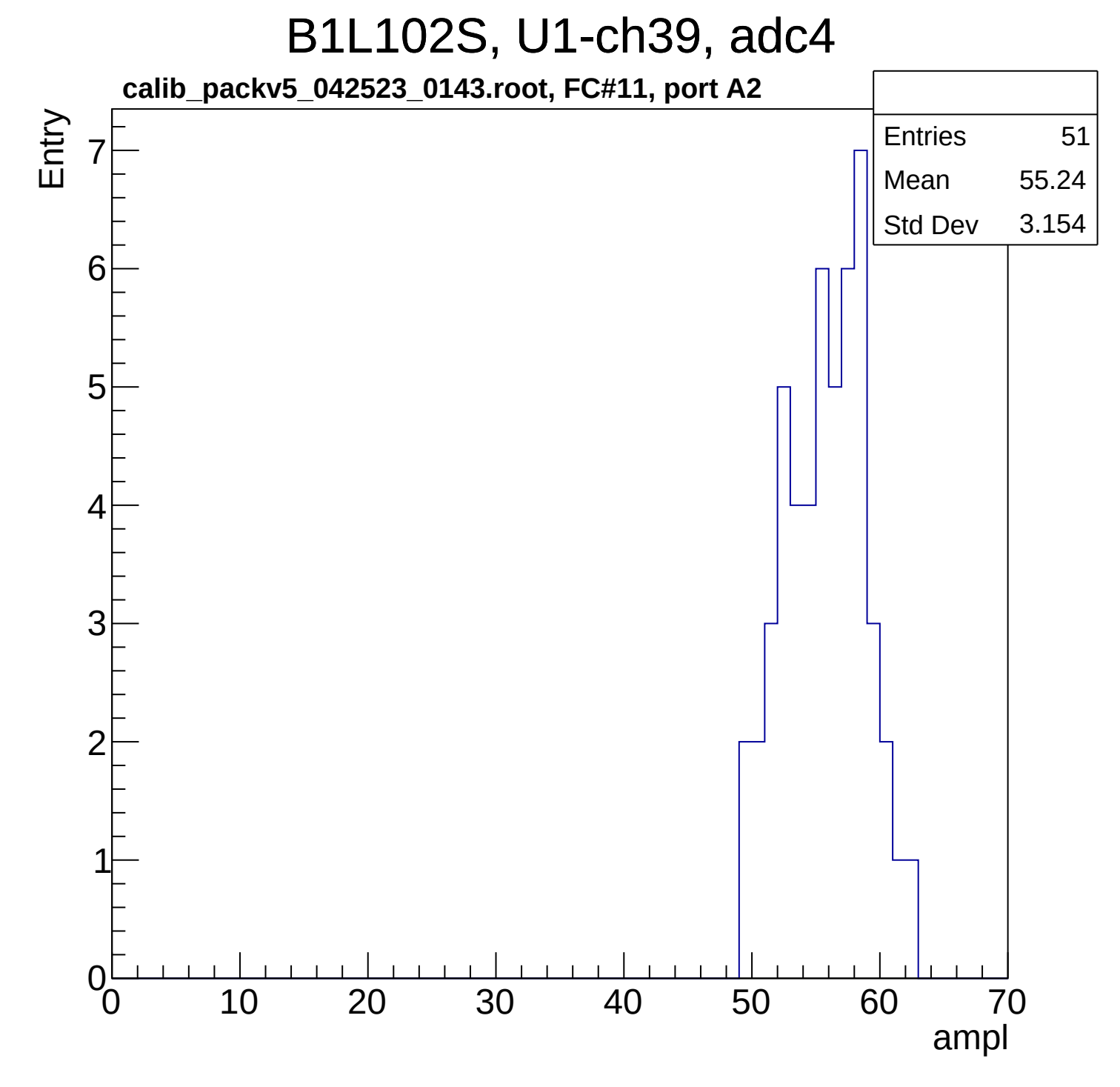
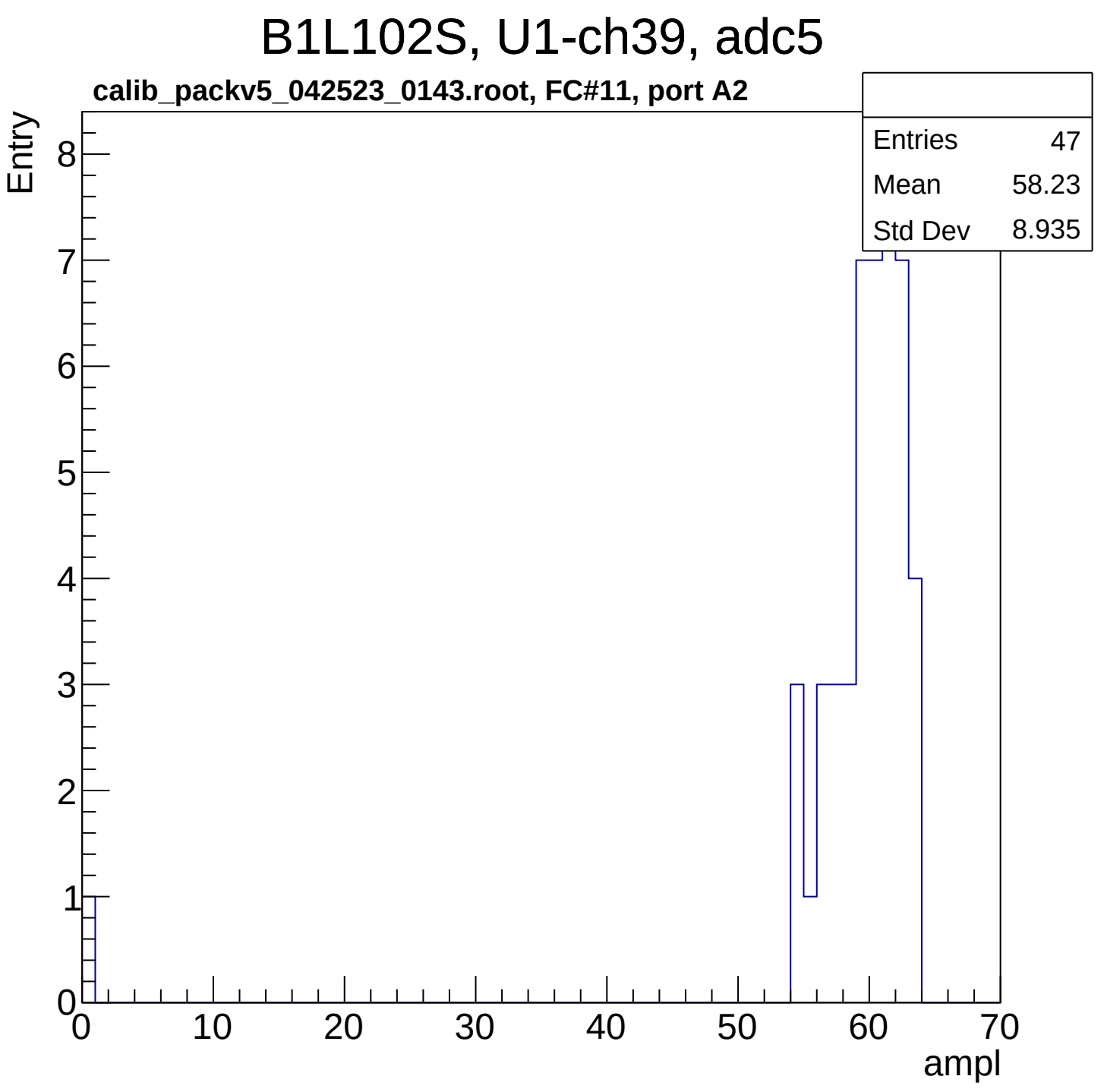

B1L102S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 55.24
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58.23
Std Dev 8.935
Entry
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2
3
4
5
6
7
8
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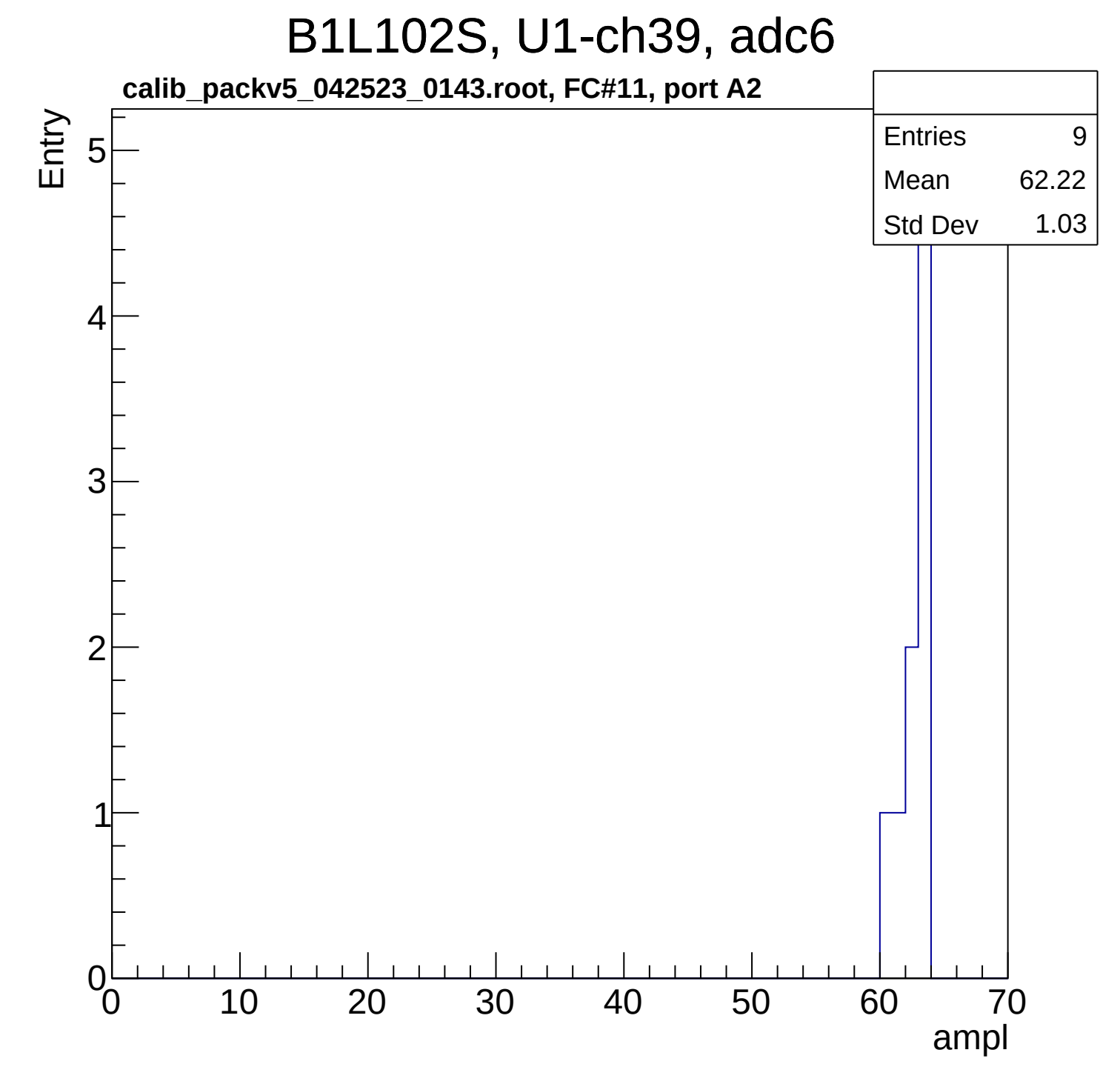
50
60
70
ampl


B1L102S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62.22
Std Dev 1.03
Entry
ampl
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1
2
3
4
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0
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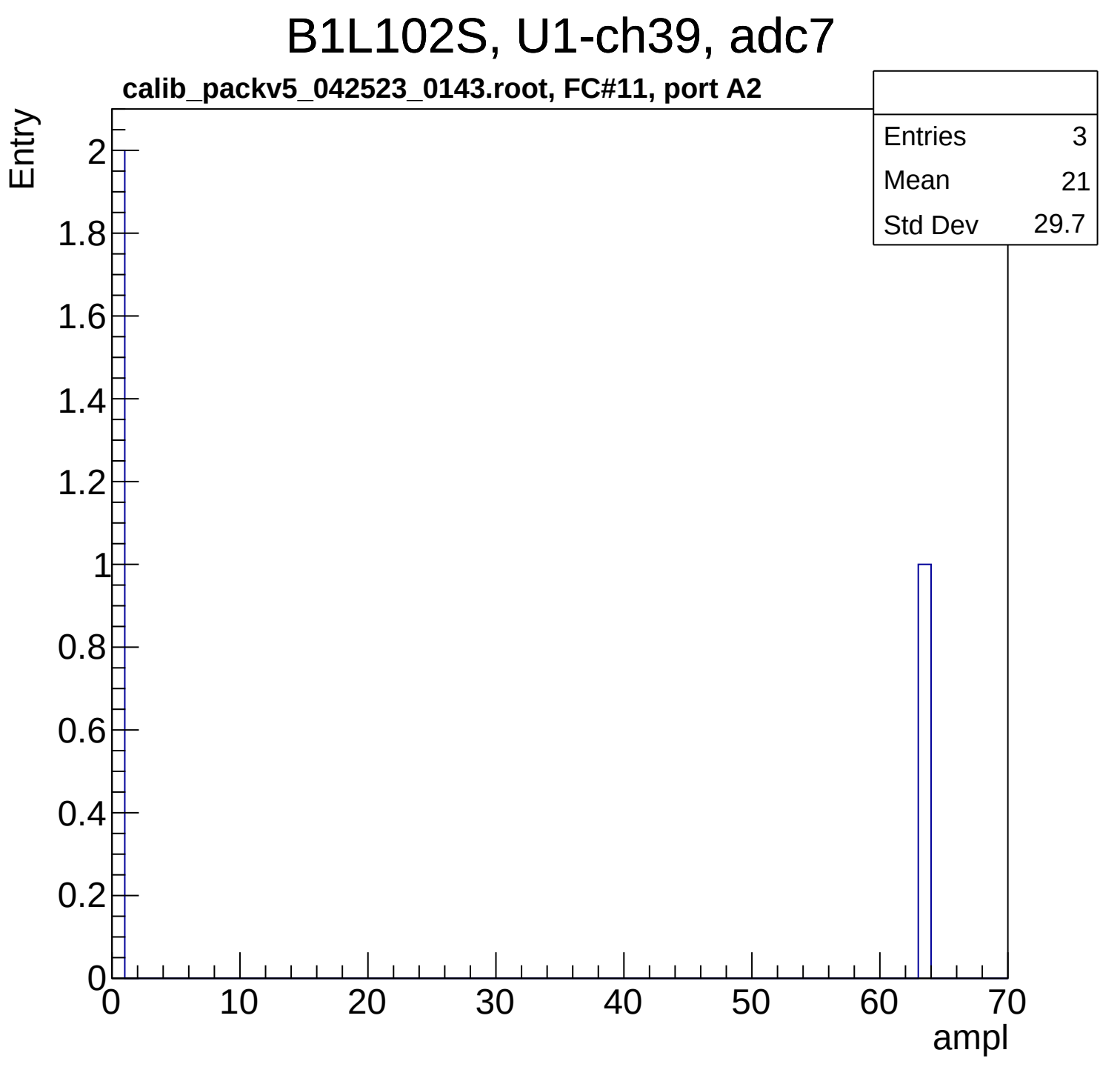
30
40
50
60
70


B1L102S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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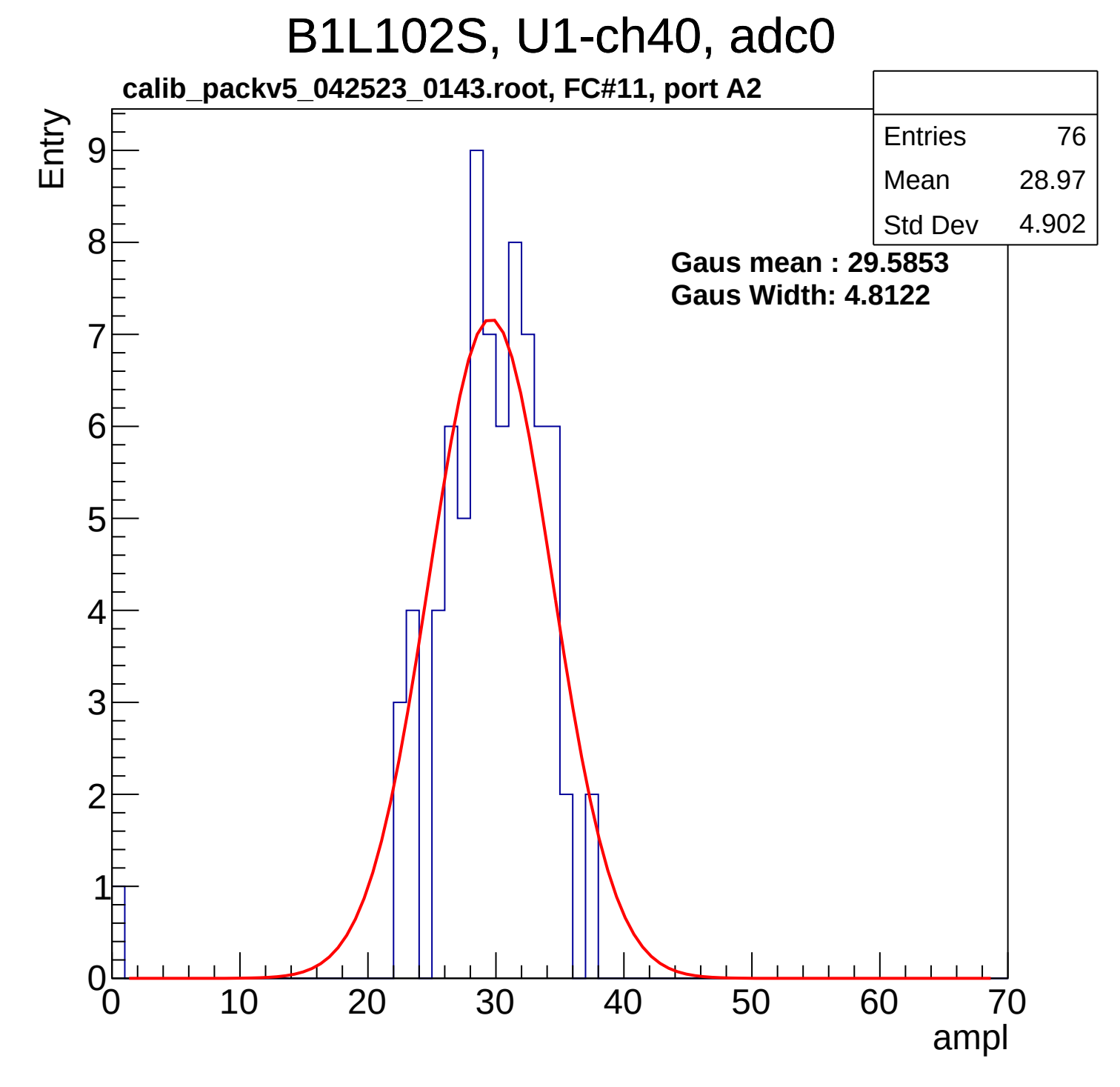
B1L102S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 28.97
Std Dev 4.902
Gaus mean : 29.5853
Gaus Width: 4.8122
Entry
ampl
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2
3
4
5
6
7
8
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0
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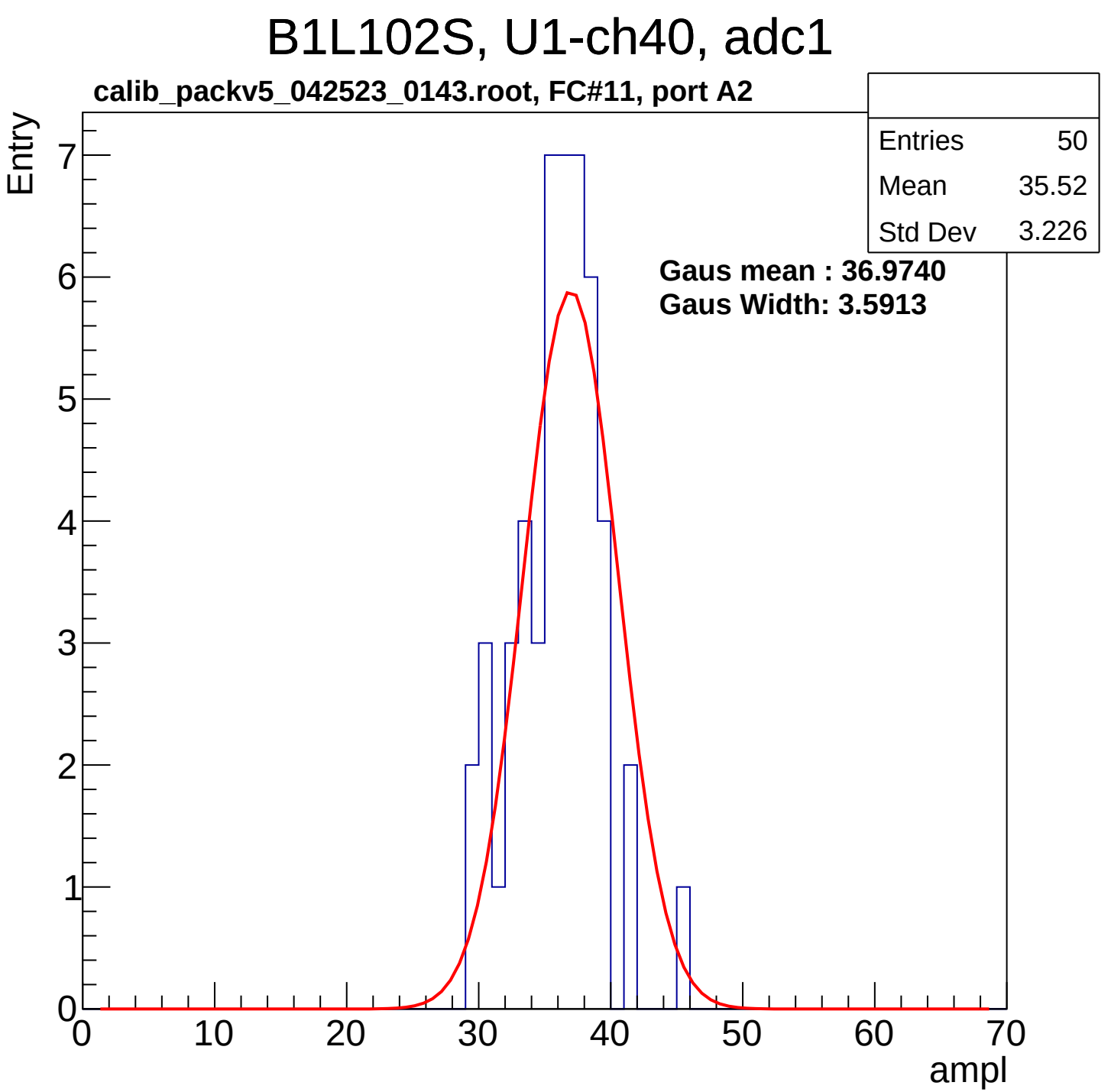
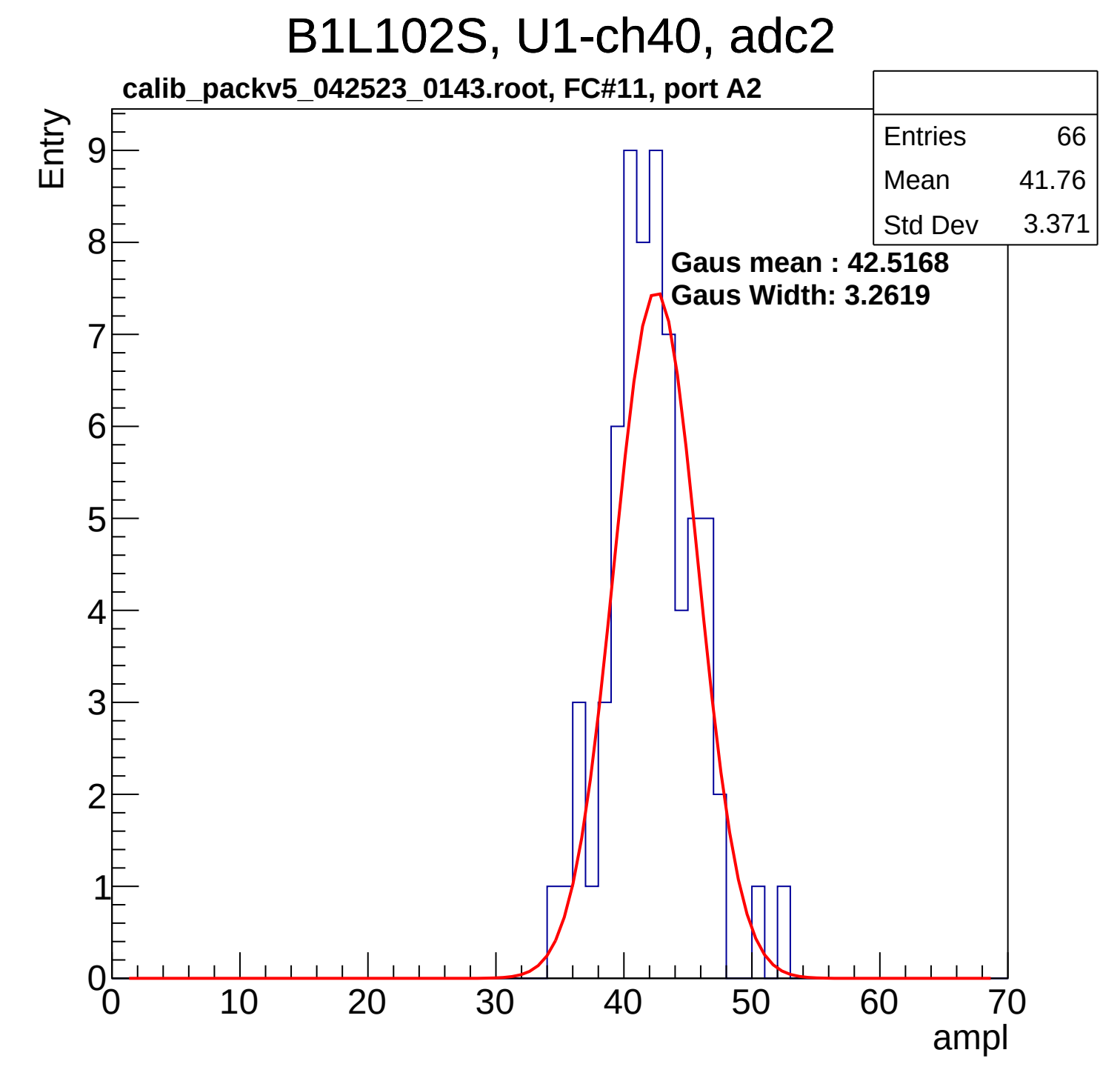
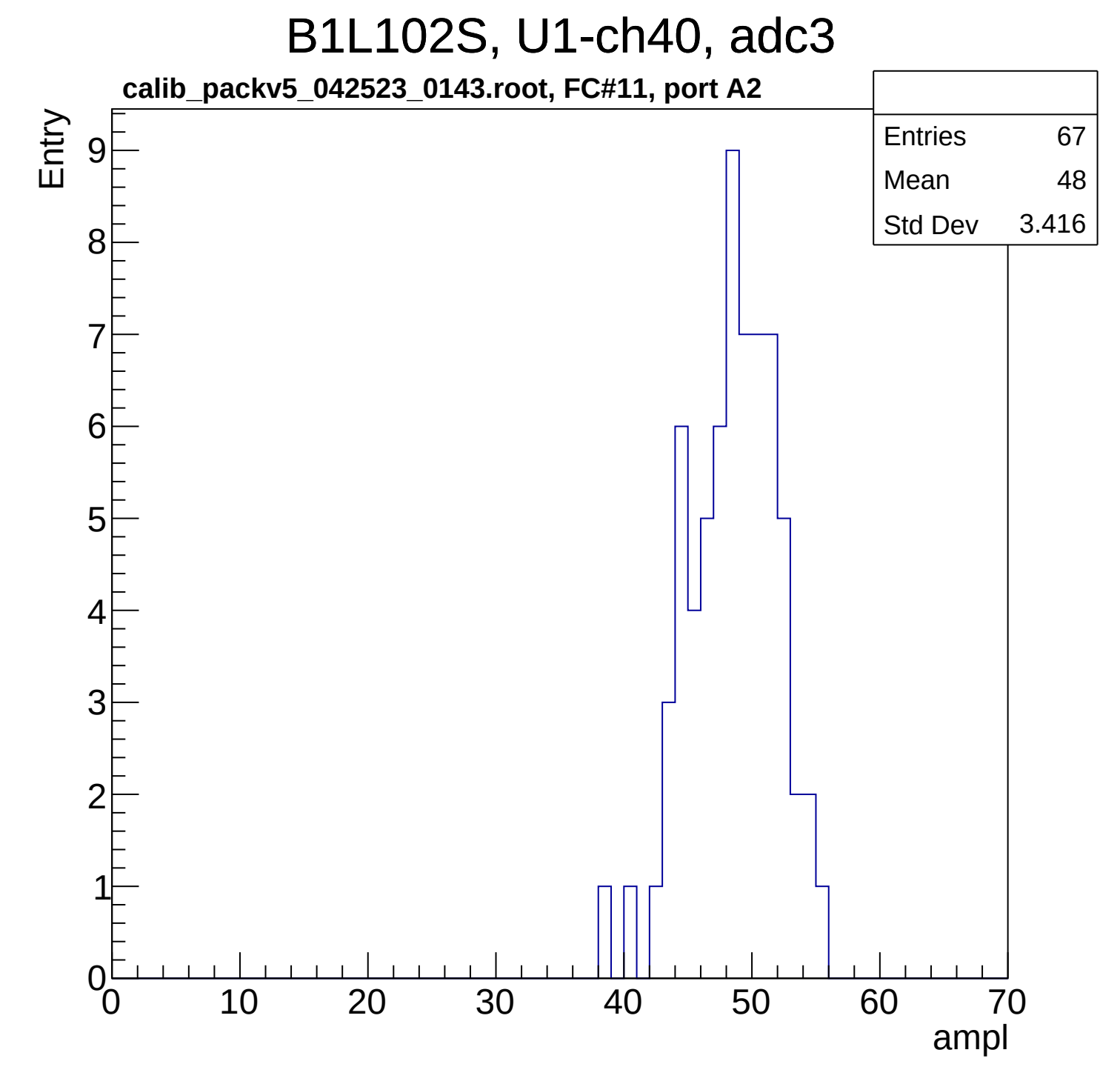
60
70


B1L102S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 35.52
Std Dev 3.226
Gaus mean : 36.9740
Gaus Width: 3.5913
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 41.76
Std Dev 3.371
Gaus mean : 42.5168
Gaus Width: 3.2619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 48
Std Dev 3.416
Entry
ampl
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3
4
5
6
7
8
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0
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60
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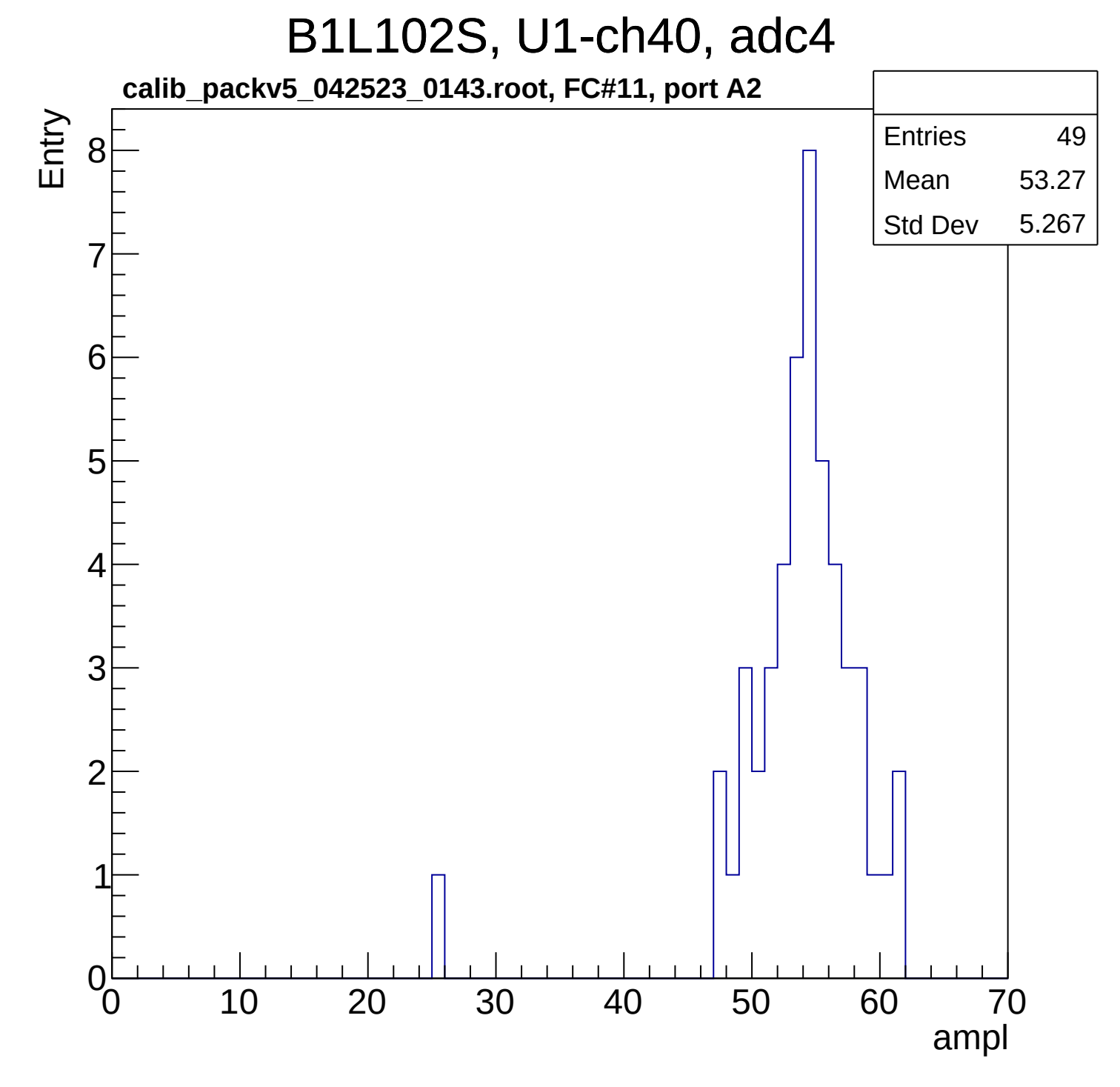
B1L102S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 53.27
Std Dev 5.267
Entry
ampl
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2
3
4
5
6
7
8
0
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40
50
60
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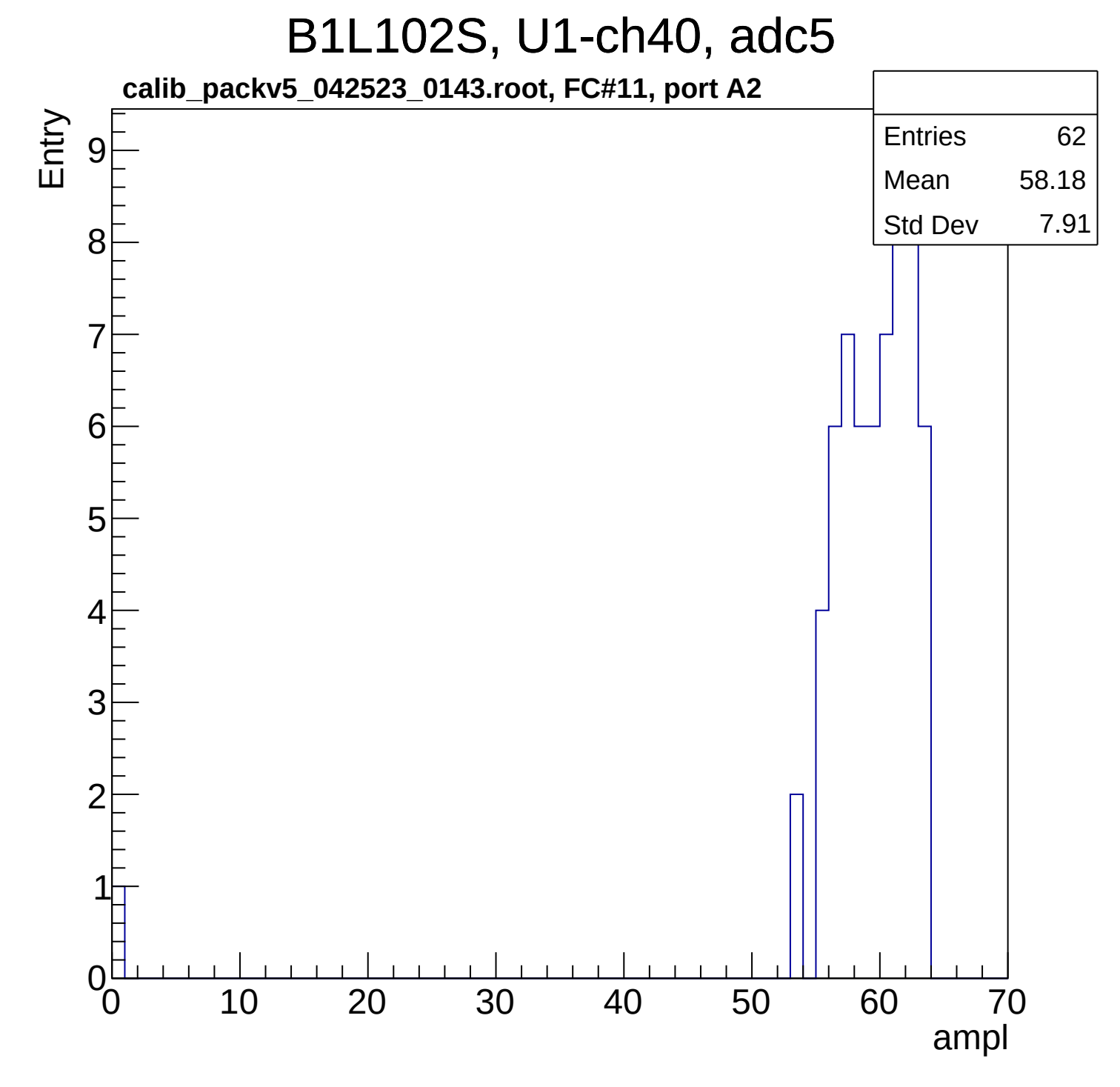
B1L102S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 58.18
Std Dev 7.91
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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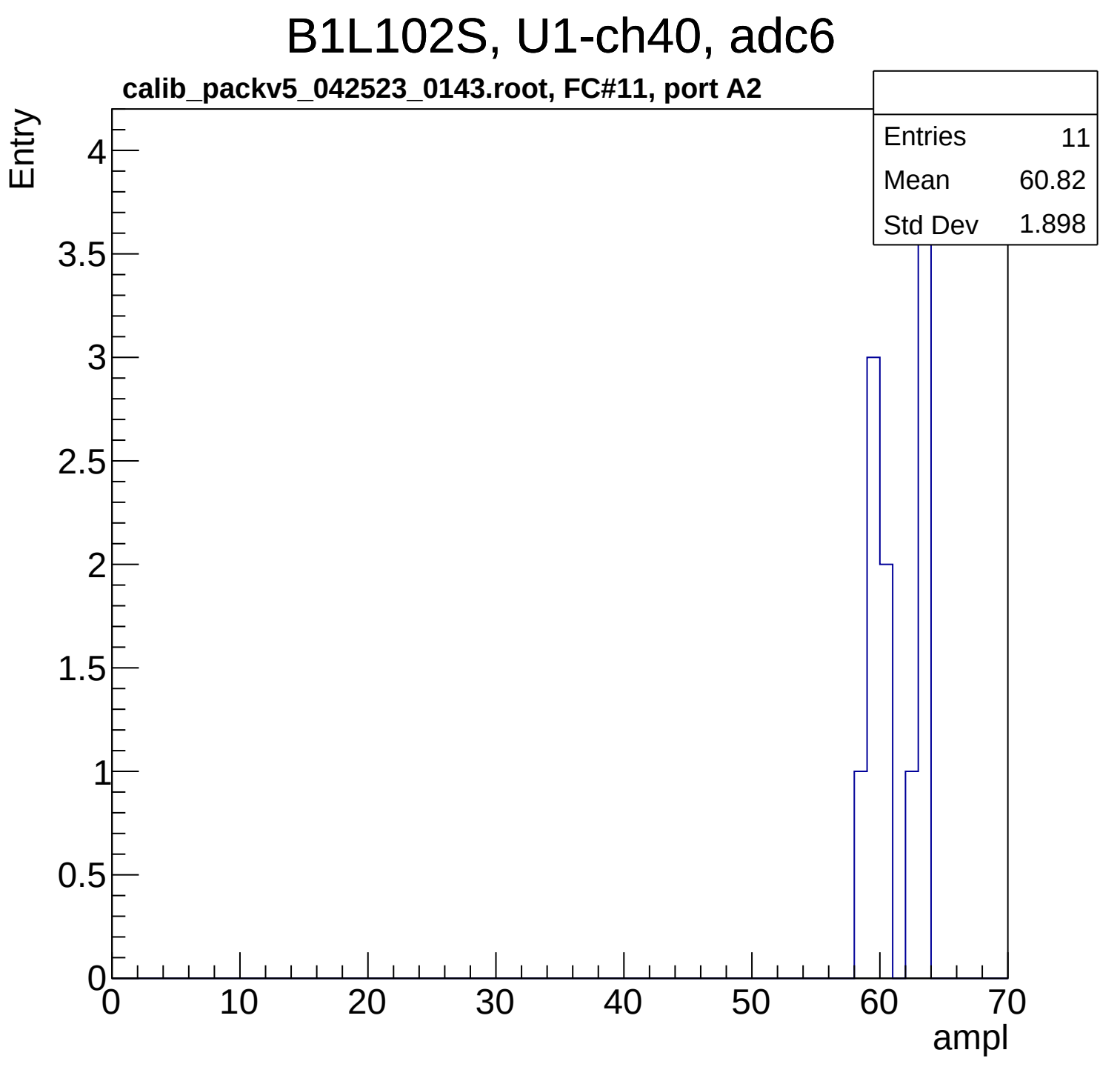
B1L102S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 11
Mean 60.82
Std Dev 1.898
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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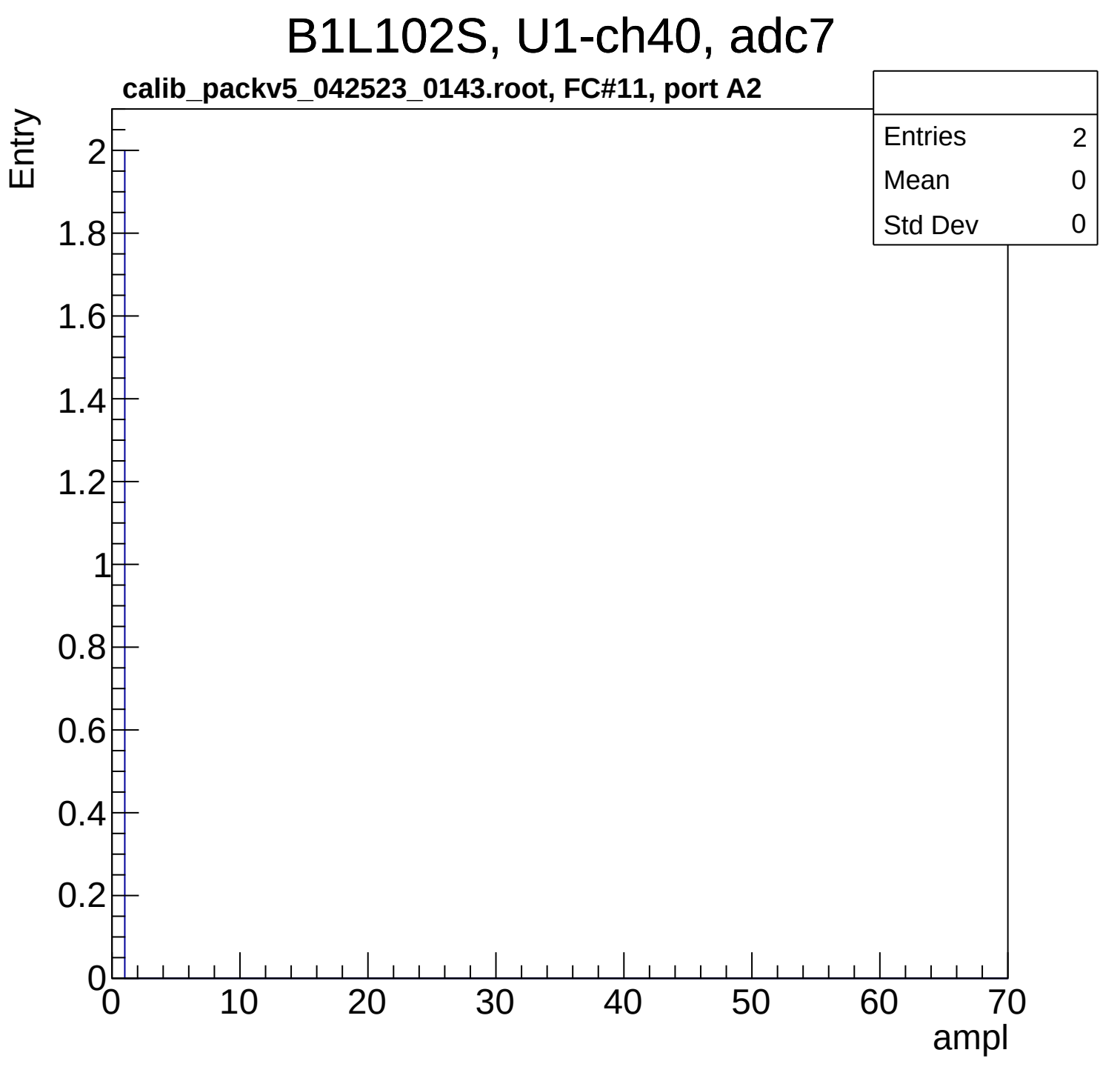
40
50
60
70


B1L102S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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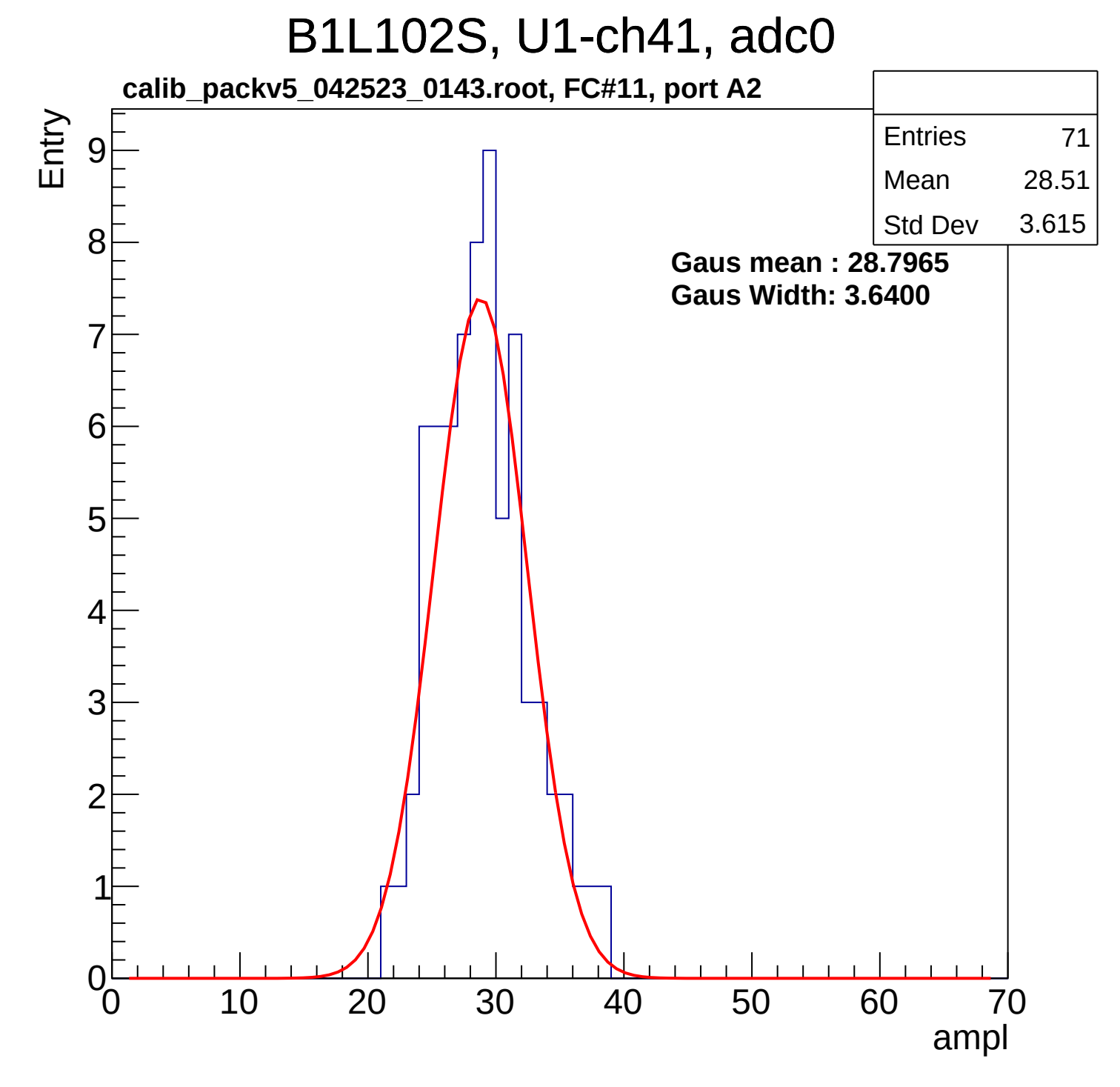
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.51
Std Dev 3.615
Gaus mean : 28.7965
Gaus Width: 3.6400
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
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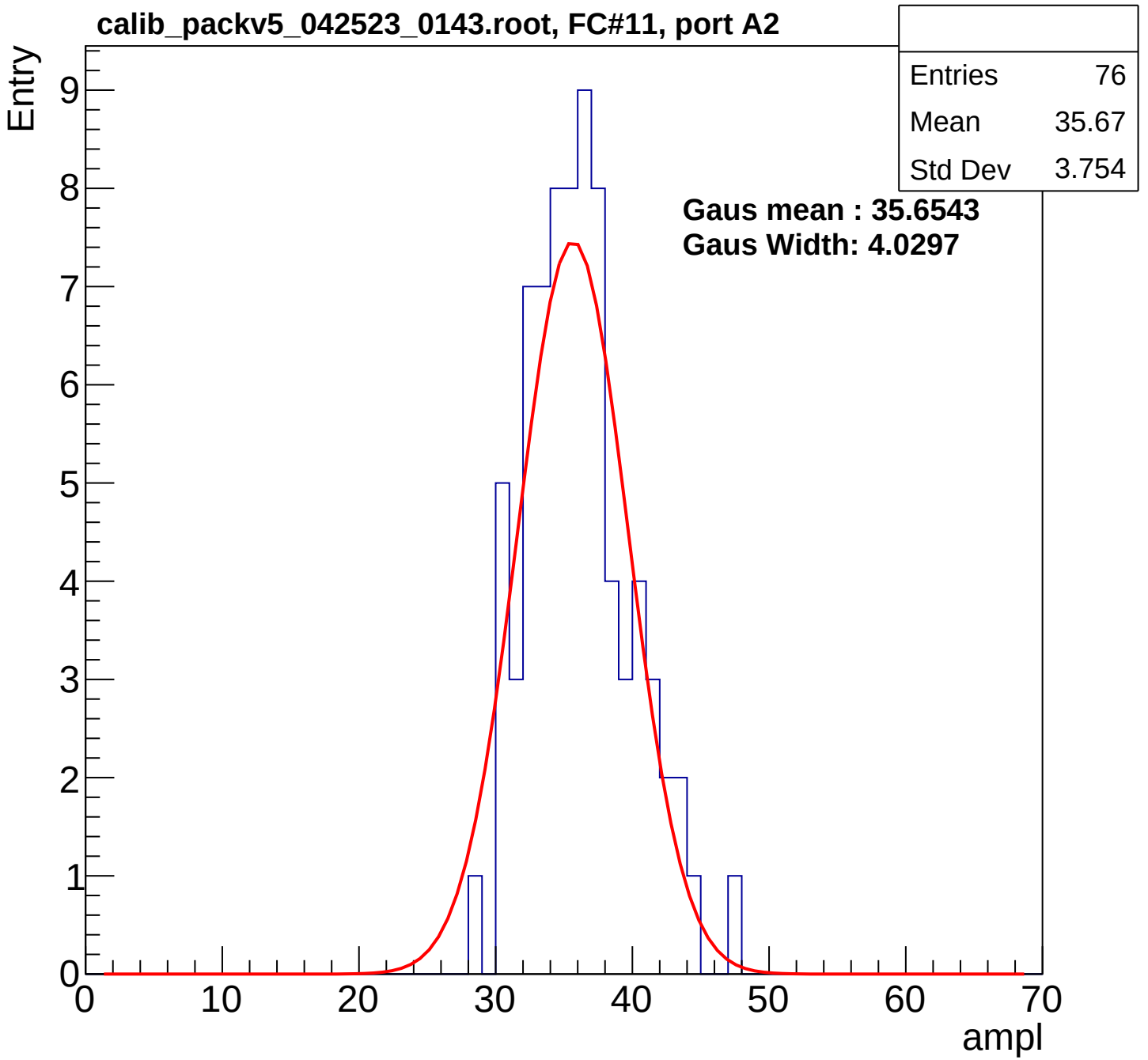
B1L102S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 35.67
Std Dev 3.754
Gaus mean : 35.6543
Gaus Width: 4.0297
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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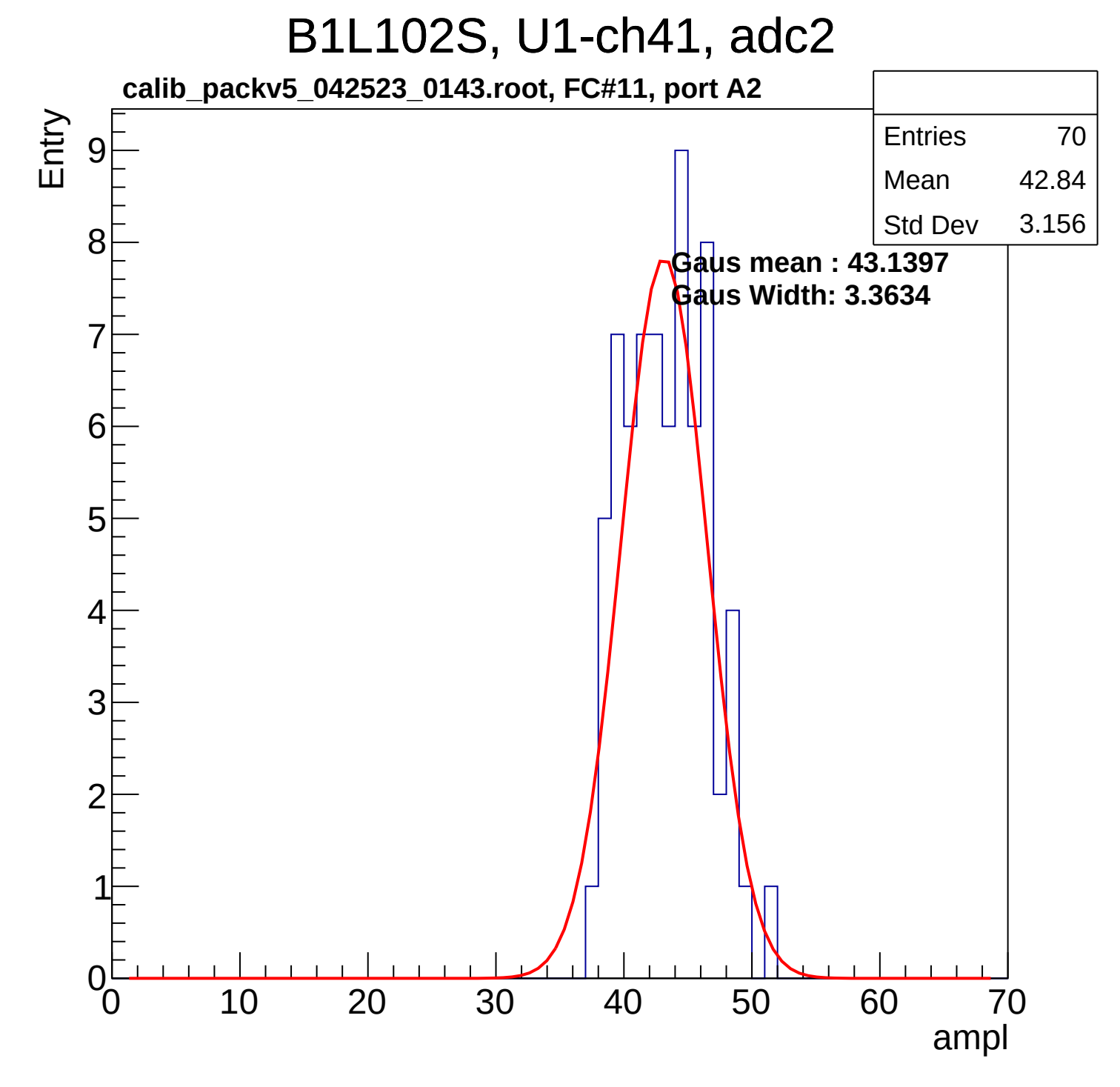
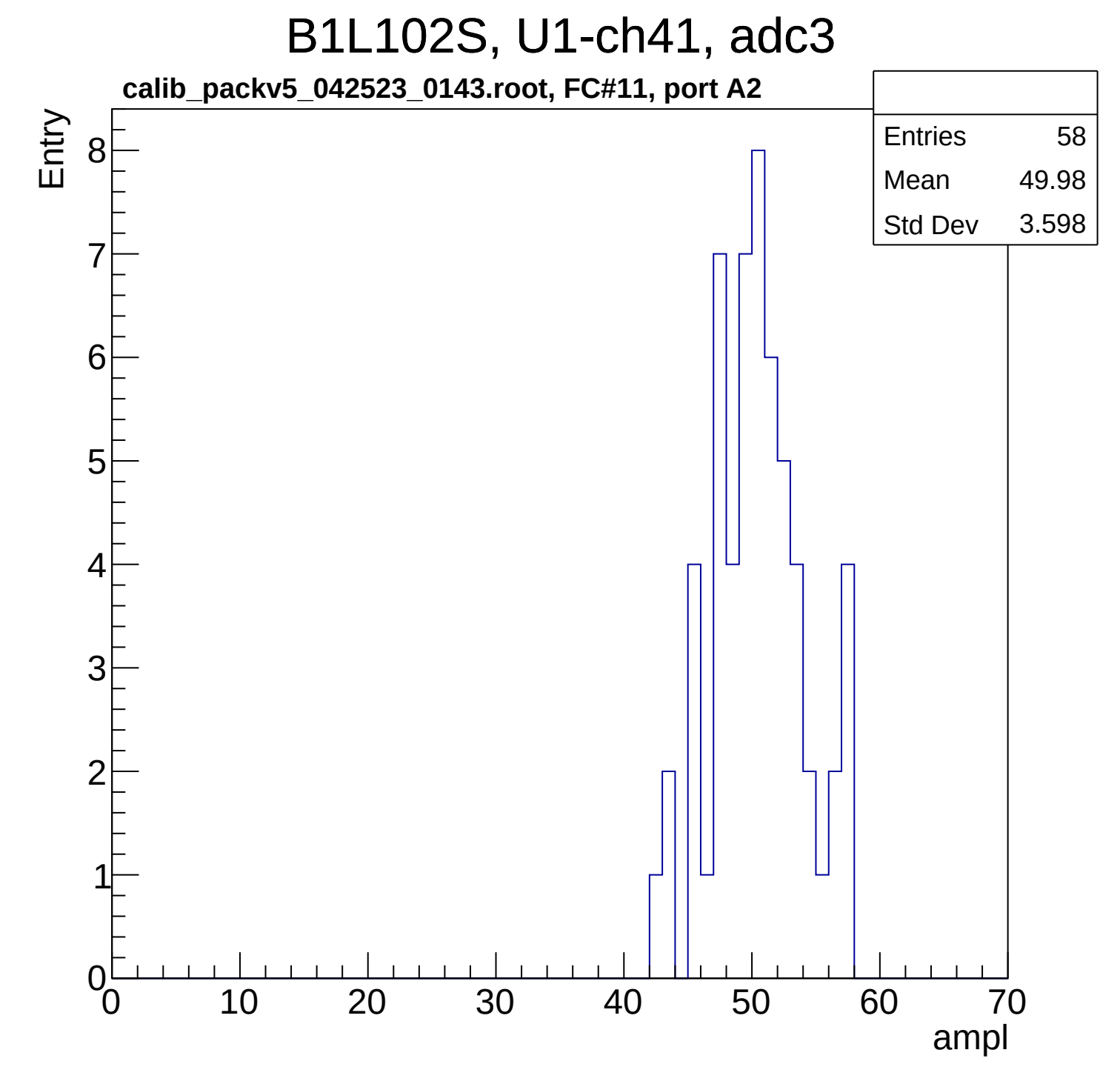

B1L102S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 42.84
Std Dev 3.156
Gaus mean : 43.1397
Gaus Width: 3.3634
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 49.98
Std Dev 3.598
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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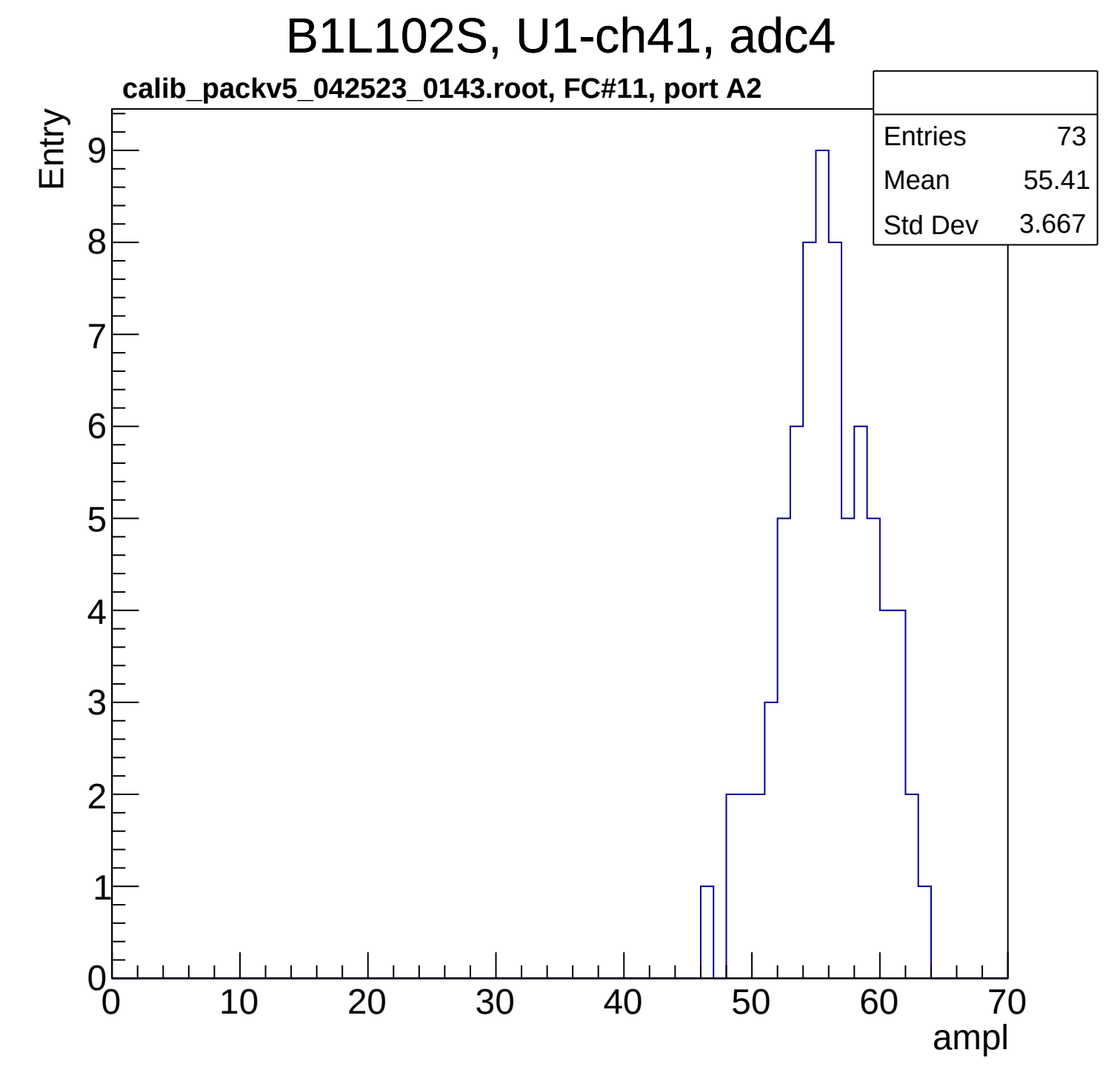
B1L102S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 55.41
Std Dev 3.667
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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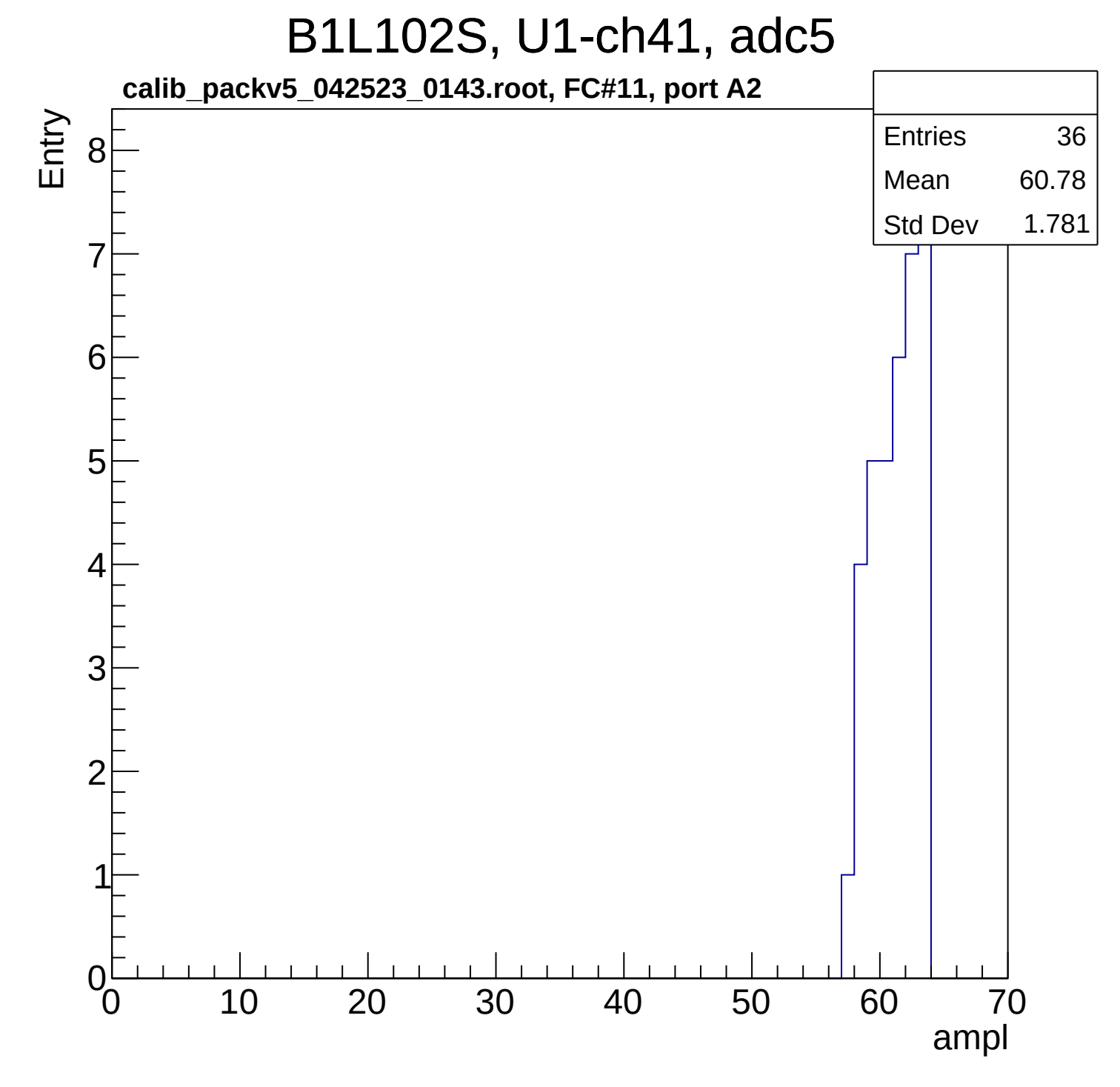
60
70


B1L102S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 36
Mean 60.78
Std Dev 1.781
Entry
ampl
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4
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7
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0
10
20
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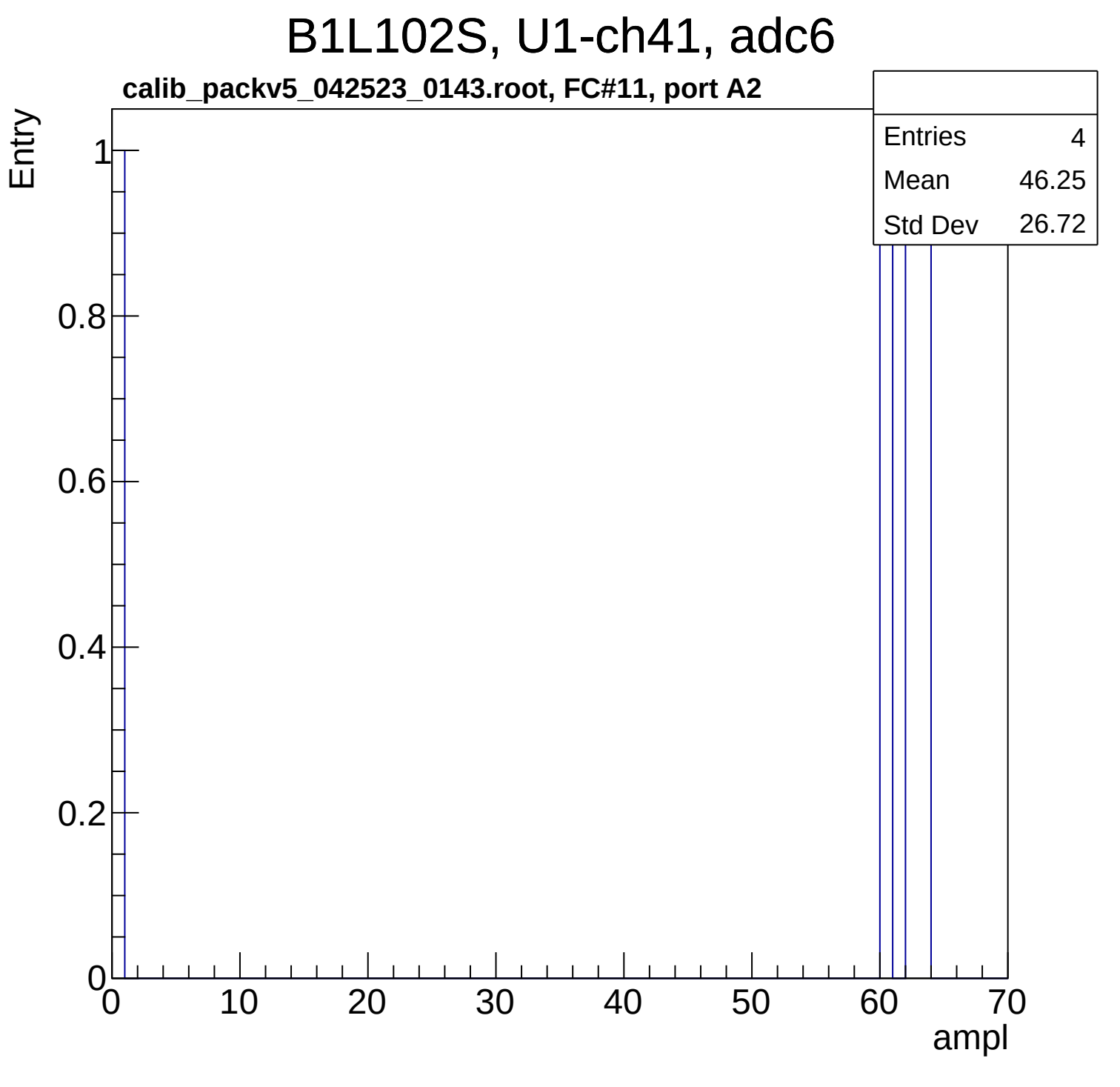
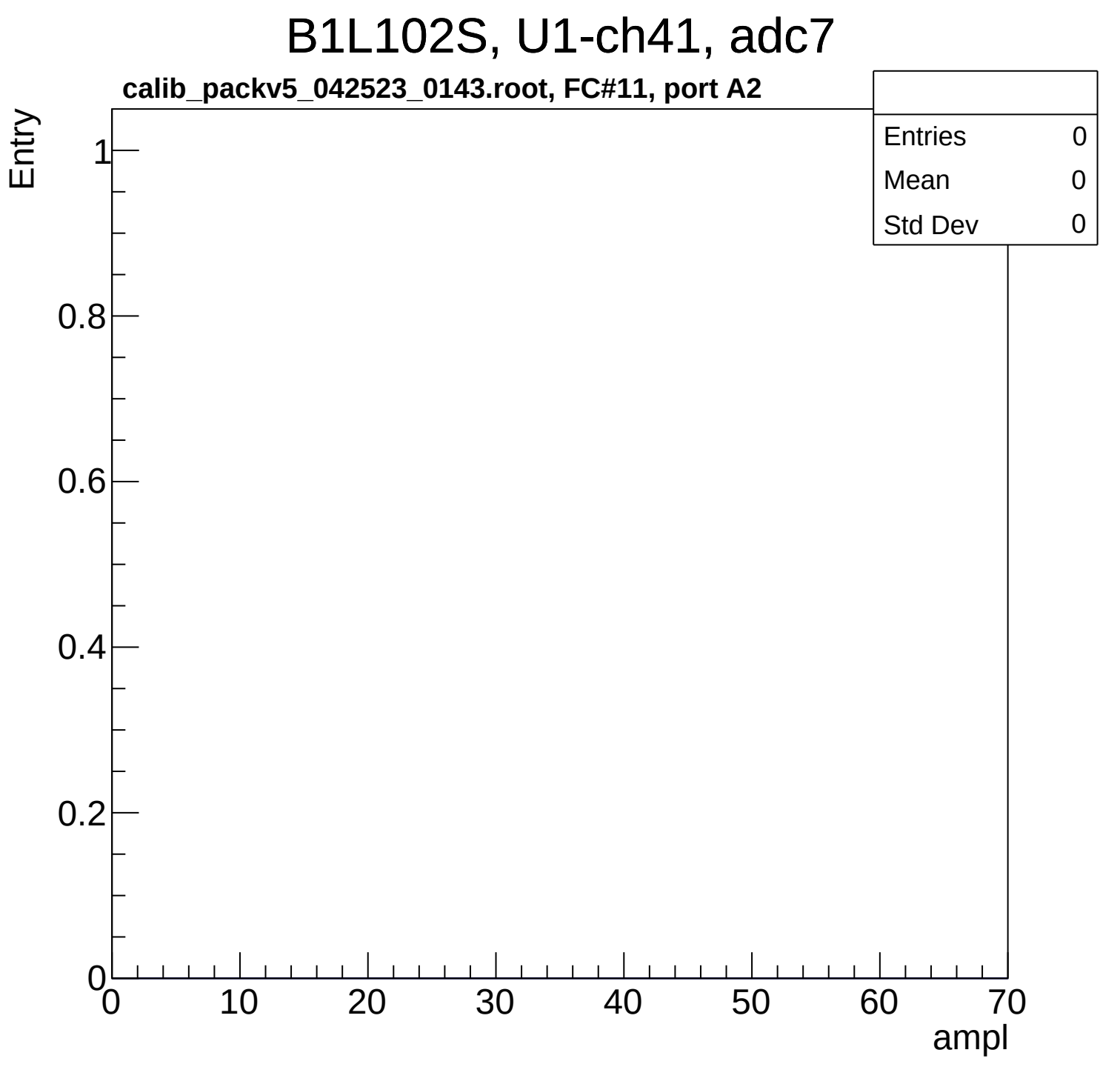
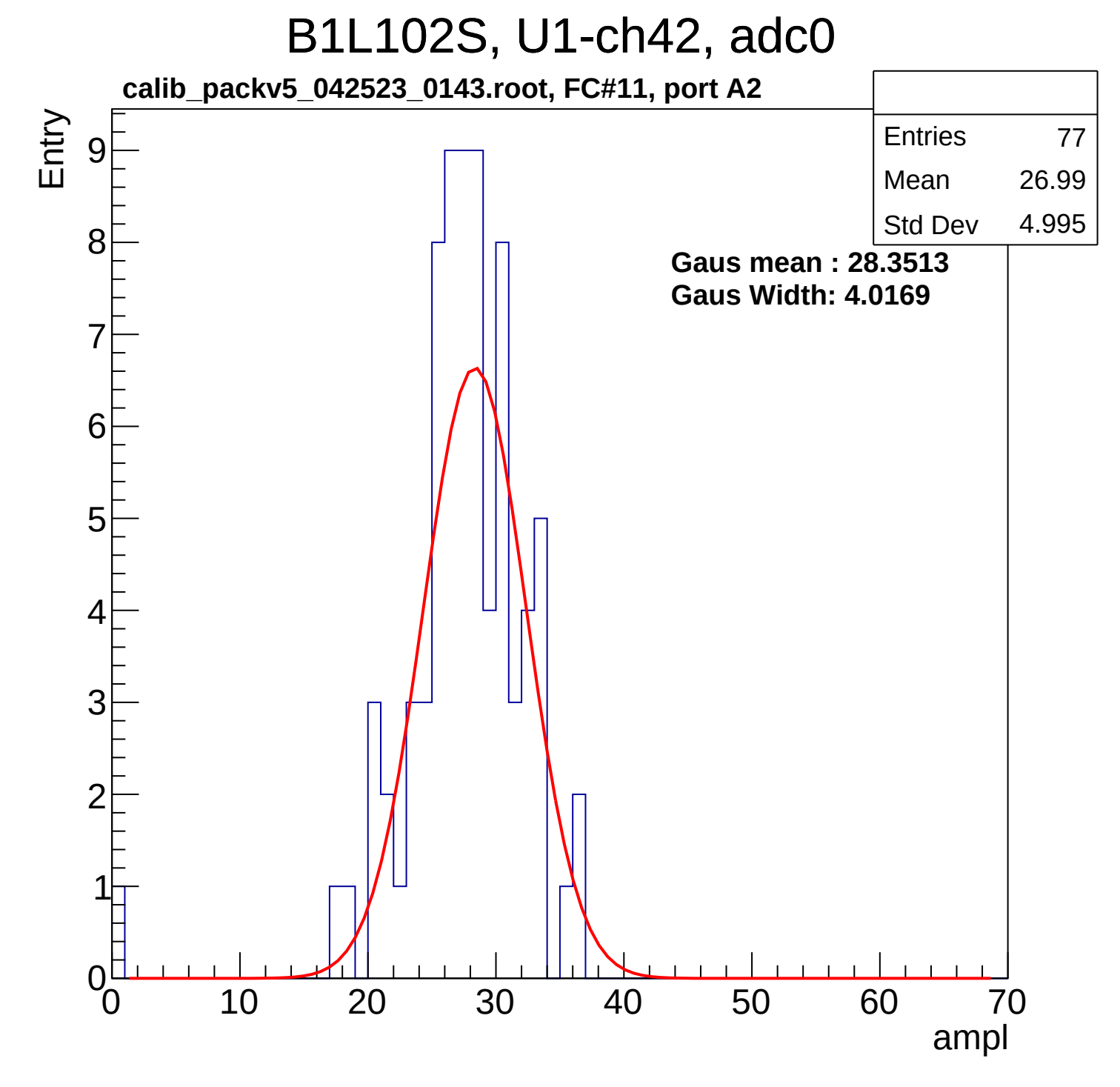
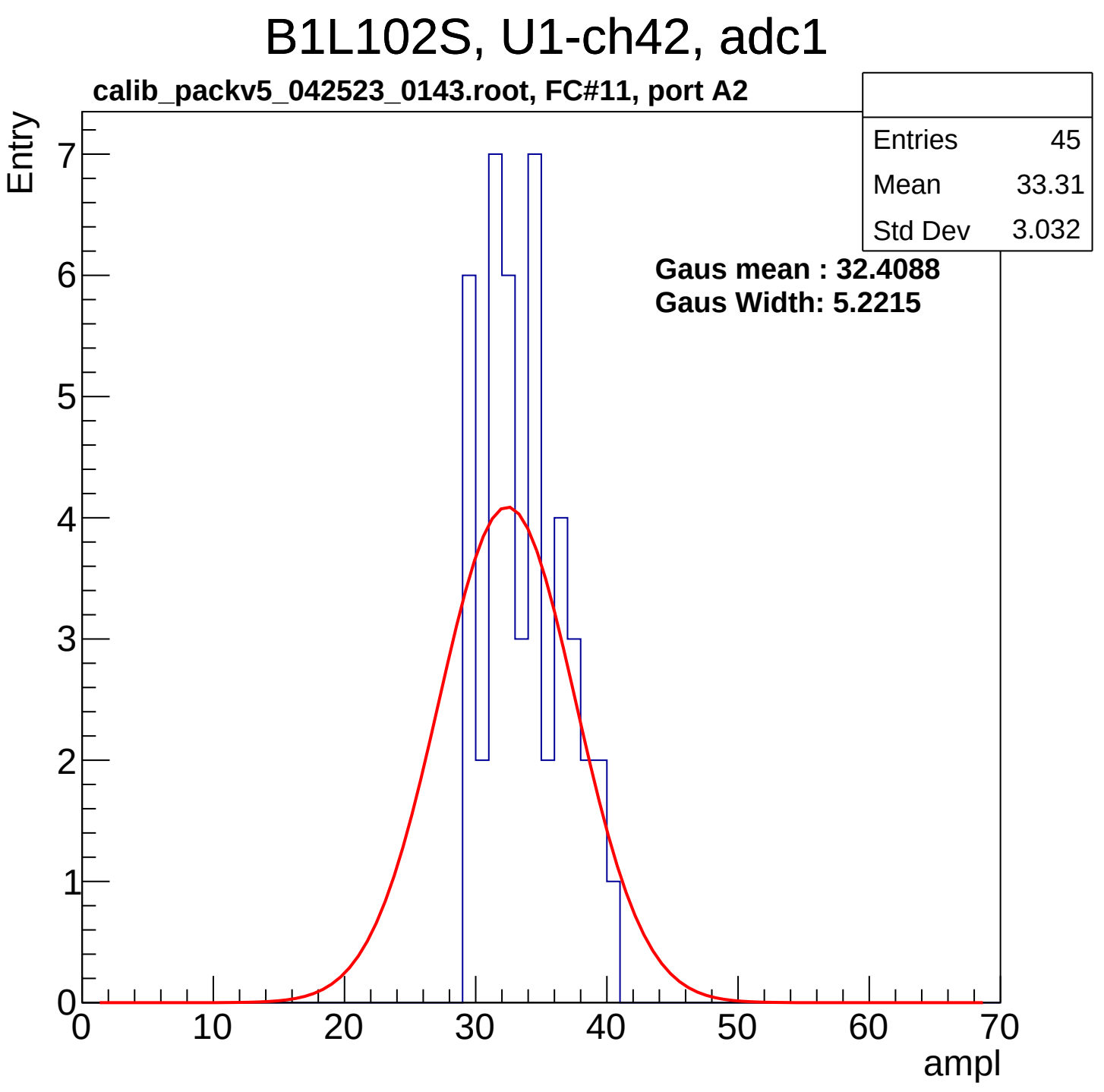
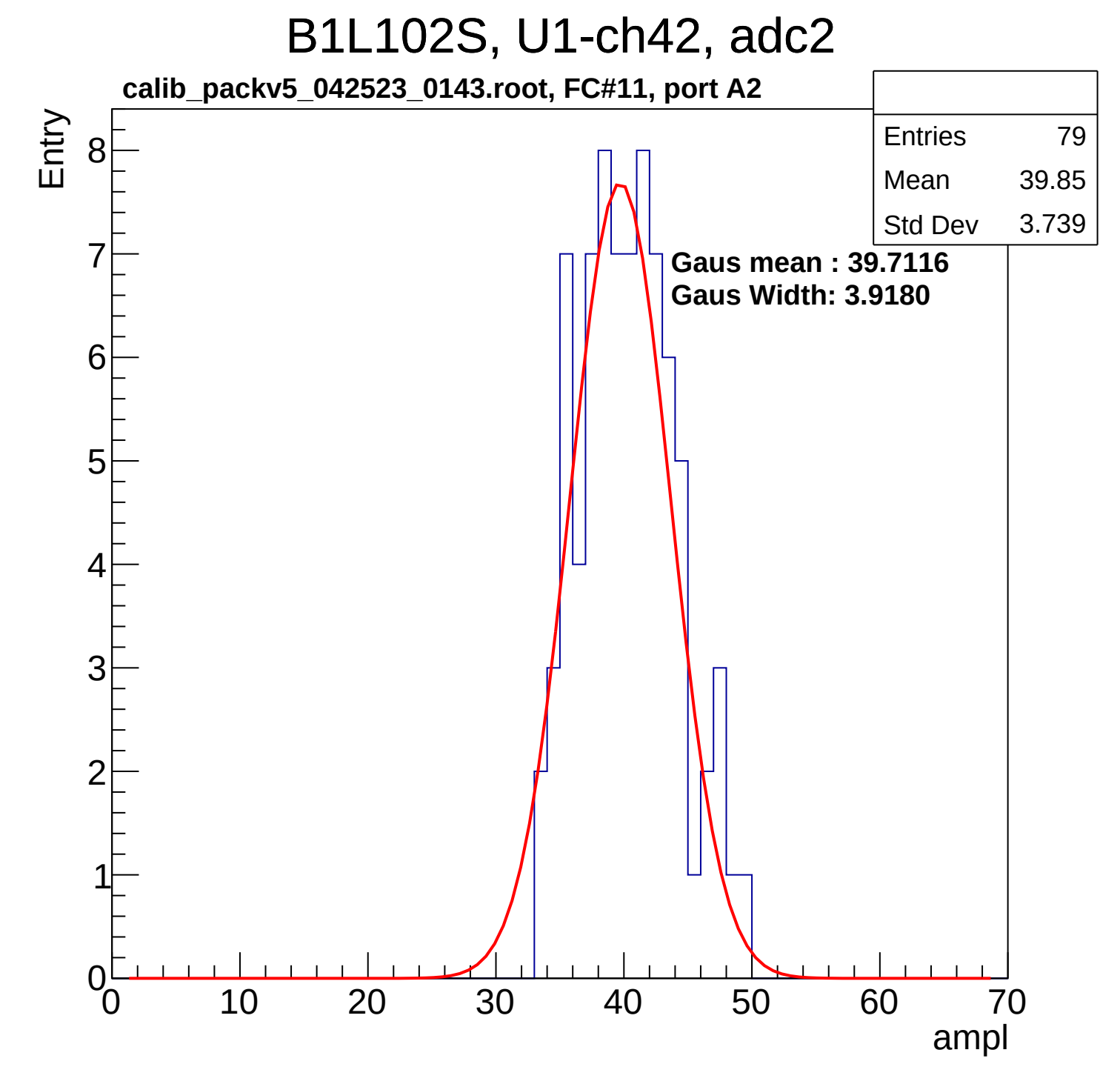
40
50
60
70

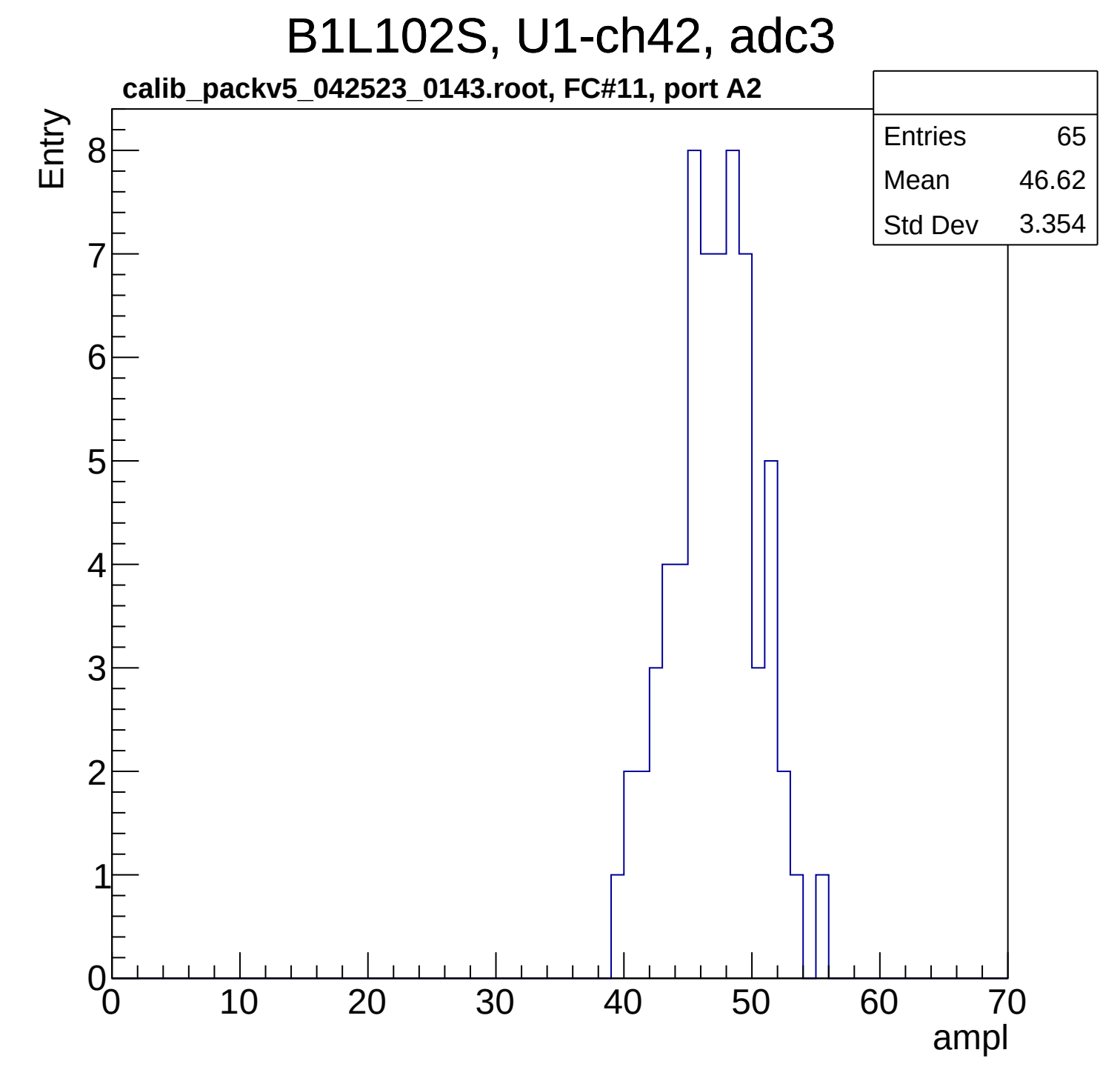

B1L102S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 46.25
Std Dev 26.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

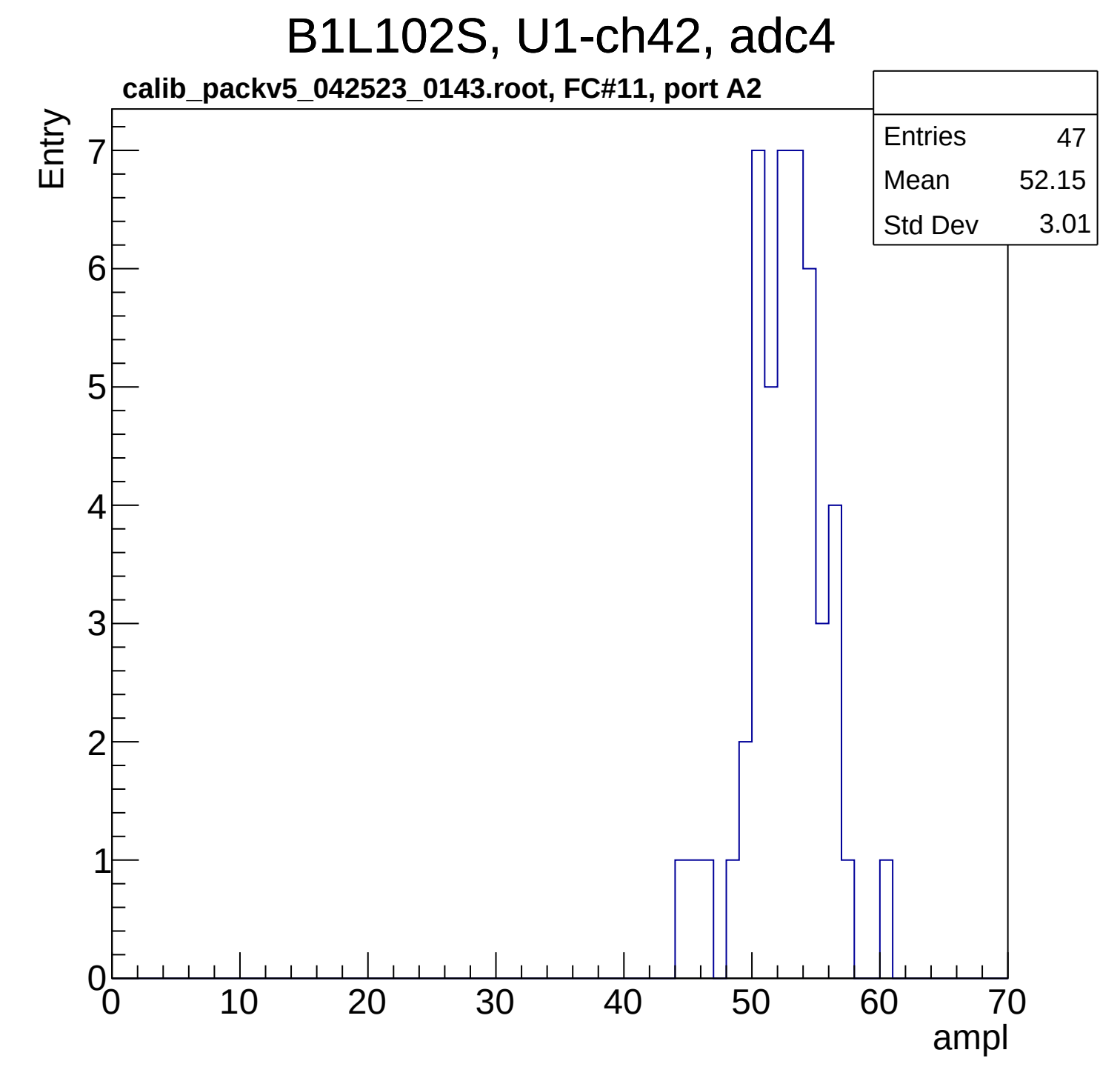

B1L102S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

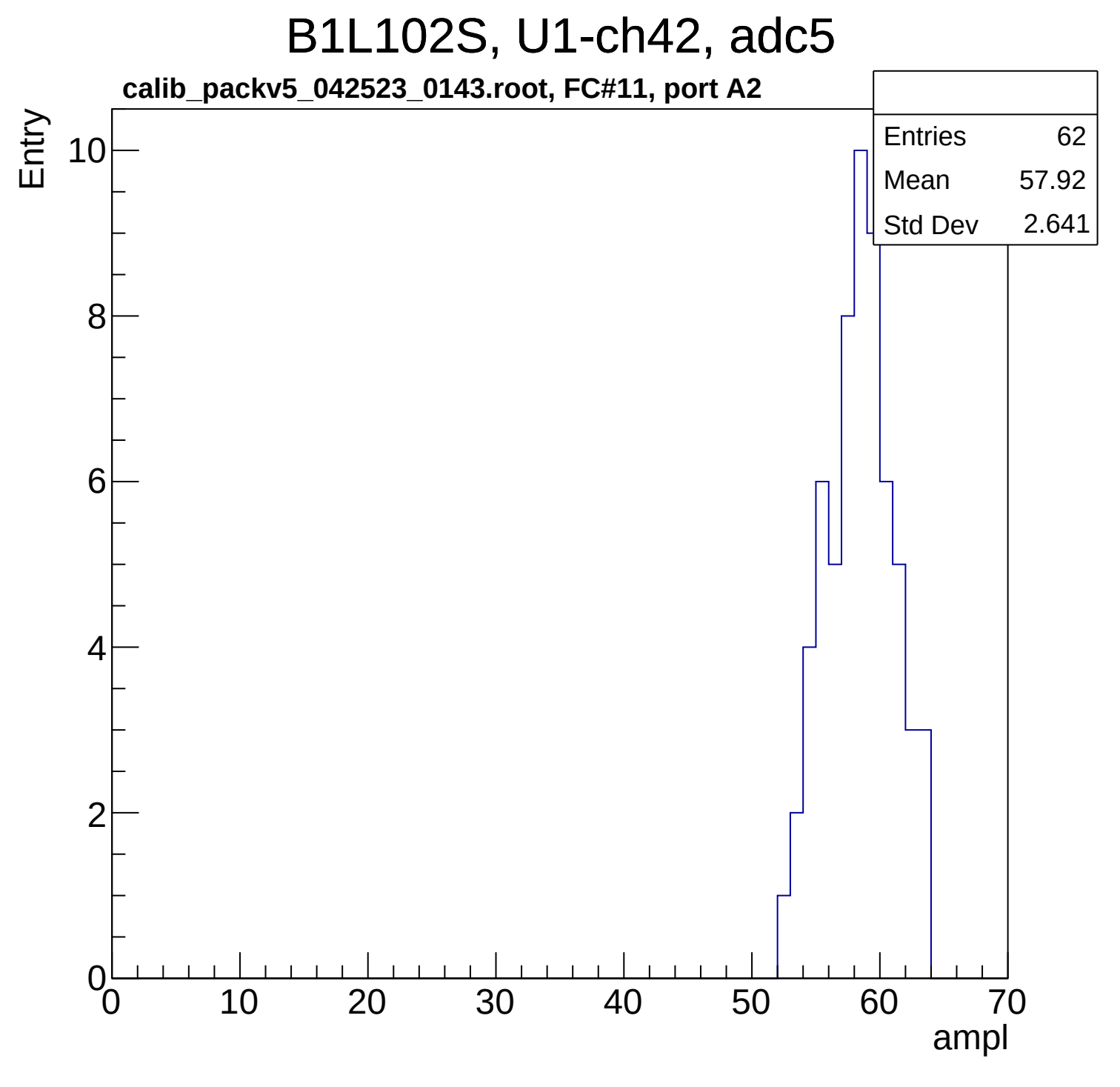

B1L102S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 26.99
Std Dev 4.995
Gaus mean : 28.3513
Gaus Width: 4.0169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

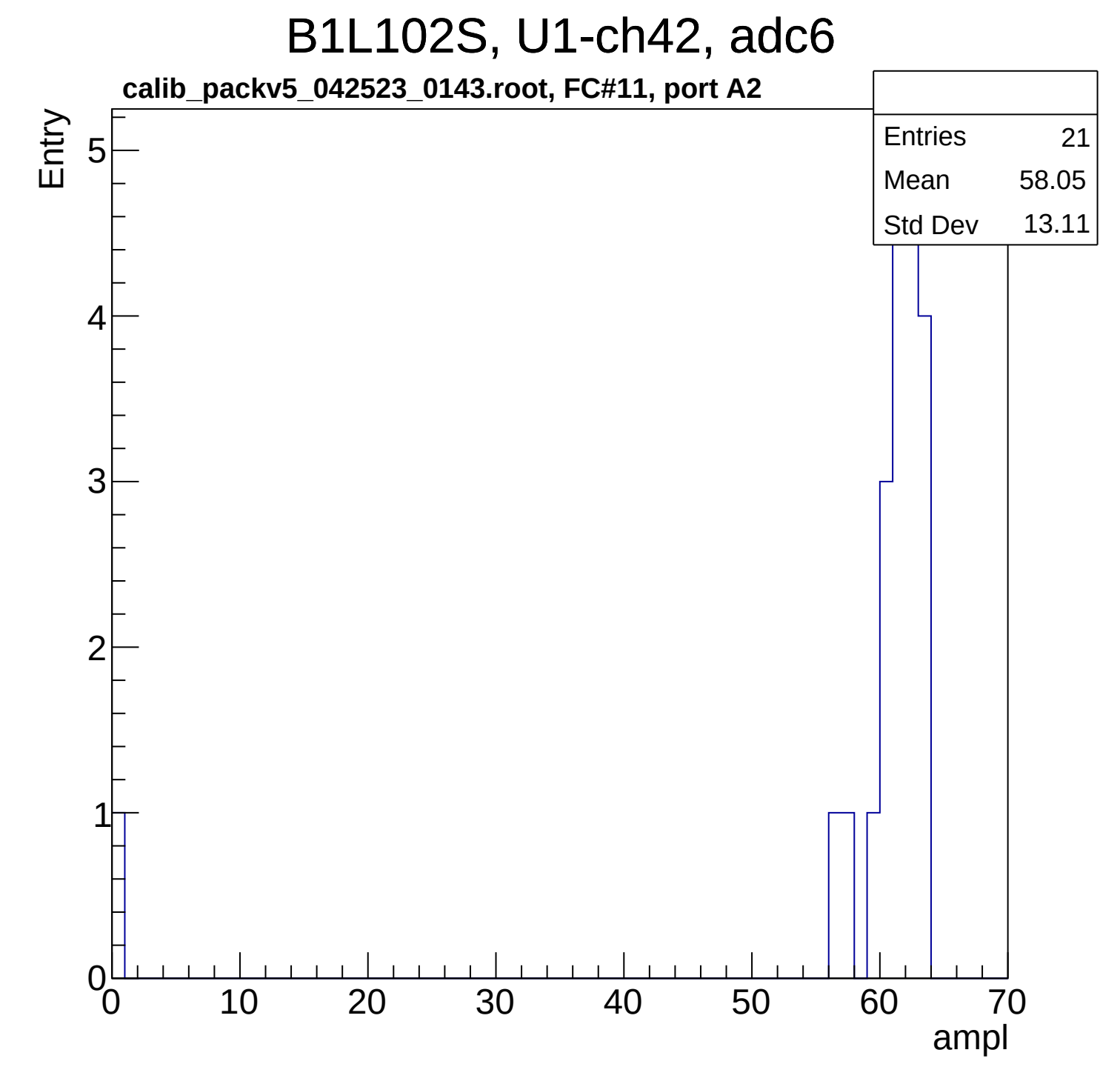

B1L102S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 33.31
Std Dev 3.032
Gaus mean : 32.4088
Gaus Width: 5.2215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

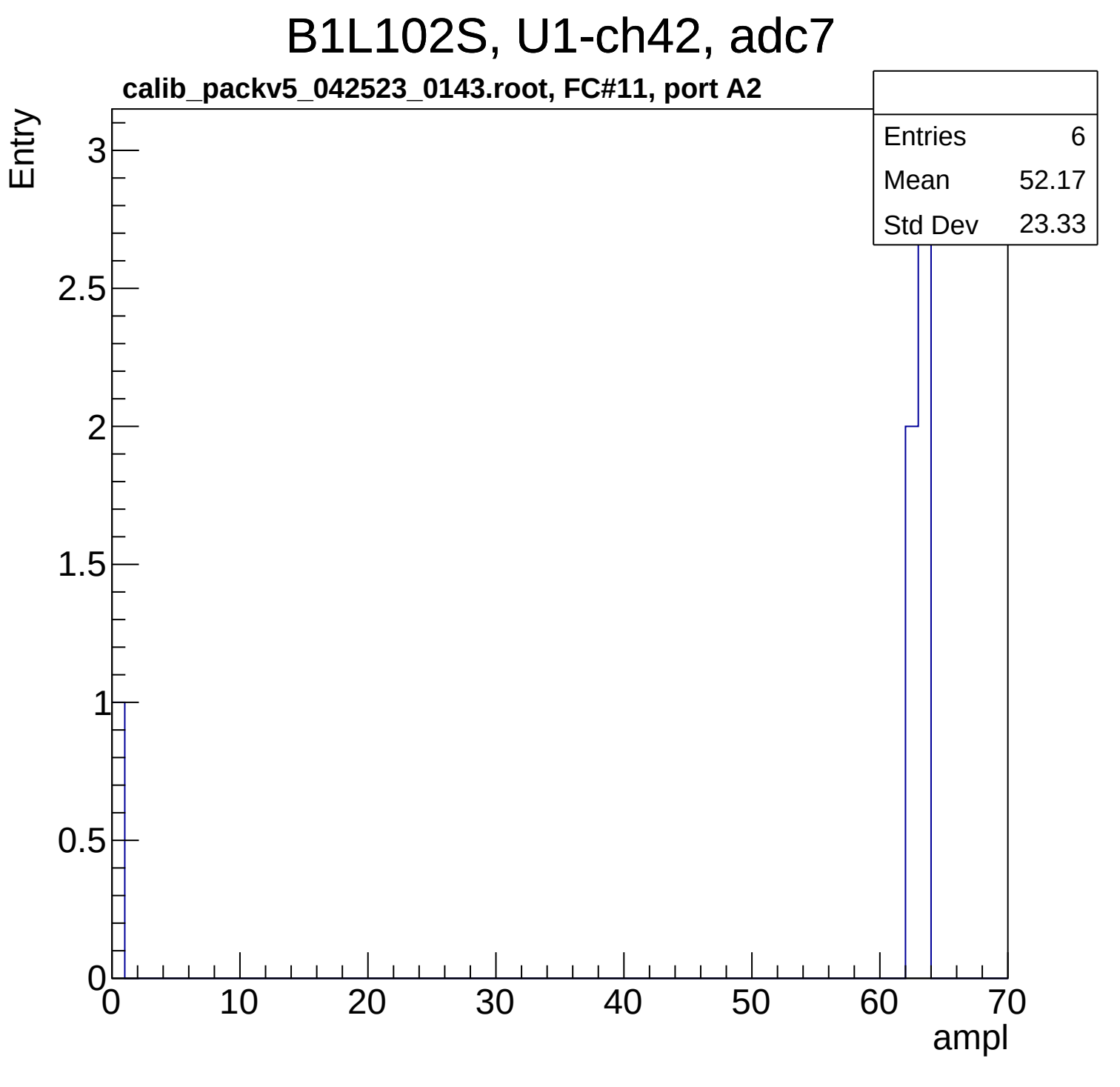

B1L102S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 39.85
Std Dev 3.739
Gaus mean : 39.7116
Gaus Width: 3.9180
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

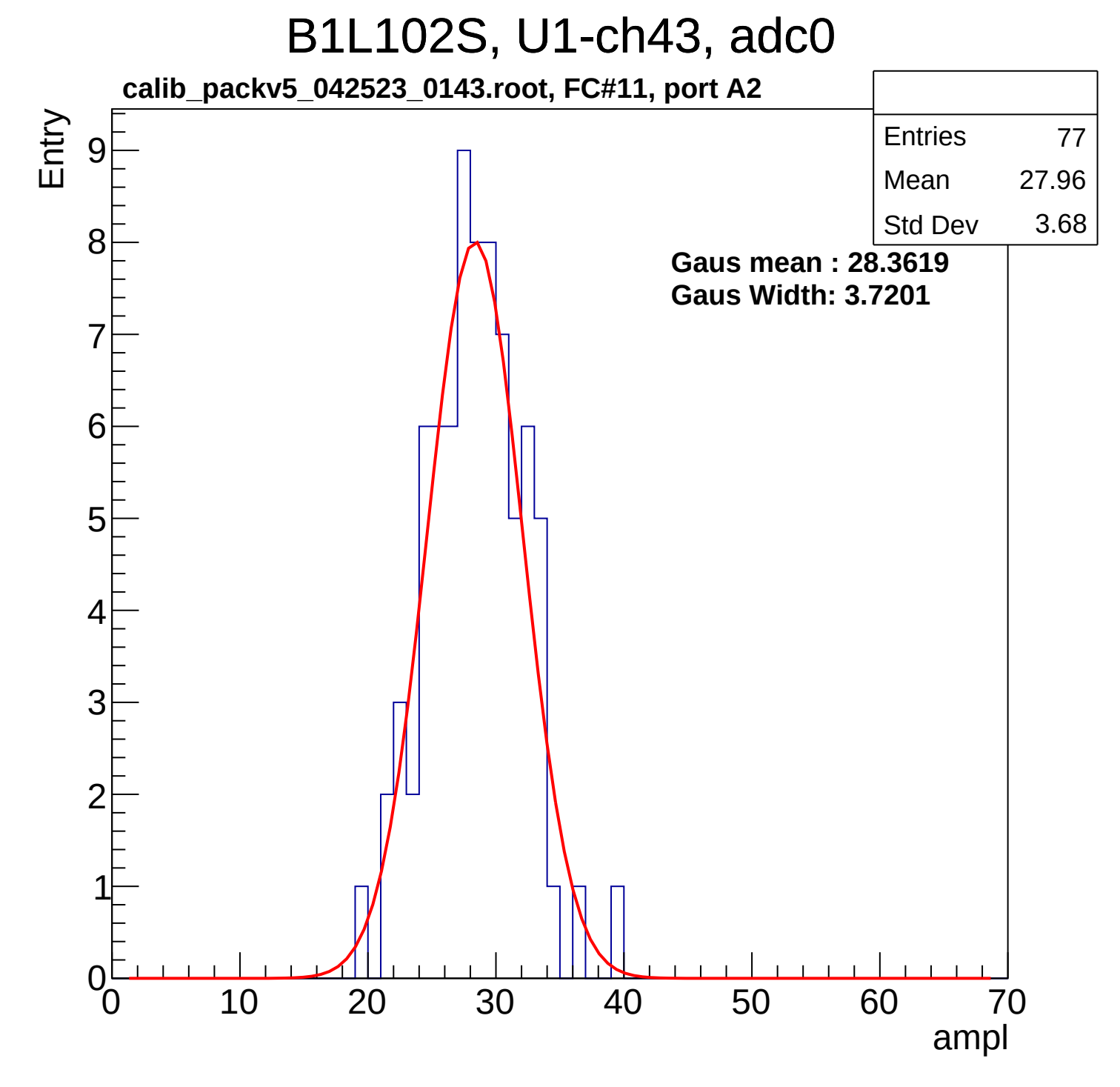

B1L102S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 46.62
Std Dev 3.354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

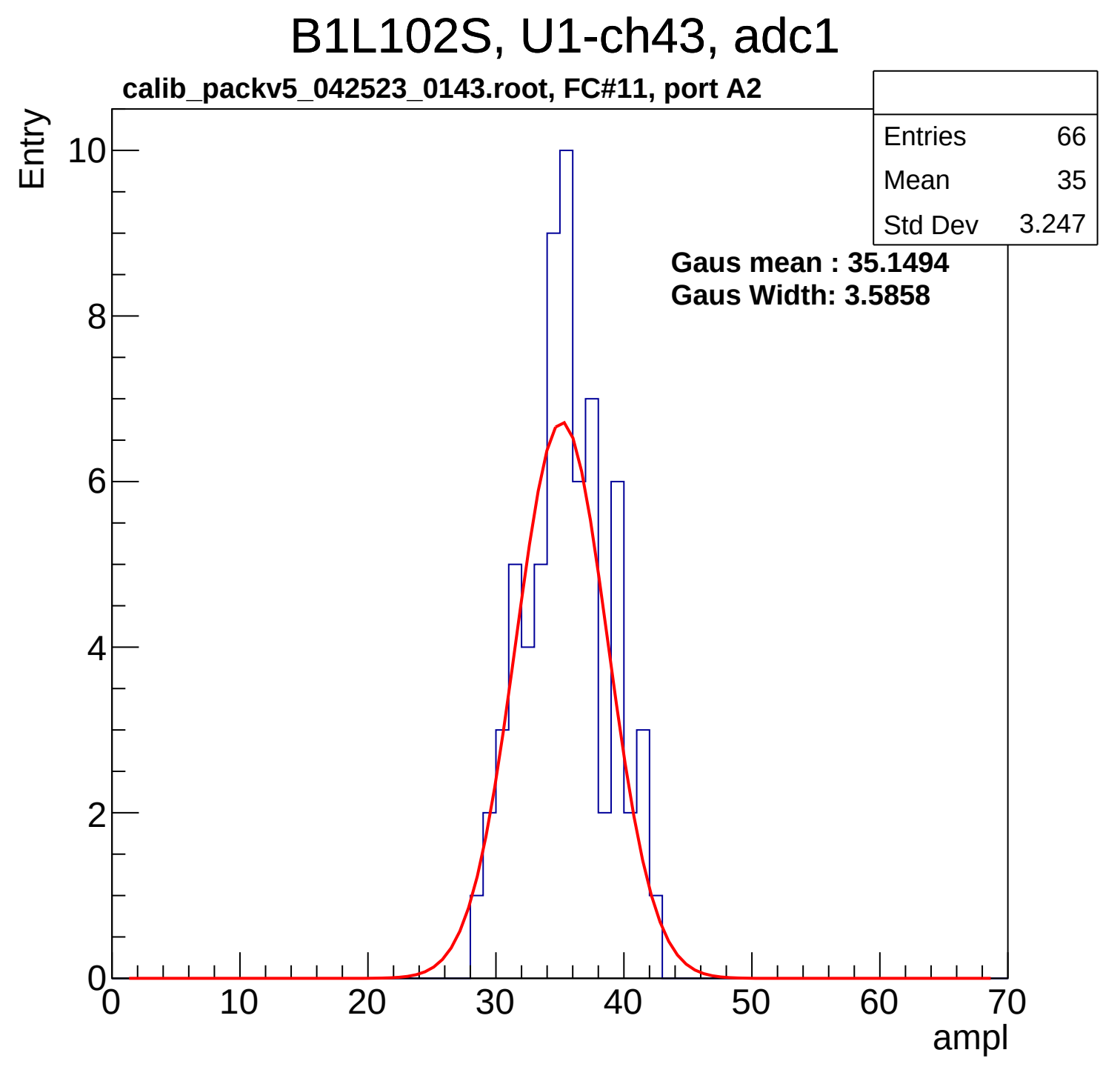

B1L102S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 52.15
Std Dev 3.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

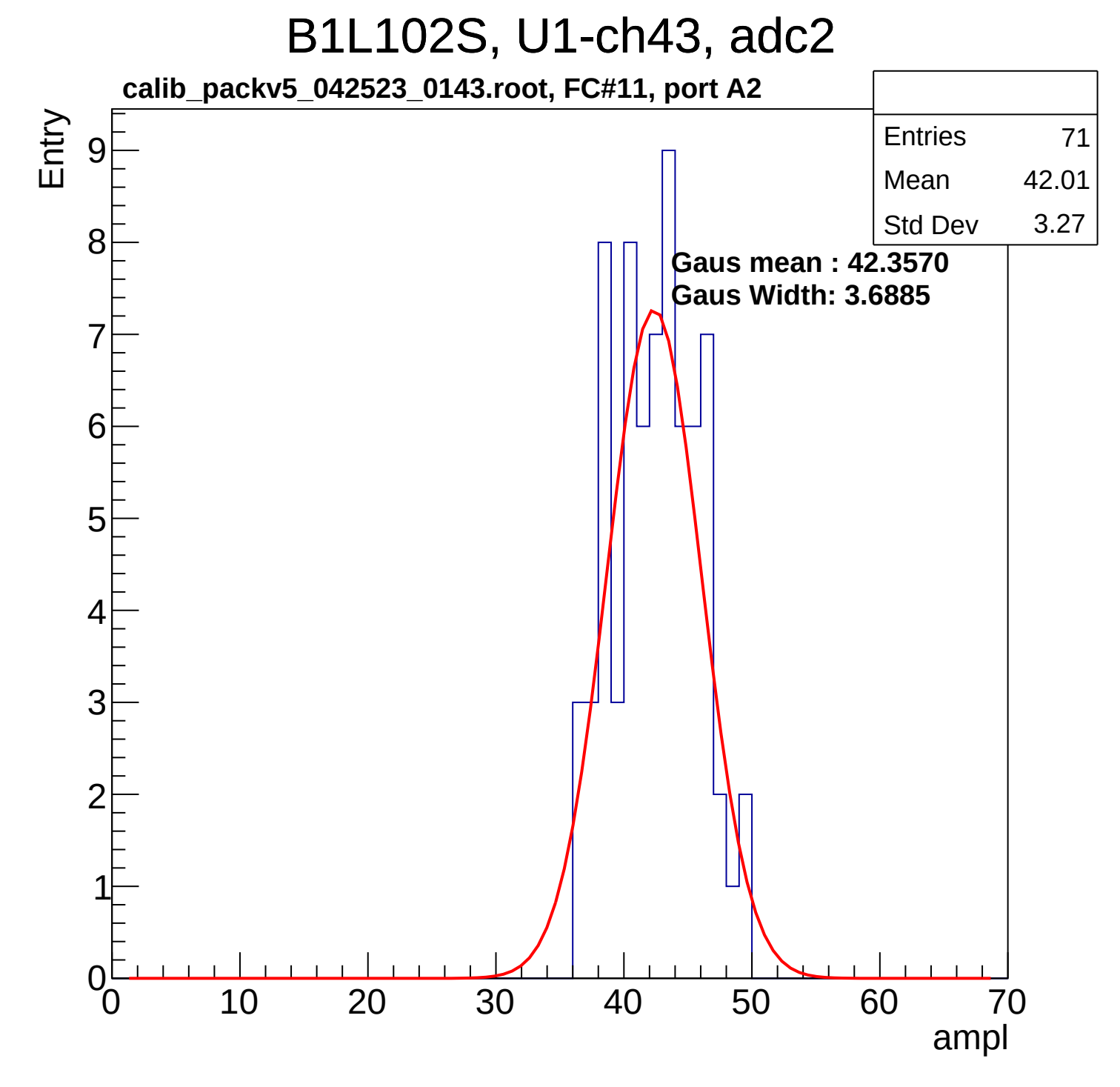

B1L102S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 57.92
Std Dev 2.641
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

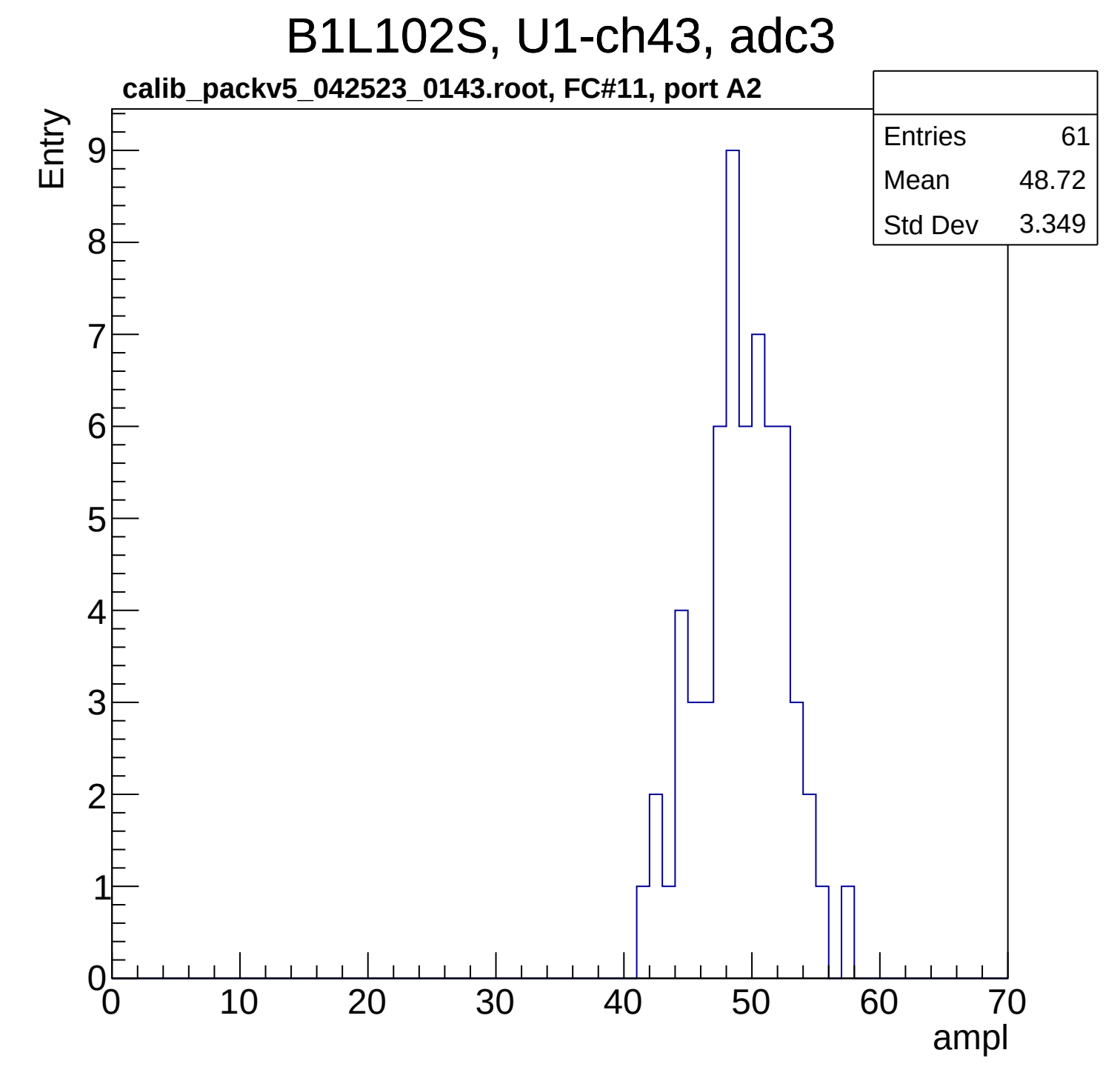

B1L102S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 21
Mean 58.05
Std Dev 13.11
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

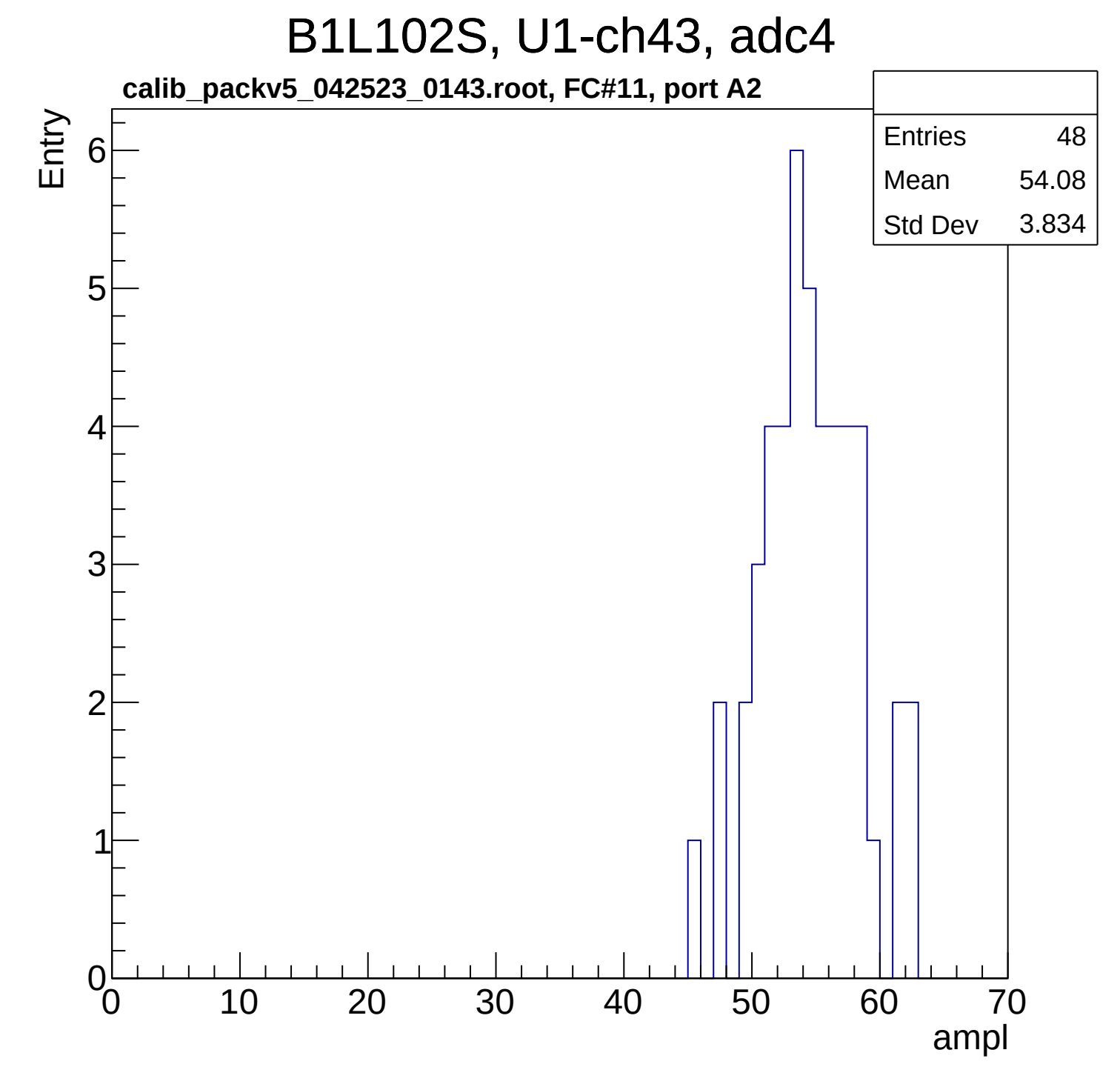

B1L102S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 52.17
Std Dev 23.33
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

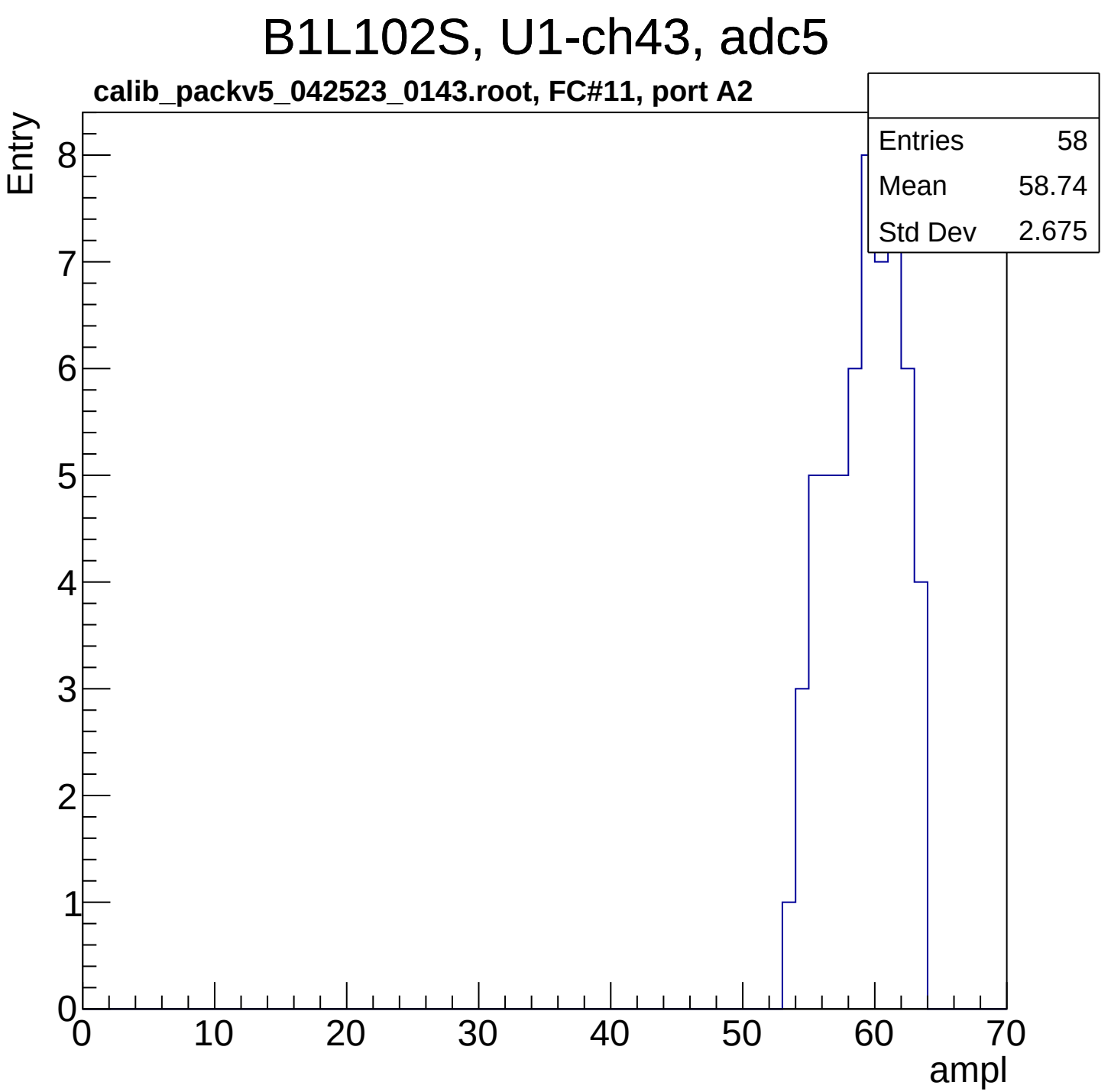

B1L102S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 27.96
Std Dev 3.68
Gaus mean : 28.3619
Gaus Width: 3.7201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 35
Std Dev 3.247
Gaus mean : 35.1494
Gaus Width: 3.5858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.01
Std Dev 3.27
Gaus mean : 42.3570
Gaus Width: 3.6885
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 48.72
Std Dev 3.349
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 54.08
Std Dev 3.834
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 58.74
Std Dev 2.675
Entry
ampl
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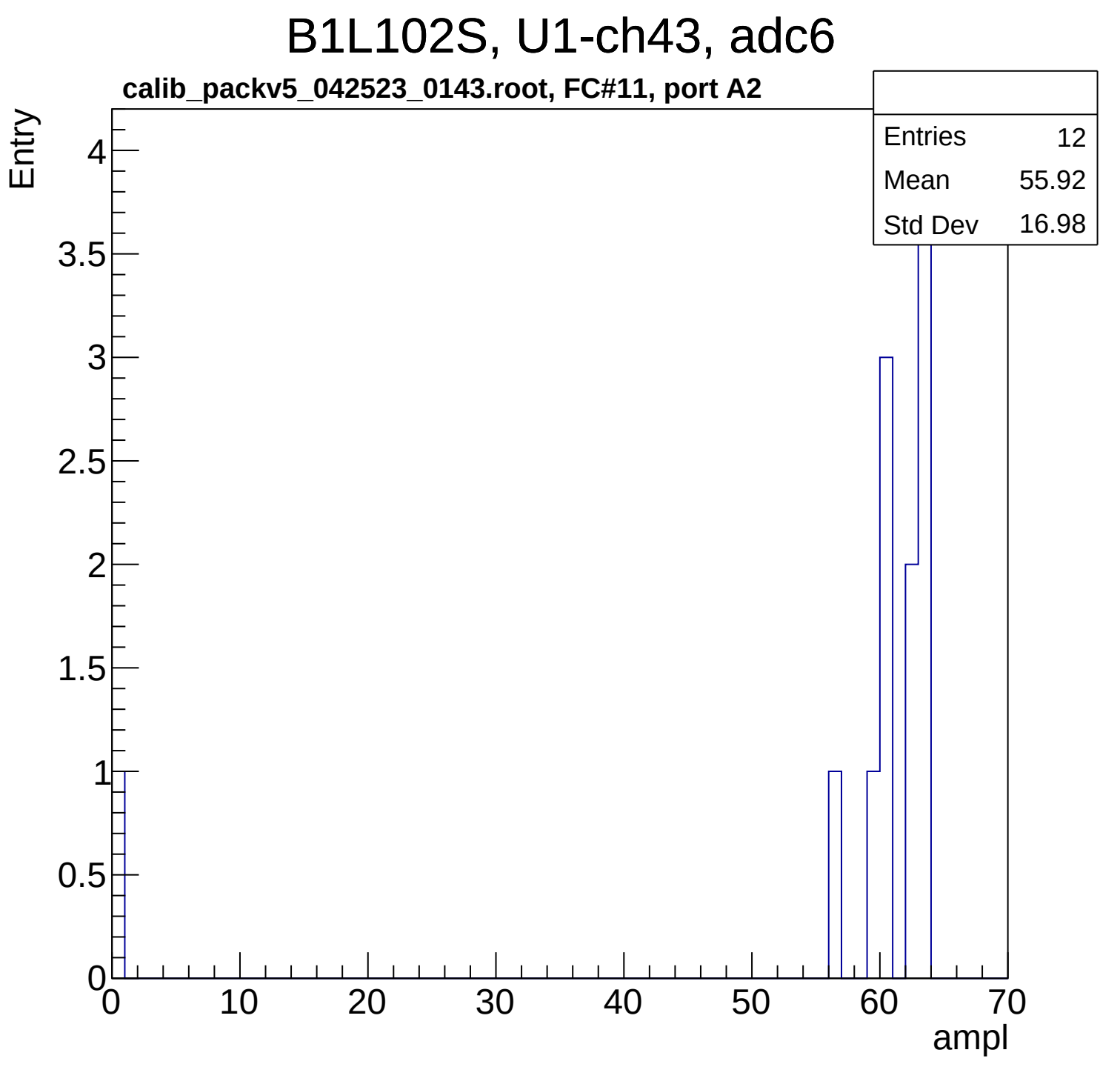
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 55.92
Std Dev 16.98
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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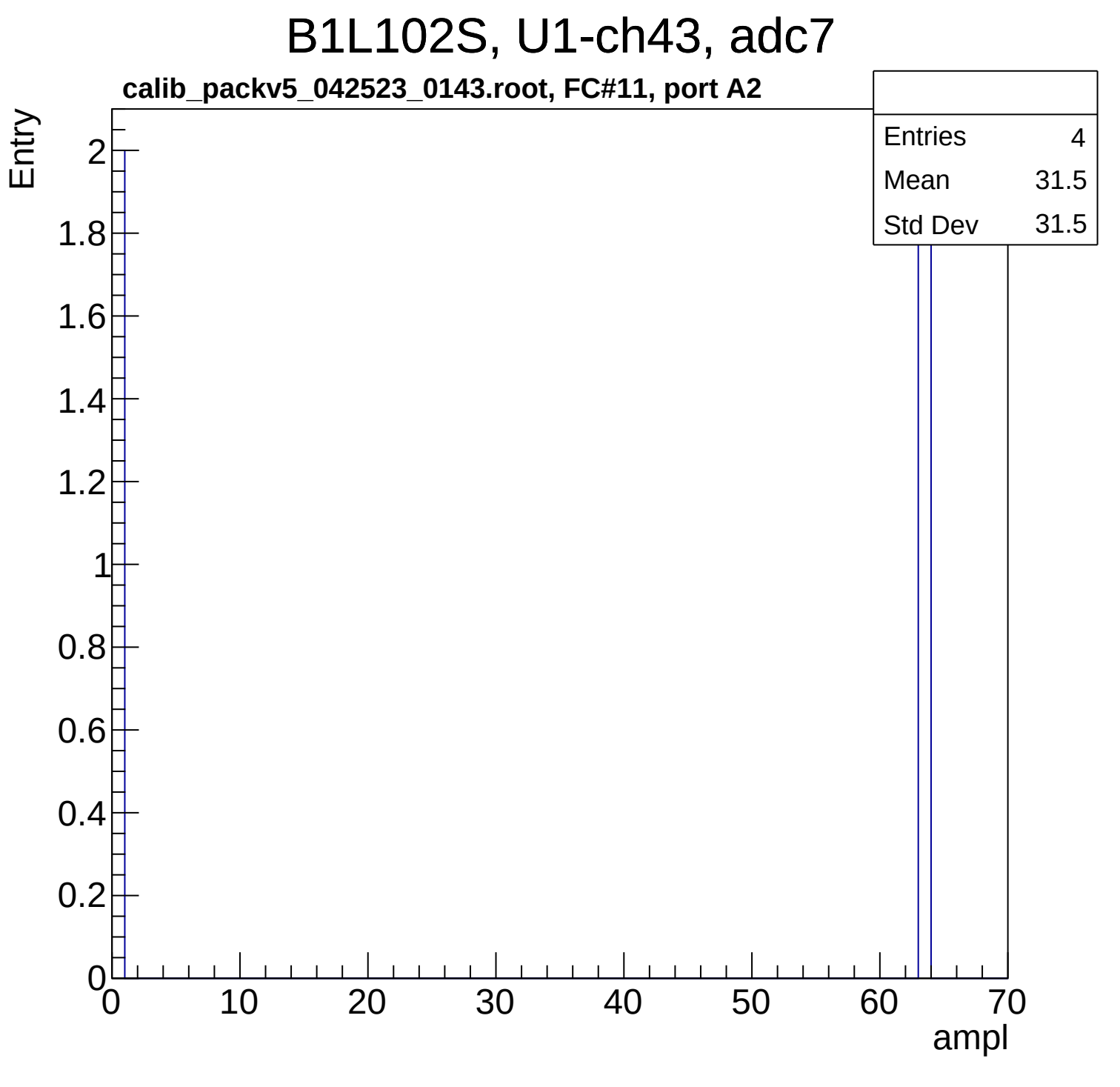
40
50
60
70


B1L102S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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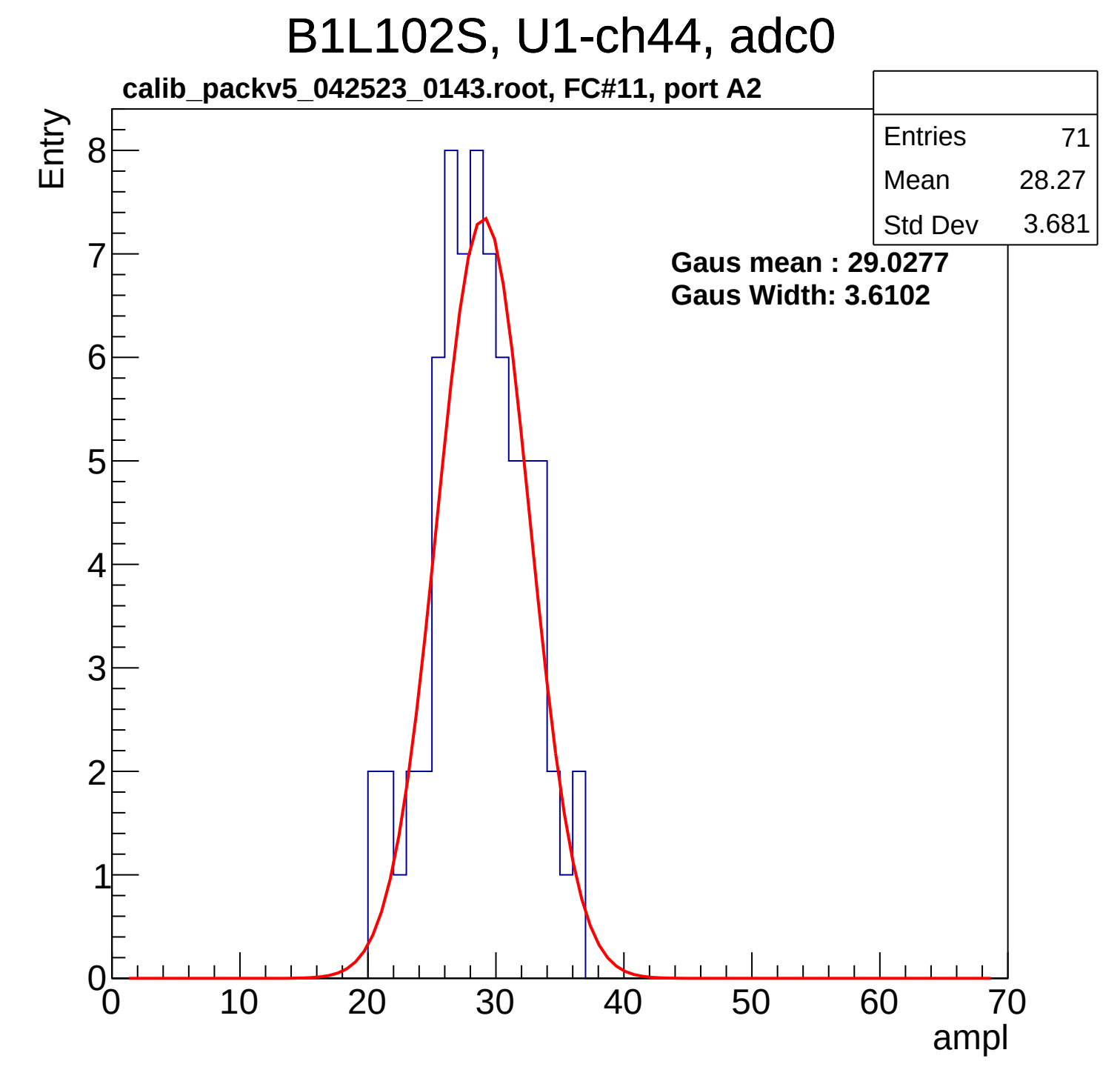
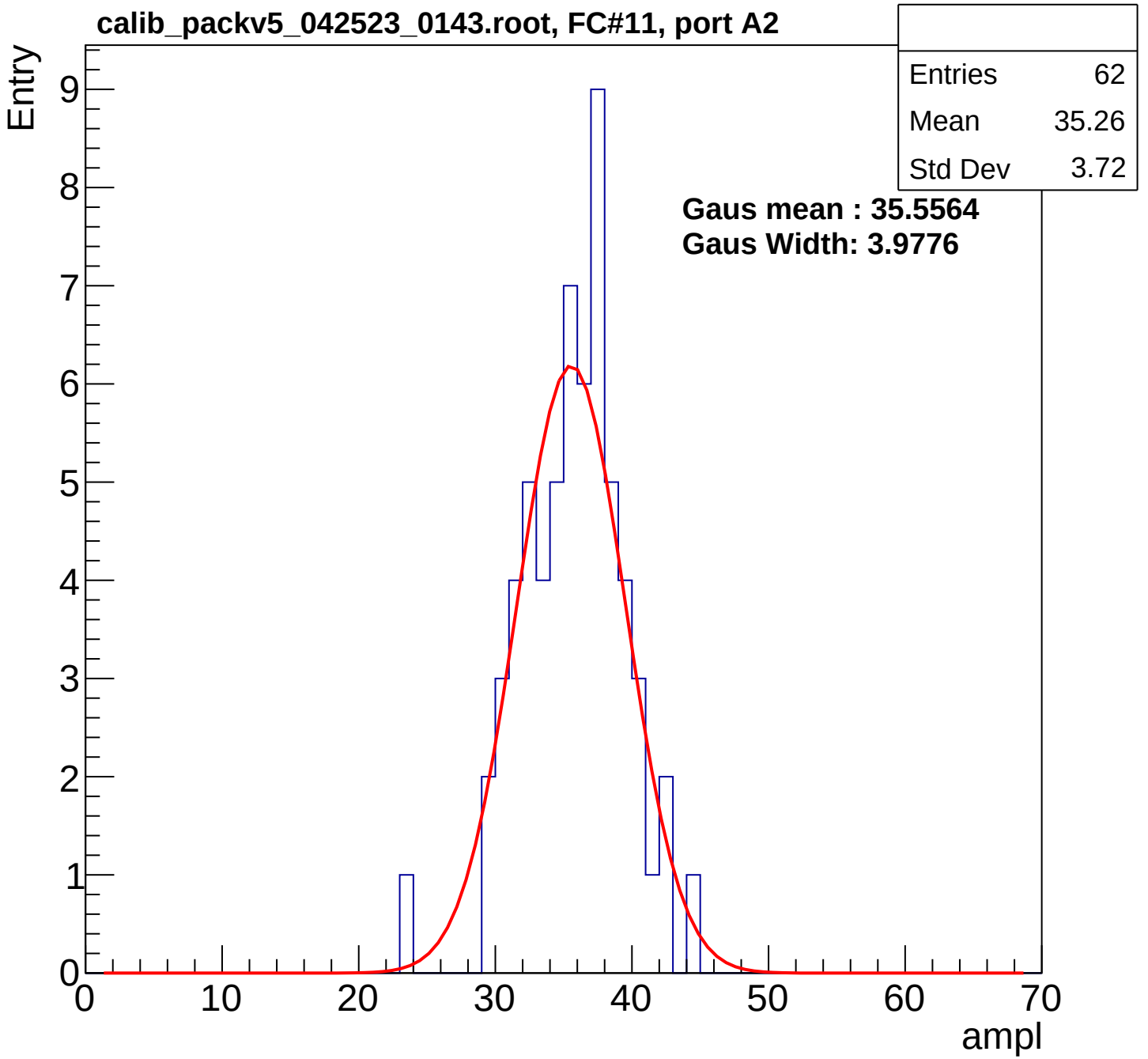
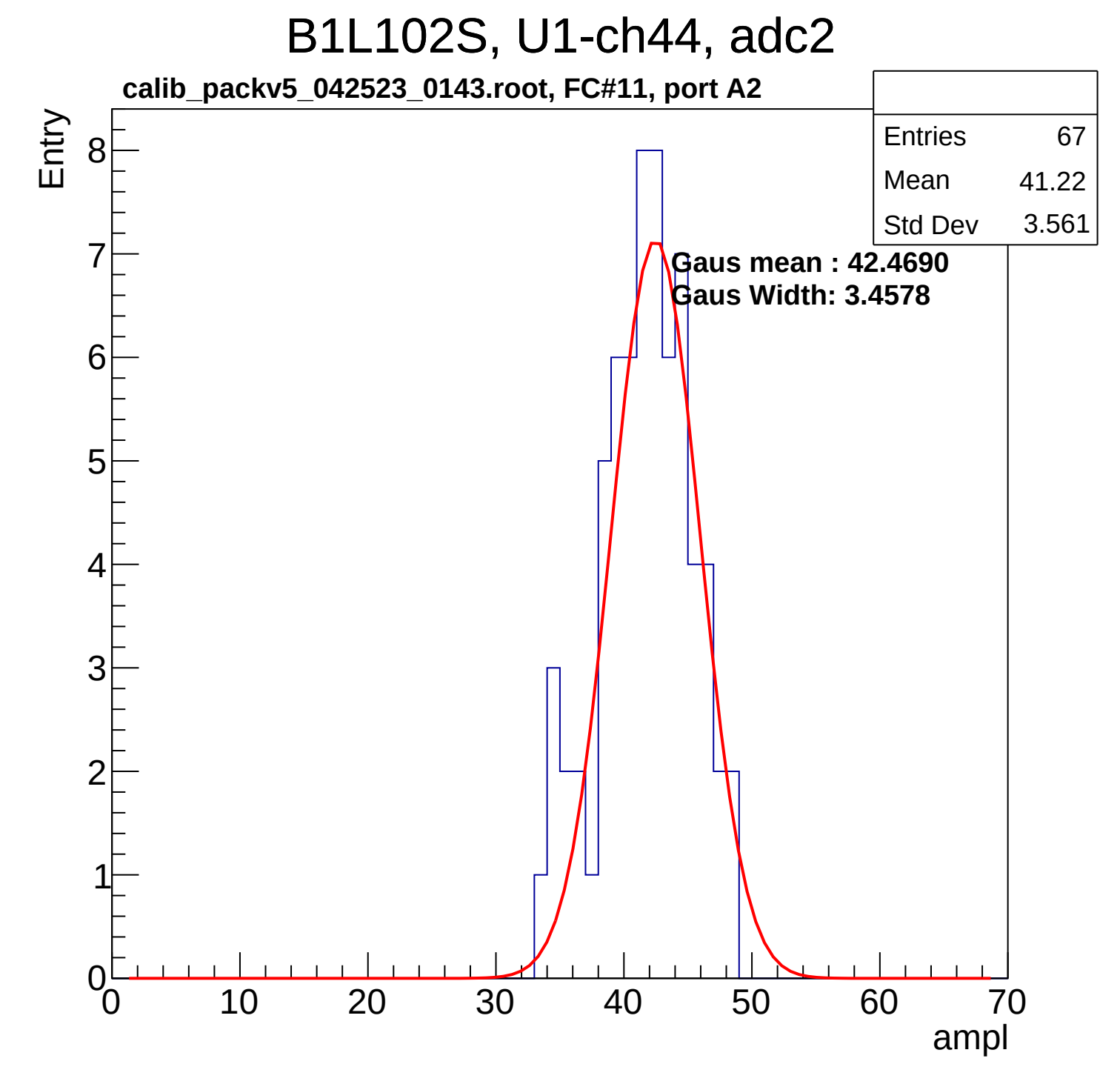
40
50
60
70


B1L102S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.27
Std Dev 3.681
Gaus mean : 29.0277
Gaus Width: 3.6102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 35.26
Std Dev 3.72
Gaus mean : 35.5564
Gaus Width: 3.9776
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 41.22
Std Dev 3.561
Gaus mean : 42.4690
Gaus Width: 3.4578
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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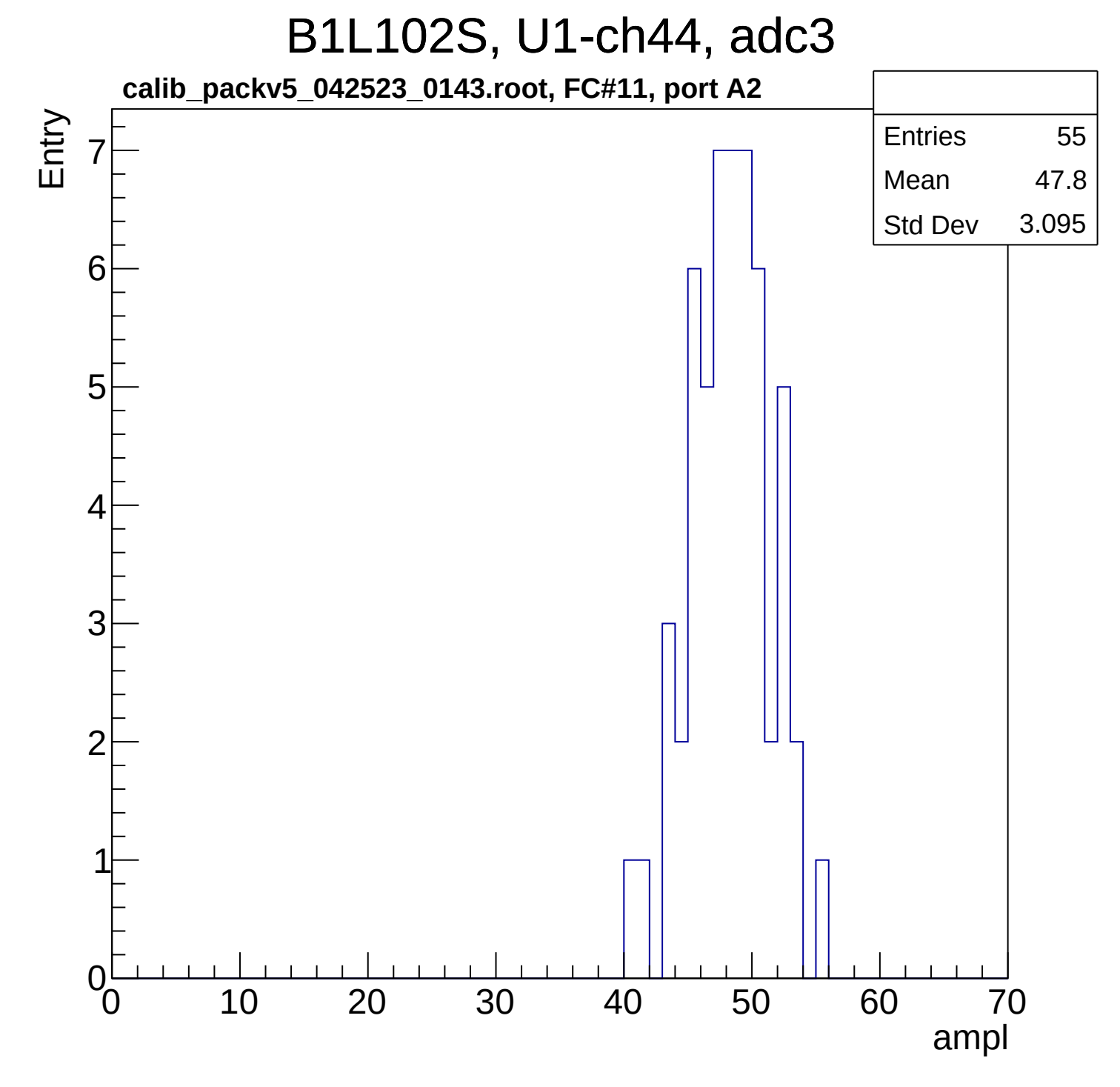
B1L102S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 47.8
Std Dev 3.095
Entry
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1
2
3
4
5
6
7
0
10
20
30
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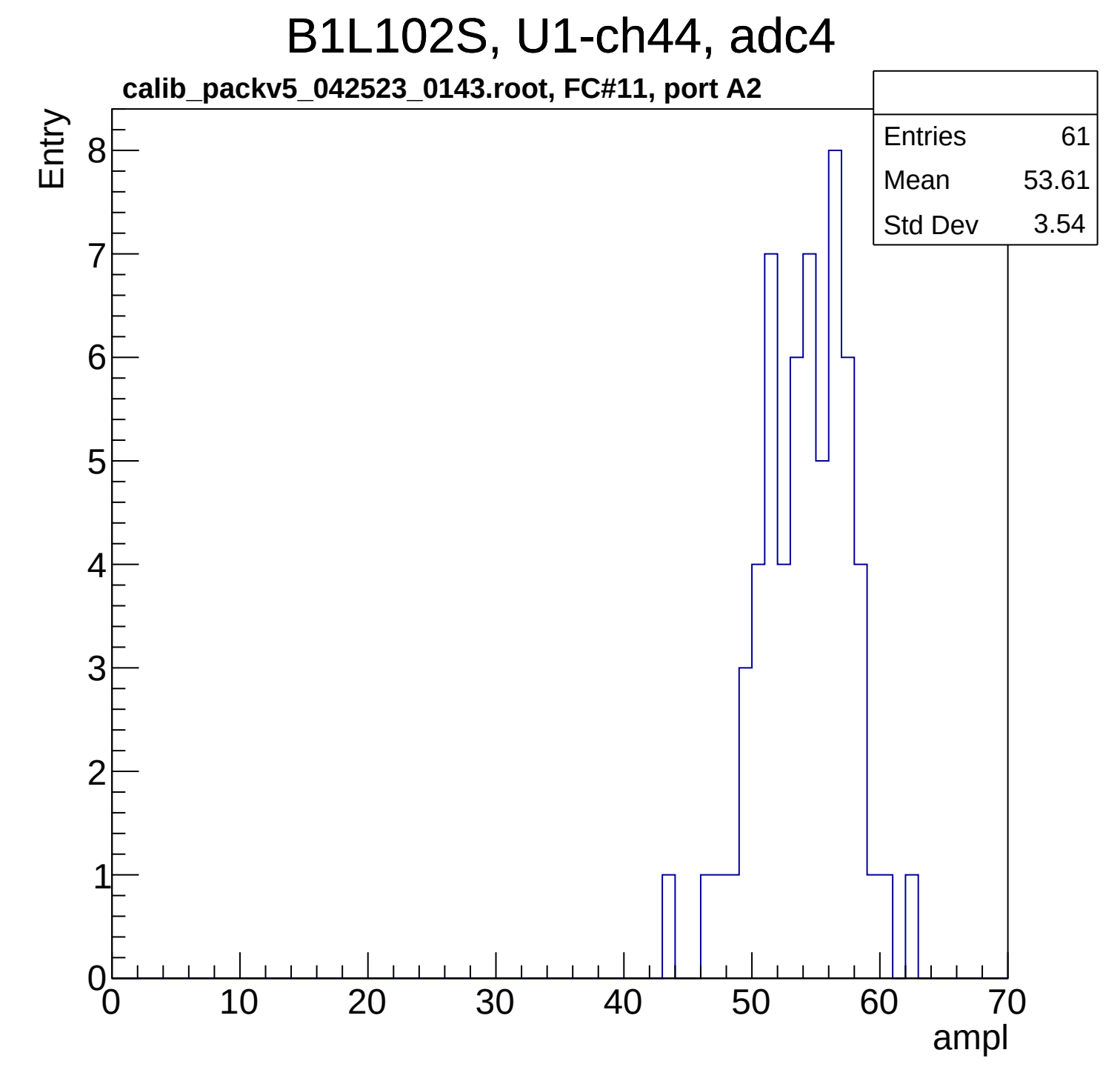
60
70
ampl


B1L102S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 53.61
Std Dev 3.54
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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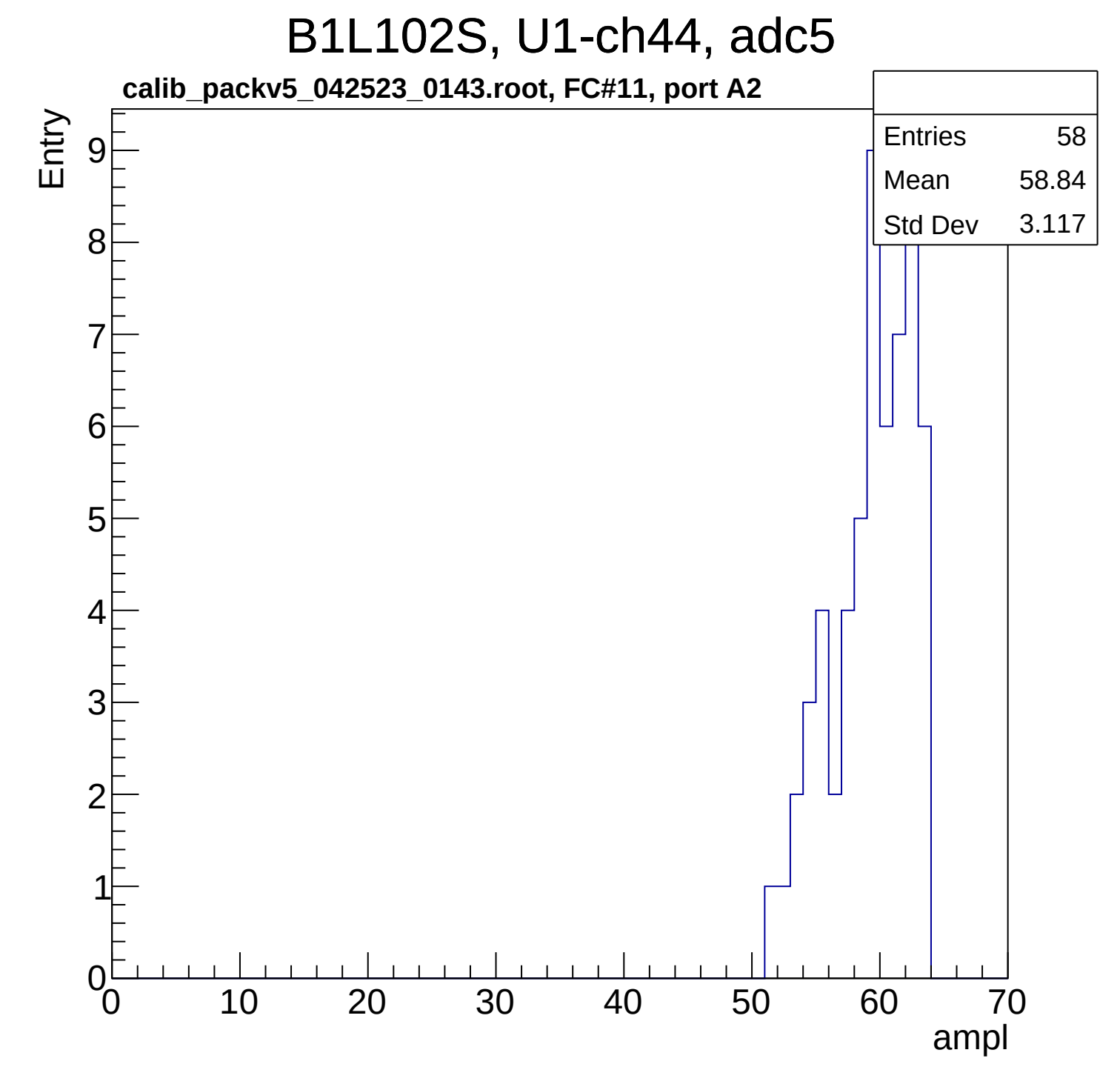
B1L102S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 58.84
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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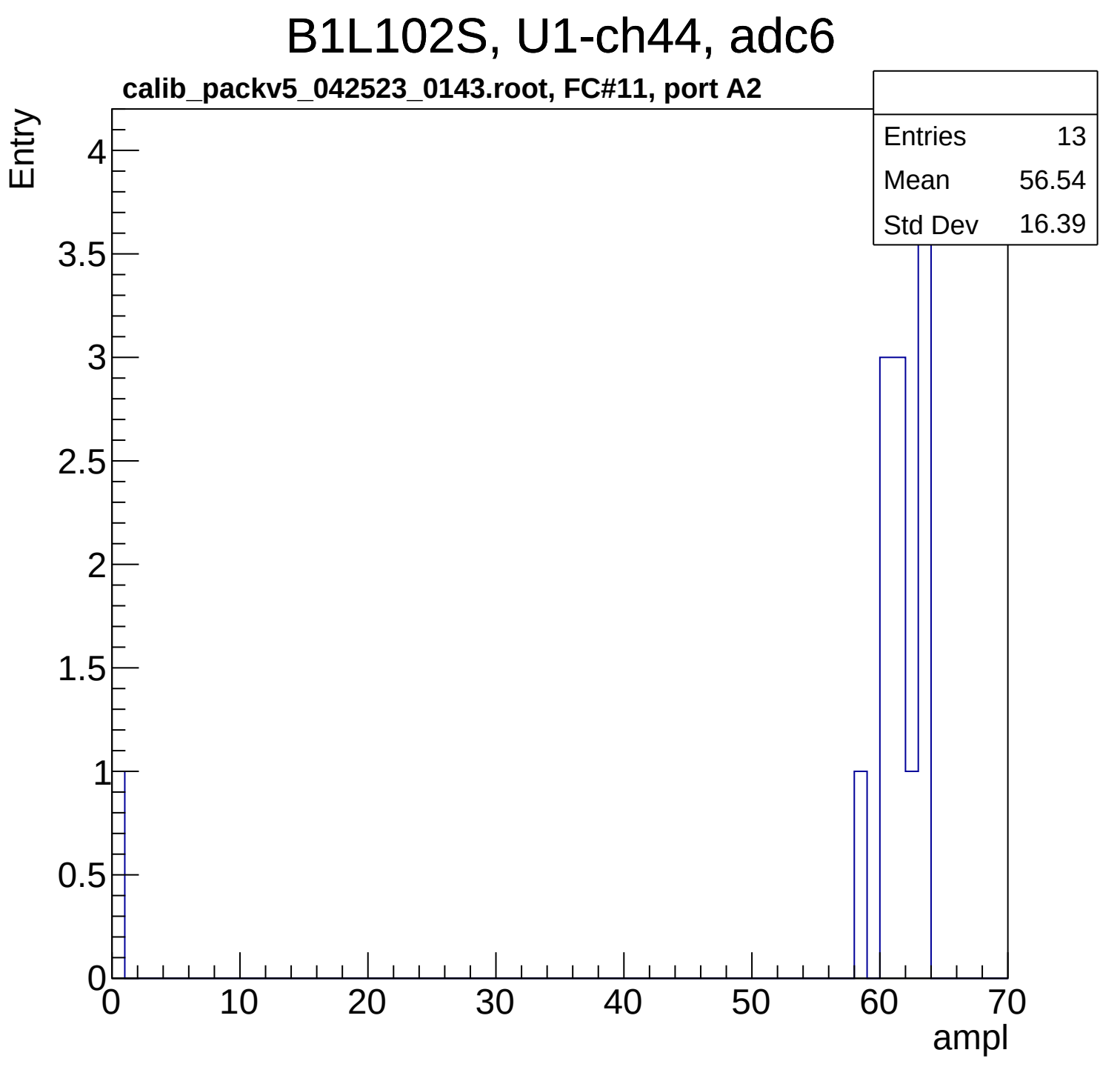
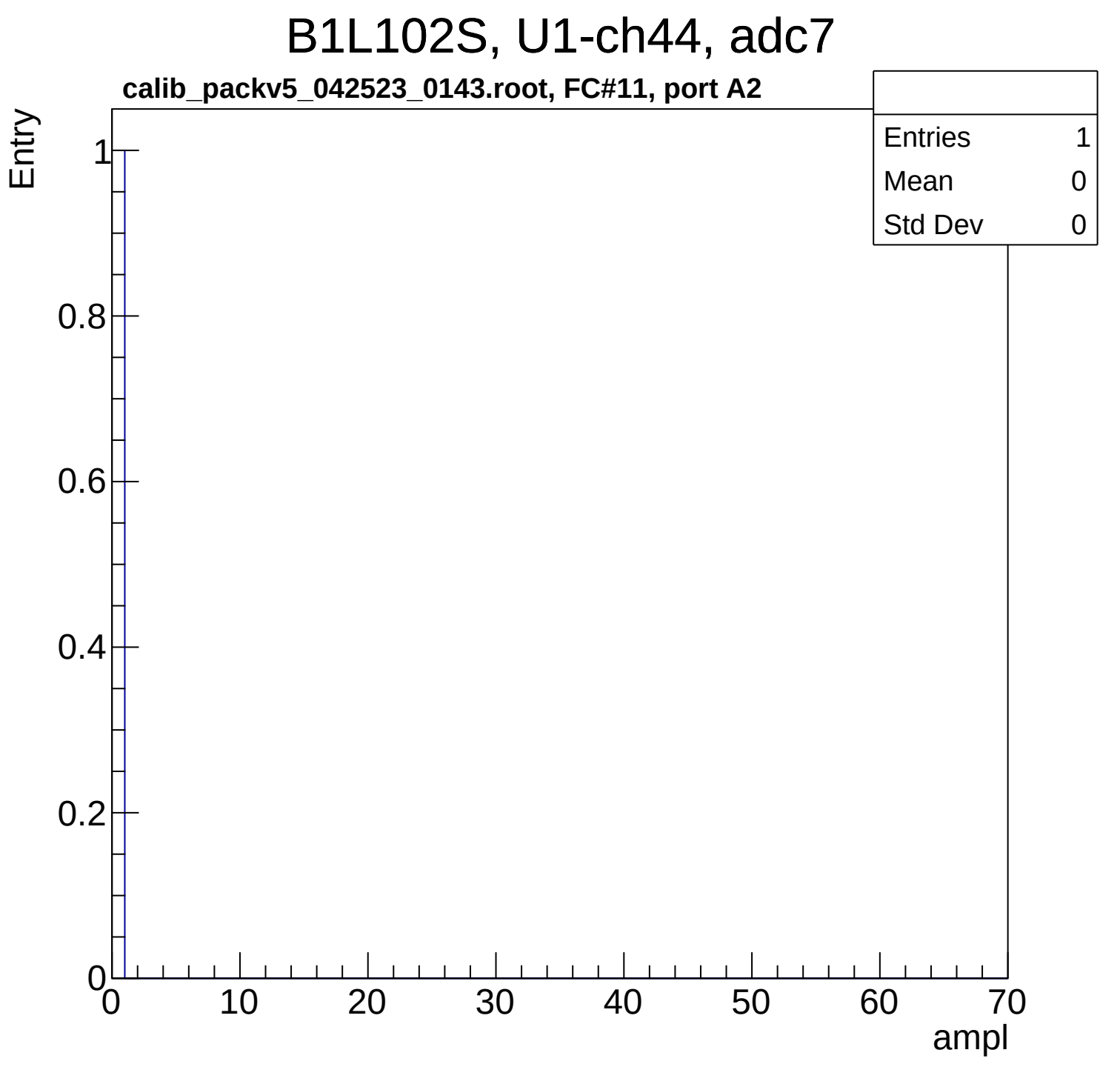

B1L102S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 56.54
Std Dev 16.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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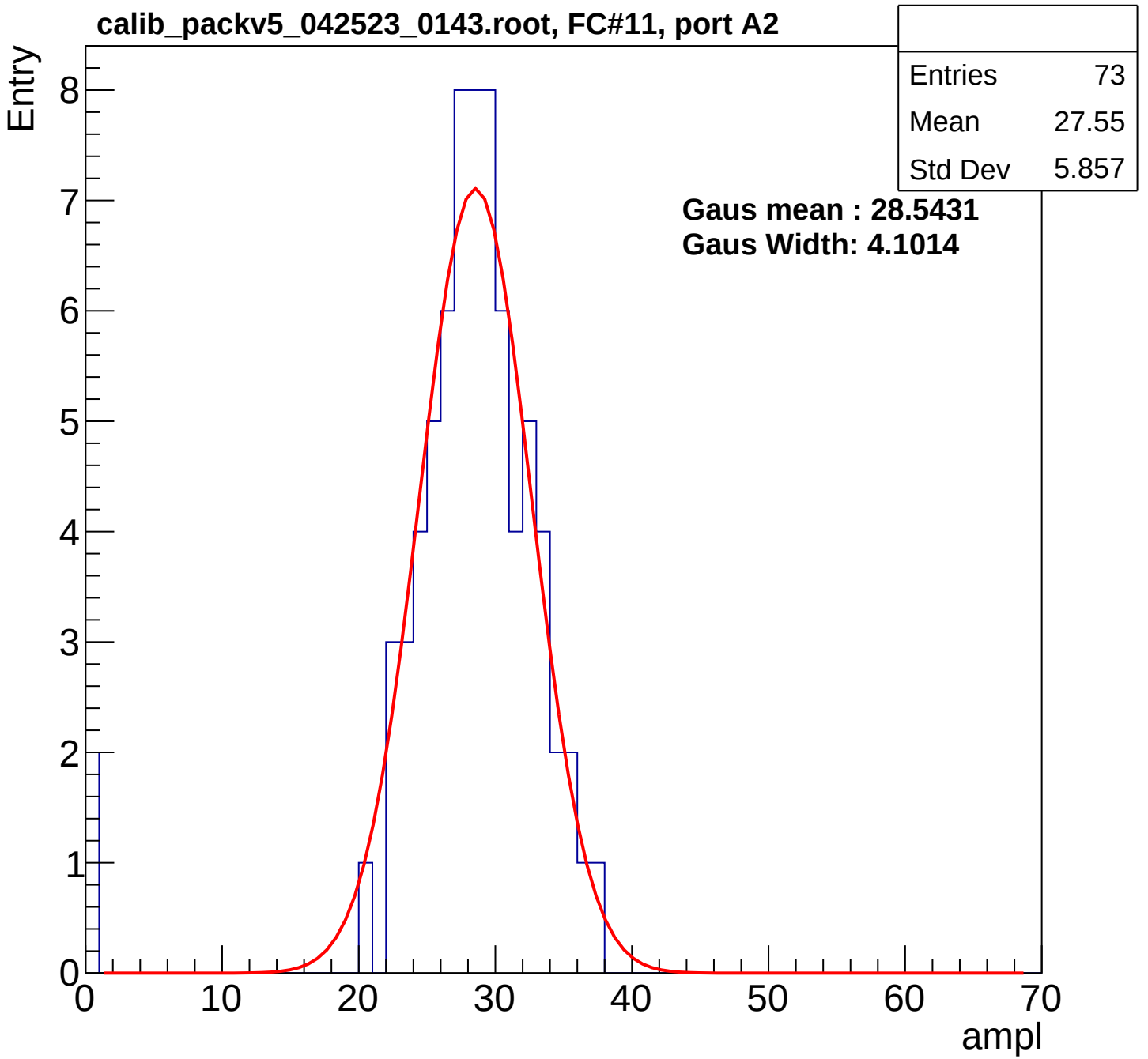
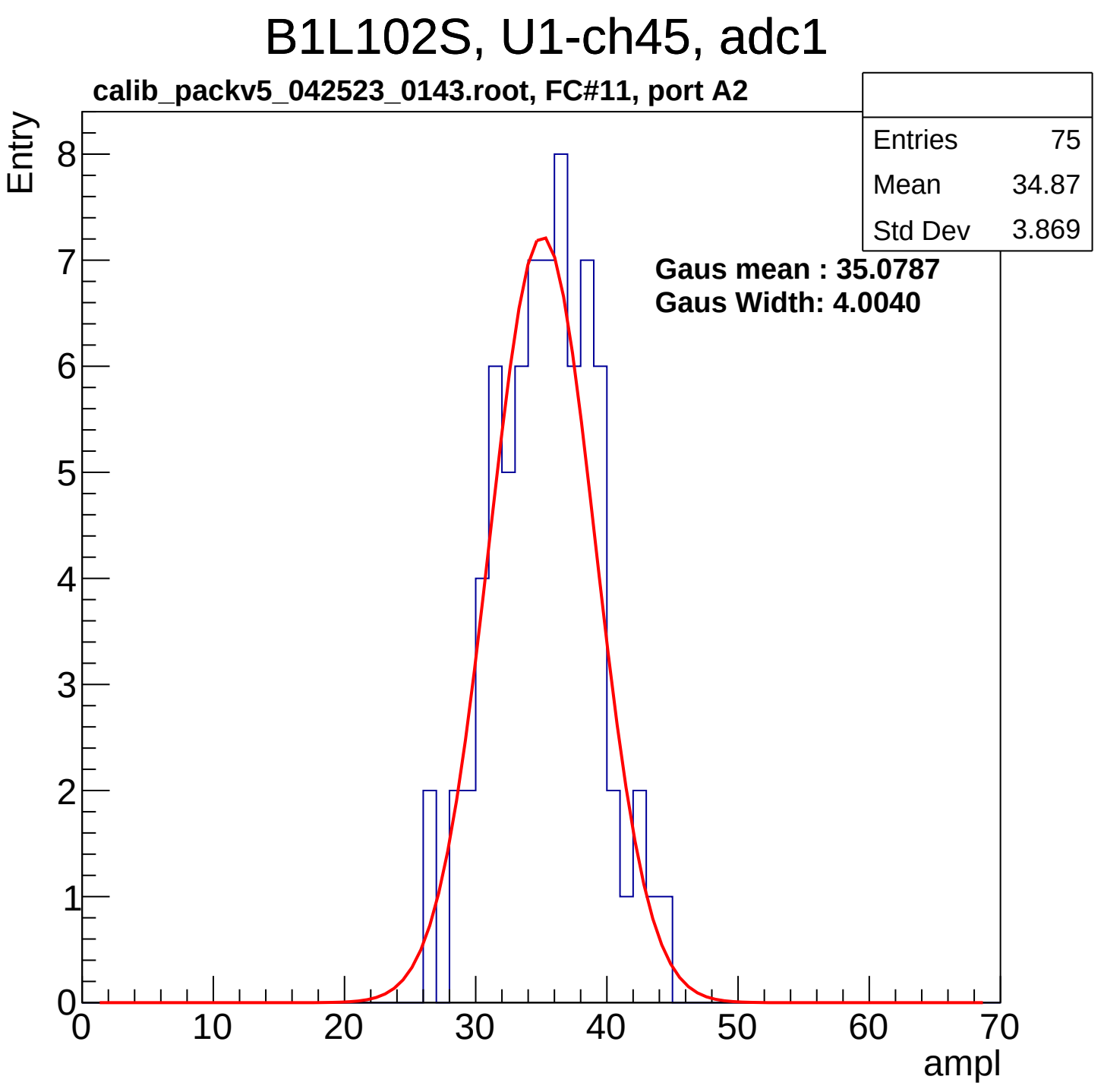
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 27.55
Std Dev 5.857
Gaus mean : 28.5431
Gaus Width: 4.1014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 34.87
Std Dev 3.869
Gaus mean : 35.0787
Gaus Width: 4.0040
Entry
ampl
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1
2
3
4
5
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0
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20
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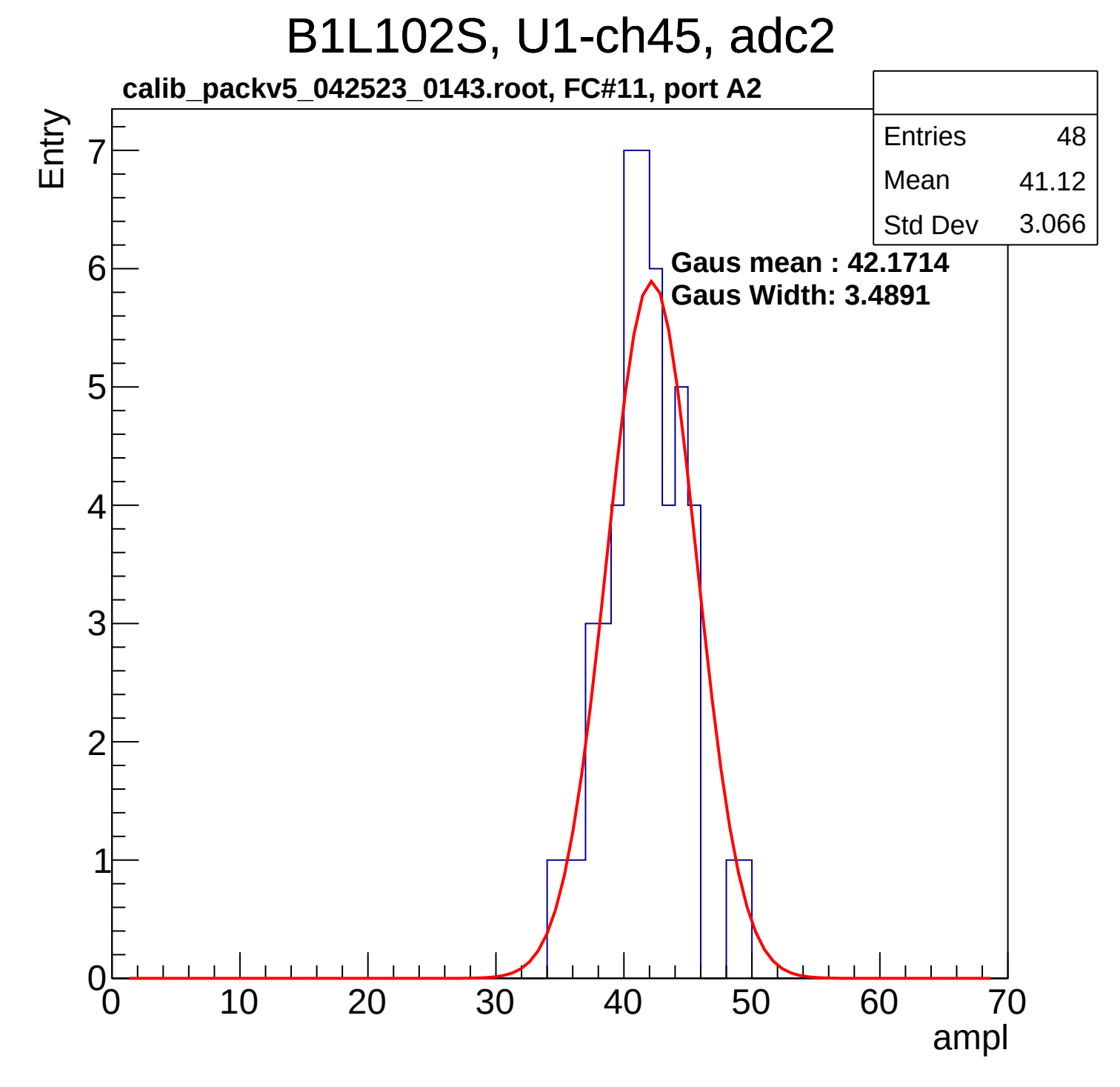
B1L102S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 41.12
Std Dev 3.066
Gaus mean : 42.1714
Gaus Width: 3.4891
Entry
ampl
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2
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4
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6
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0
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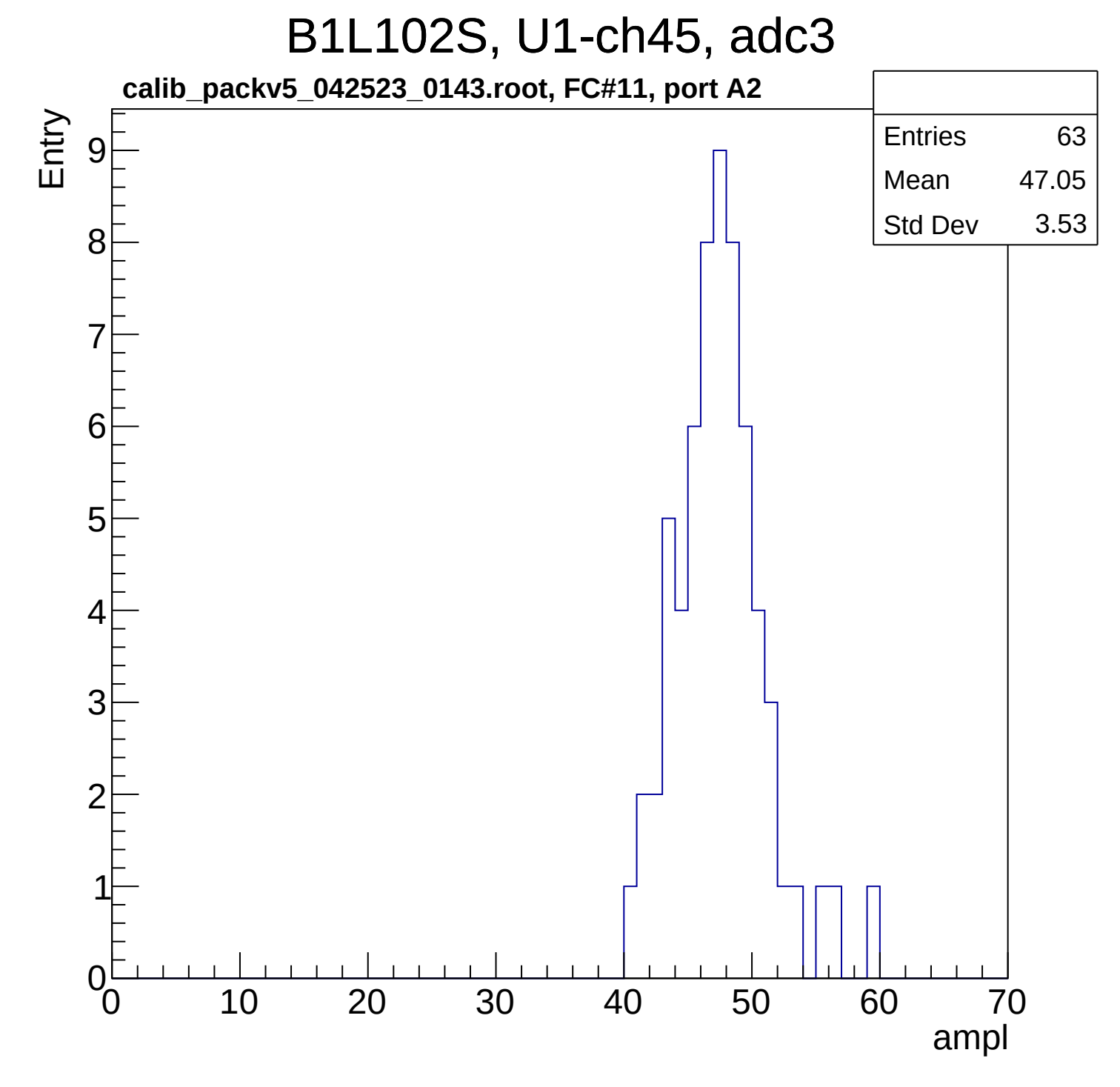
B1L102S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 47.05
Std Dev 3.53
Entry
ampl
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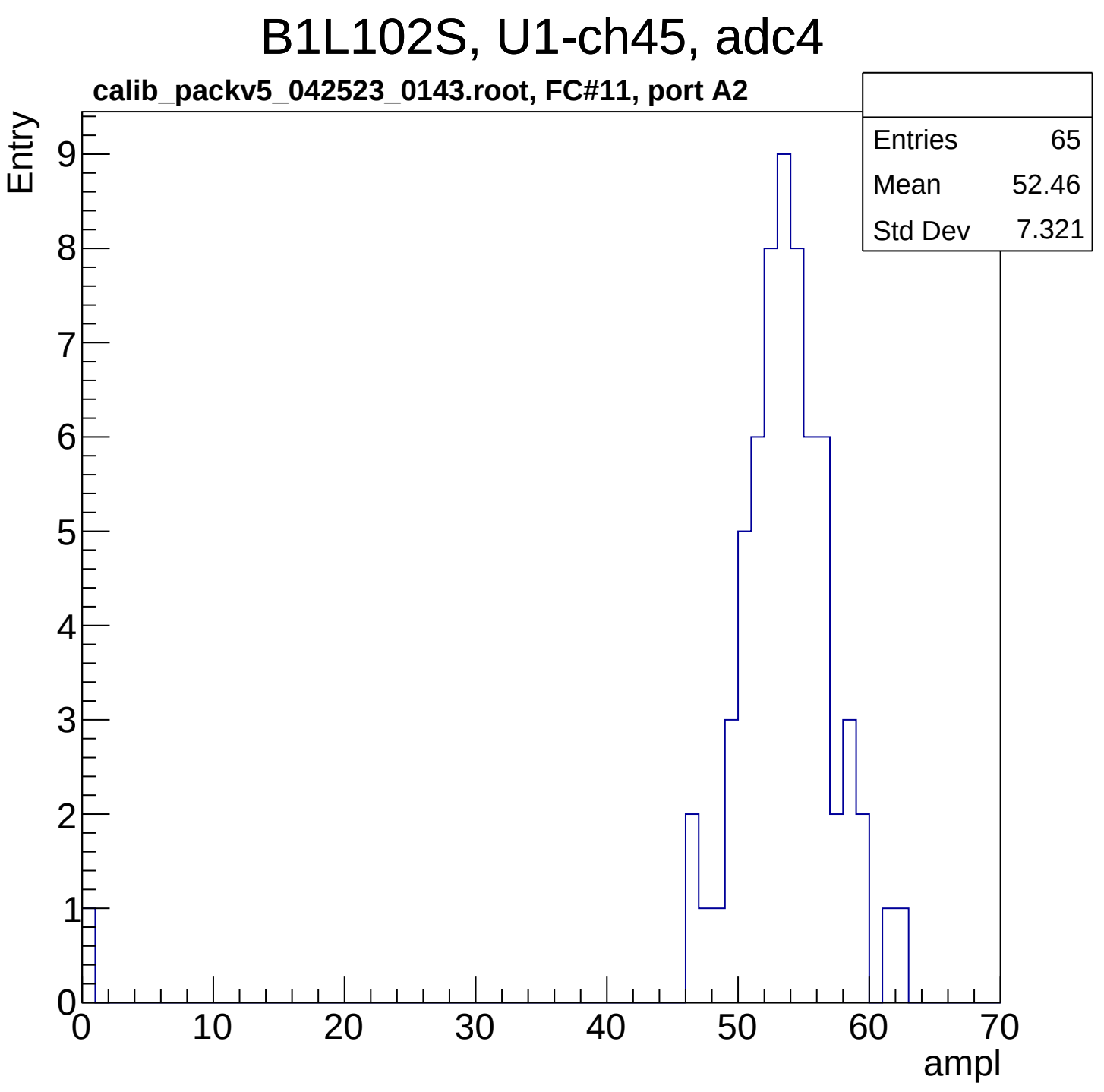
B1L102S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 52.46
Std Dev 7.321
Entry
ampl
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0
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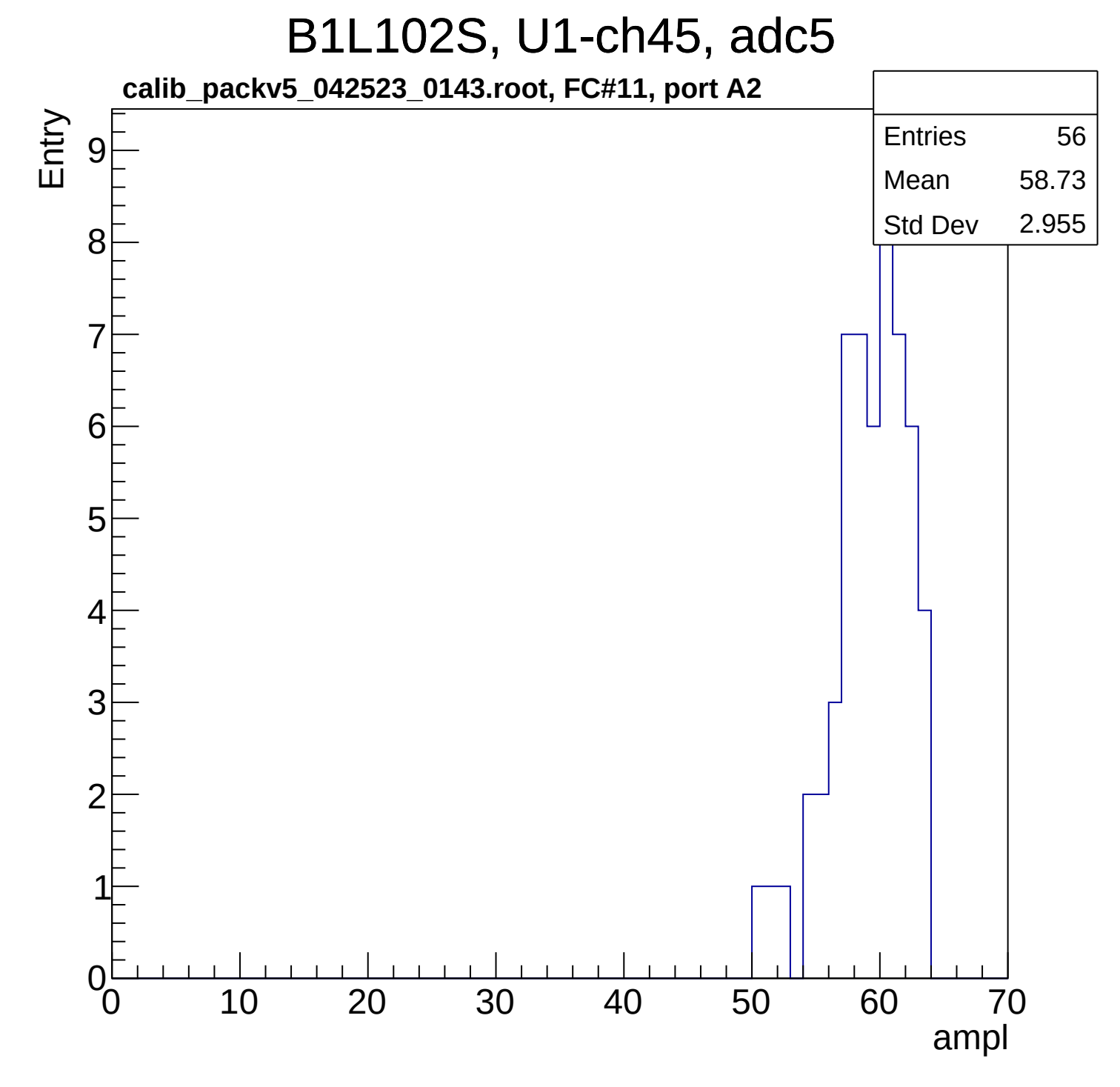
B1L102S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 58.73
Std Dev 2.955
Entry
ampl
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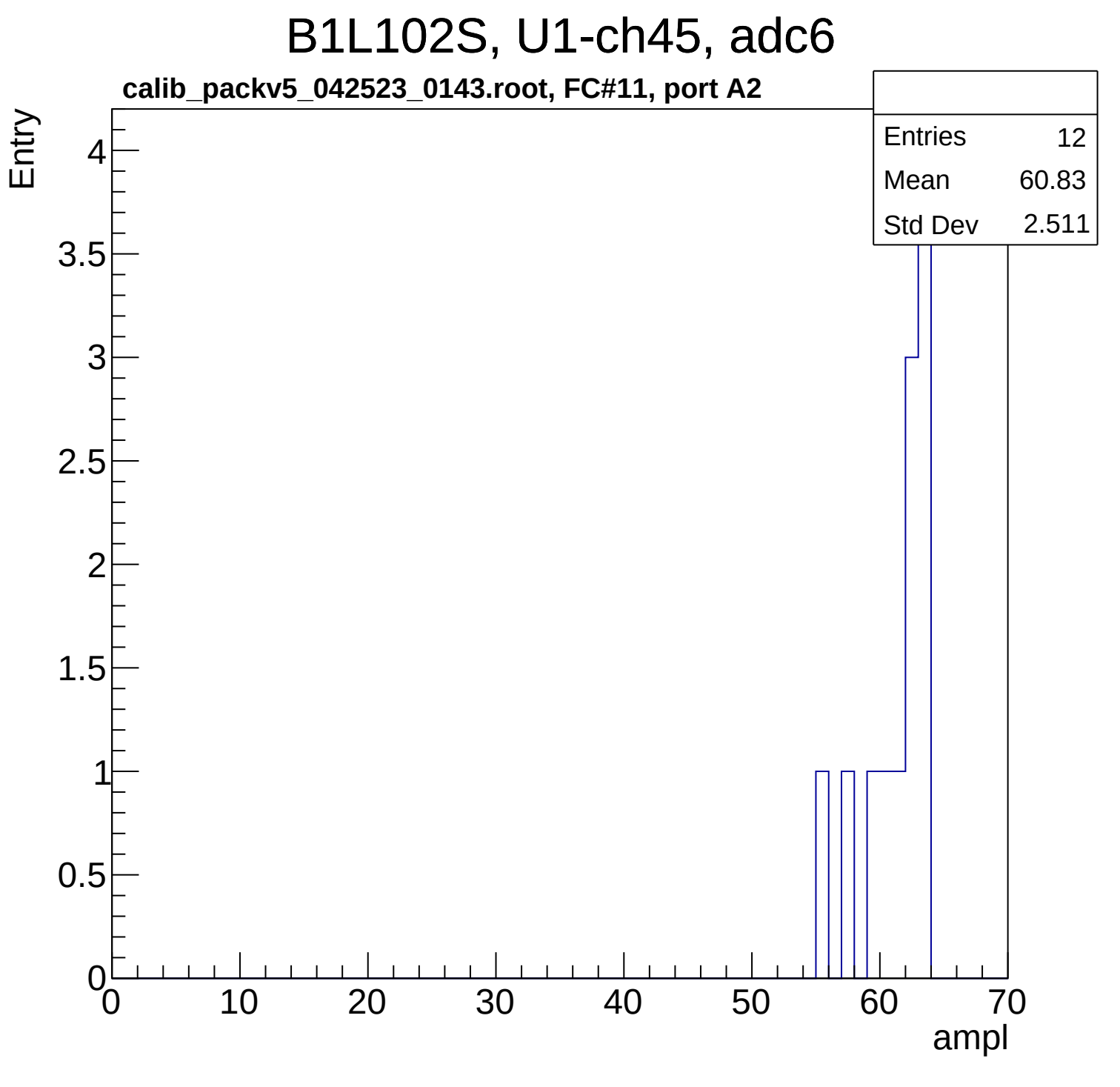
B1L102S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 60.83
Std Dev 2.511
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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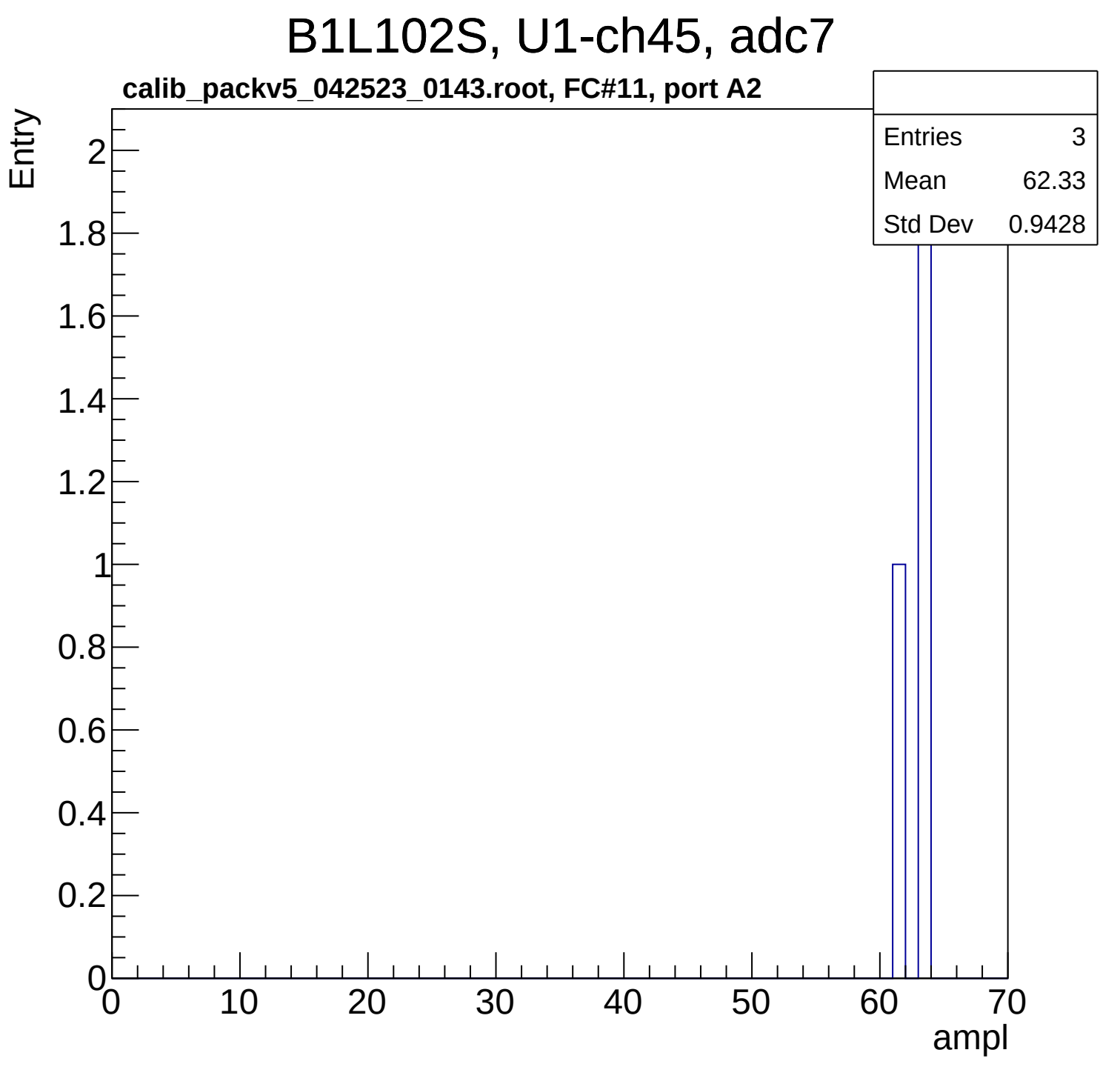
B1L102S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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60
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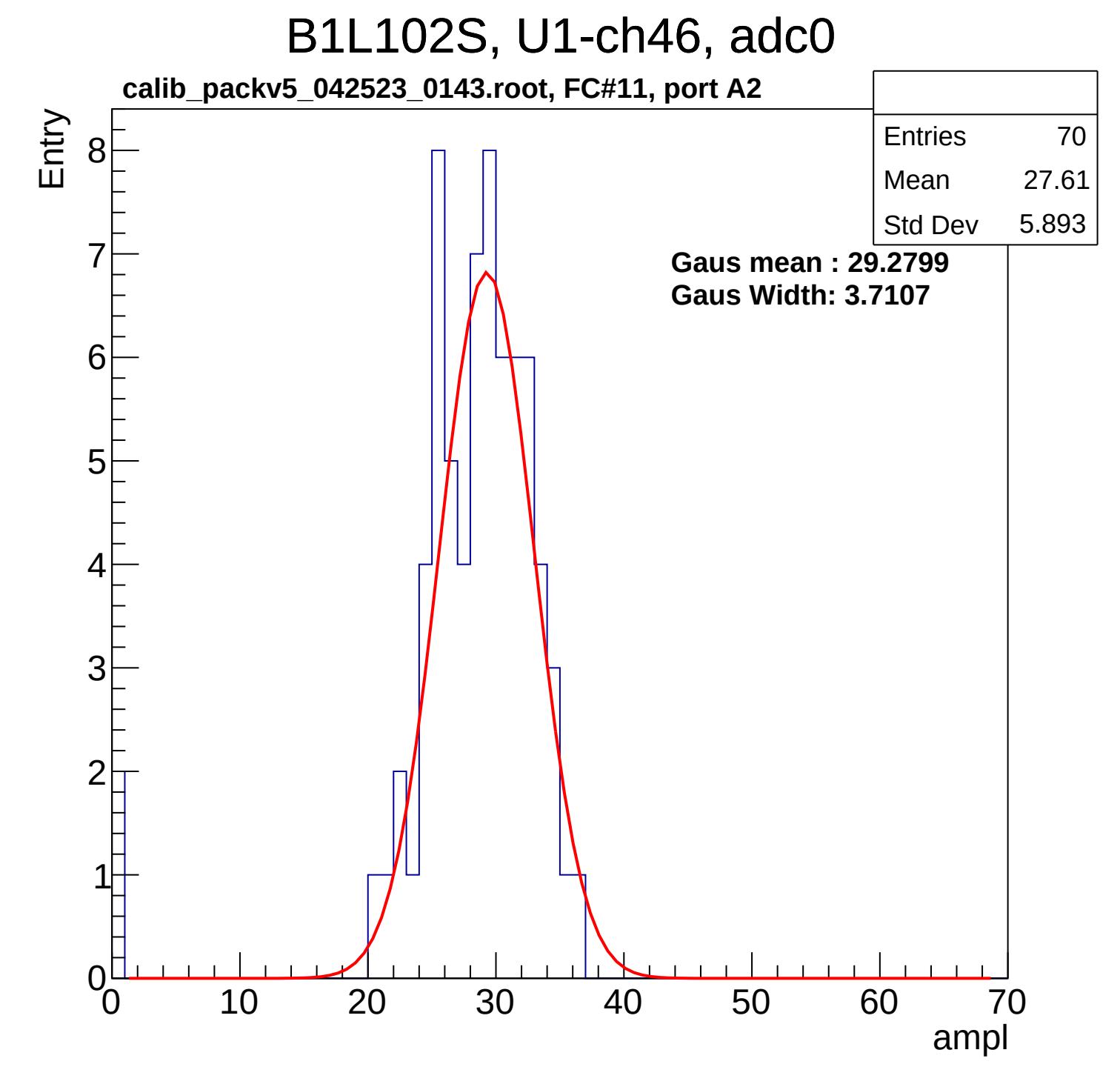
B1L102S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 27.61
Std Dev 5.893
Gaus mean : 29.2799
Gaus Width: 3.7107
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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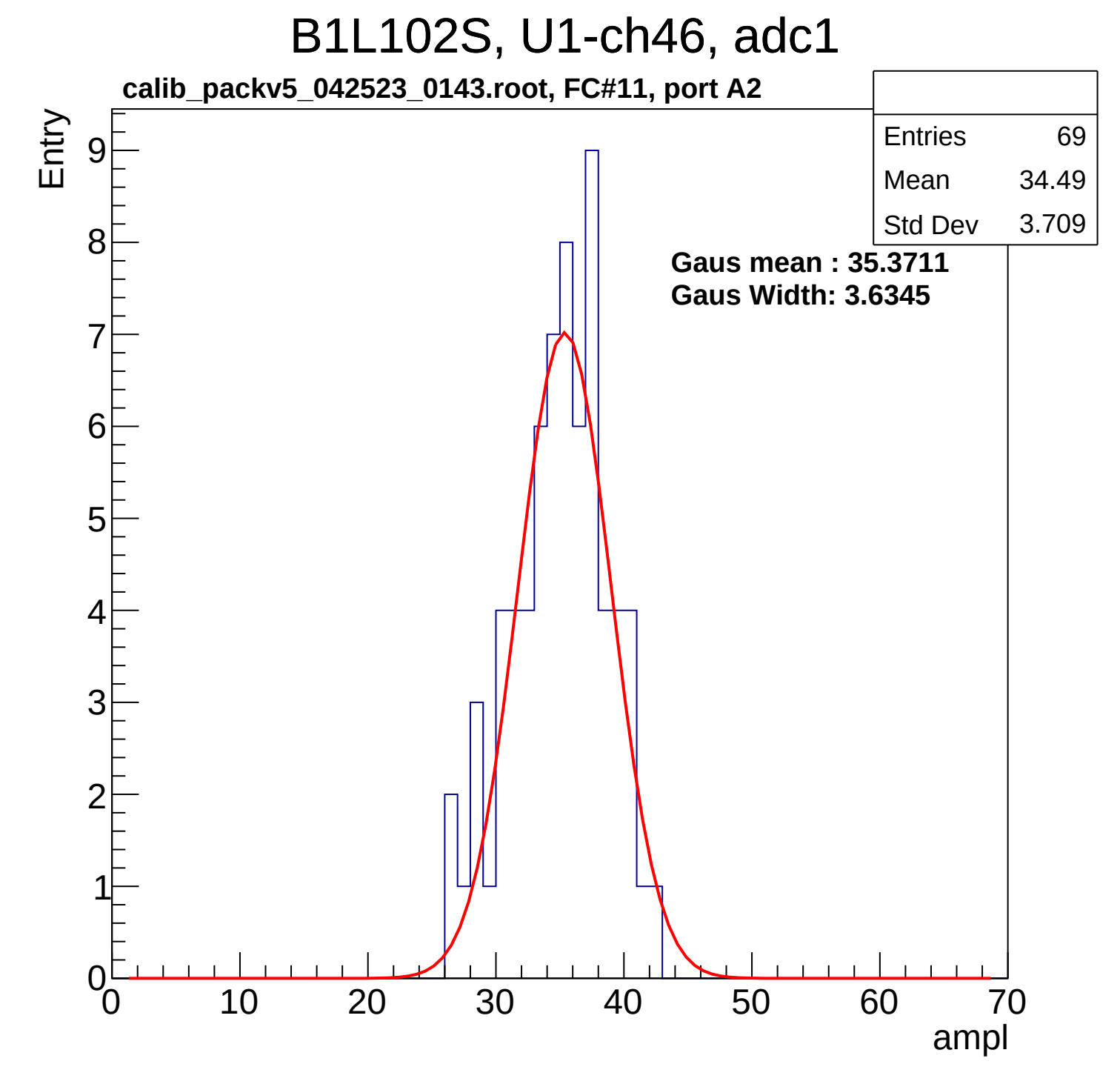
B1L102S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 34.49
Std Dev 3.709
Gaus mean : 35.3711
Gaus Width: 3.6345
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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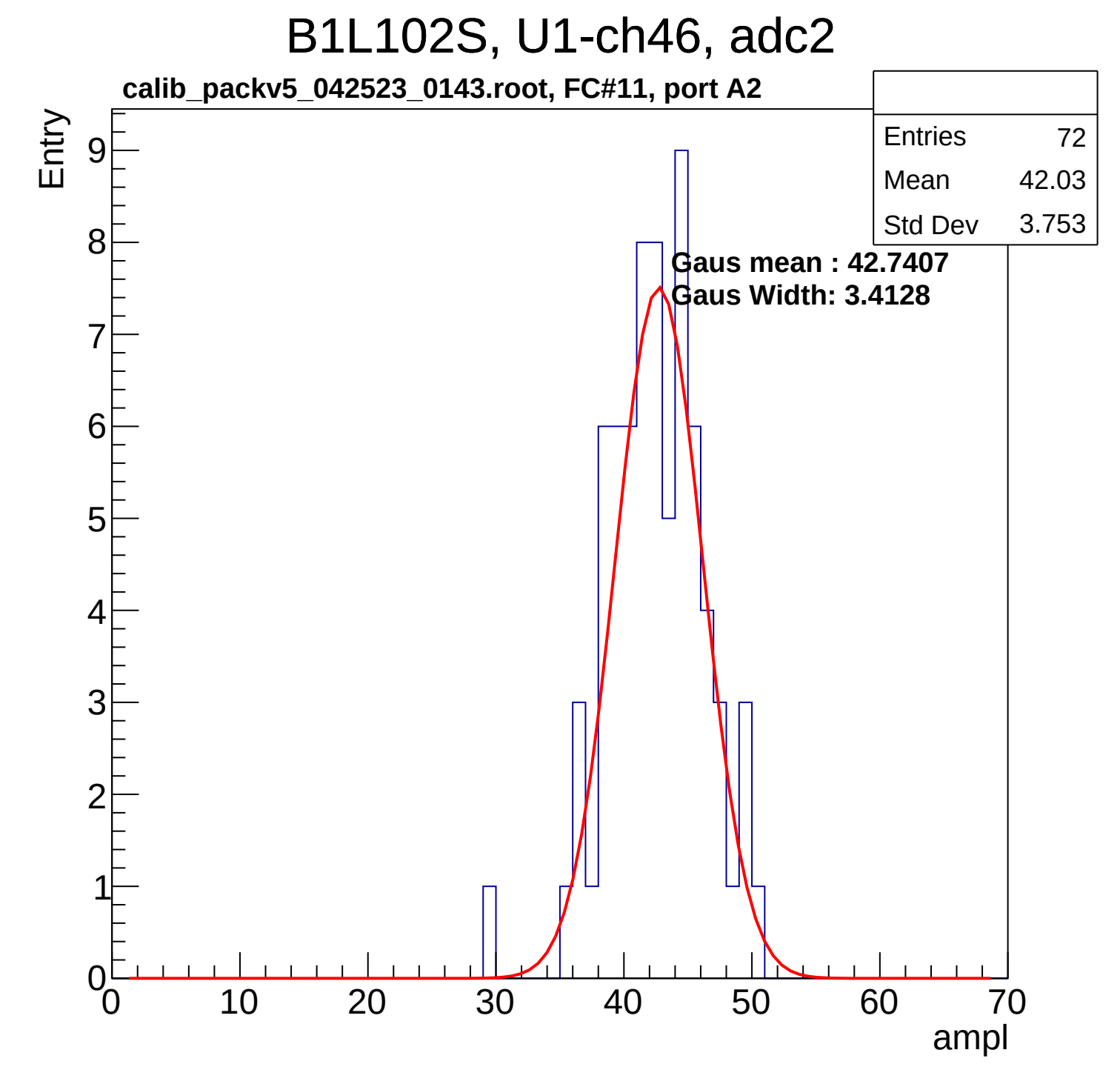
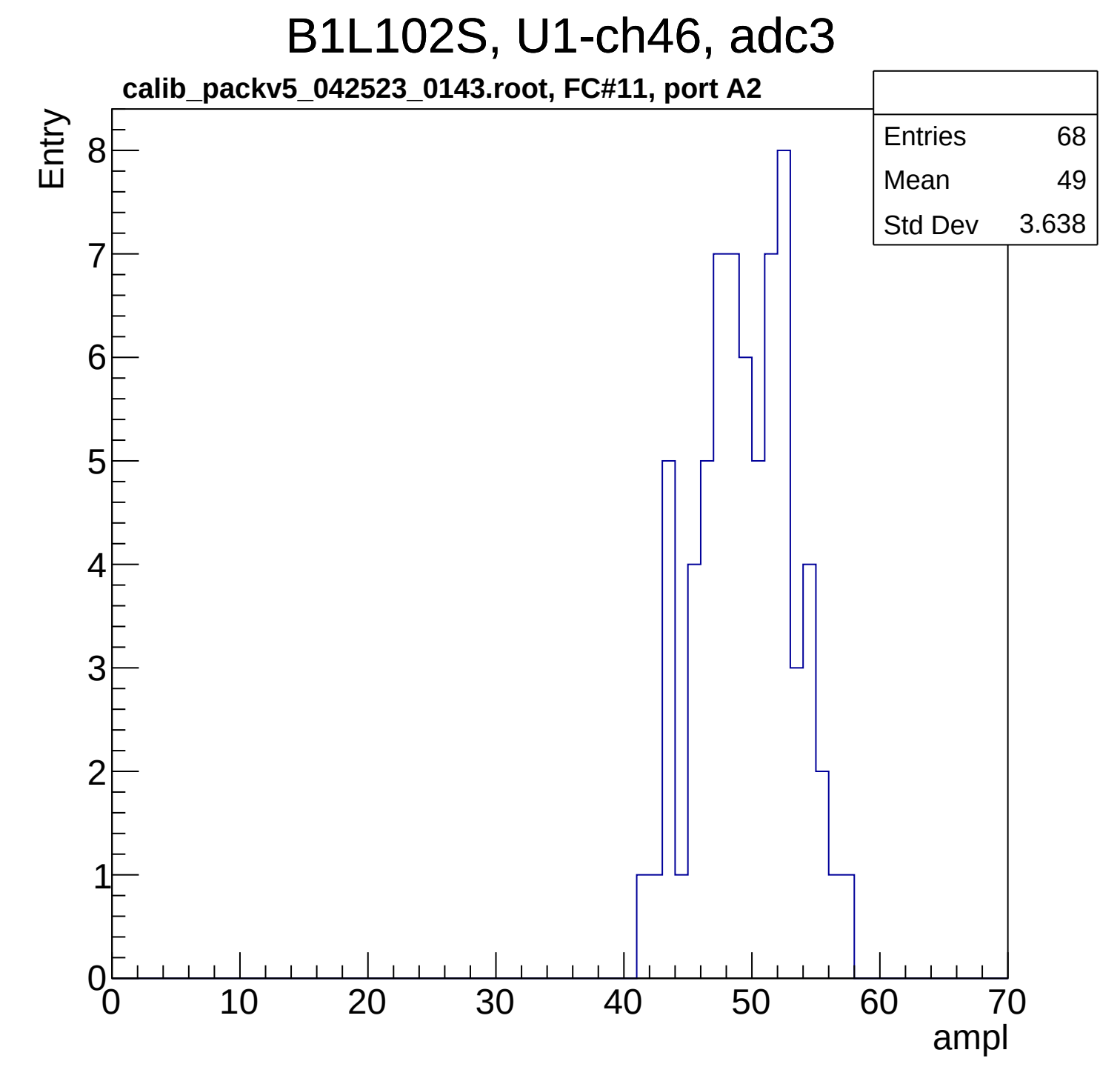

B1L102S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 42.03
Std Dev 3.753
Gaus mean : 42.7407
Gaus Width: 3.4128
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 49
Std Dev 3.638
Entry
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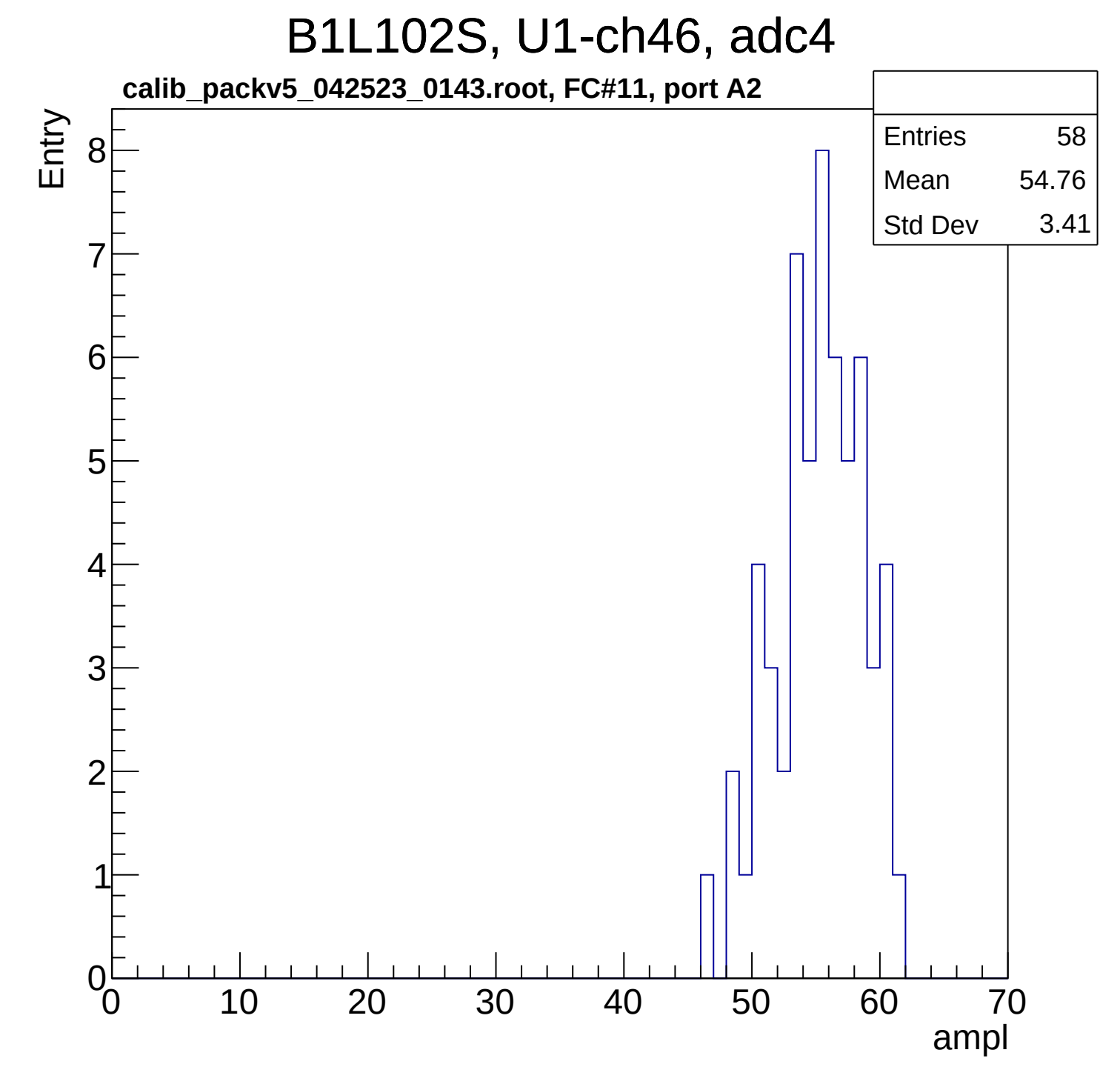
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 54.76
Std Dev 3.41
Entry
ampl
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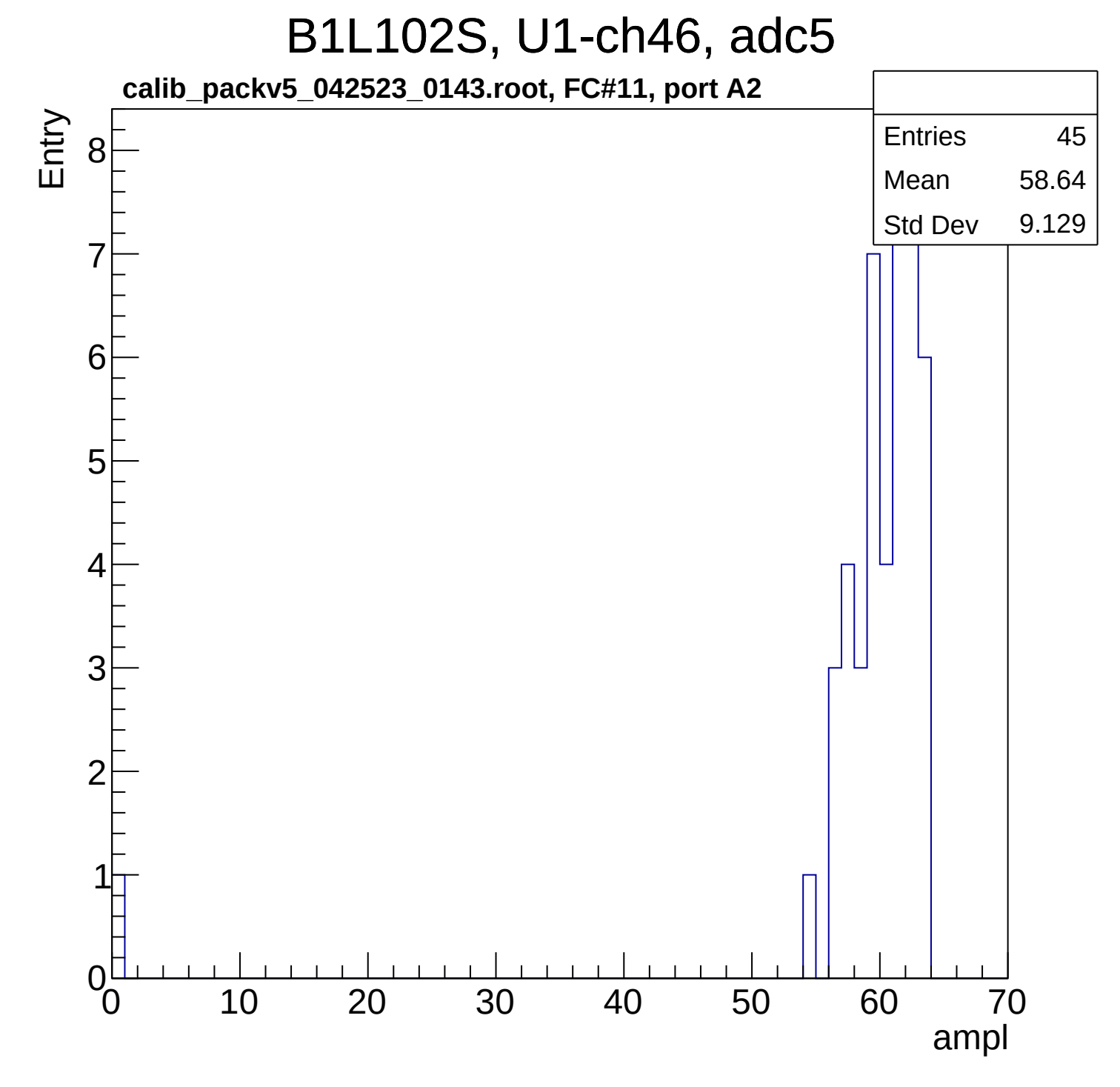
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 58.64
Std Dev 9.129
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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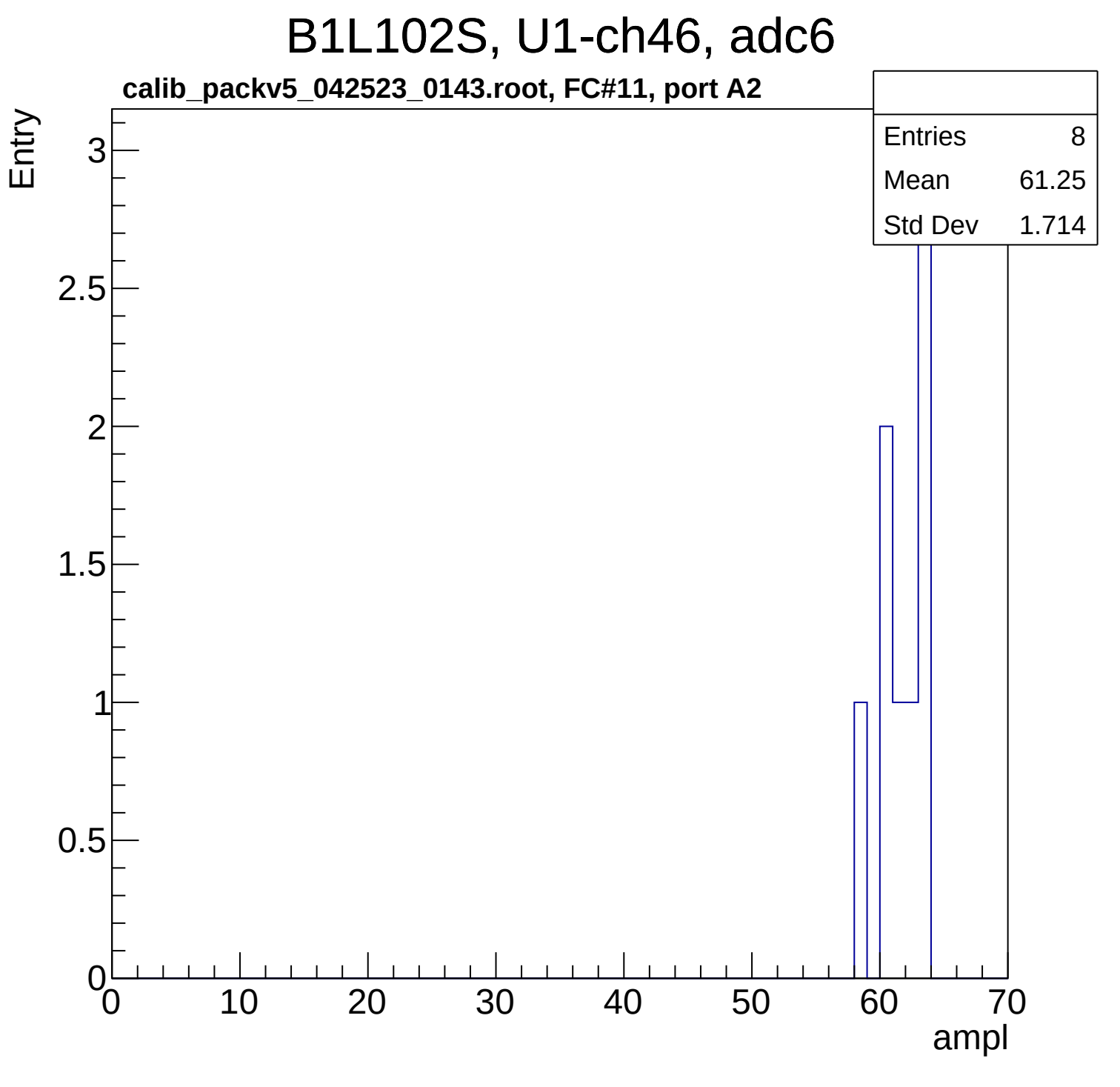
B1L102S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.25
Std Dev 1.714
Entry
ampl
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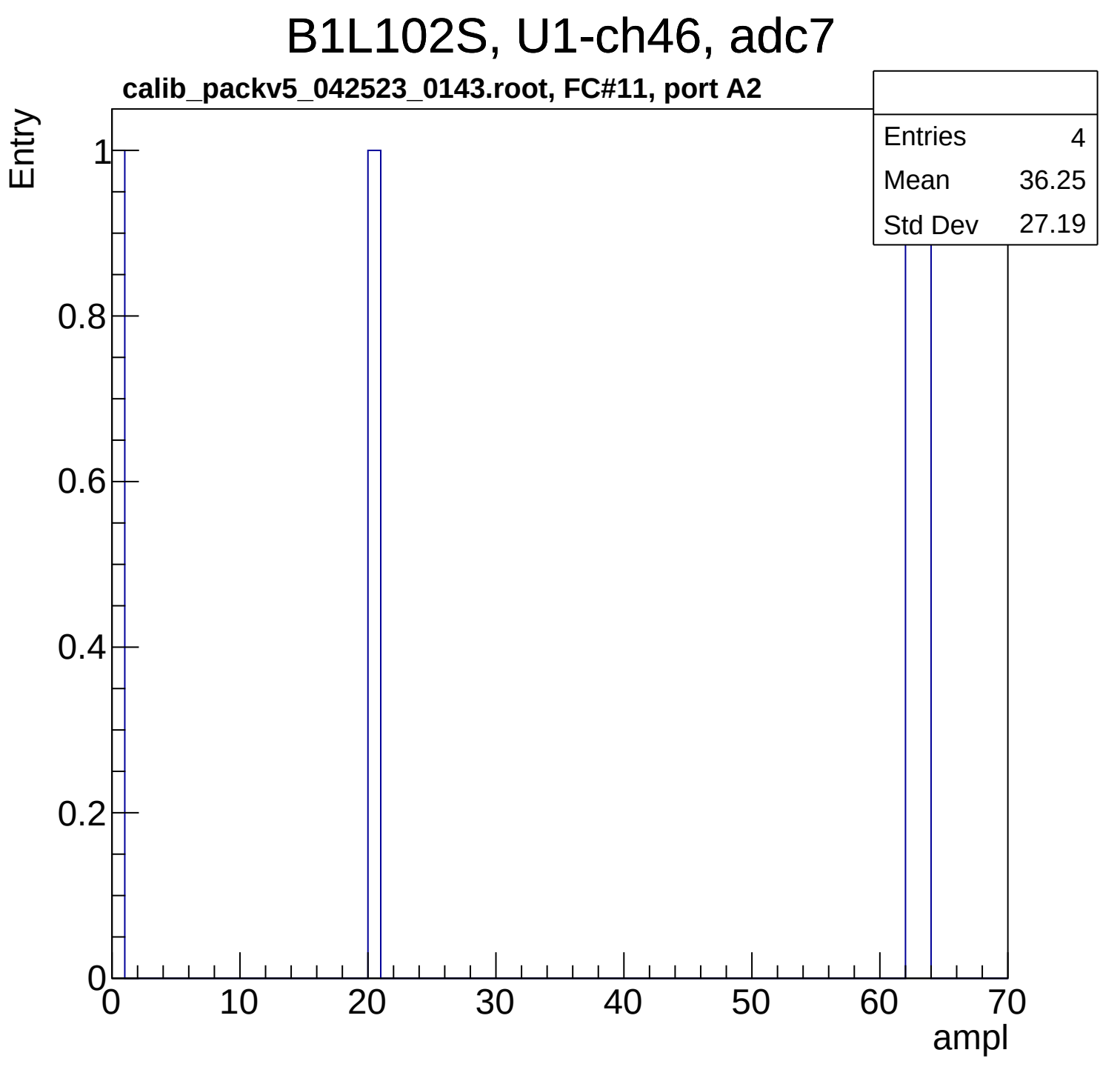
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 36.25
Std Dev 27.19
Entry
ampl
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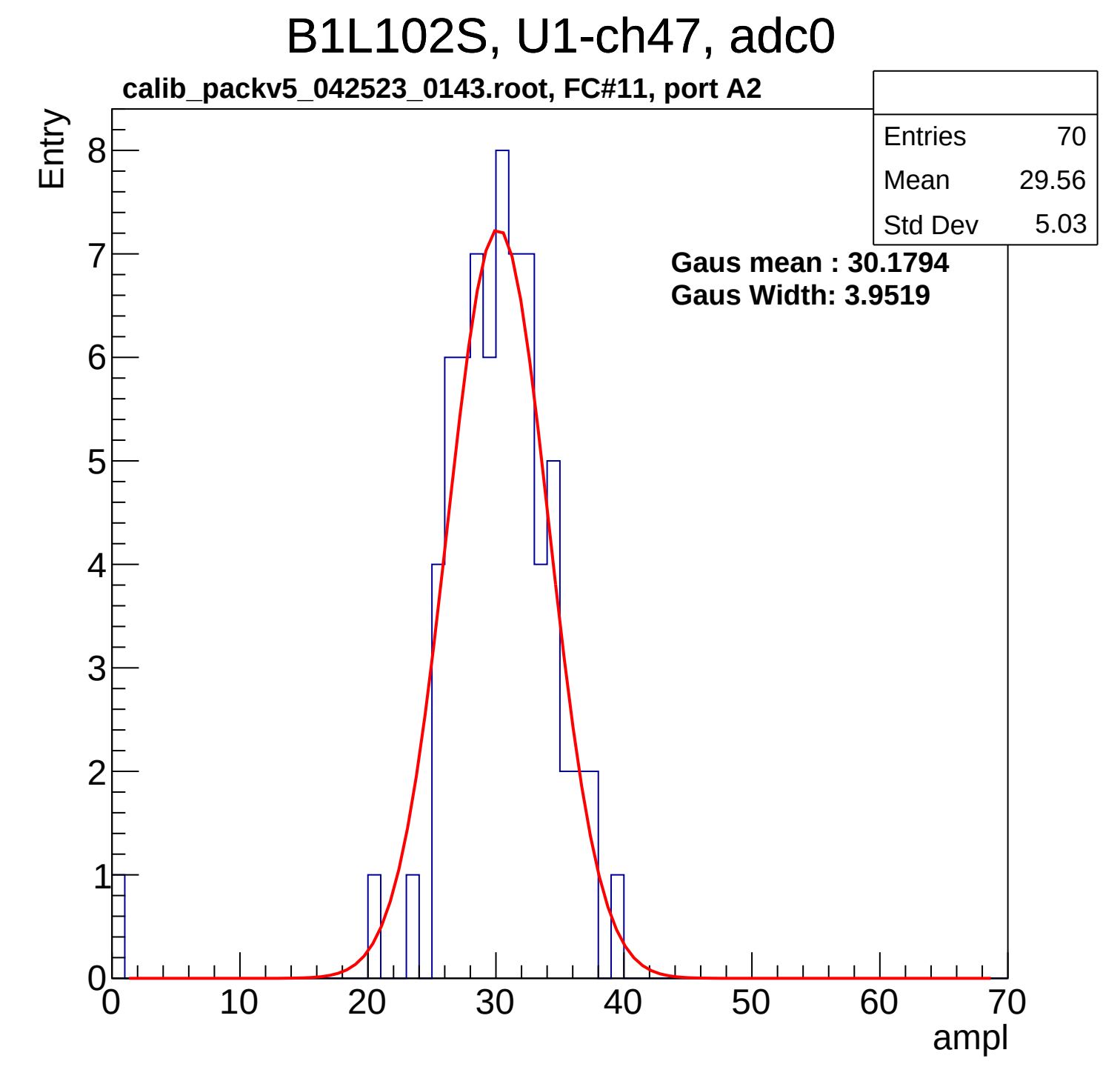
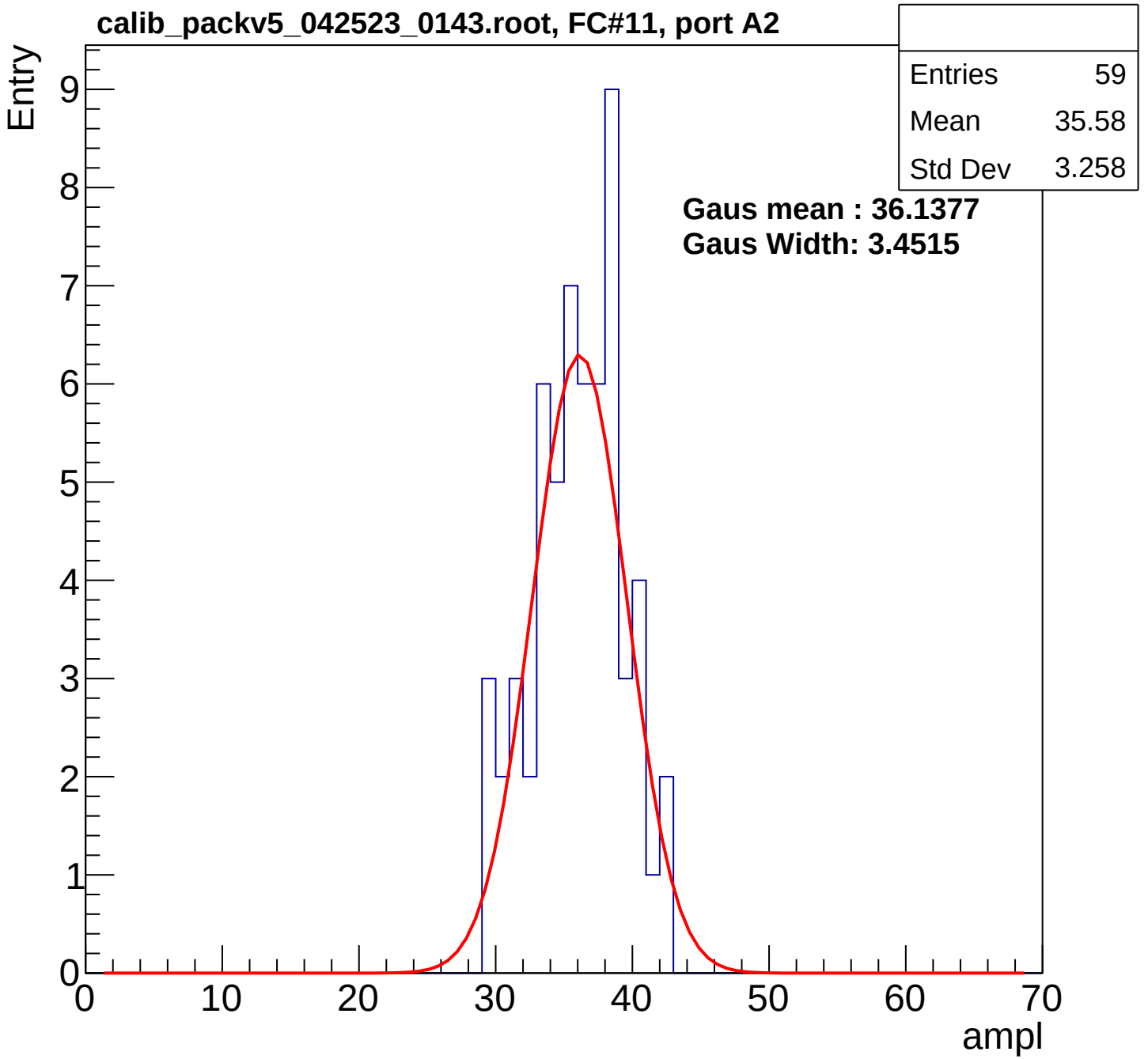
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L102S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 29.56
Std Dev 5.03
Gaus mean : 30.1794
Gaus Width: 3.9519
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 35.58
Std Dev 3.258
Gaus mean : 36.1377
Gaus Width: 3.4515
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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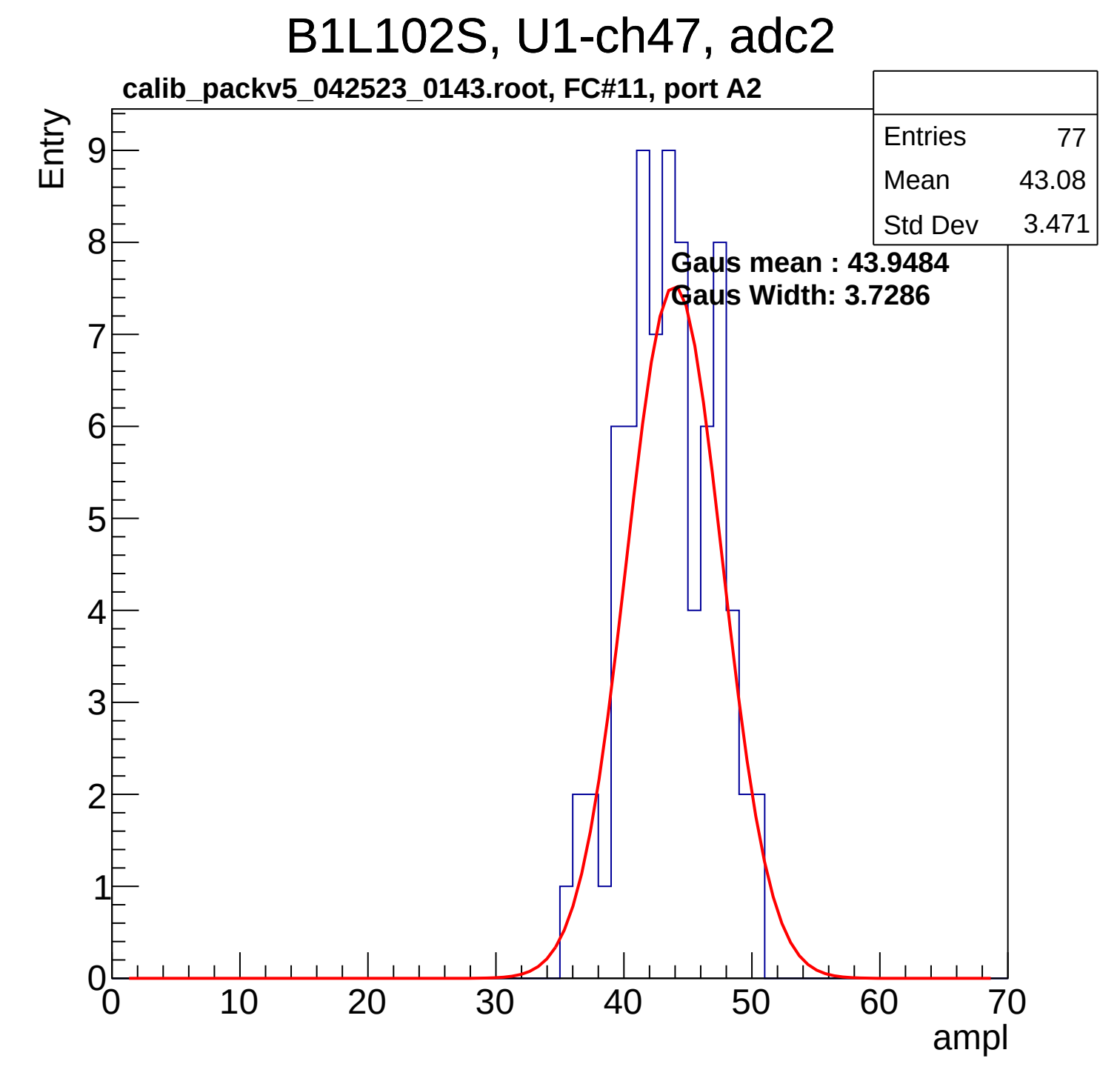
B1L102S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 43.08
Std Dev 3.471
Gaus mean : 43.9484
Gaus Width: 3.7286
Entry
ampl
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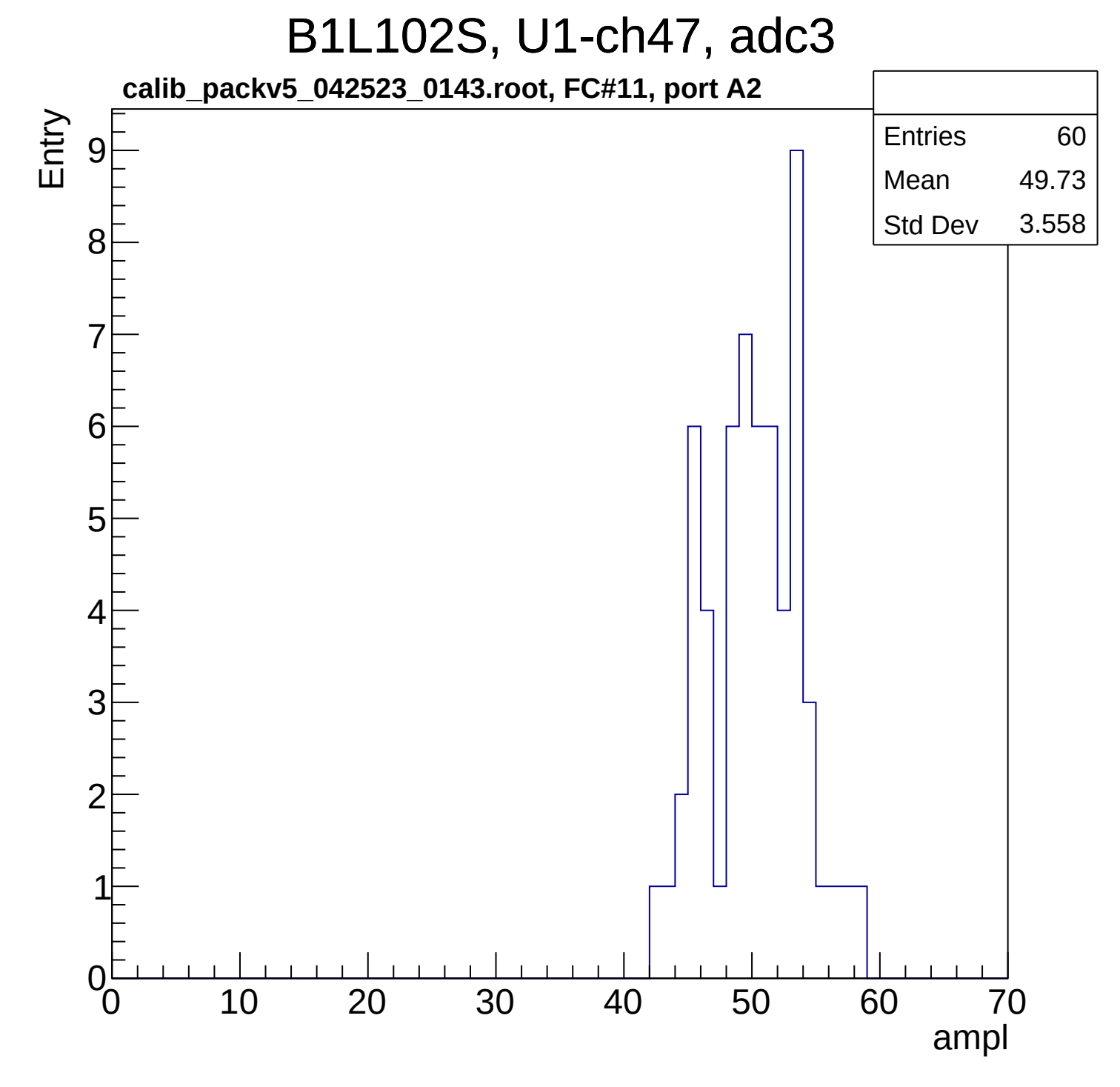
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 49.73
Std Dev 3.558
Entry
ampl
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2
3
4
5
6
7
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9
0
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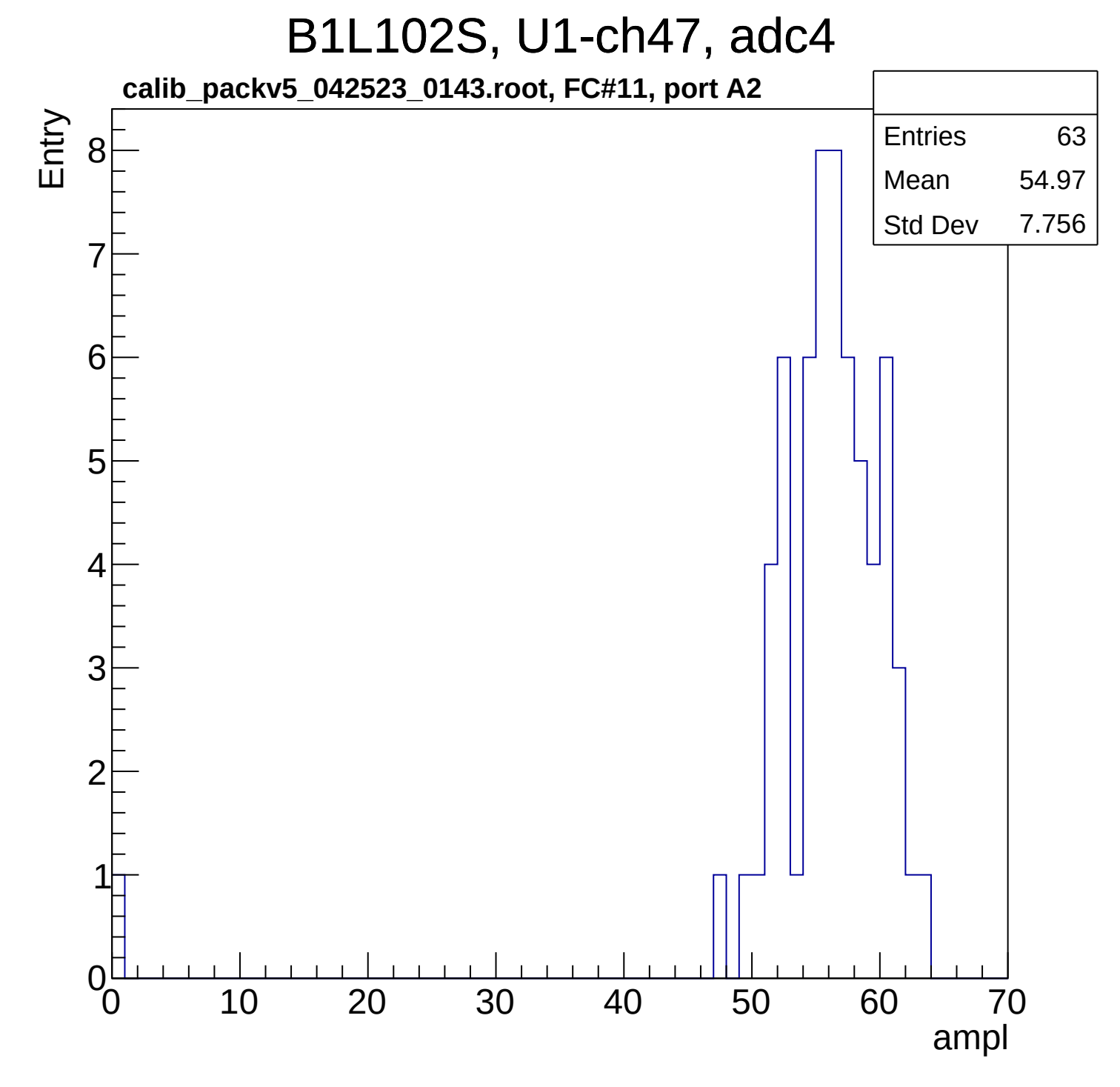
B1L102S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 54.97
Std Dev 7.756
Entry
ampl
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1
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7
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0
10
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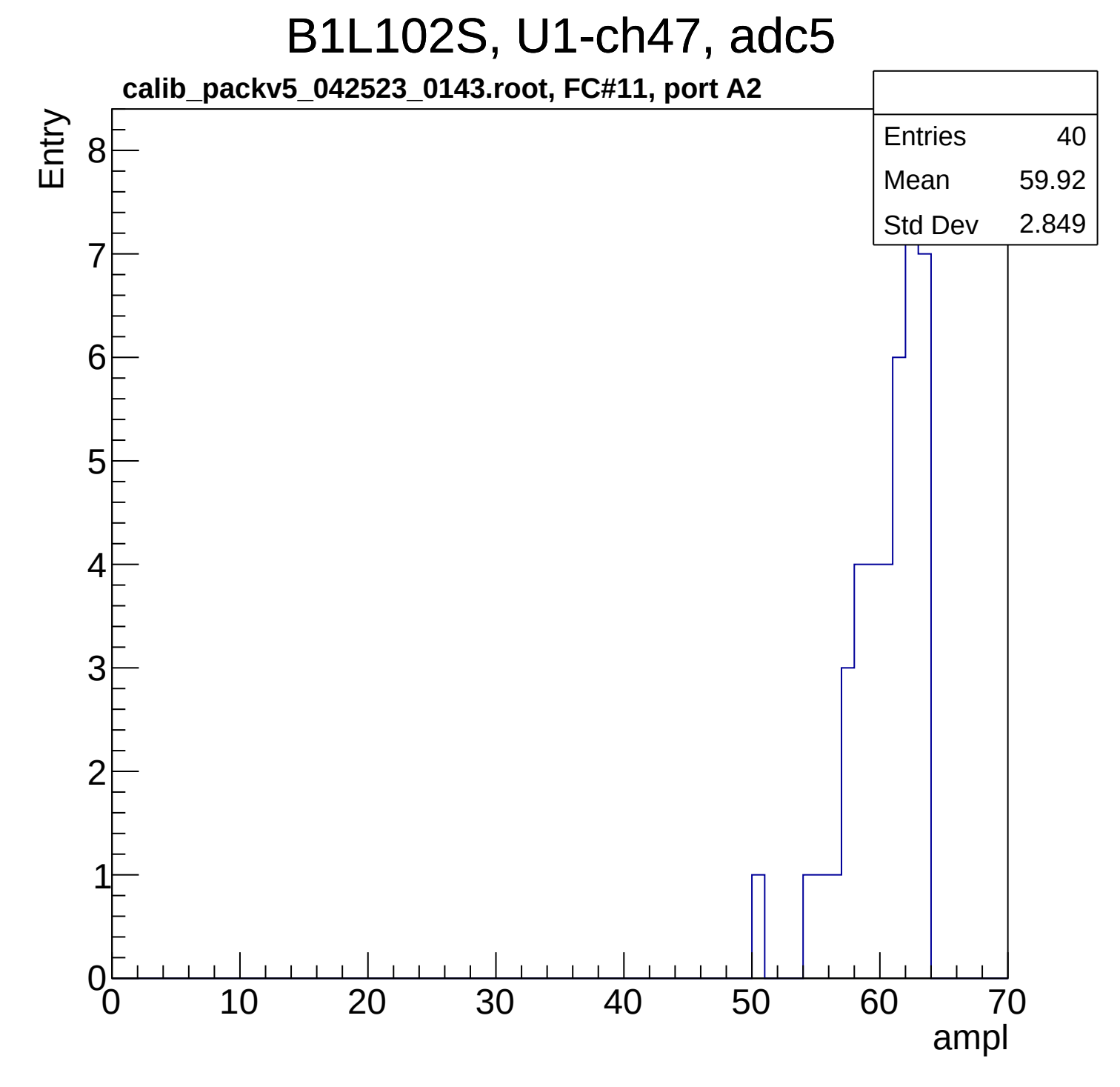
B1L102S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 59.92
Std Dev 2.849
Entry
ampl
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5
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7
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0
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20
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60
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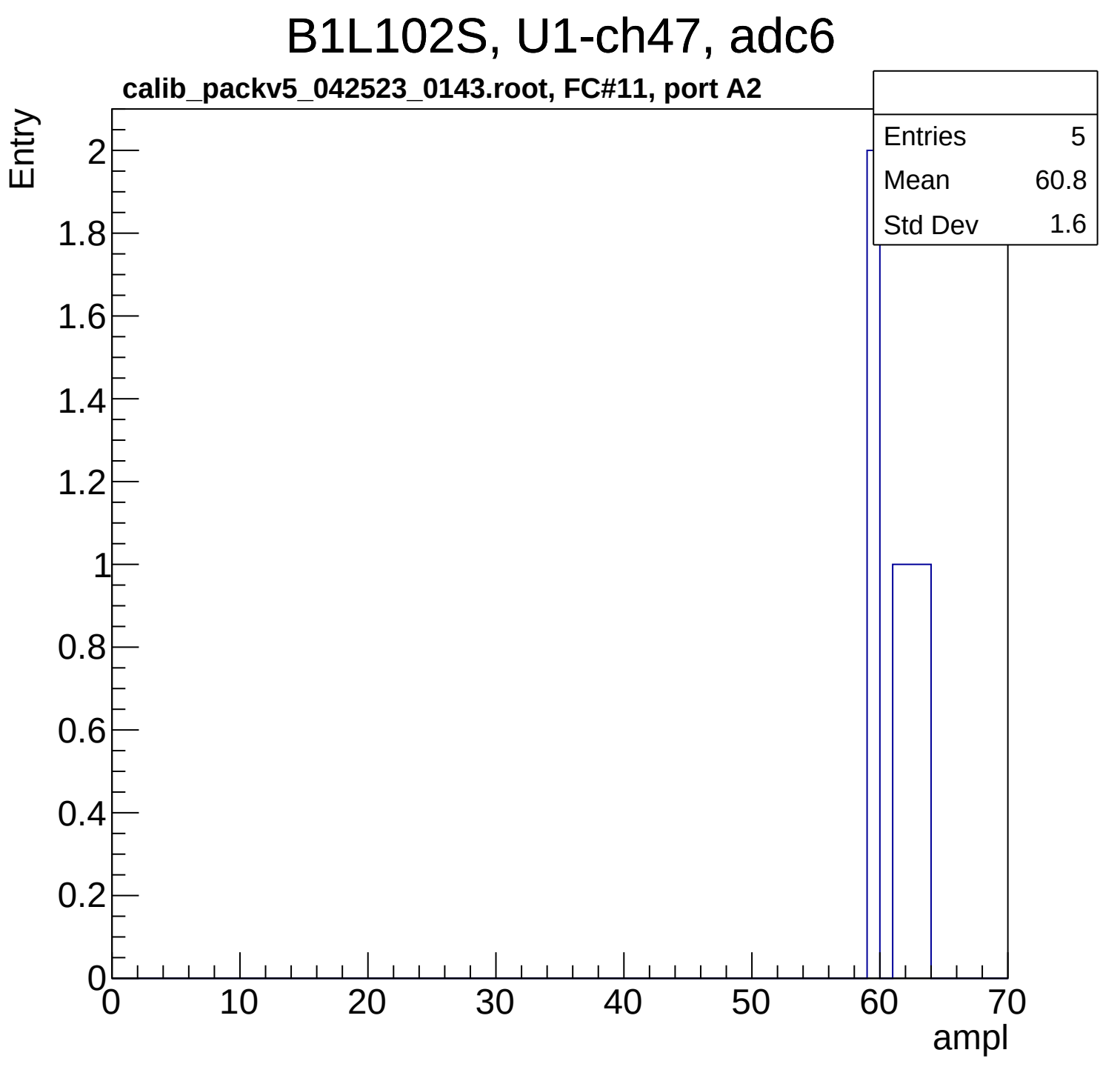
B1L102S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 60.8
Std Dev 1.6
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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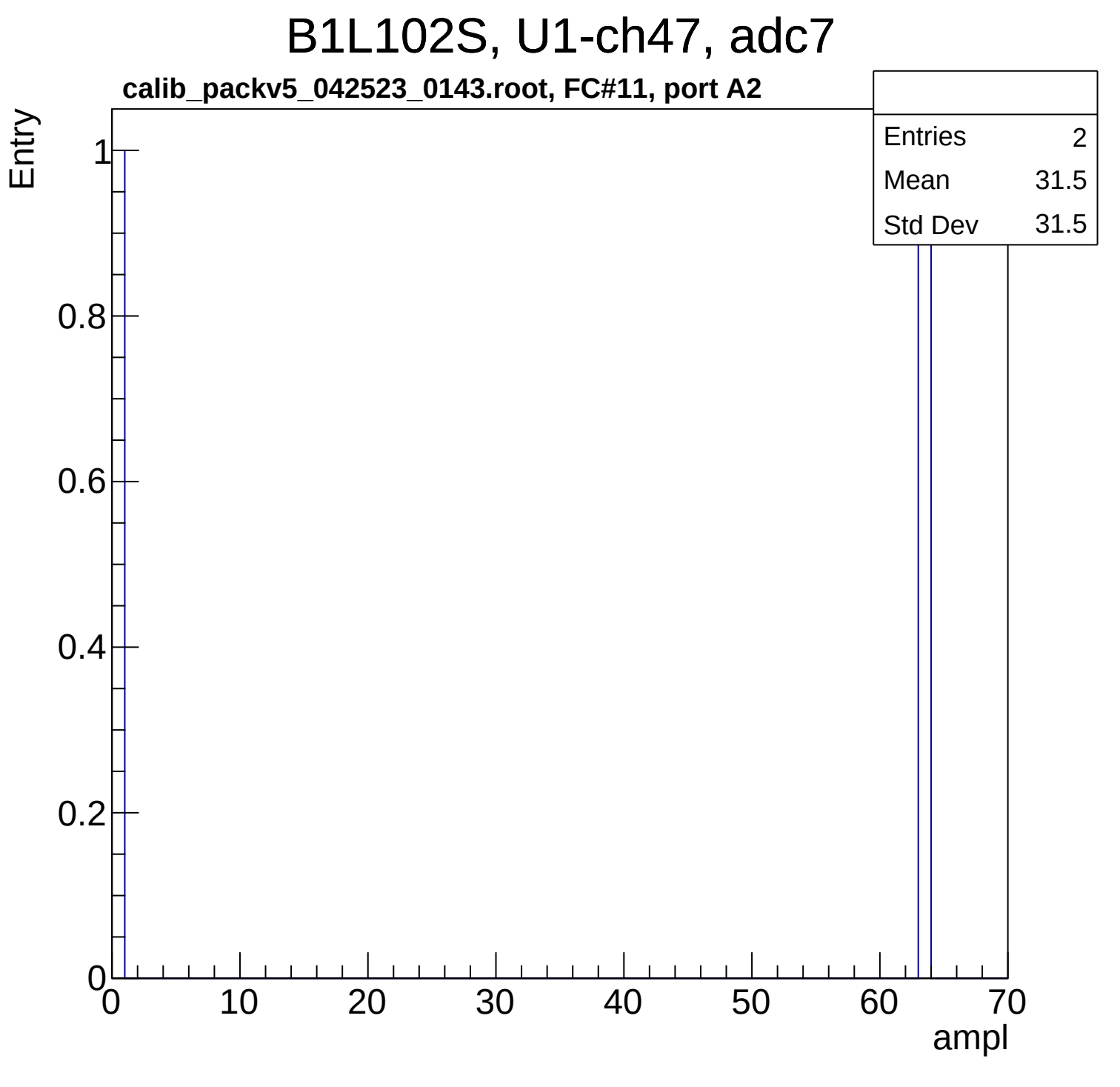
40
50
60
70


B1L102S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
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10
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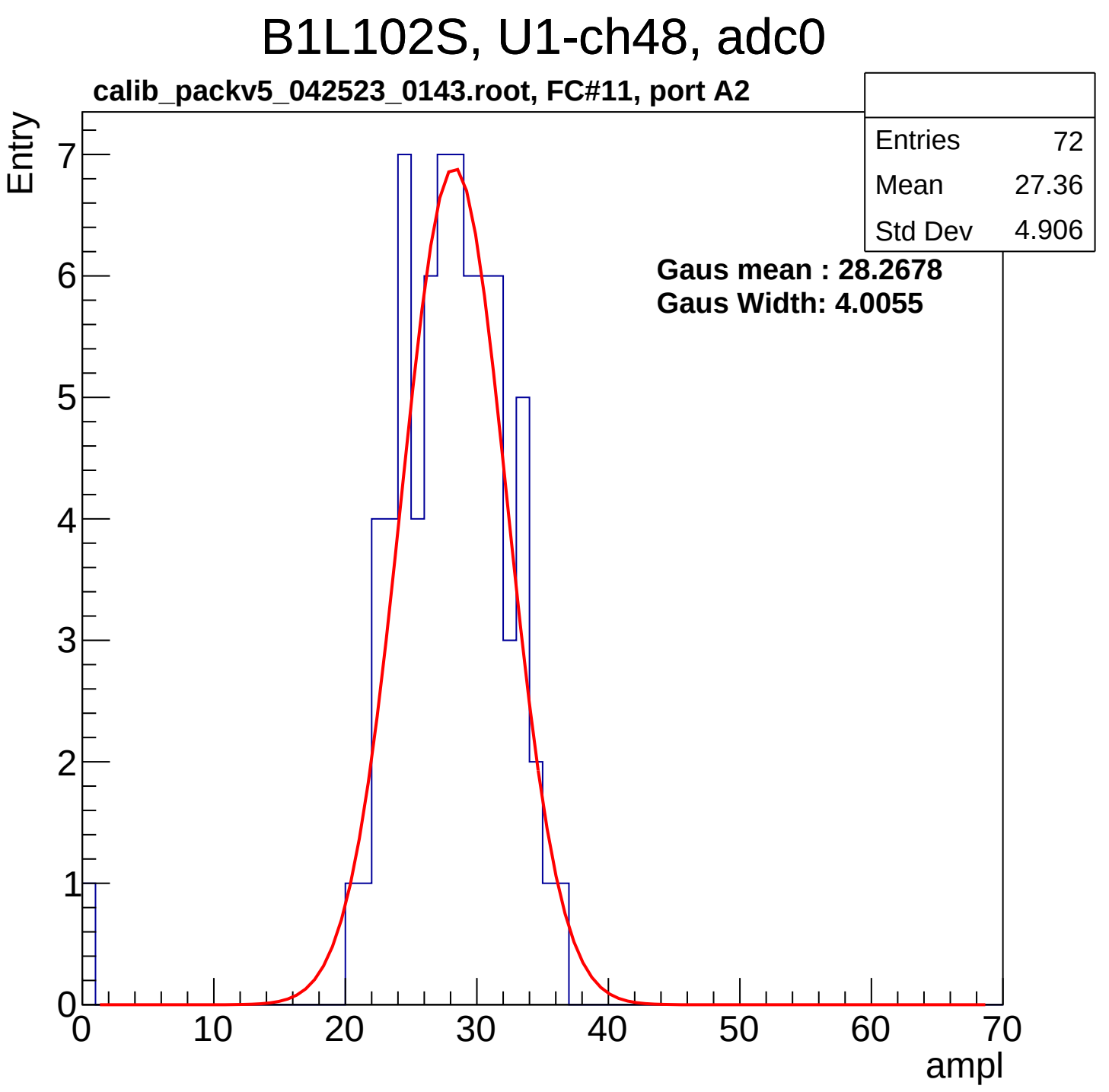
50
60
70
ampl


B1L102S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 27.36
Std Dev 4.906
Gaus mean : 28.2678
Gaus Width: 4.0055
Entry
ampl
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2
3
4
5
6
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0
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40
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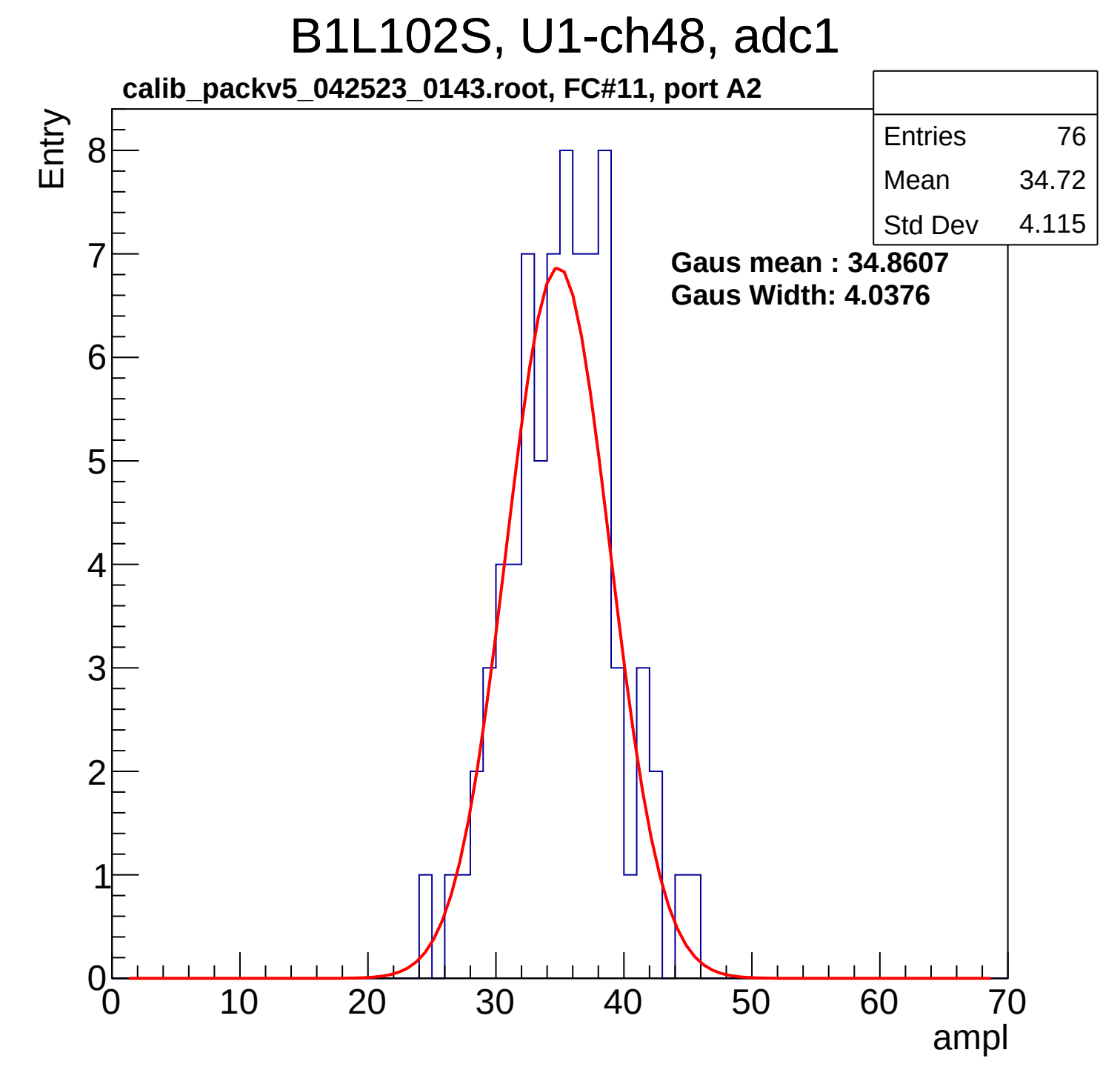
60
70


B1L102S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 34.72
Std Dev 4.115
Gaus mean : 34.8607
Gaus Width: 4.0376
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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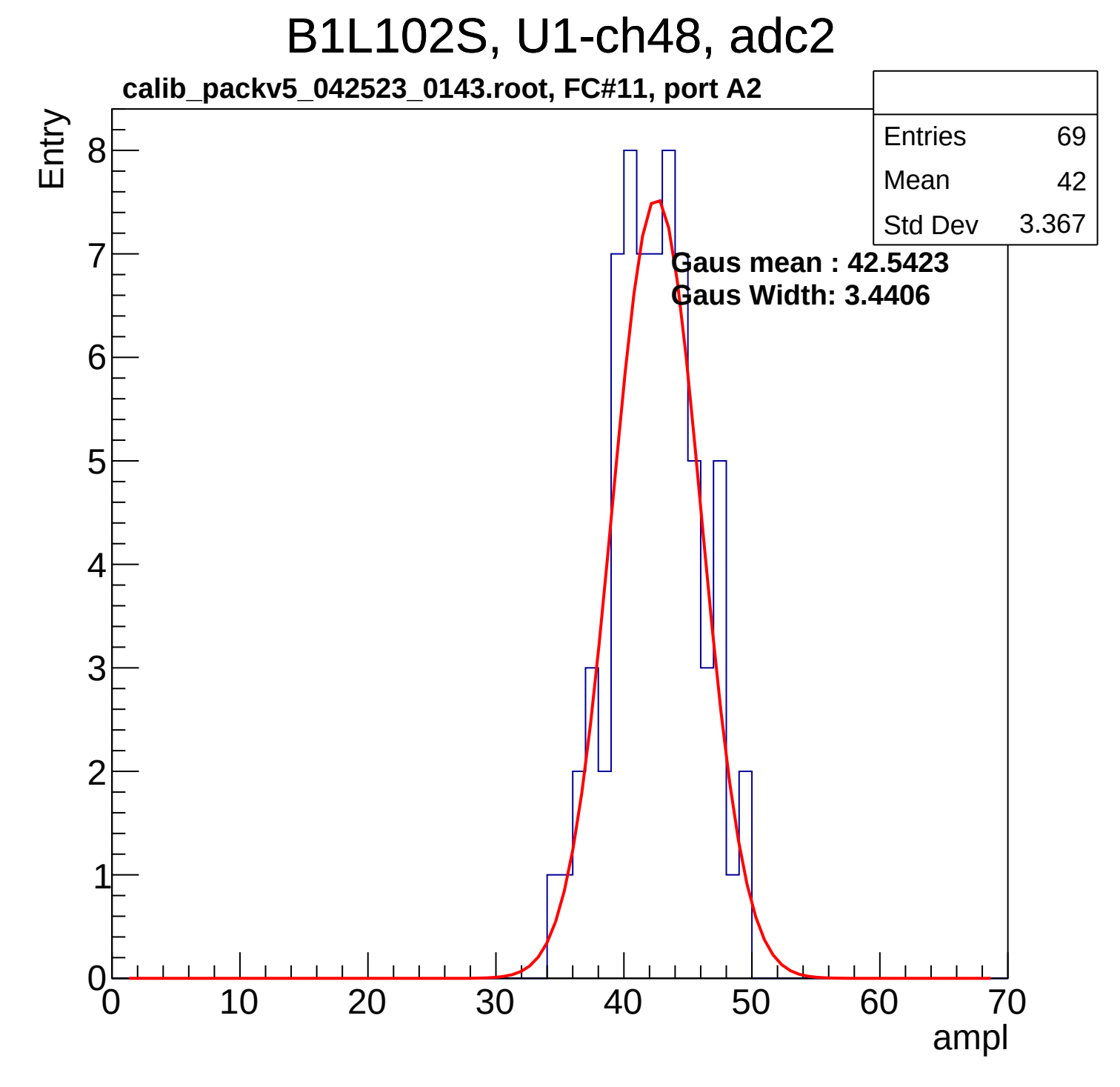
B1L102S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 42
Std Dev 3.367
Gaus mean : 42.5423
Gaus Width: 3.4406
Entry
ampl
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1
2
3
4
5
6
7
8
0
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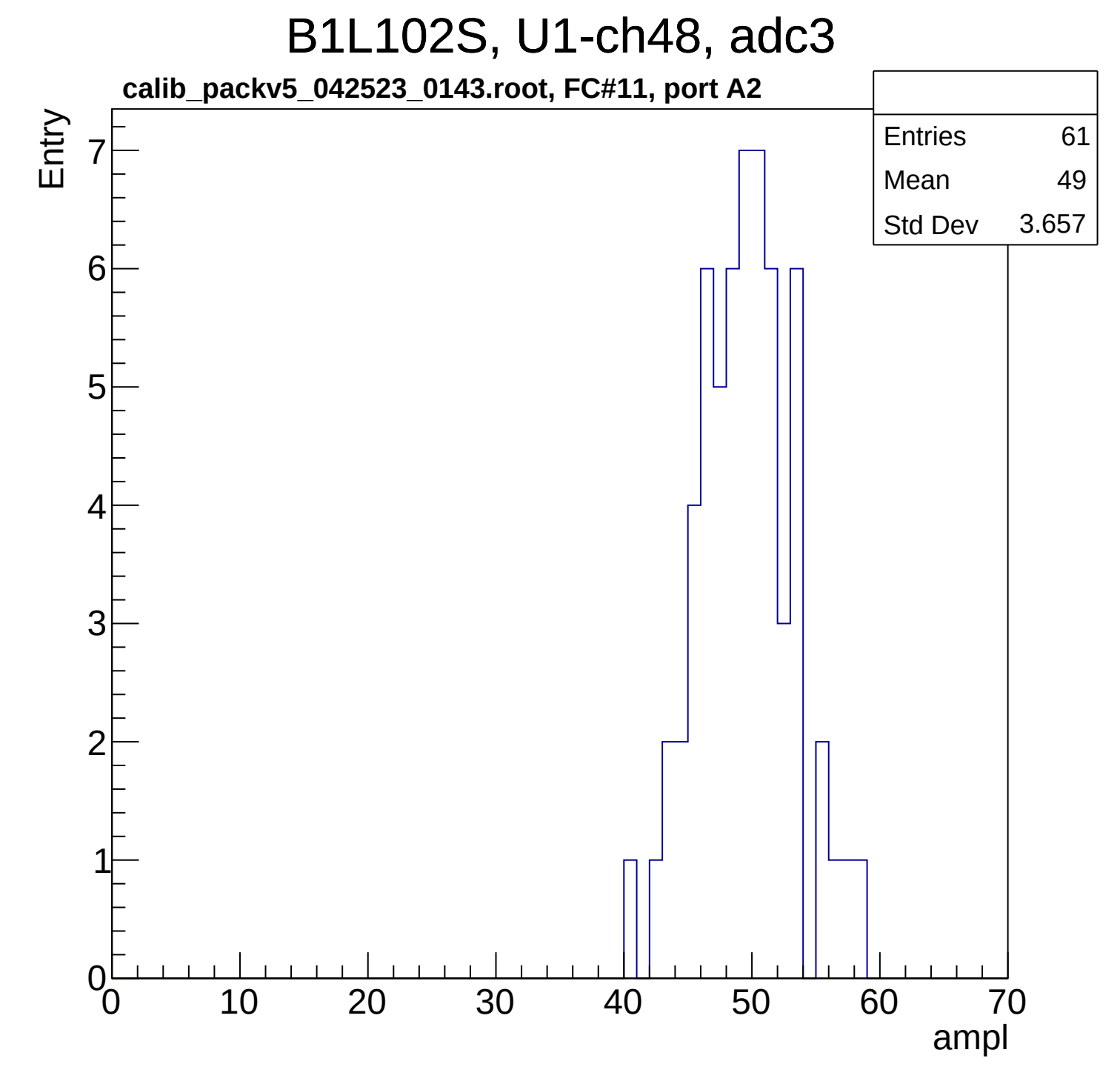
B1L102S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 49
Std Dev 3.657
Entry
ampl
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4
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0
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60
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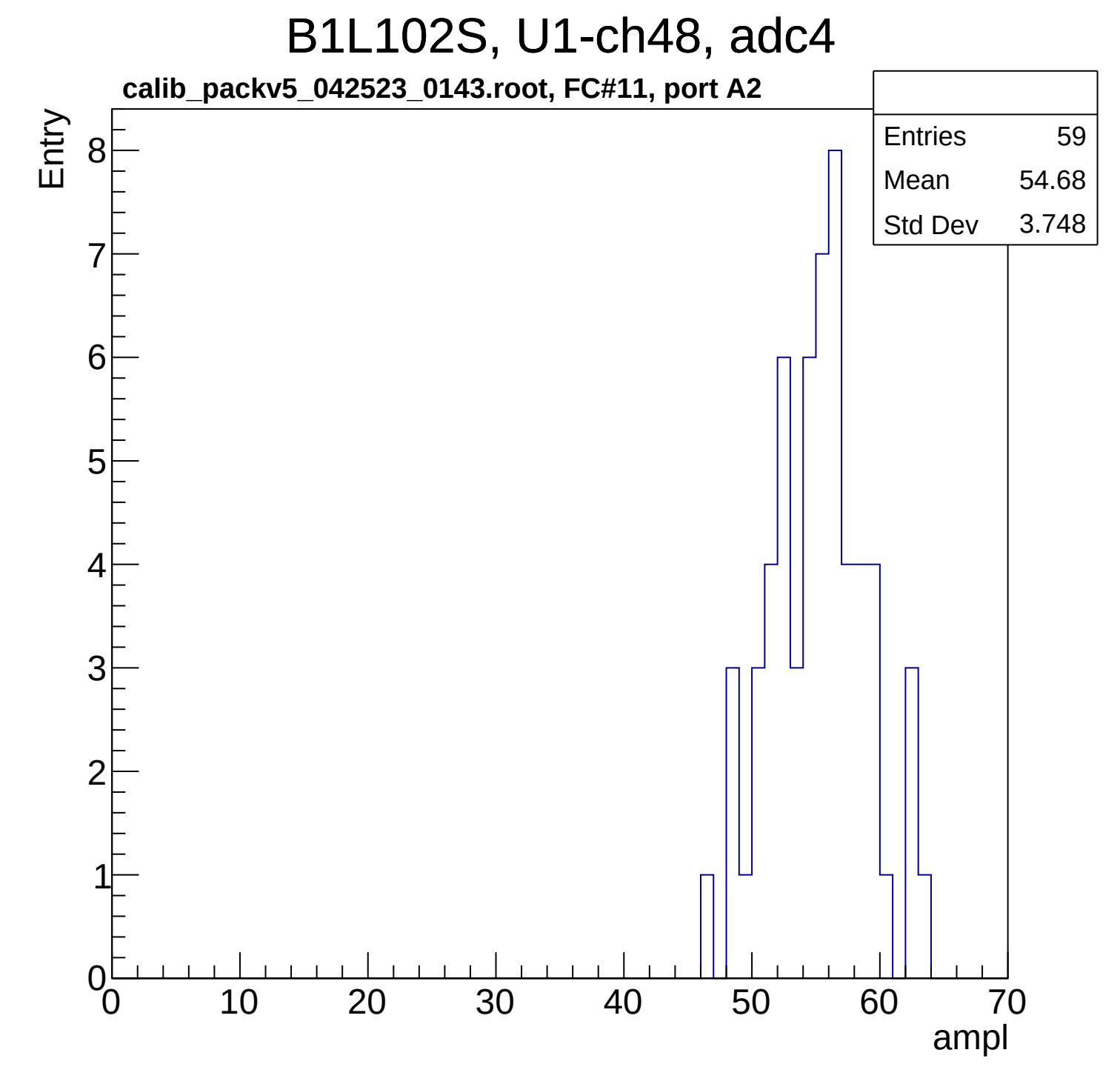
B1L102S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 54.68
Std Dev 3.748
Entry
ampl
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4
5
6
7
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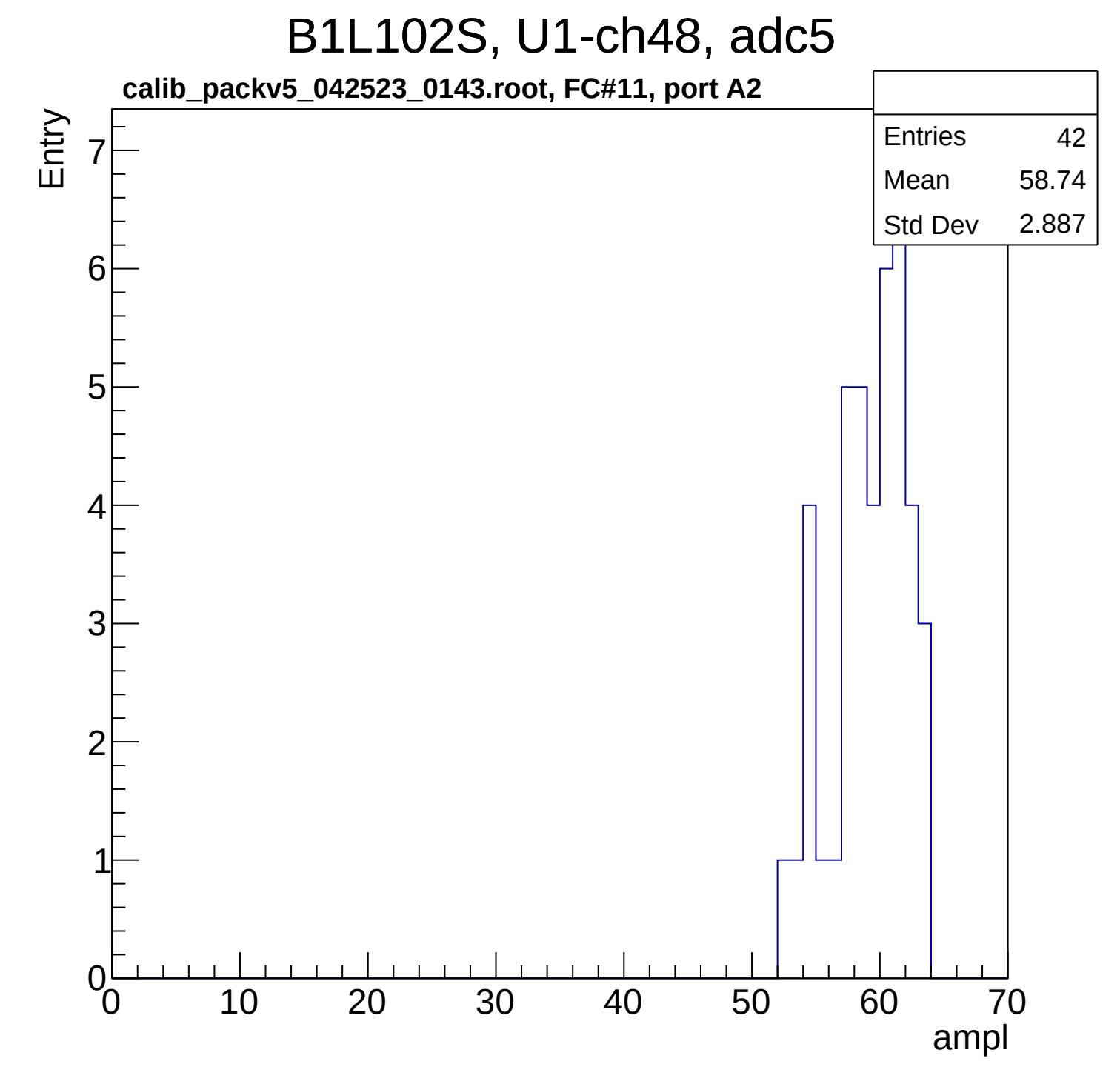
B1L102S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.74
Std Dev 2.887
Entry
ampl
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1
2
3
4
5
6
7
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10
20
30
40
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60
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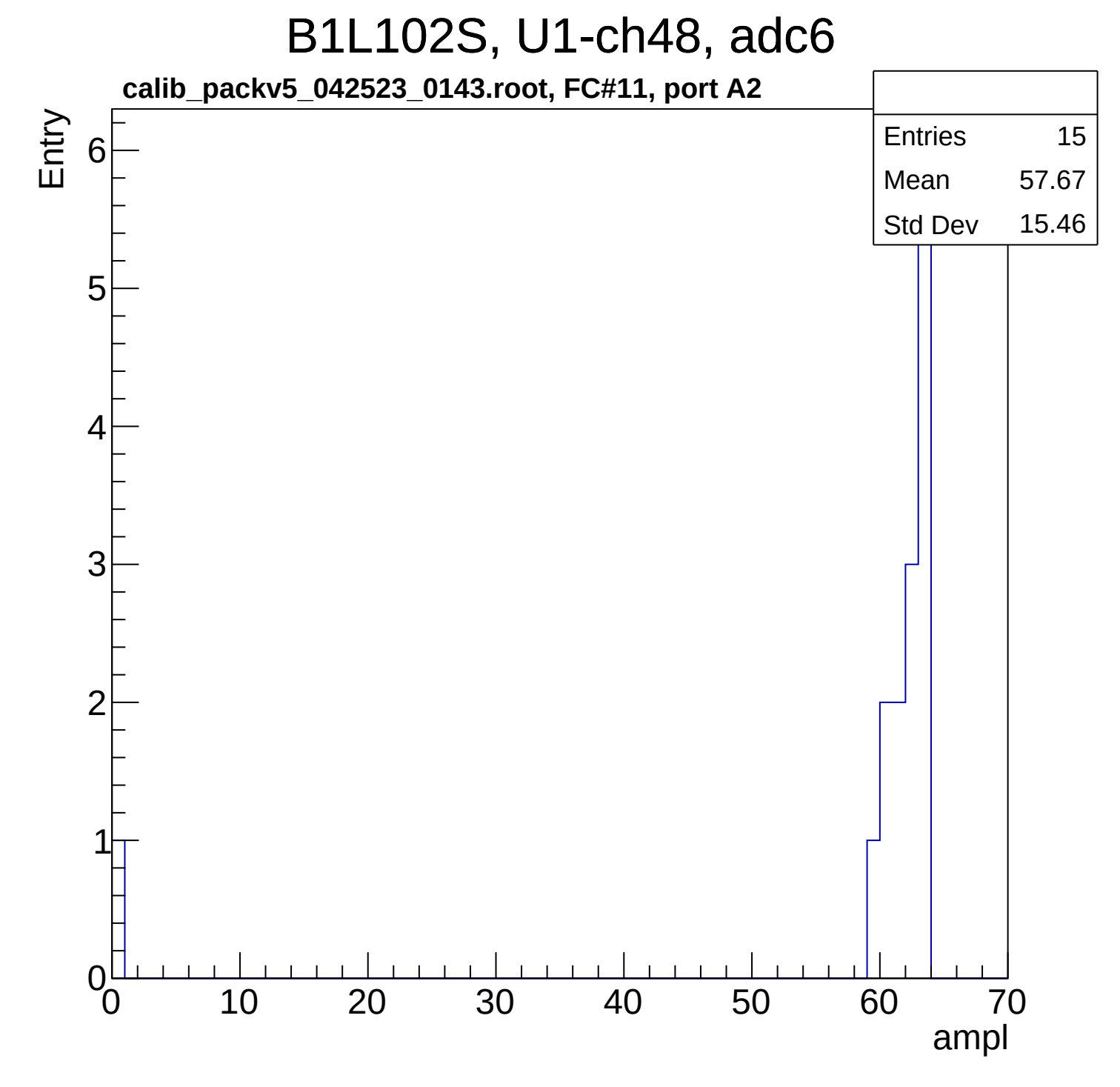
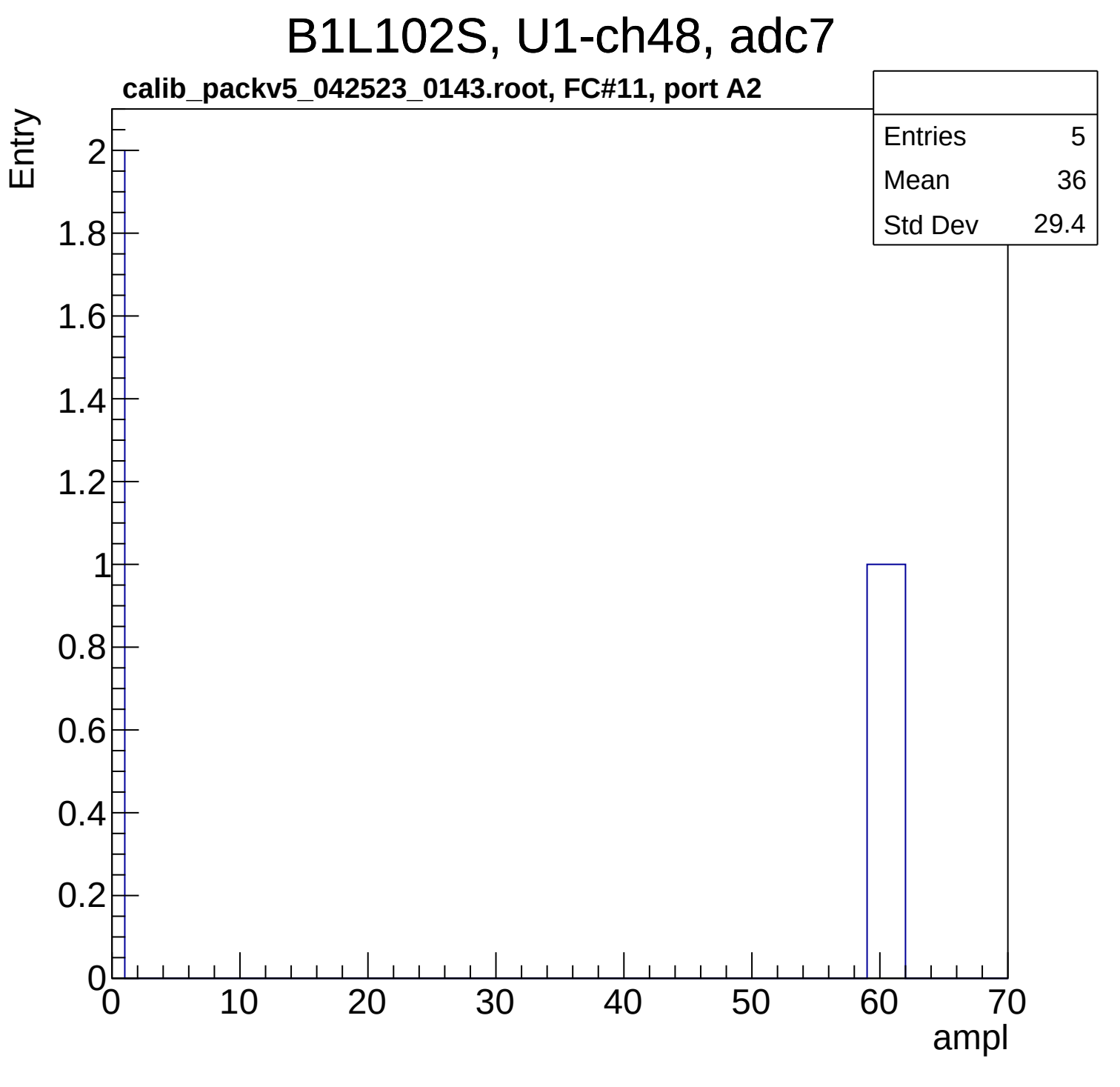
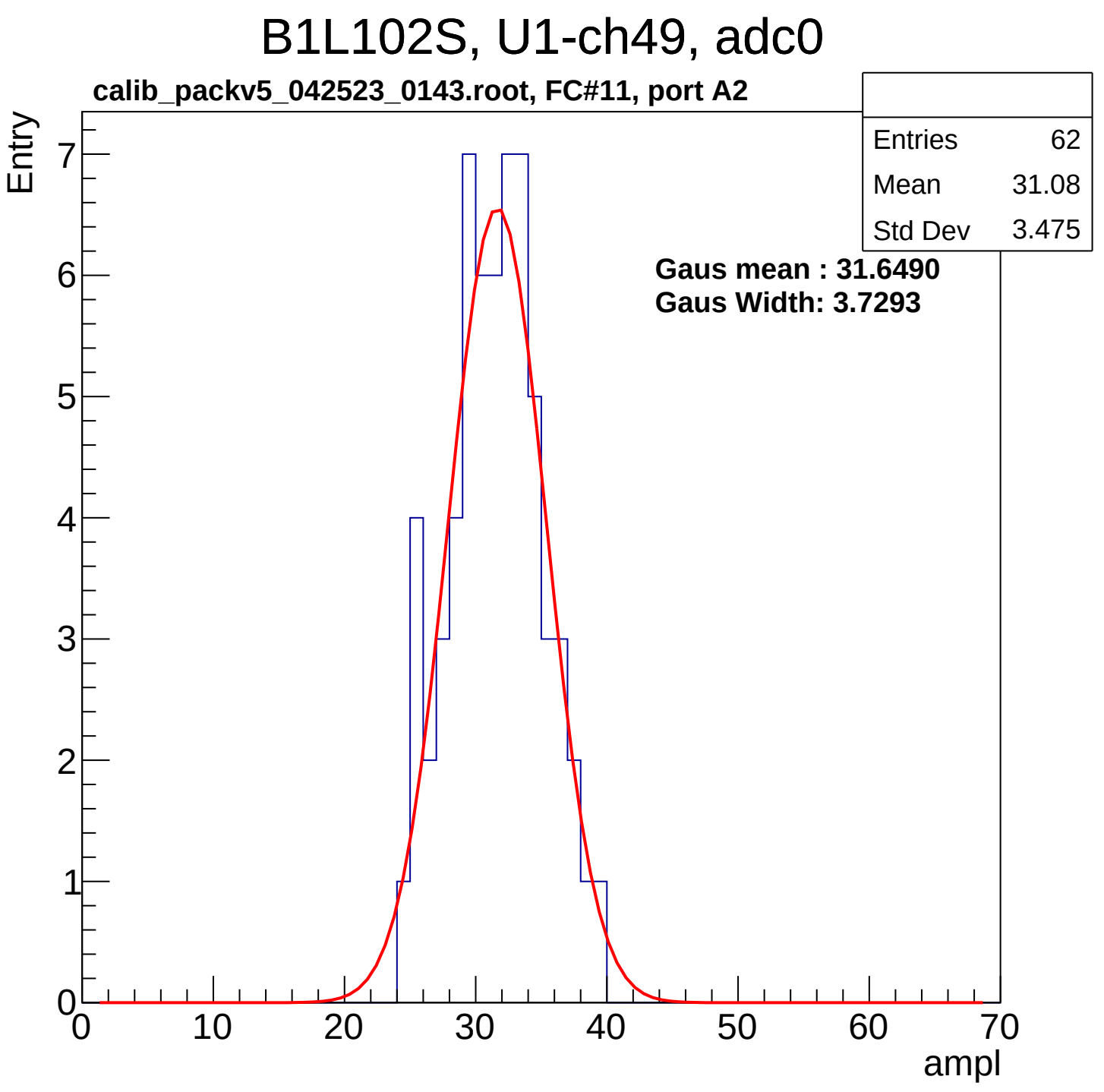
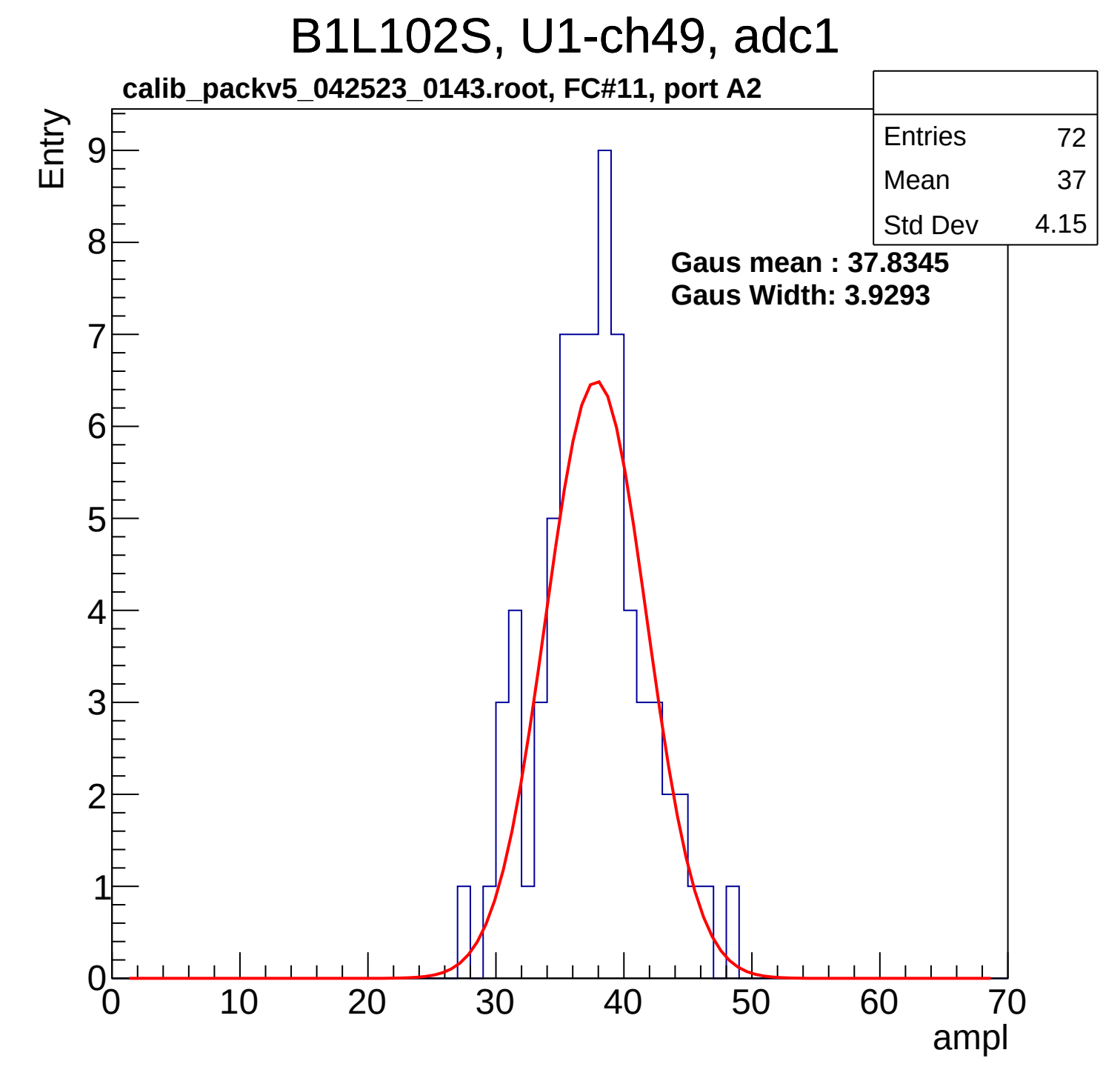
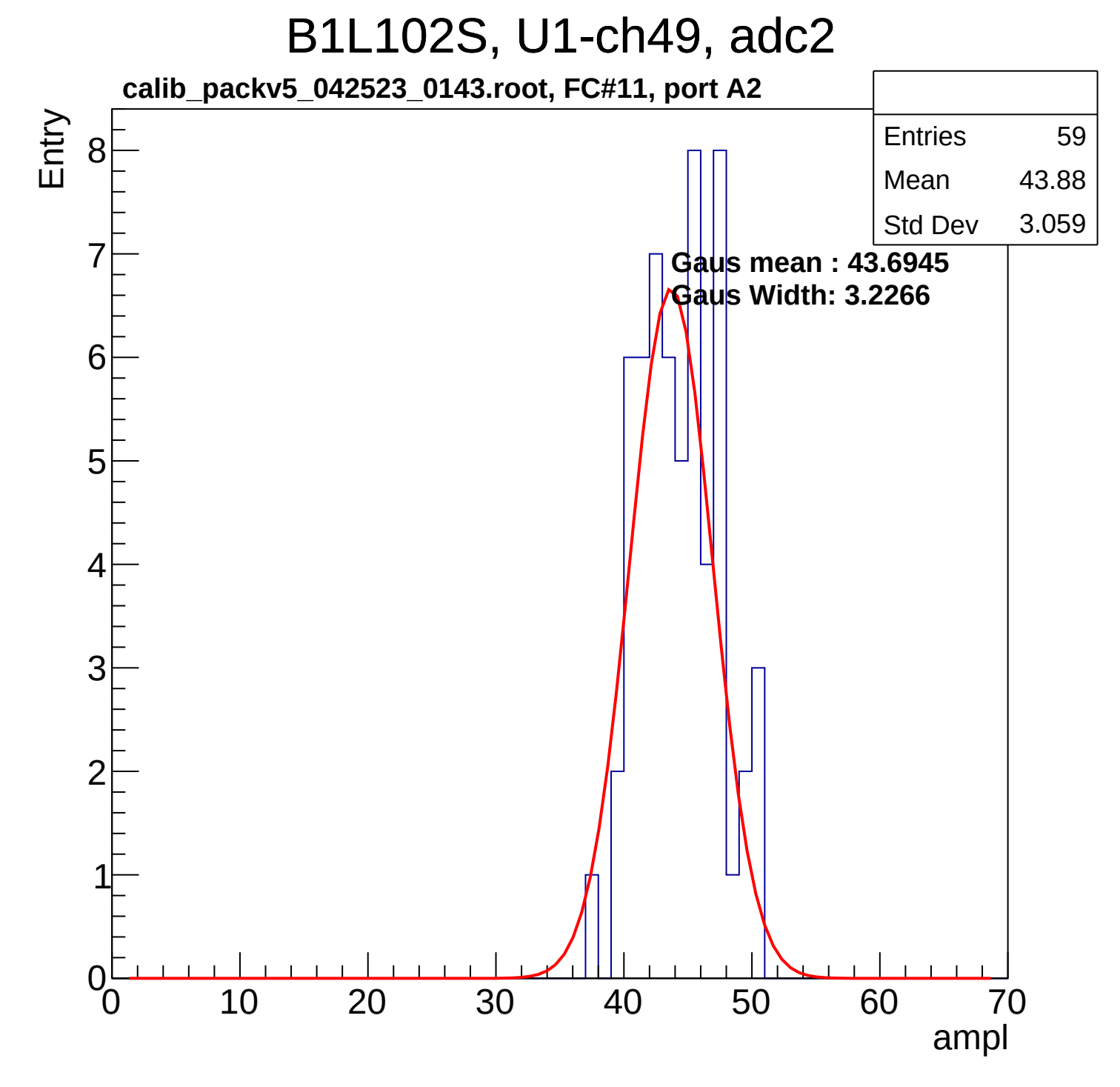
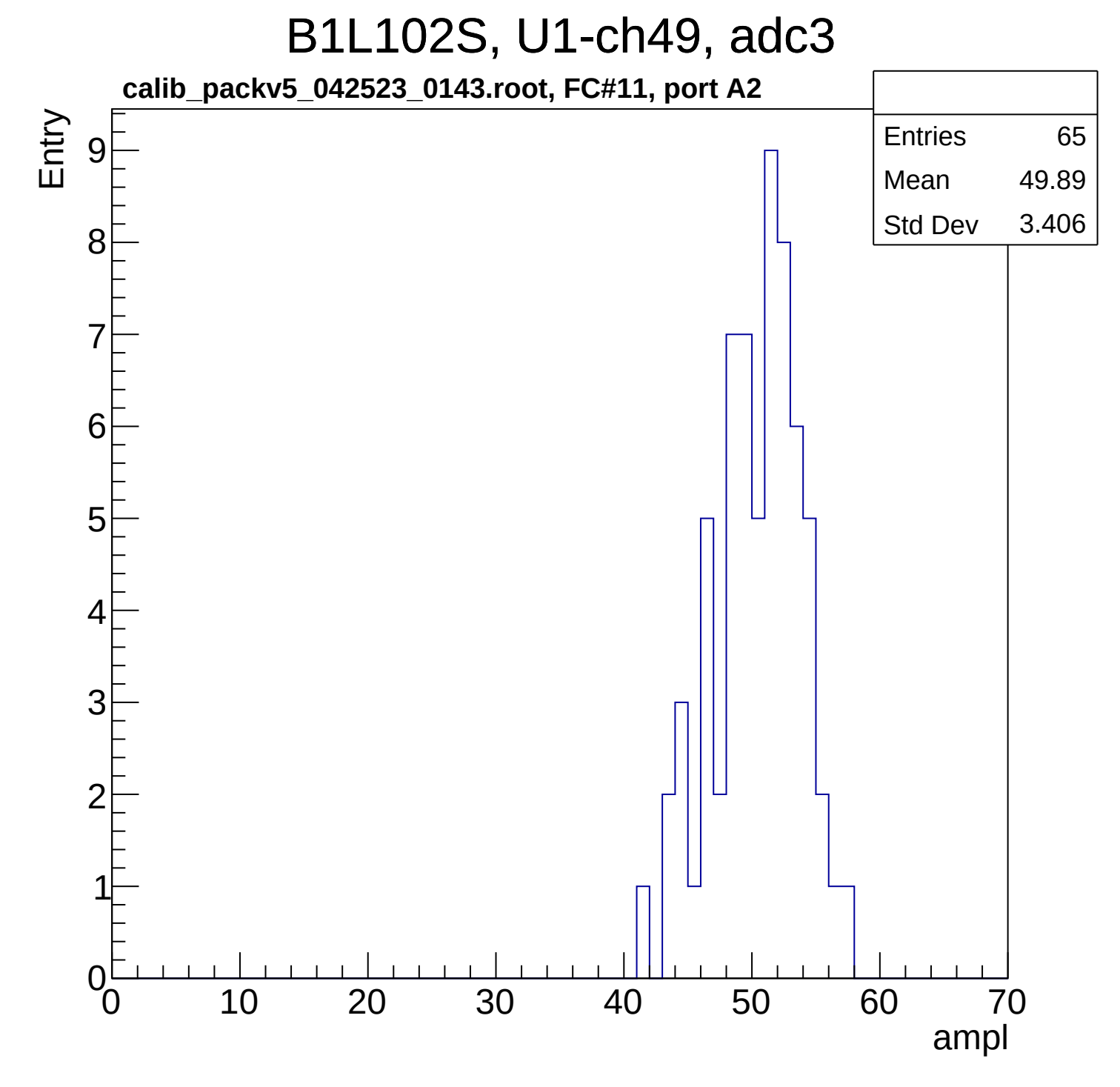

B1L102S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 57.67
Std Dev 15.46
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

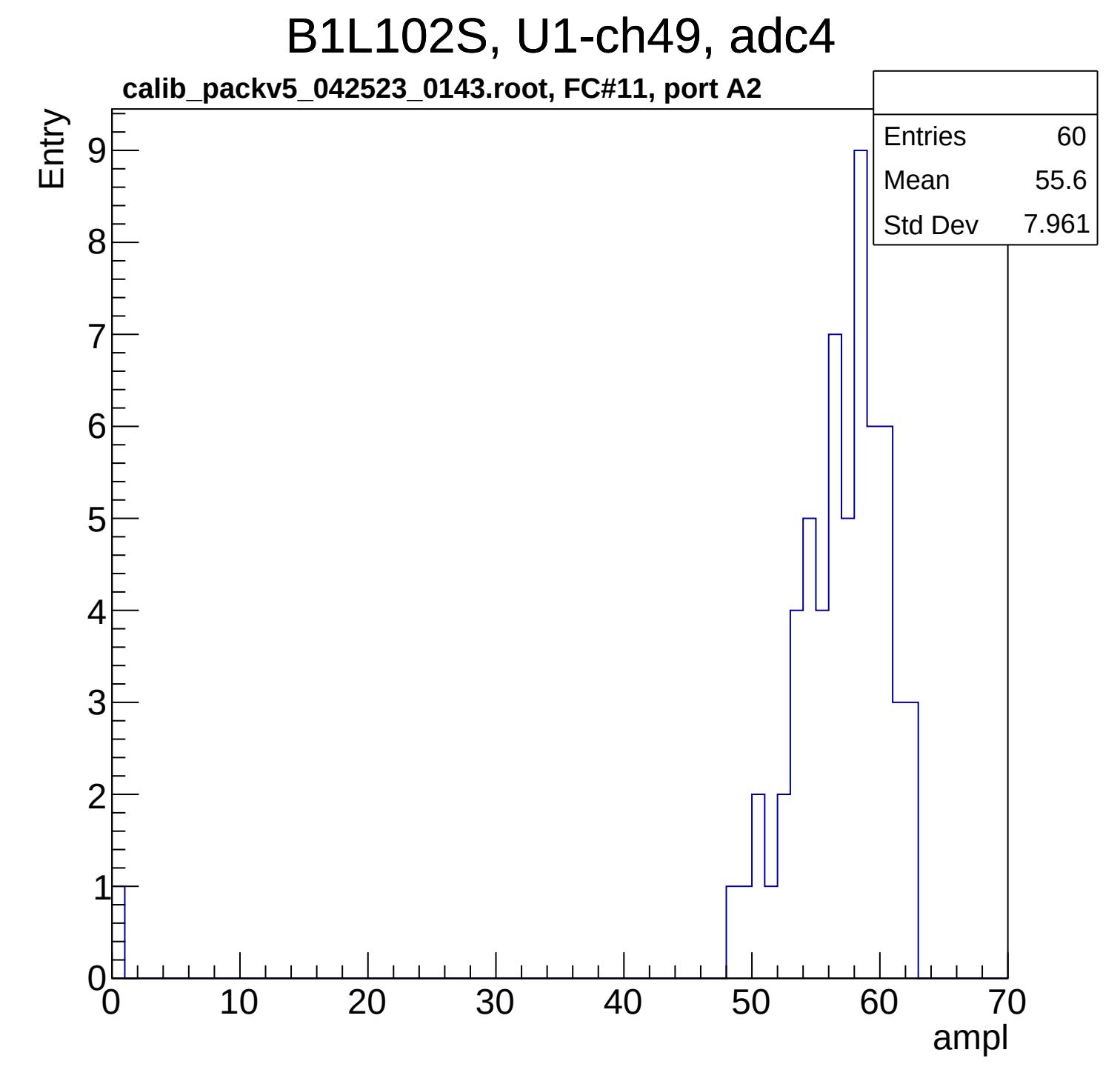

B1L102S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 36
Std Dev 29.4
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

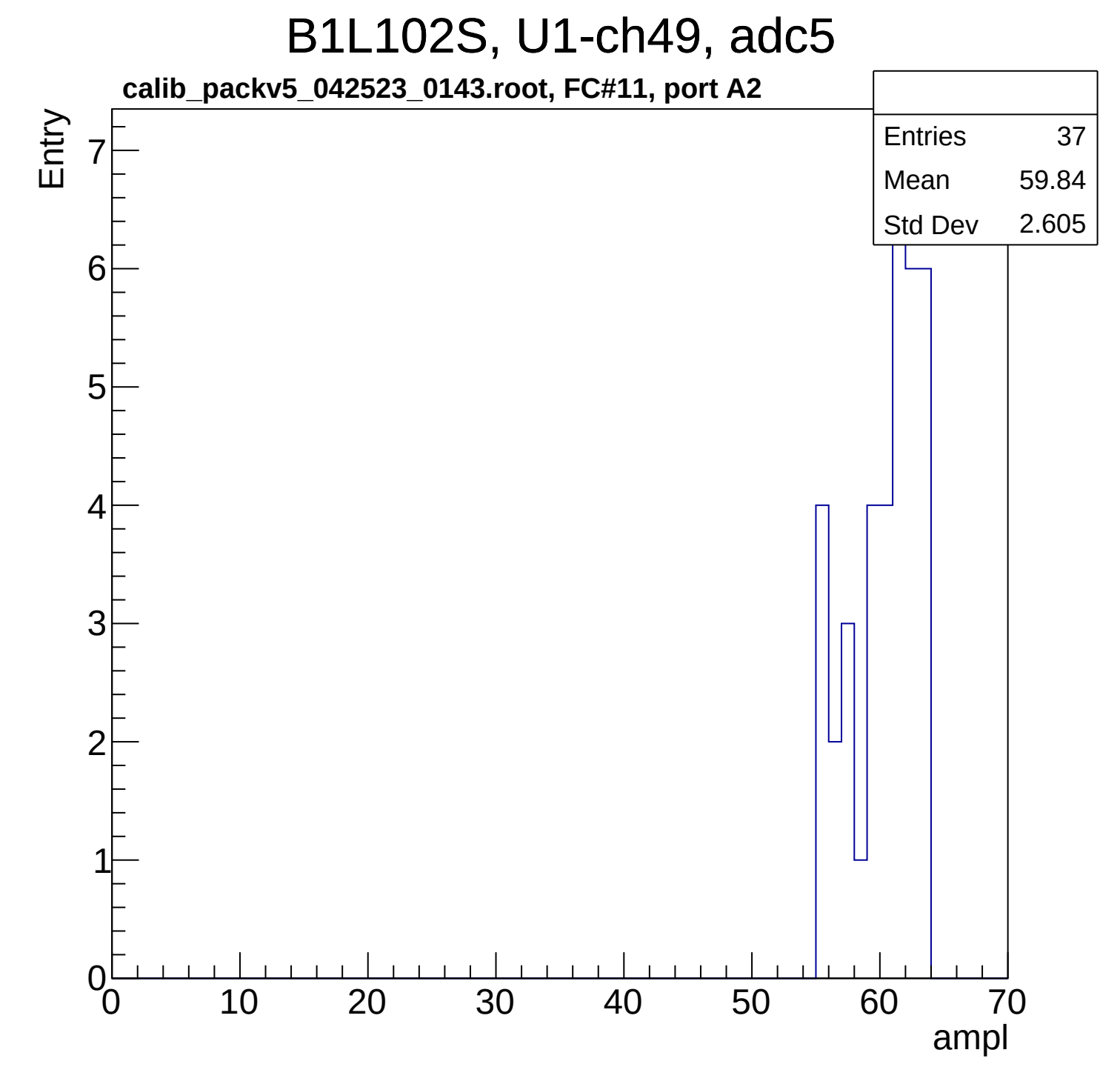

B1L102S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 31.08
Std Dev 3.475
Gaus mean : 31.6490
Gaus Width: 3.7293
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

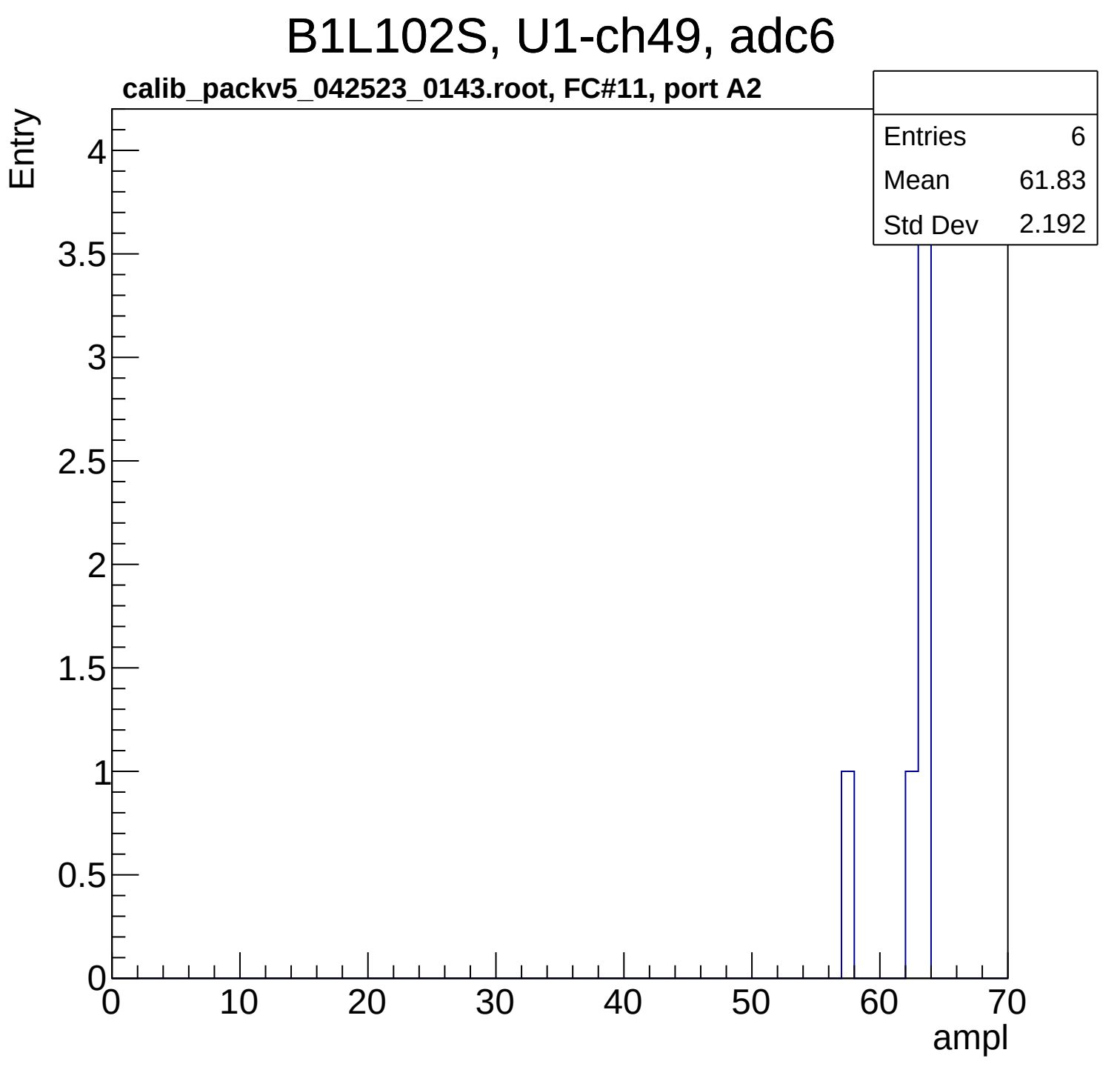

B1L102S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 37
Std Dev 4.15
Gaus mean : 37.8345
Gaus Width: 3.9293
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

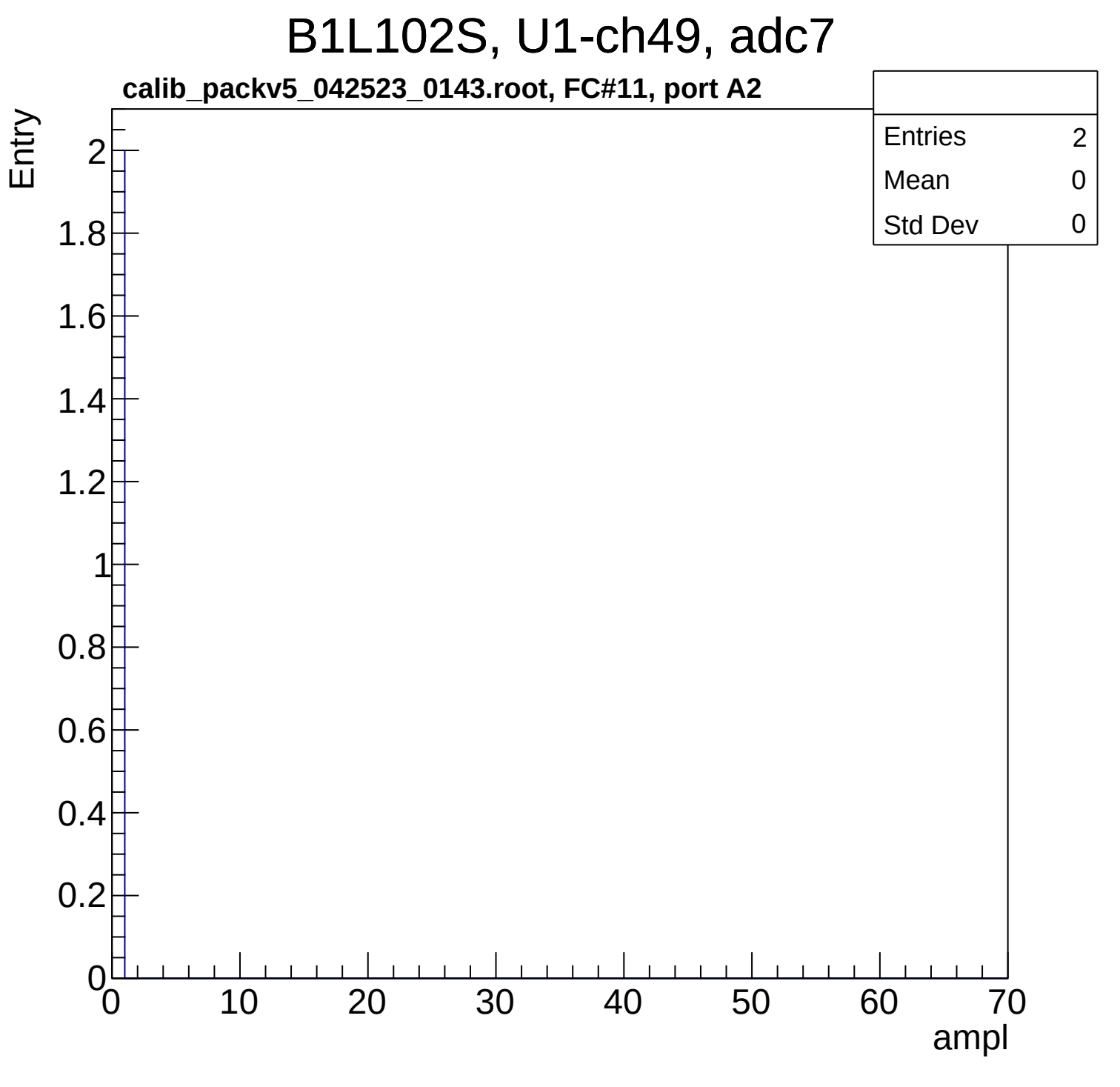

B1L102S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 43.88
Std Dev 3.059
Gaus mean : 43.6945
Gaus Width: 3.2266
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

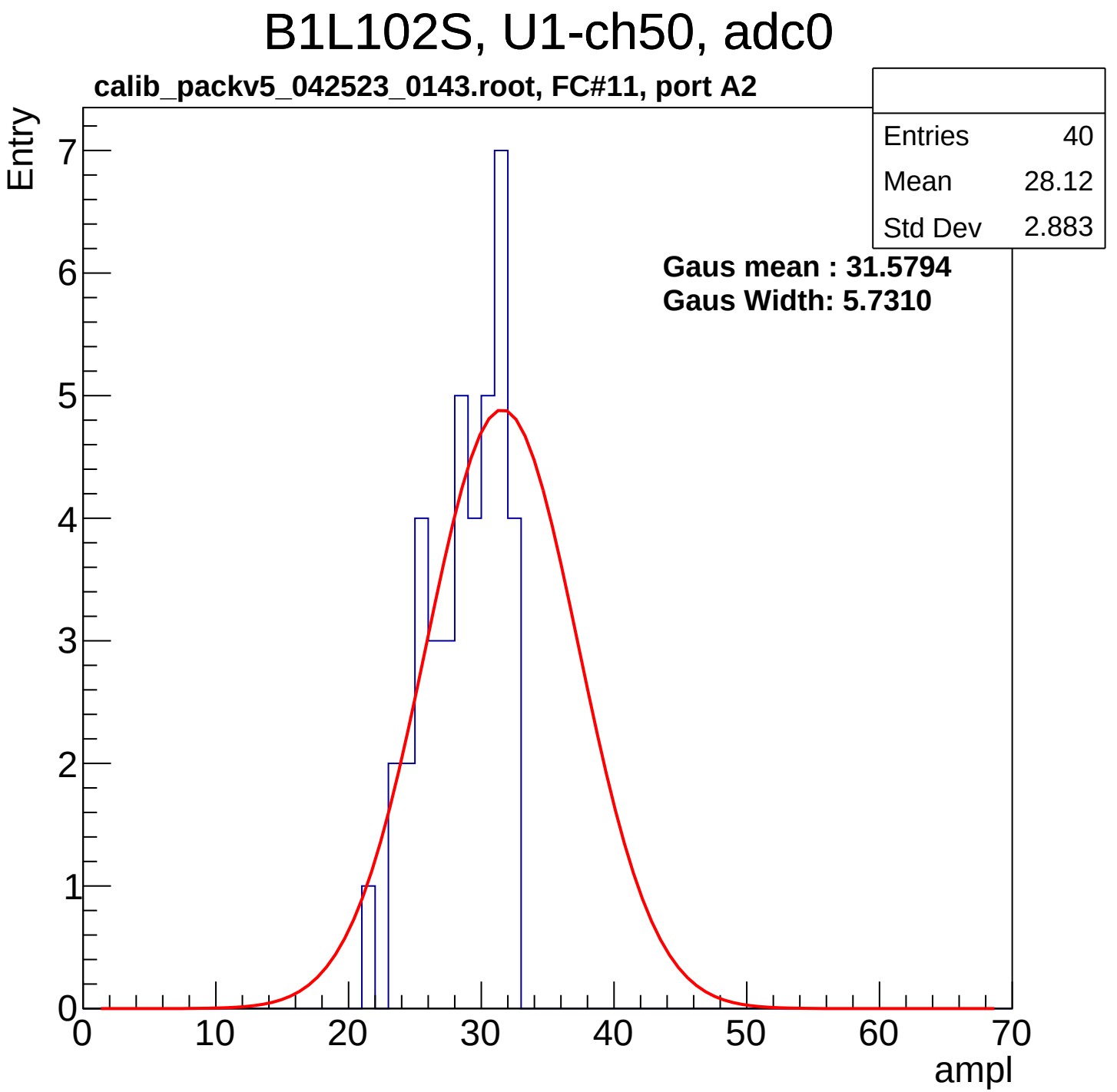

B1L102S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 49.89
Std Dev 3.406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

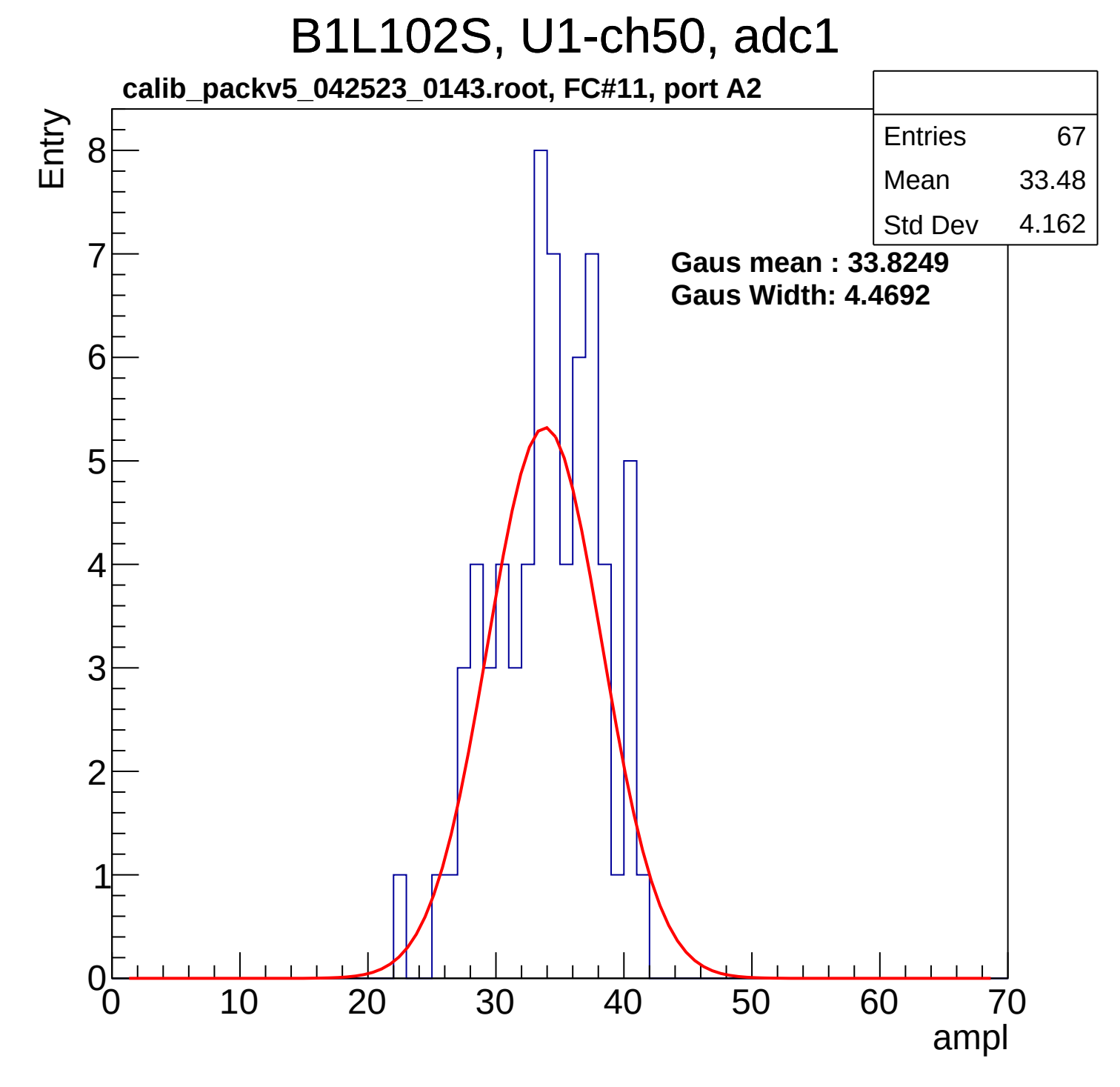

B1L102S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 55.6
Std Dev 7.961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

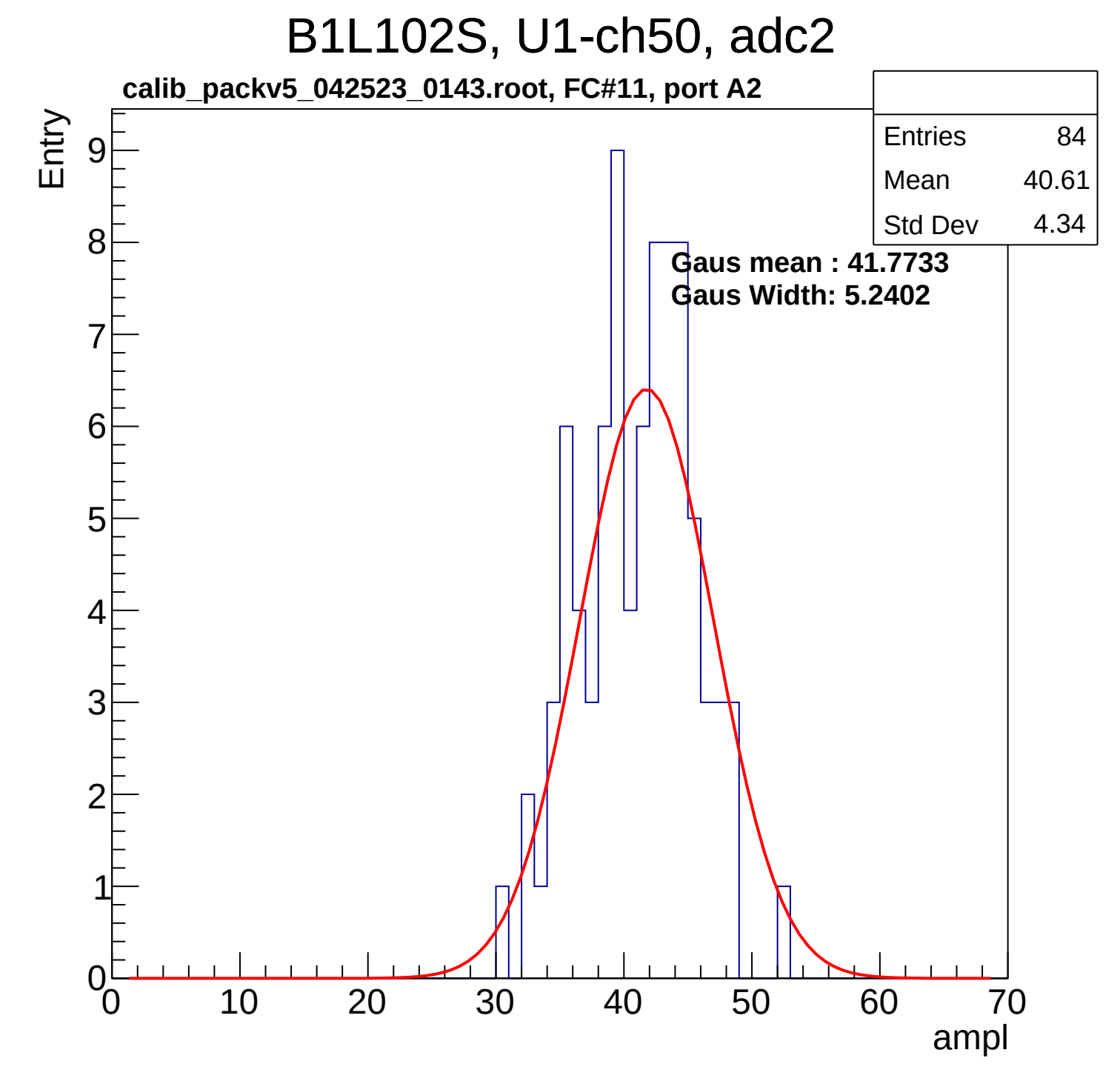

B1L102S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 59.84
Std Dev 2.605
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

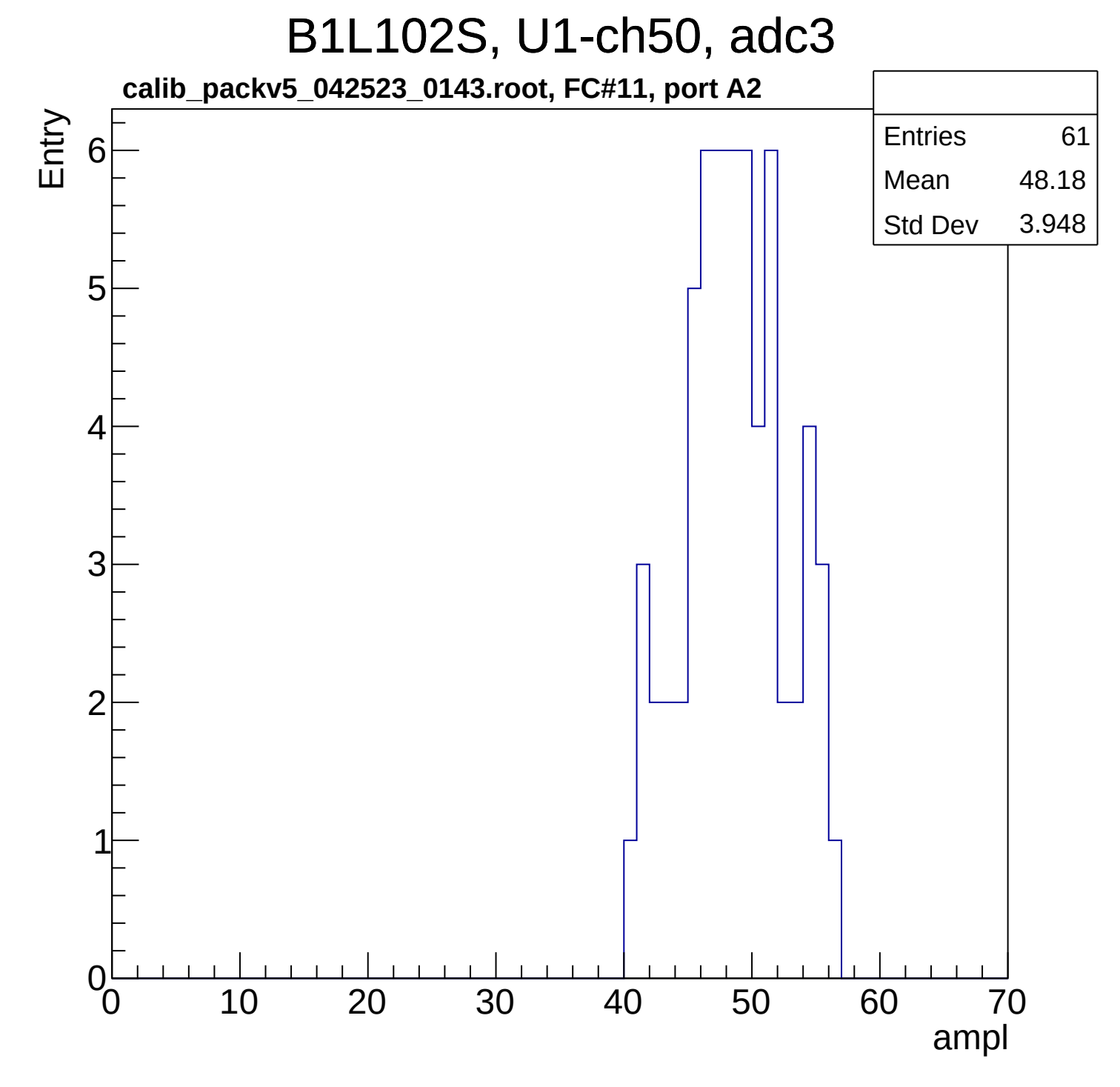

B1L102S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.83
Std Dev 2.192
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

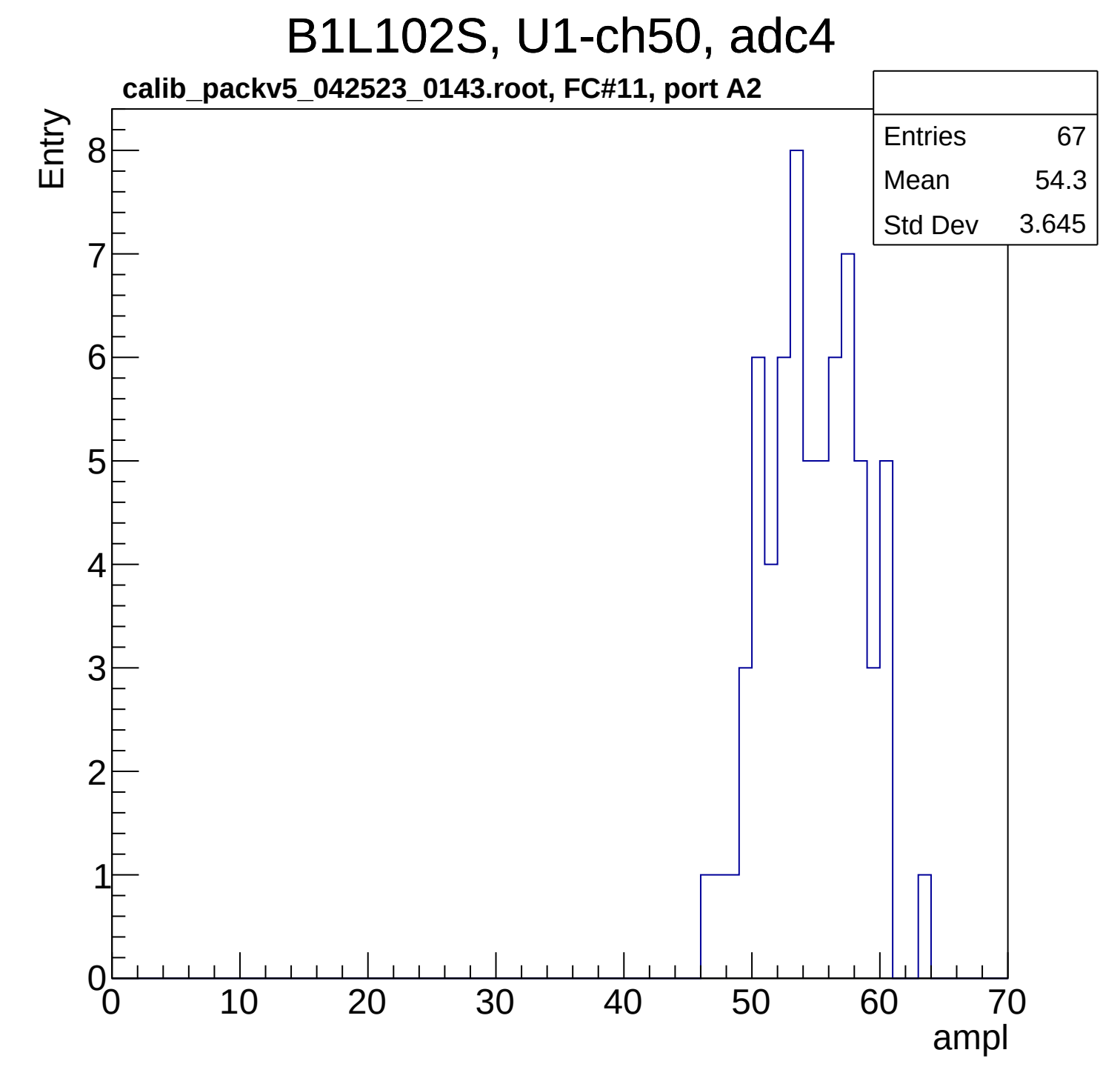

B1L102S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

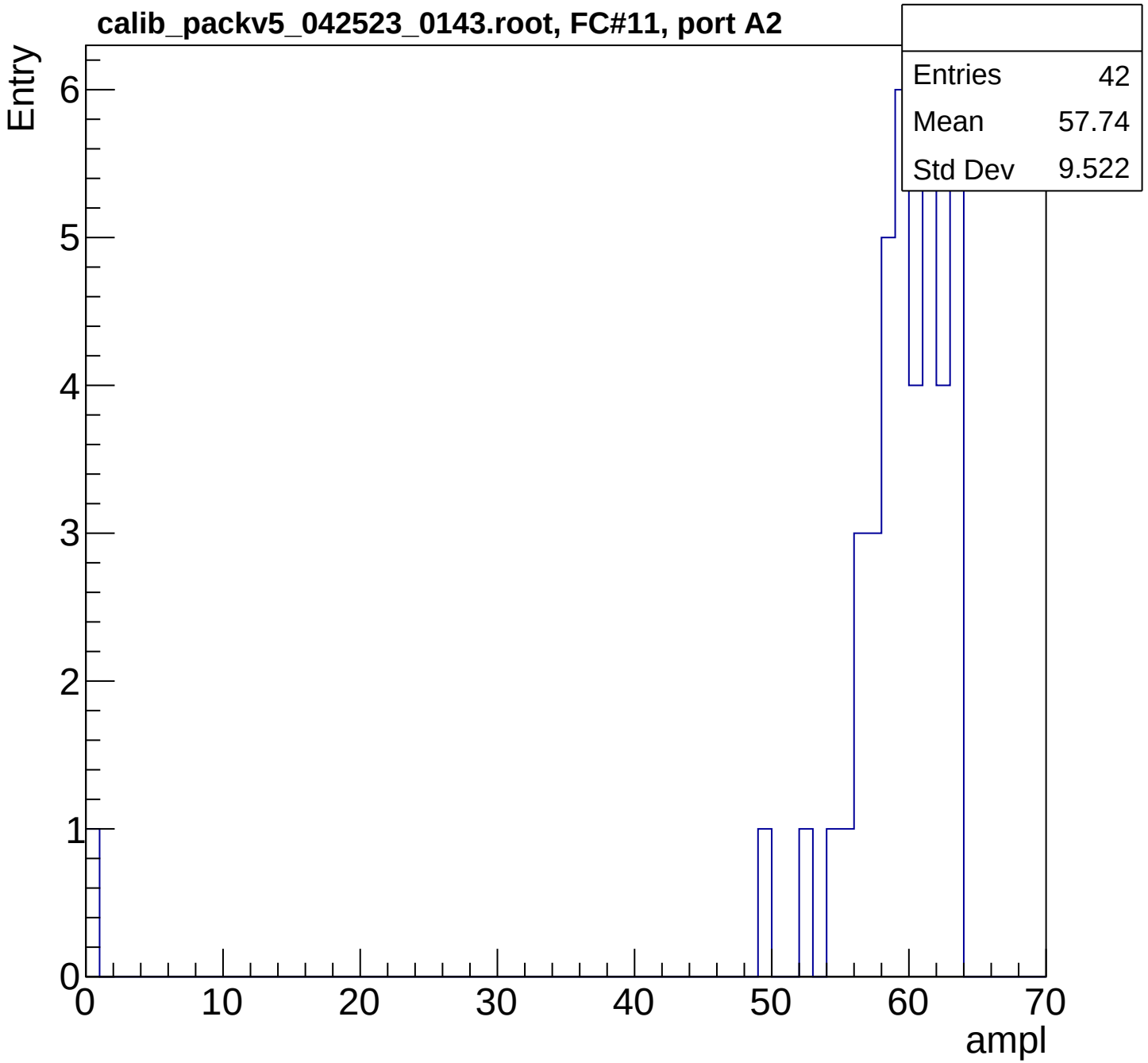

B1L102S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 28.12
Std Dev 2.883
Gaus mean : 31.5794
Gaus Width: 5.7310
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

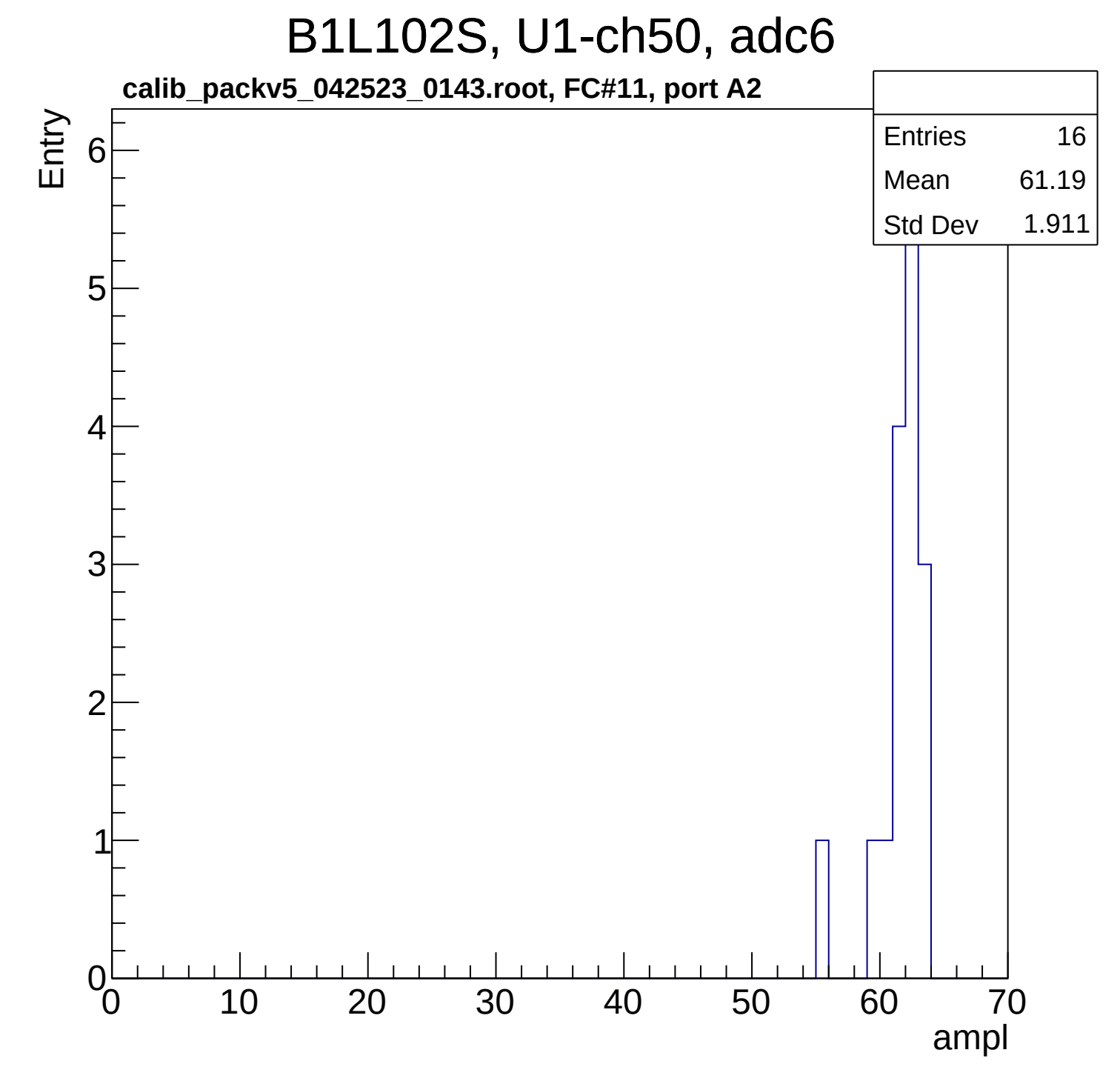

B1L102S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 33.48
Std Dev 4.162
Gaus mean : 33.8249
Gaus Width: 4.4692
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

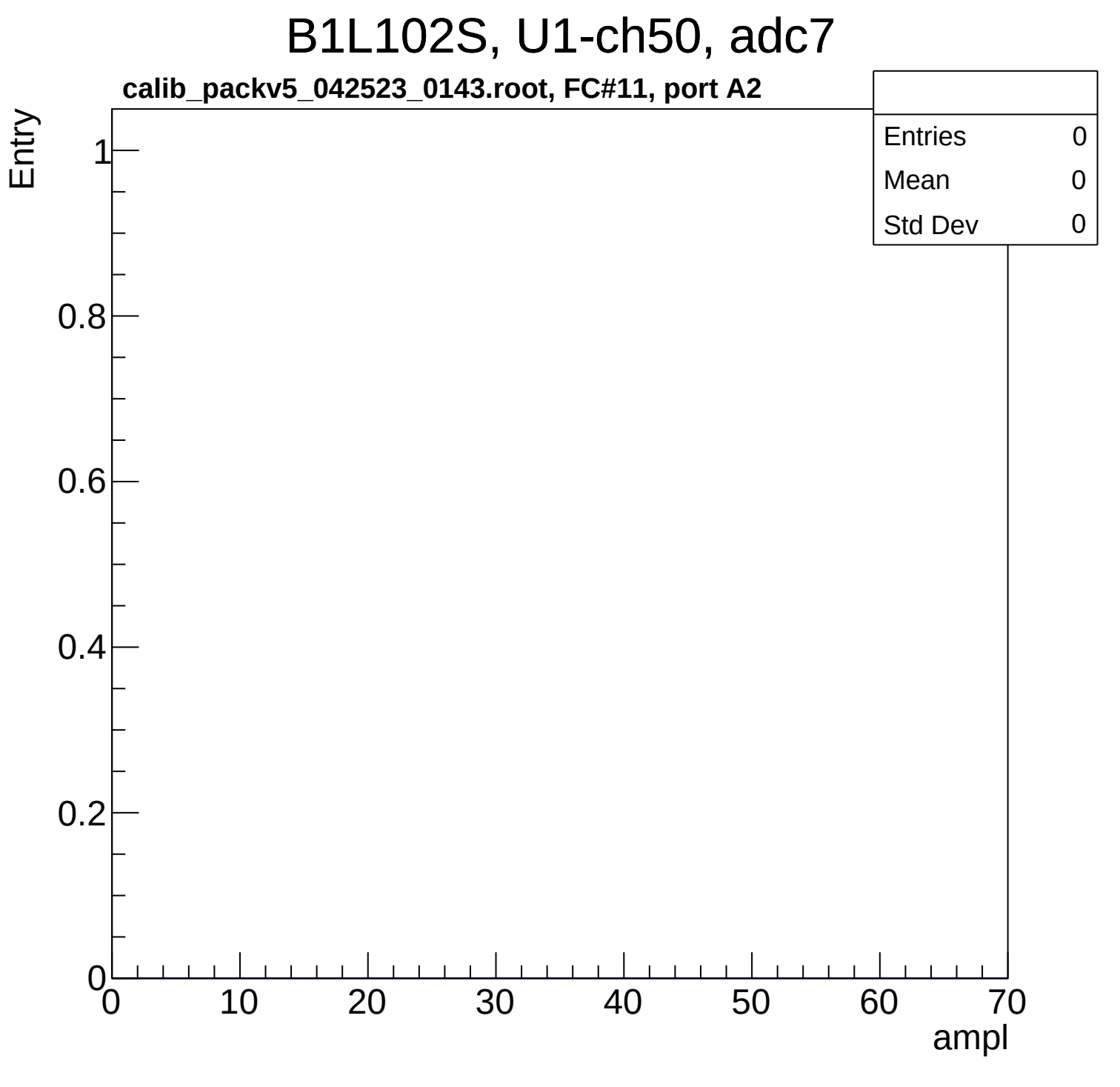

B1L102S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 40.61
Std Dev 4.34
Gaus mean : 41.7733
Gaus Width: 5.2402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

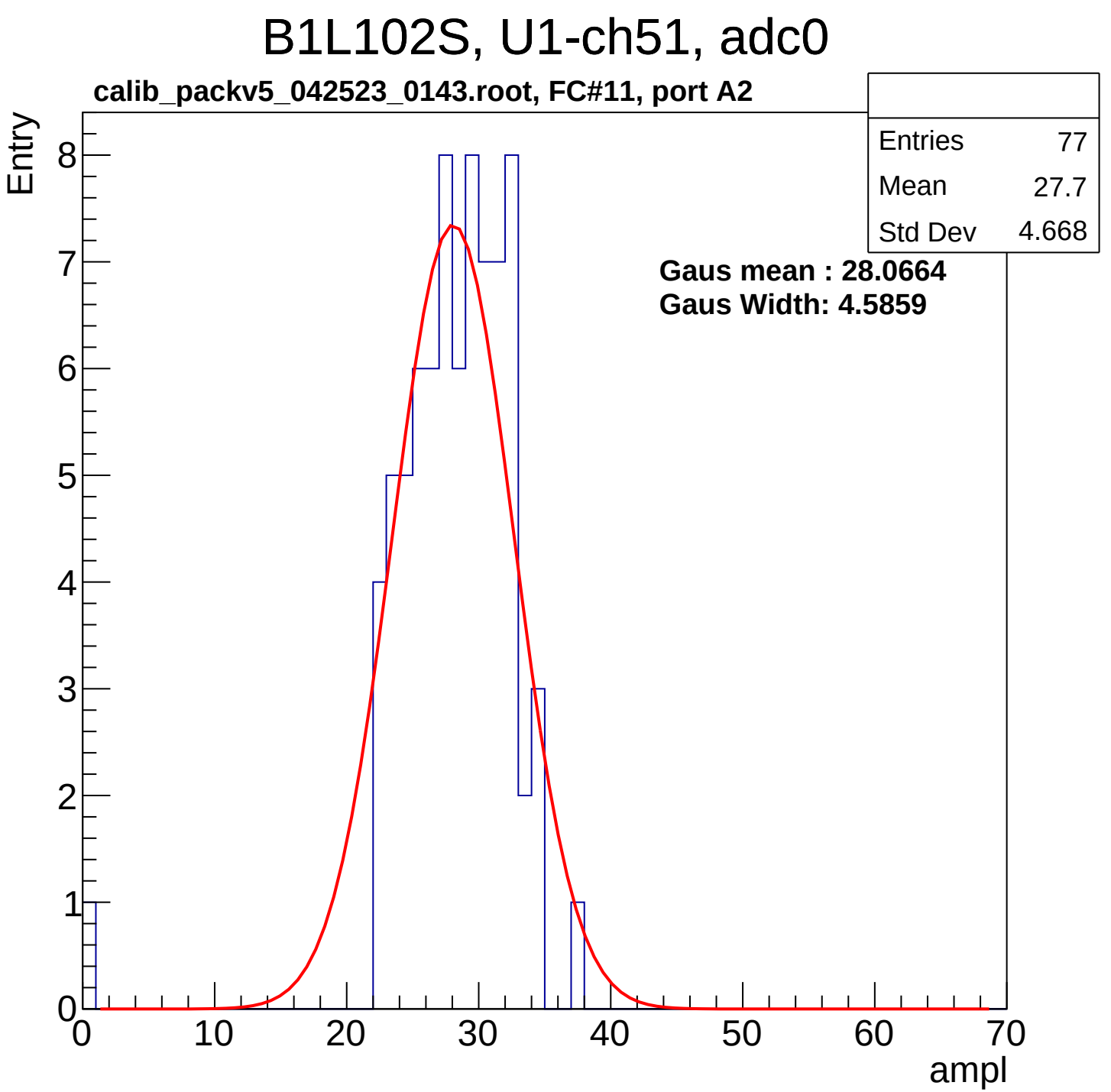

B1L102S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 48.18
Std Dev 3.948
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

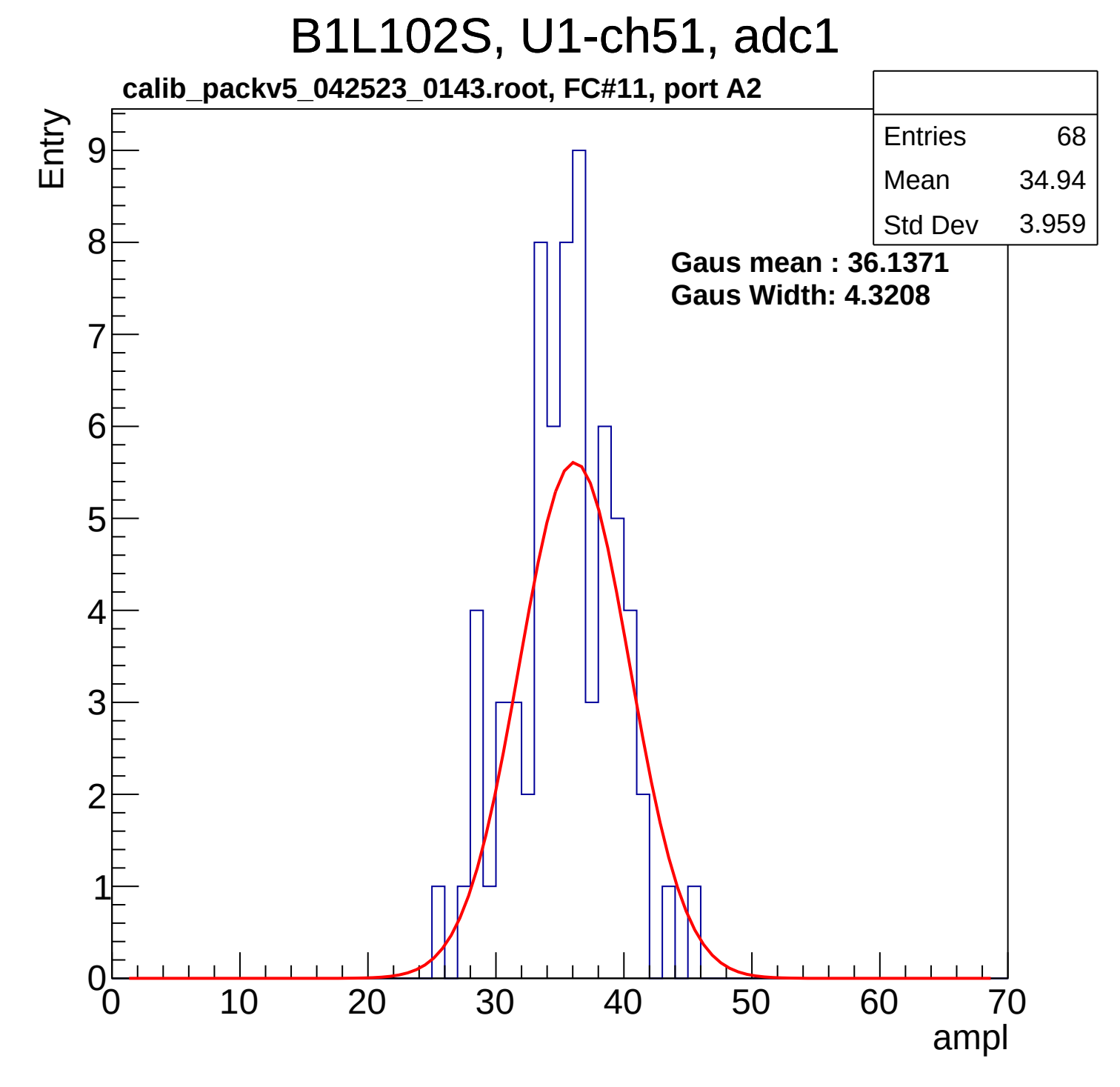

B1L102S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 54.3
Std Dev 3.645
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

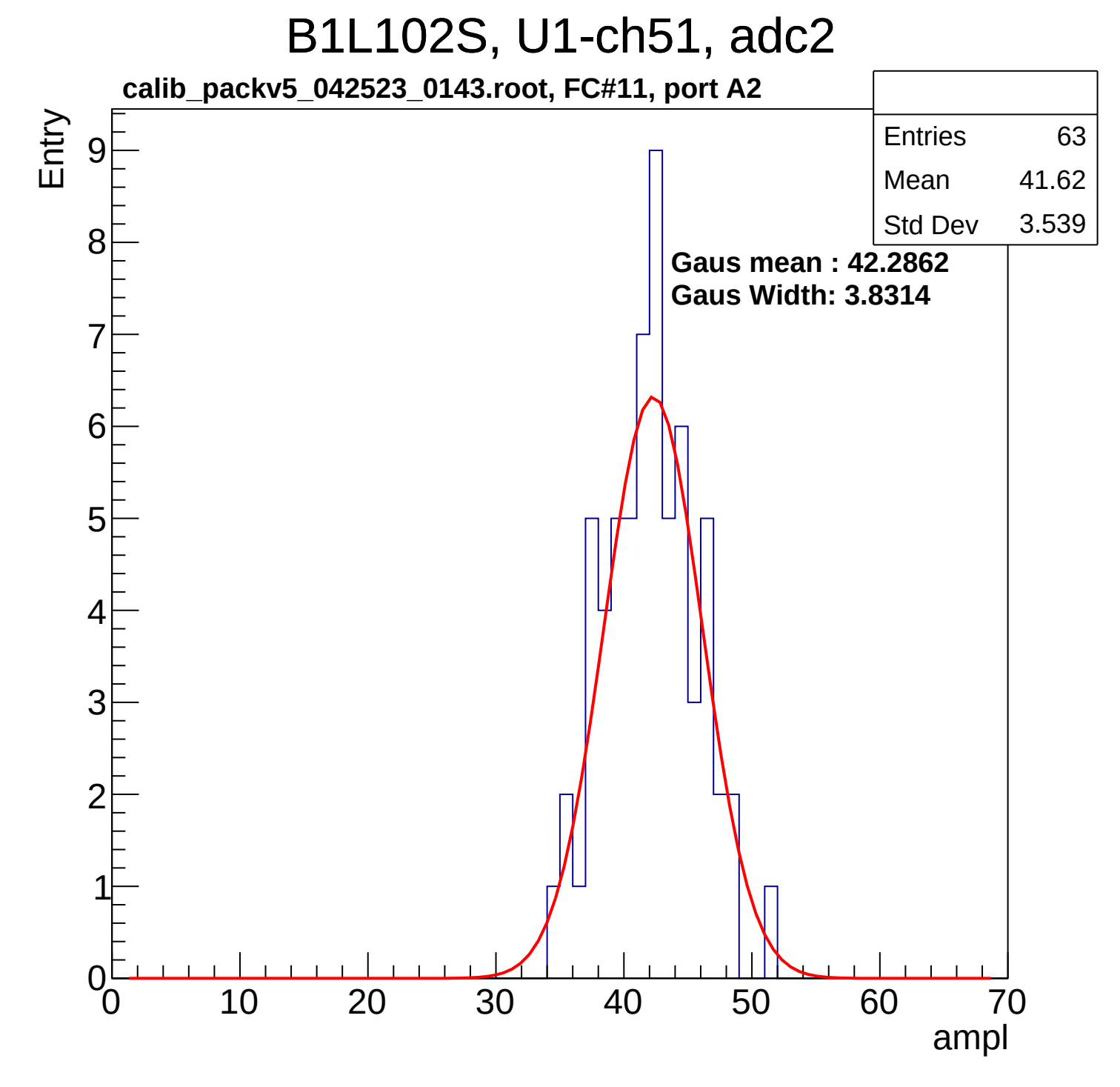

B1L102S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 57.74
Std Dev 9.522
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

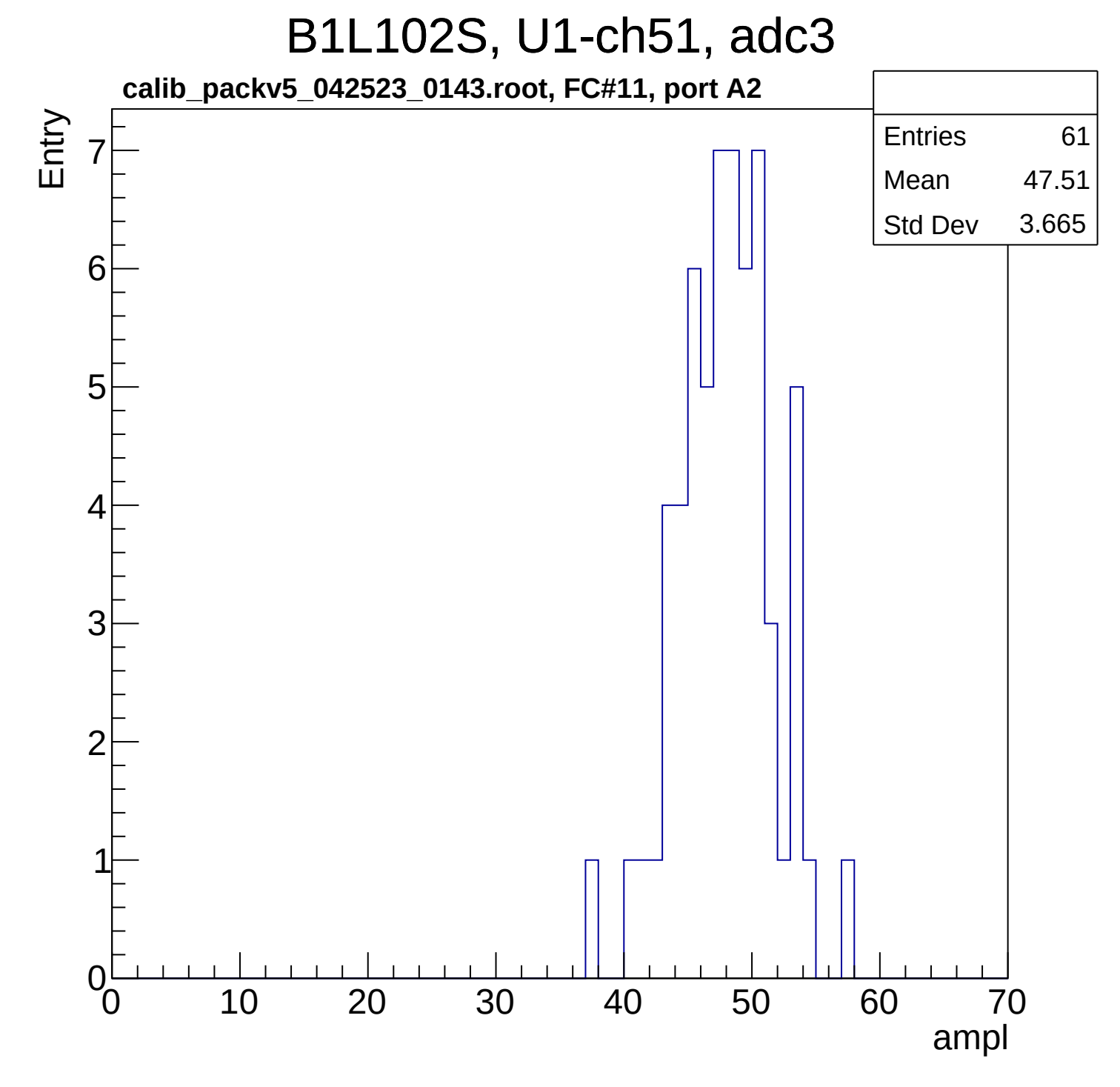

B1L102S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 61.19
Std Dev 1.911
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

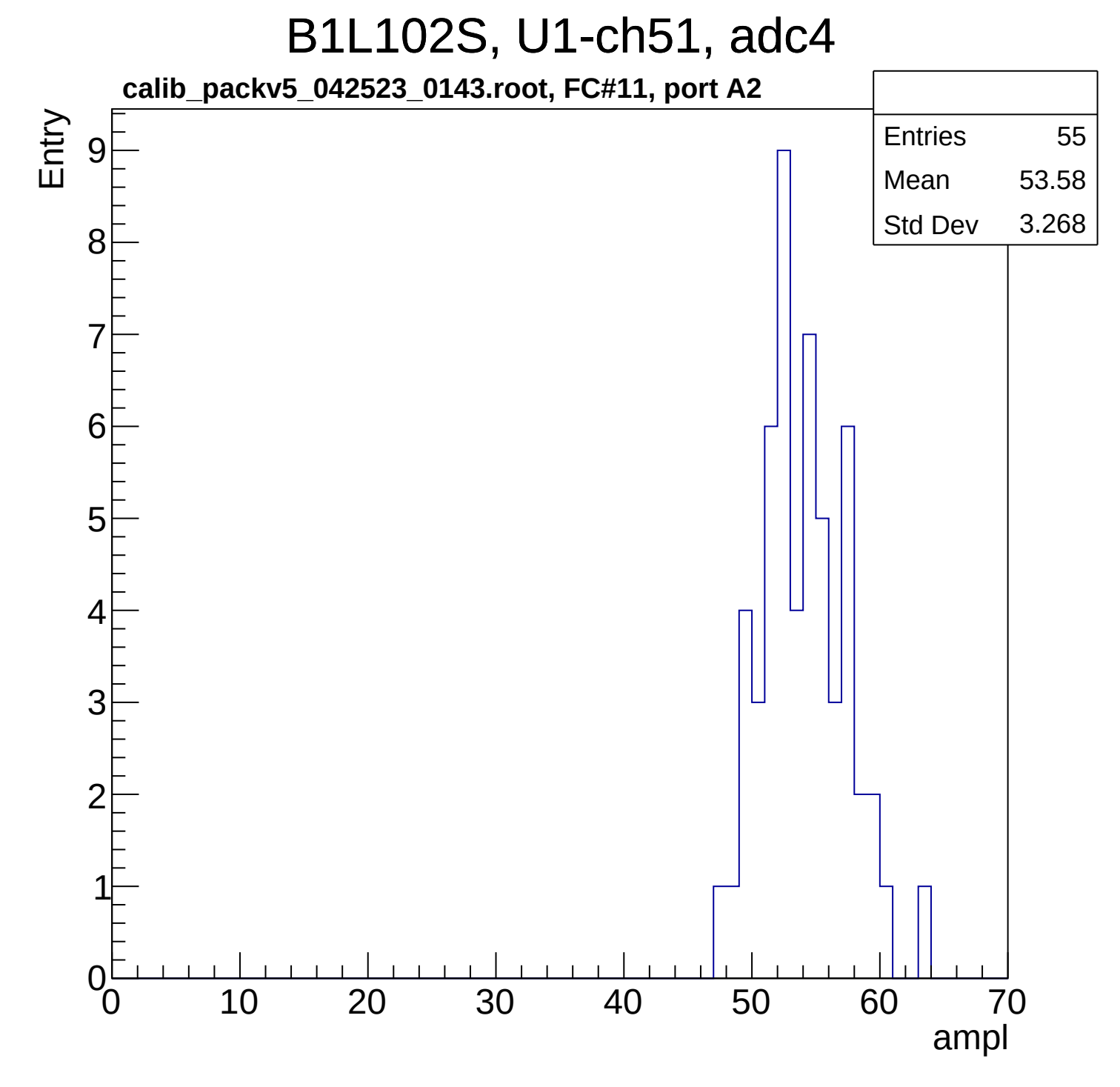

B1L102S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

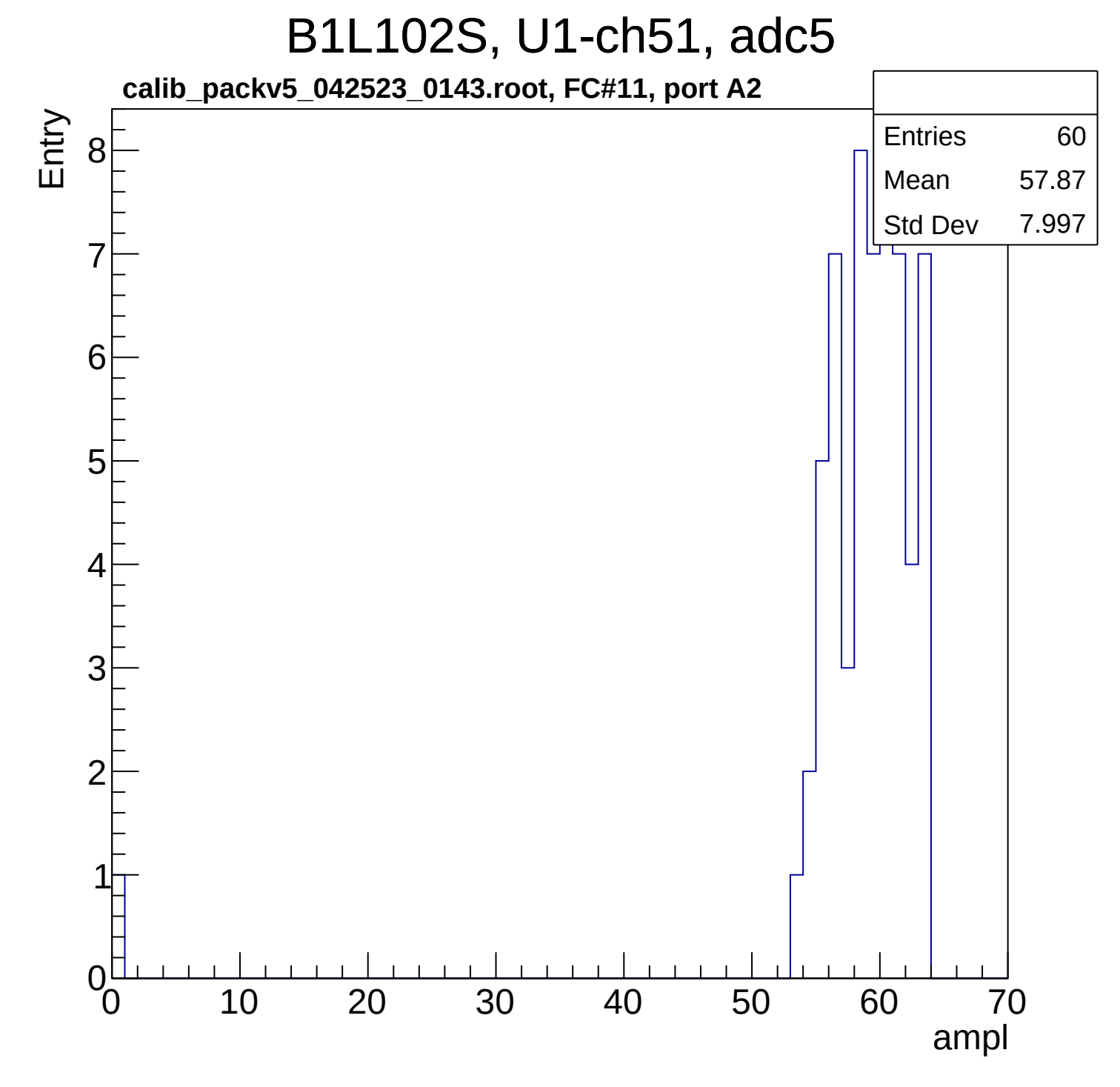

B1L102S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 27.7
Std Dev 4.668
Gaus mean : 28.0664
Gaus Width: 4.5859
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

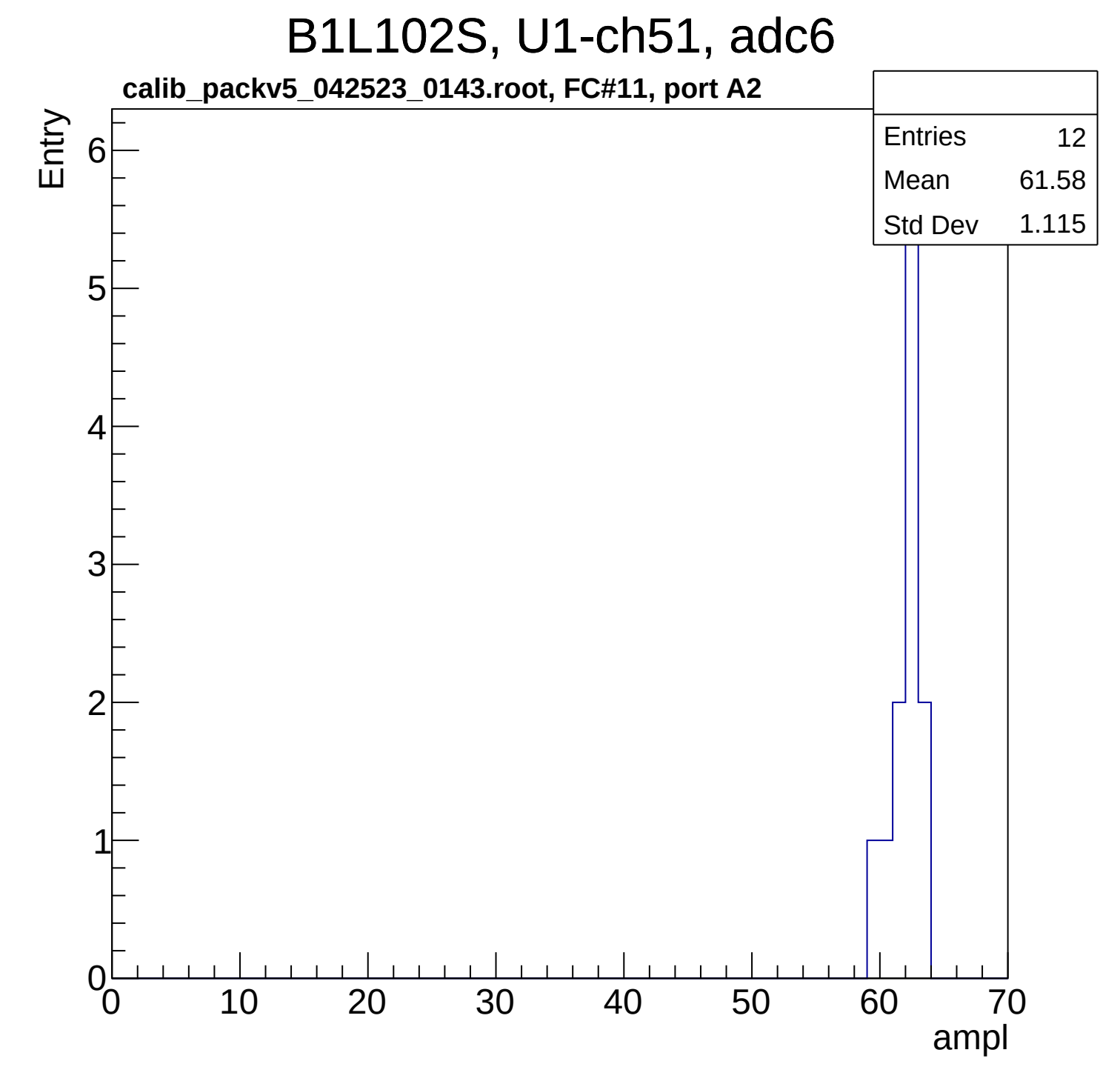

B1L102S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 34.94
Std Dev 3.959
Gaus mean : 36.1371
Gaus Width: 4.3208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 41.62
Std Dev 3.539
Gaus mean : 42.2862
Gaus Width: 3.8314
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 47.51
Std Dev 3.665
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.58
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 57.87
Std Dev 7.997
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.58
Std Dev 1.115
Entry
ampl
0
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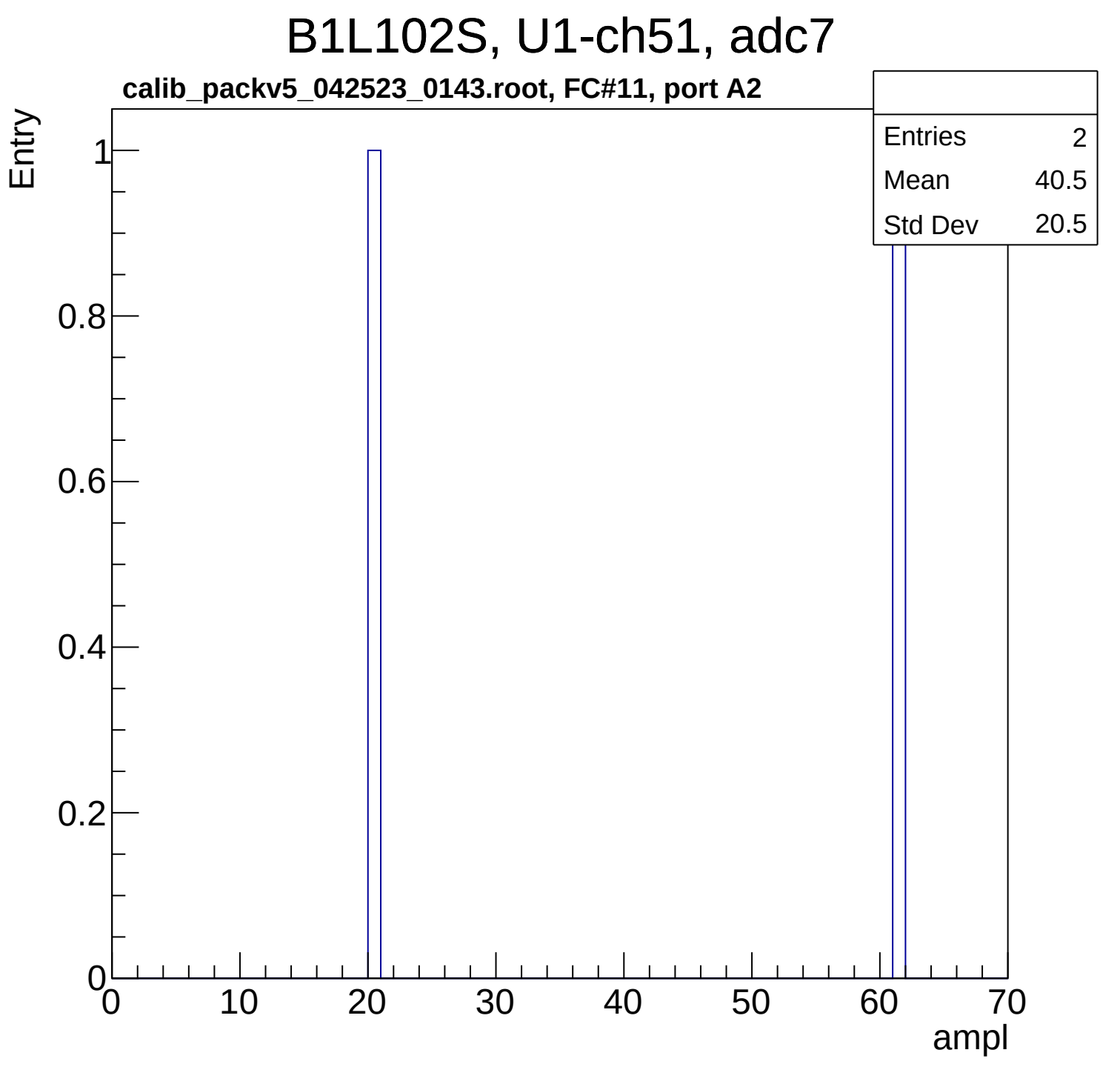
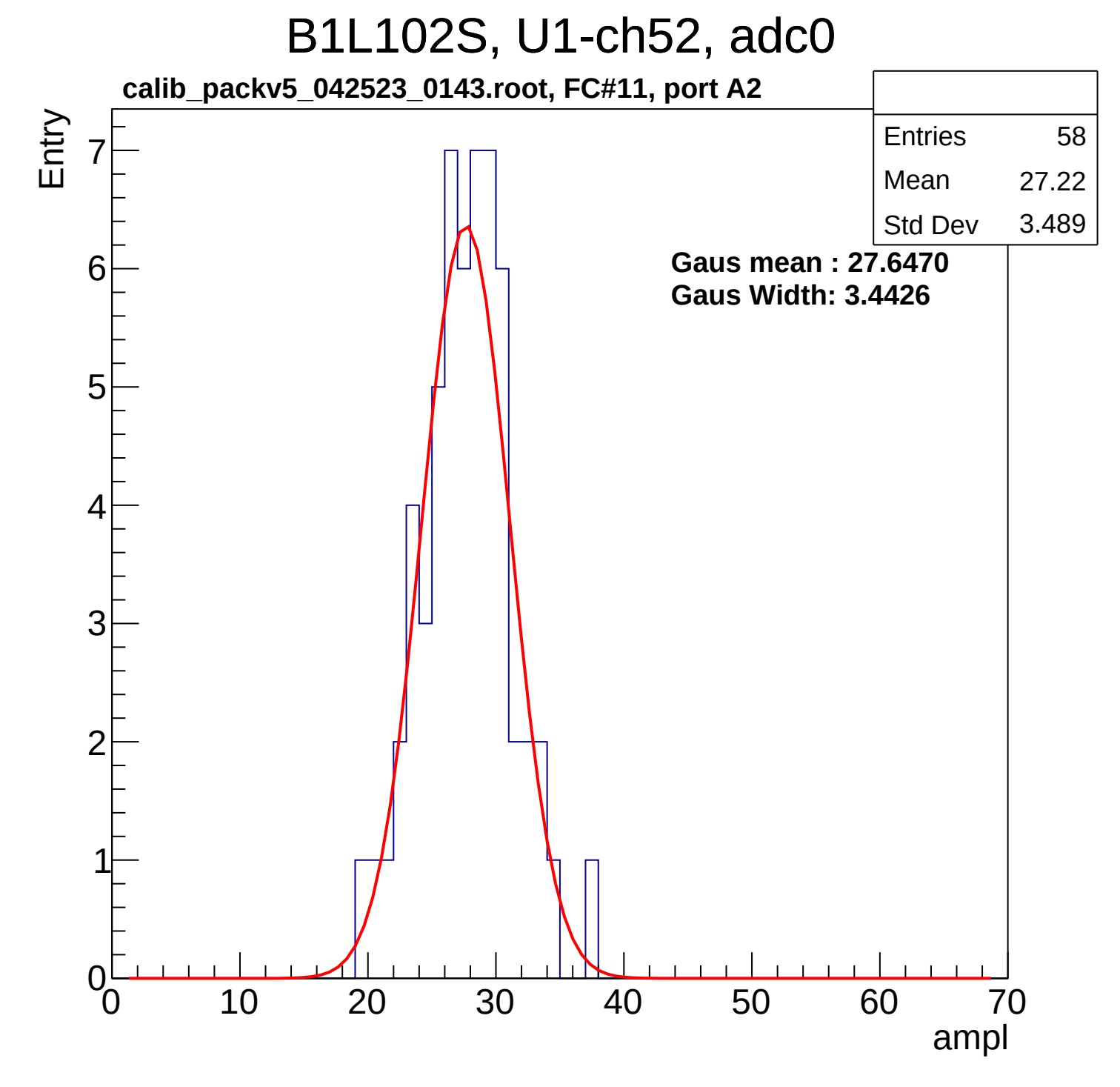
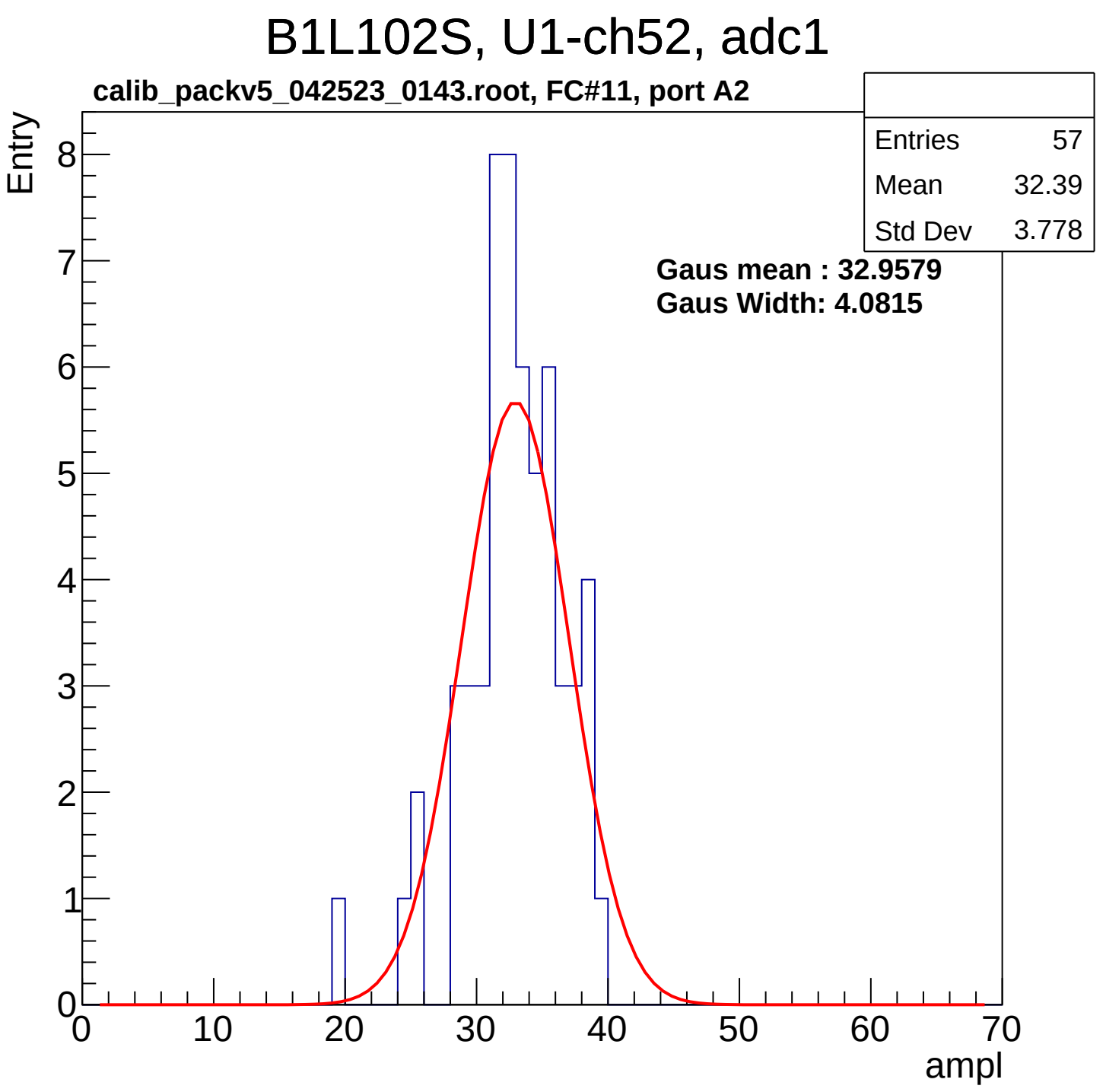
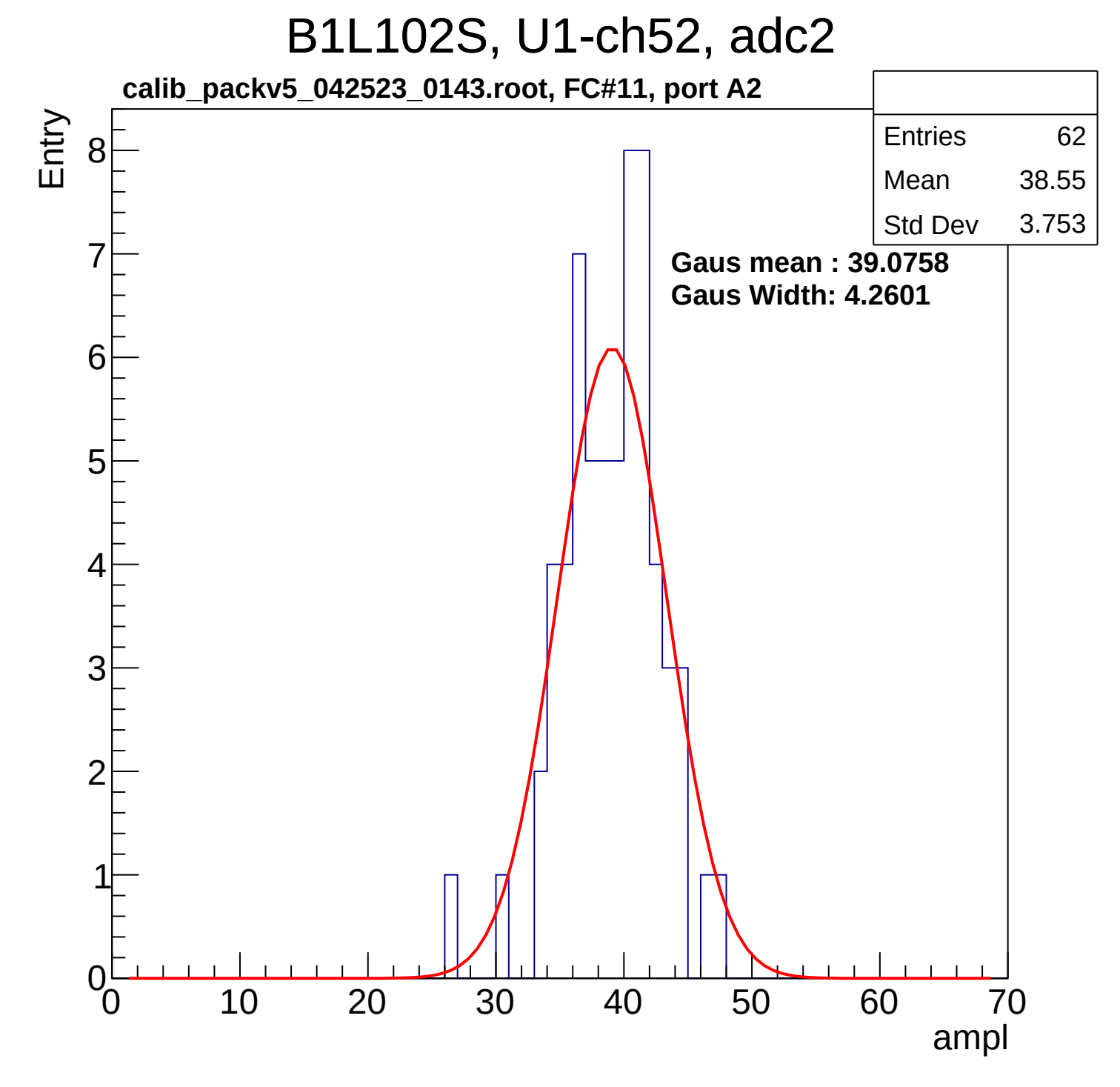
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 40.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L102S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 27.22
Std Dev 3.489
Gaus mean : 27.6470
Gaus Width: 3.4426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 32.39
Std Dev 3.778
Gaus mean : 32.9579
Gaus Width: 4.0815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 38.55
Std Dev 3.753
Gaus mean : 39.0758
Gaus Width: 4.2601
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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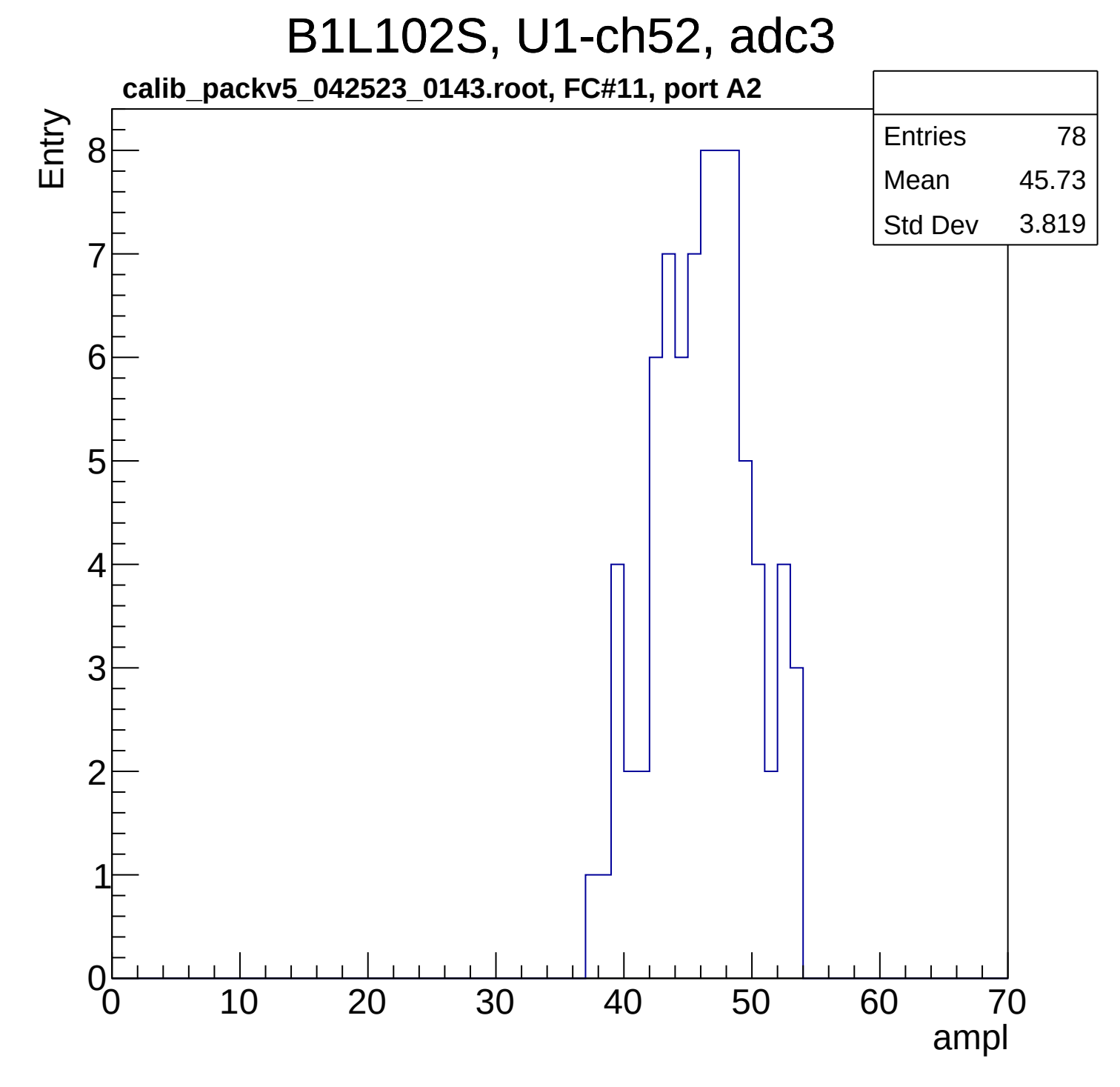
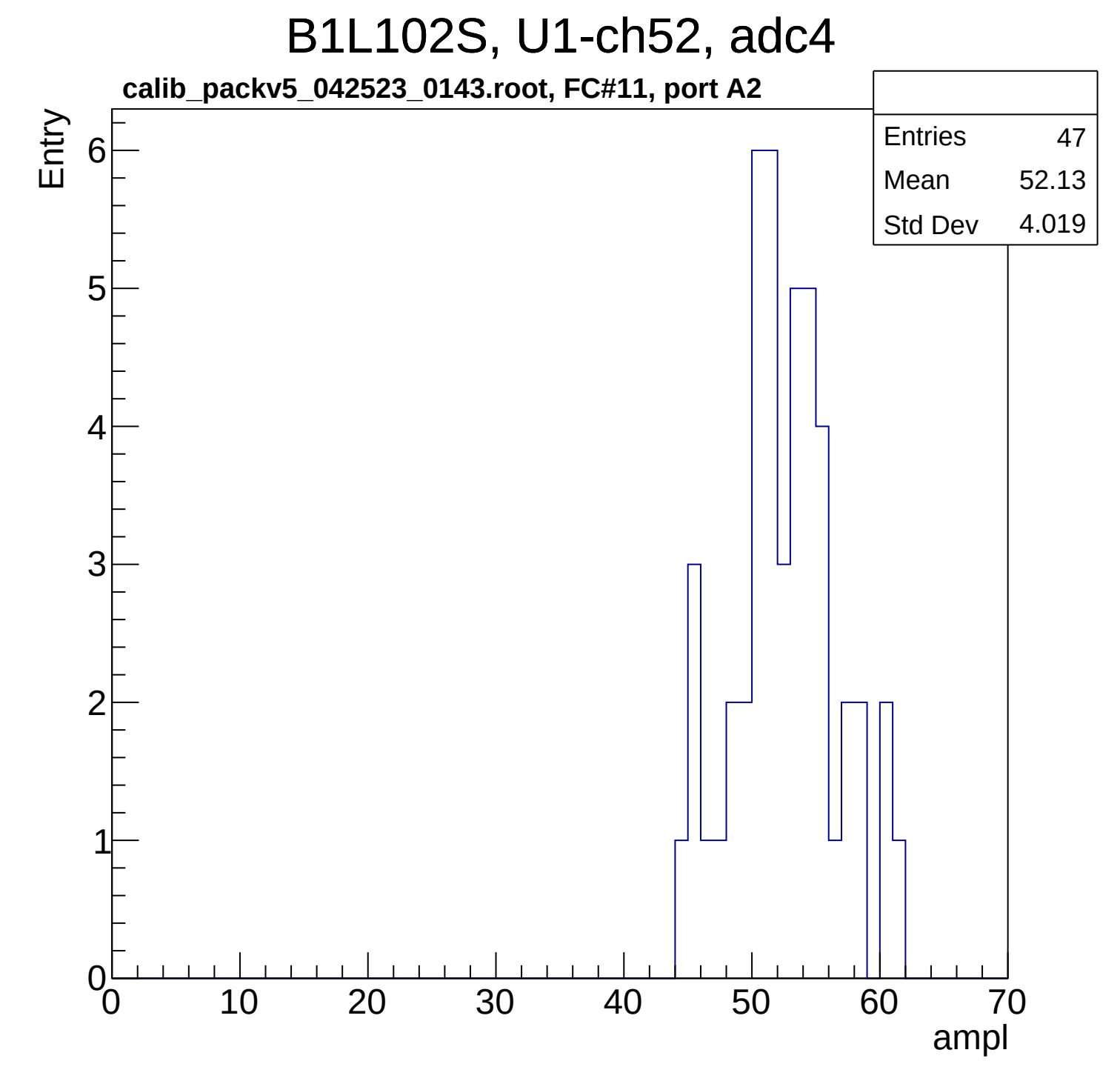
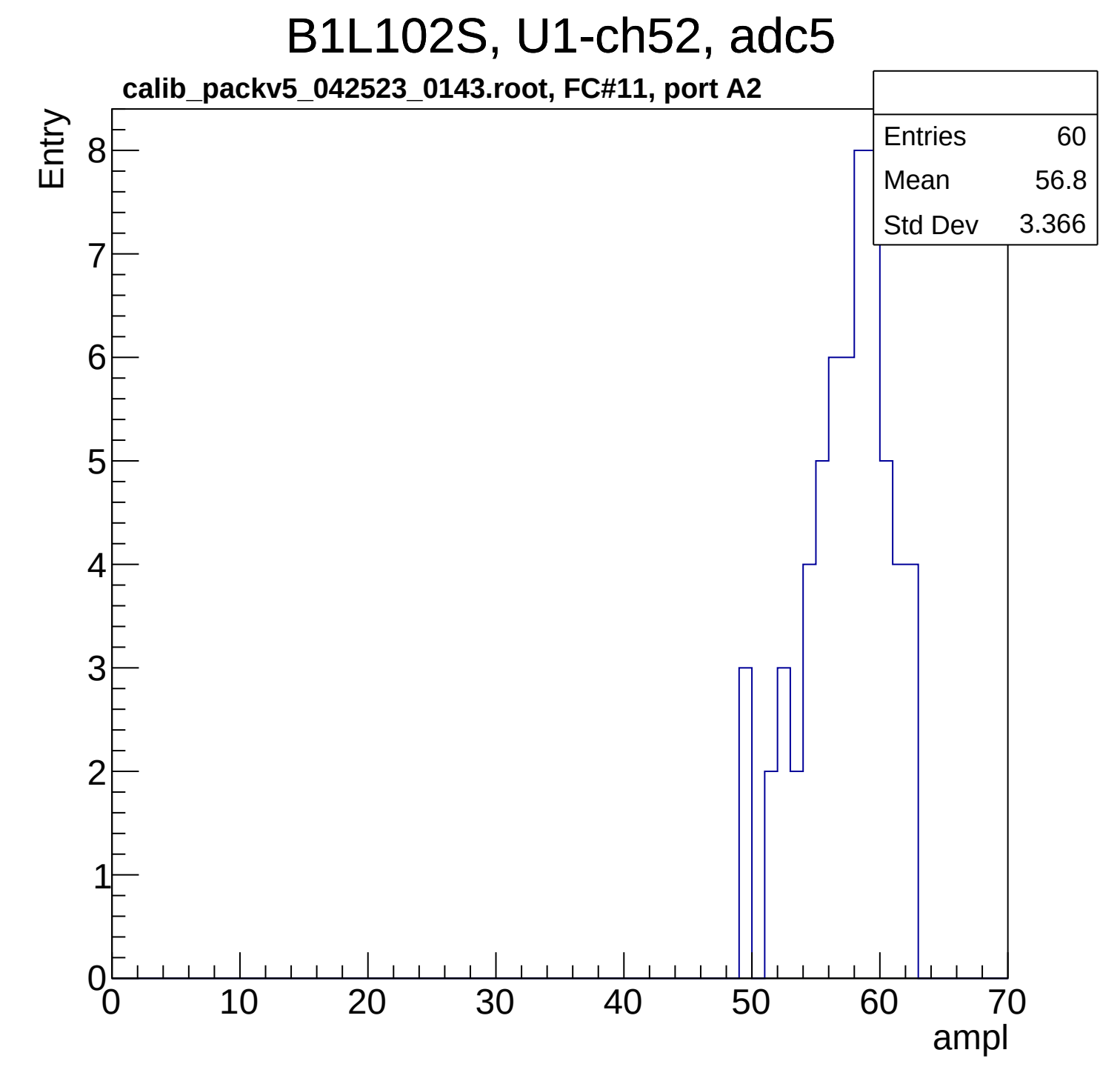

B1L102S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 45.73
Std Dev 3.819
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 52.13
Std Dev 4.019
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 56.8
Std Dev 3.366
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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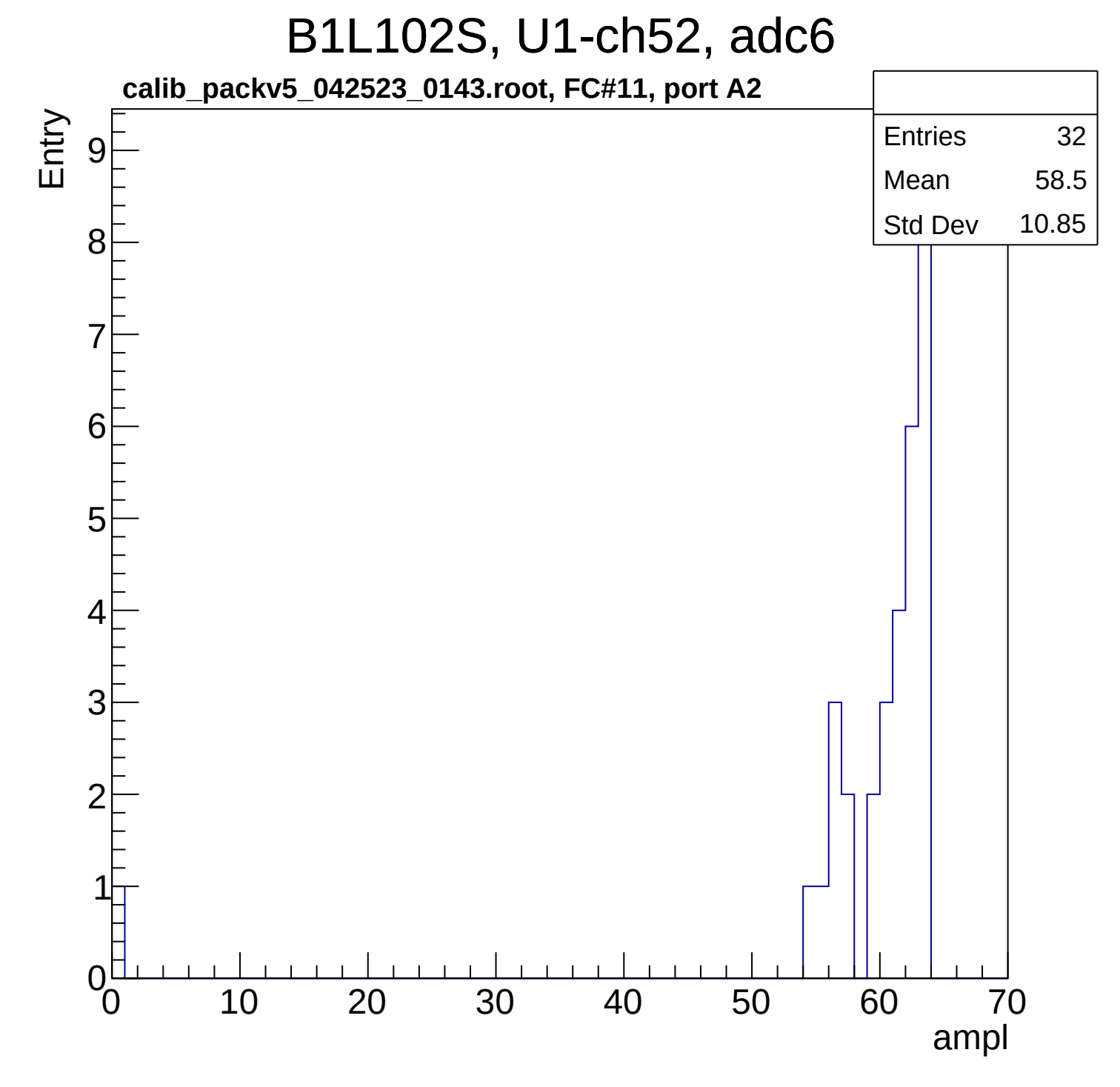
B1L102S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 32
Mean 58.5
Std Dev 10.85
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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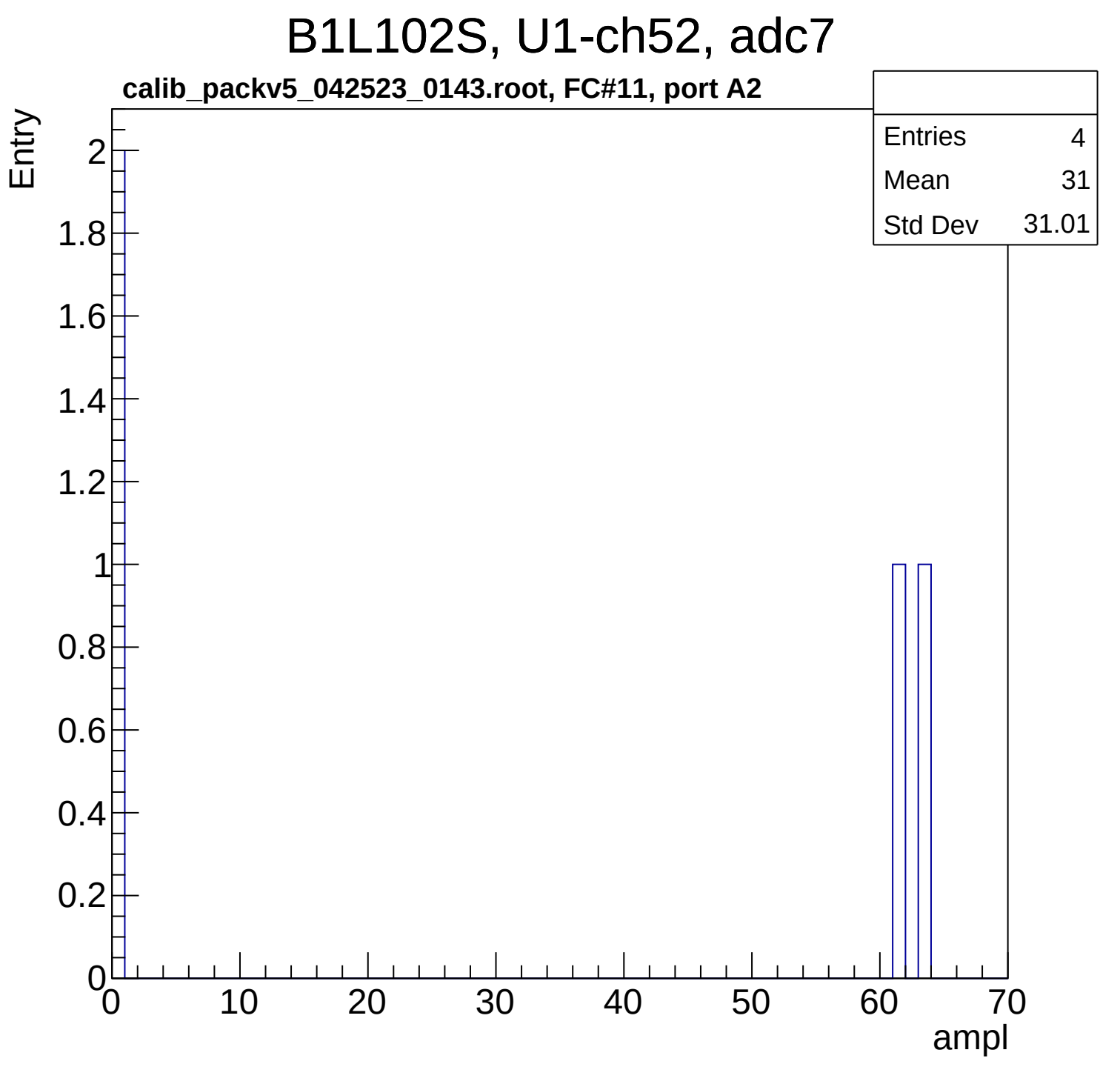
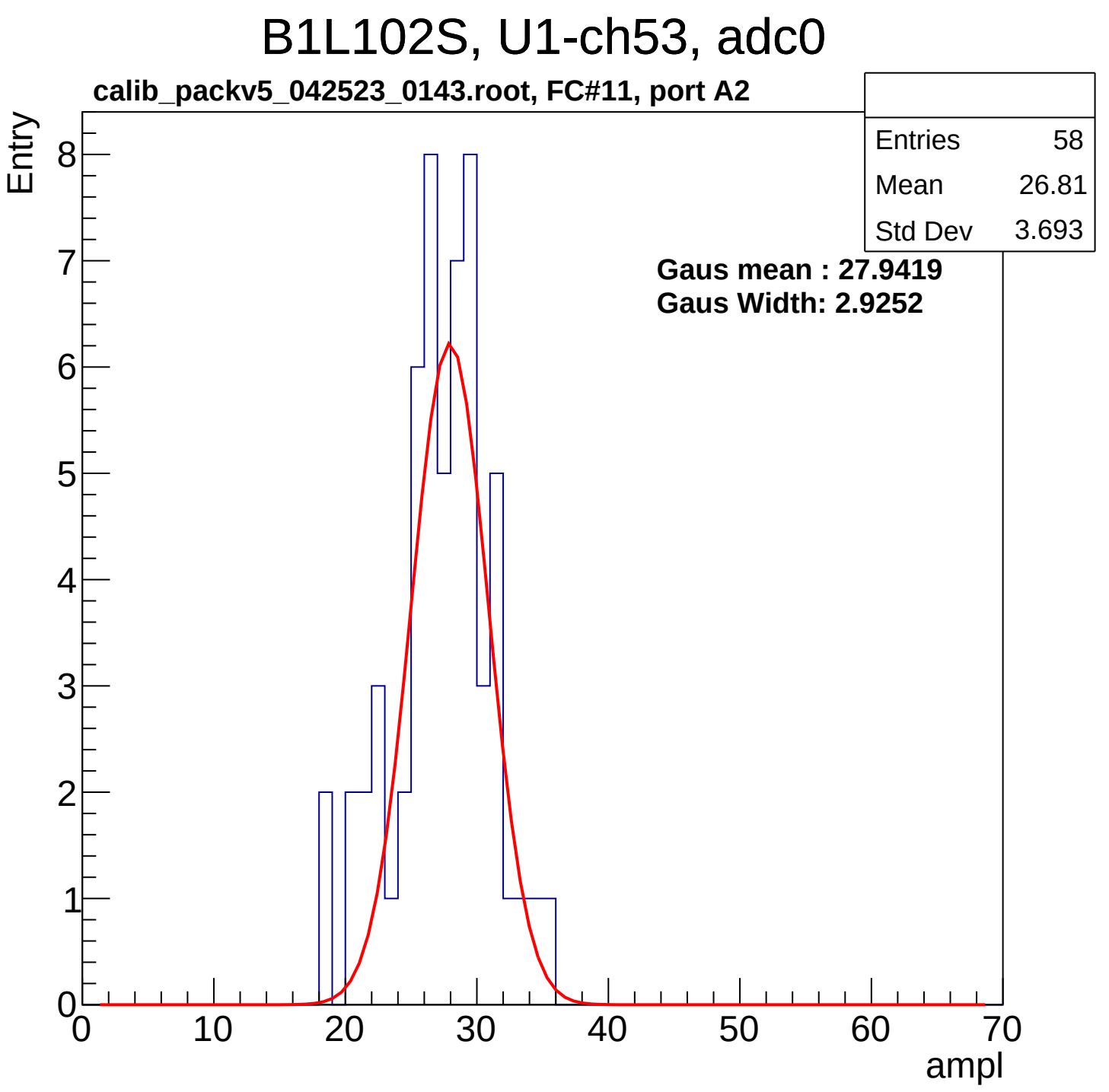
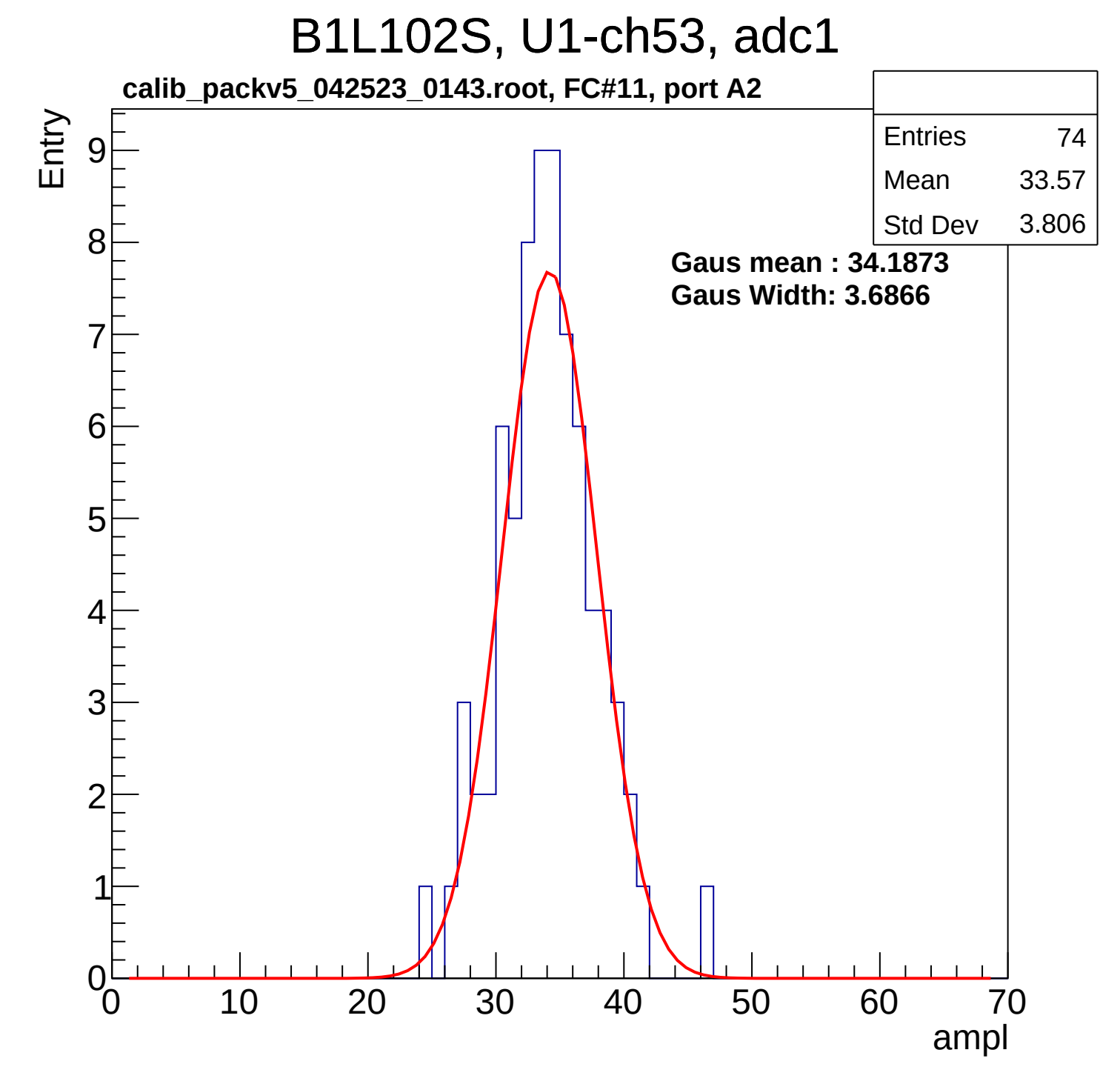
40
50
60
70


B1L102S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 26.81
Std Dev 3.693
Gaus mean : 27.9419
Gaus Width: 2.9252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 33.57
Std Dev 3.806
Gaus mean : 34.1873
Gaus Width: 3.6866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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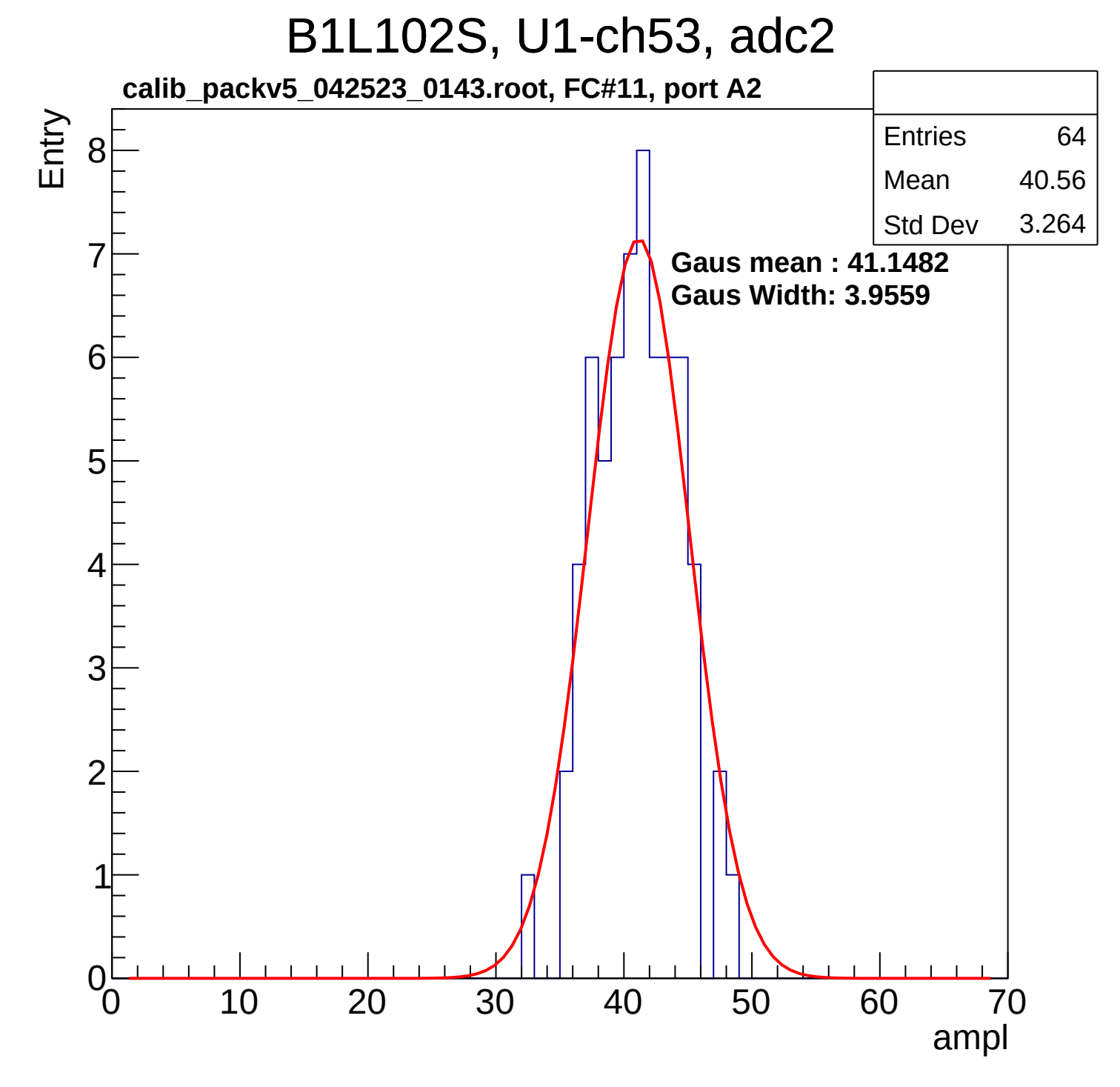
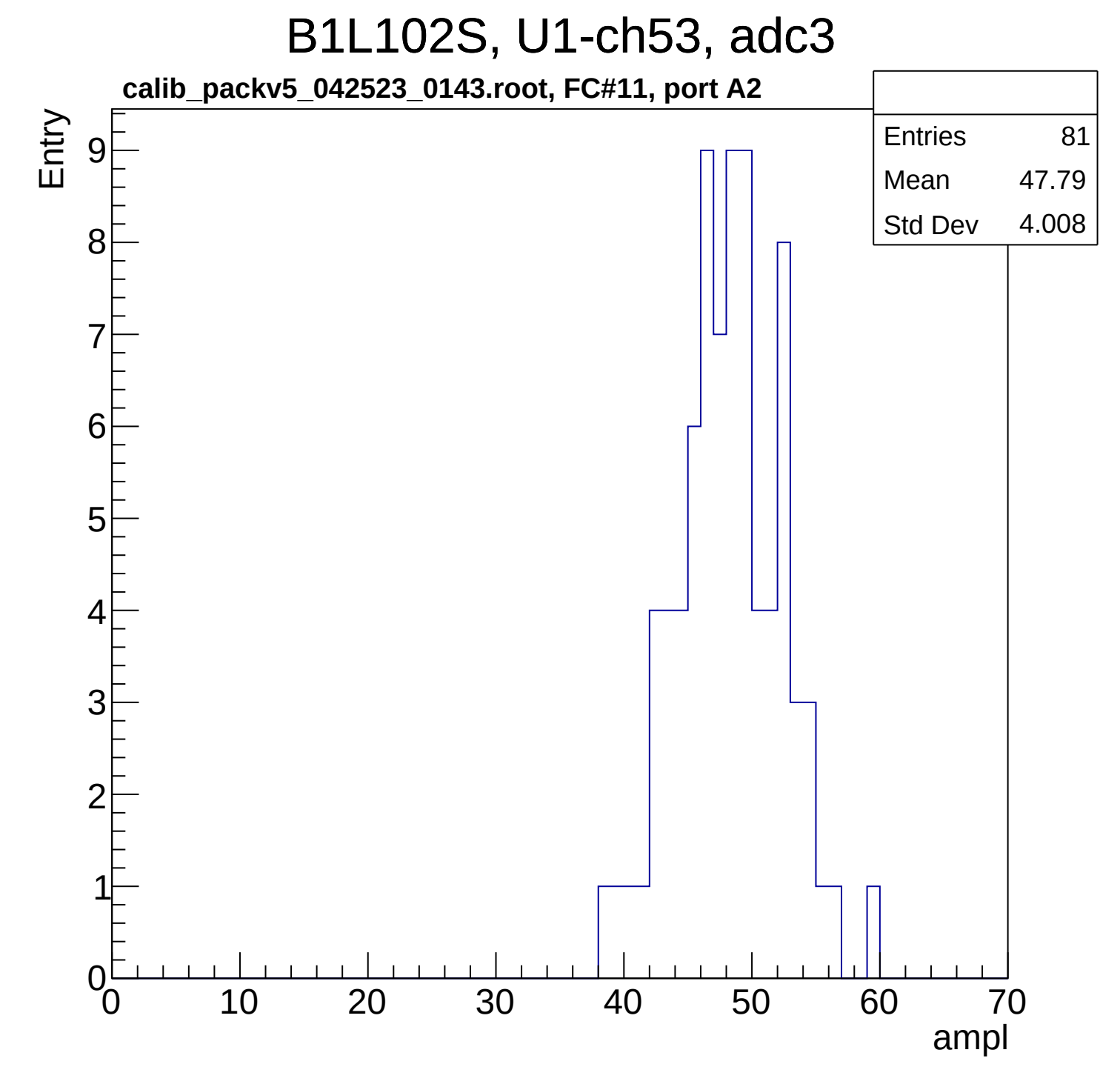
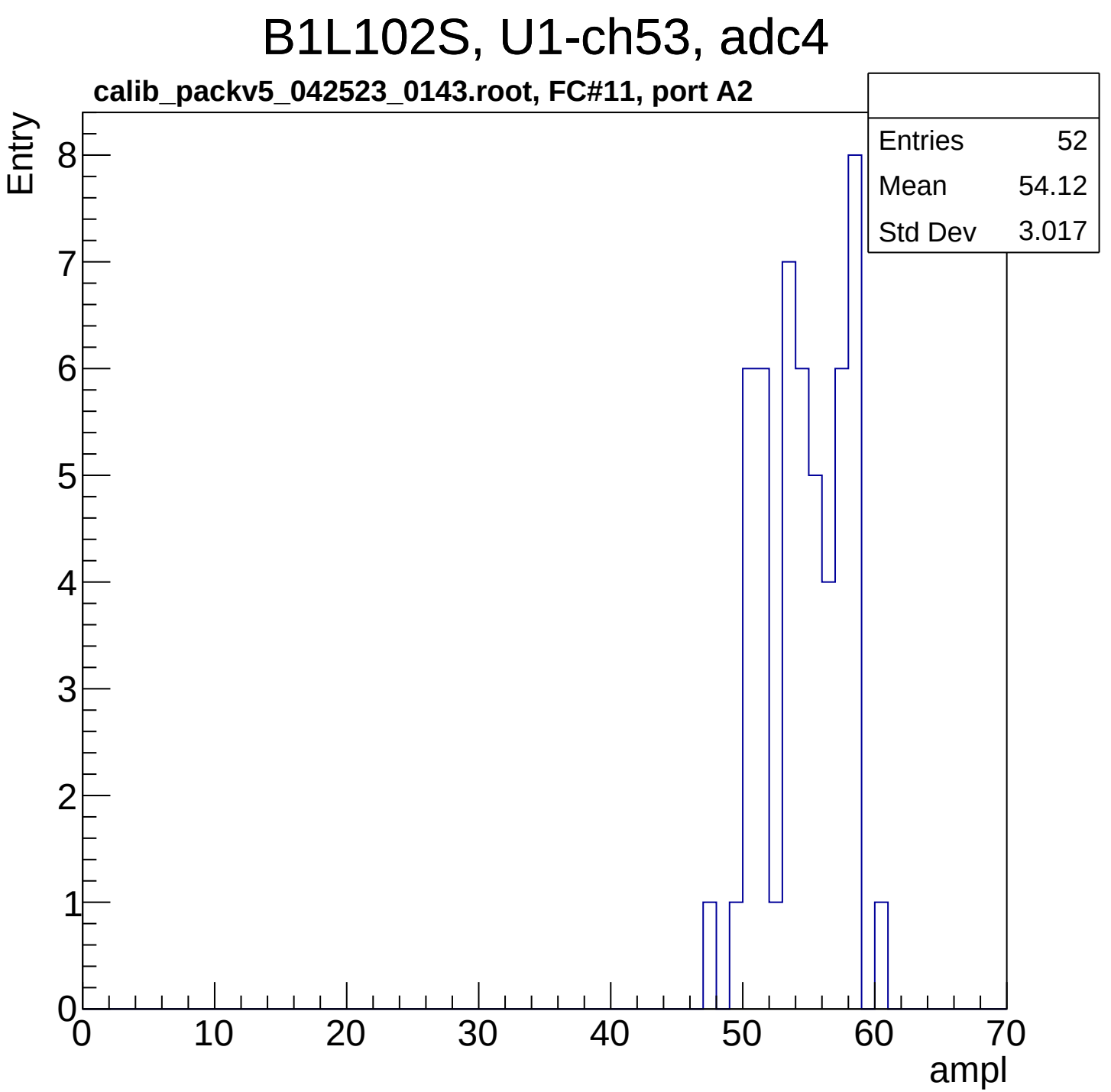
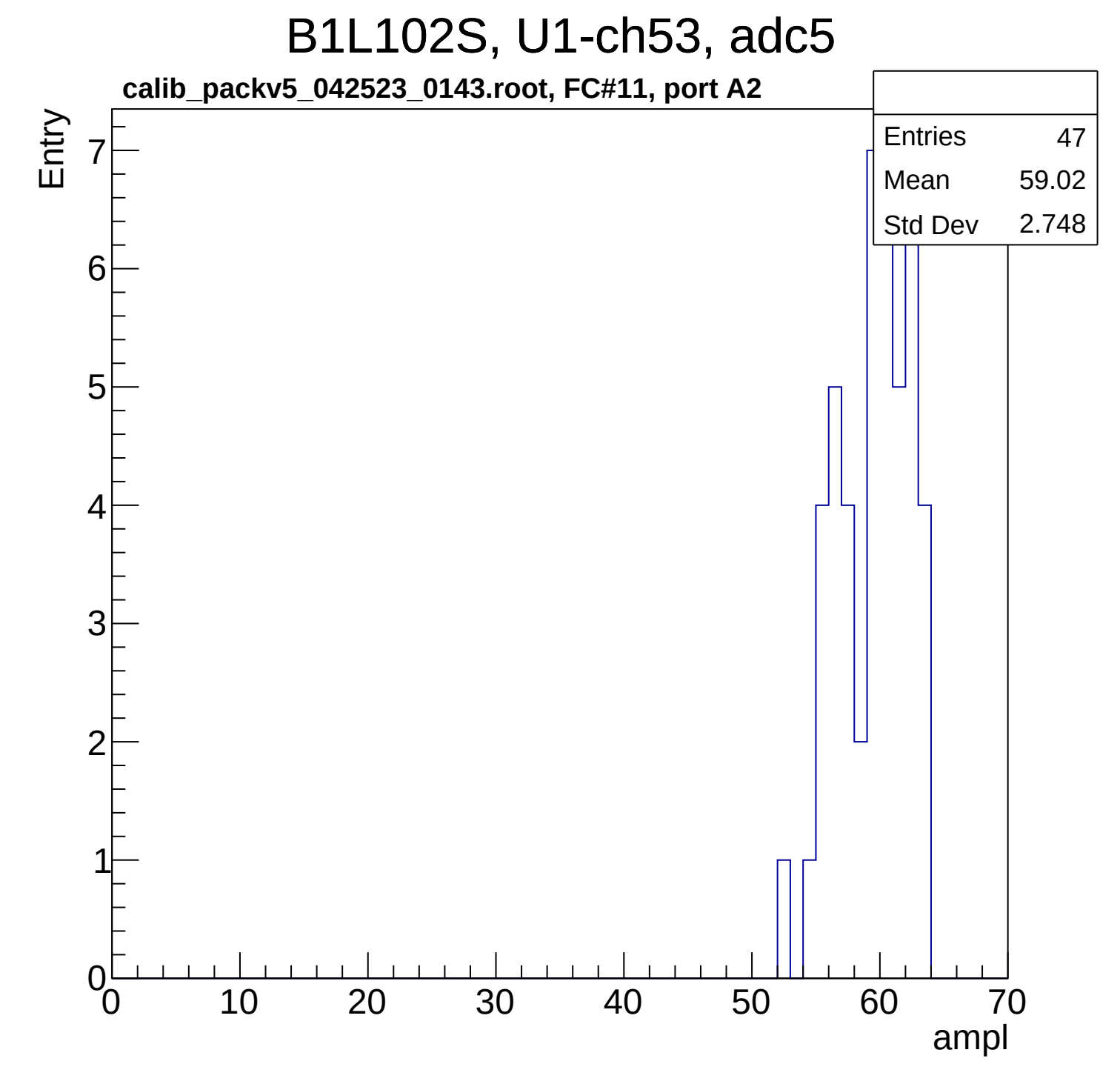
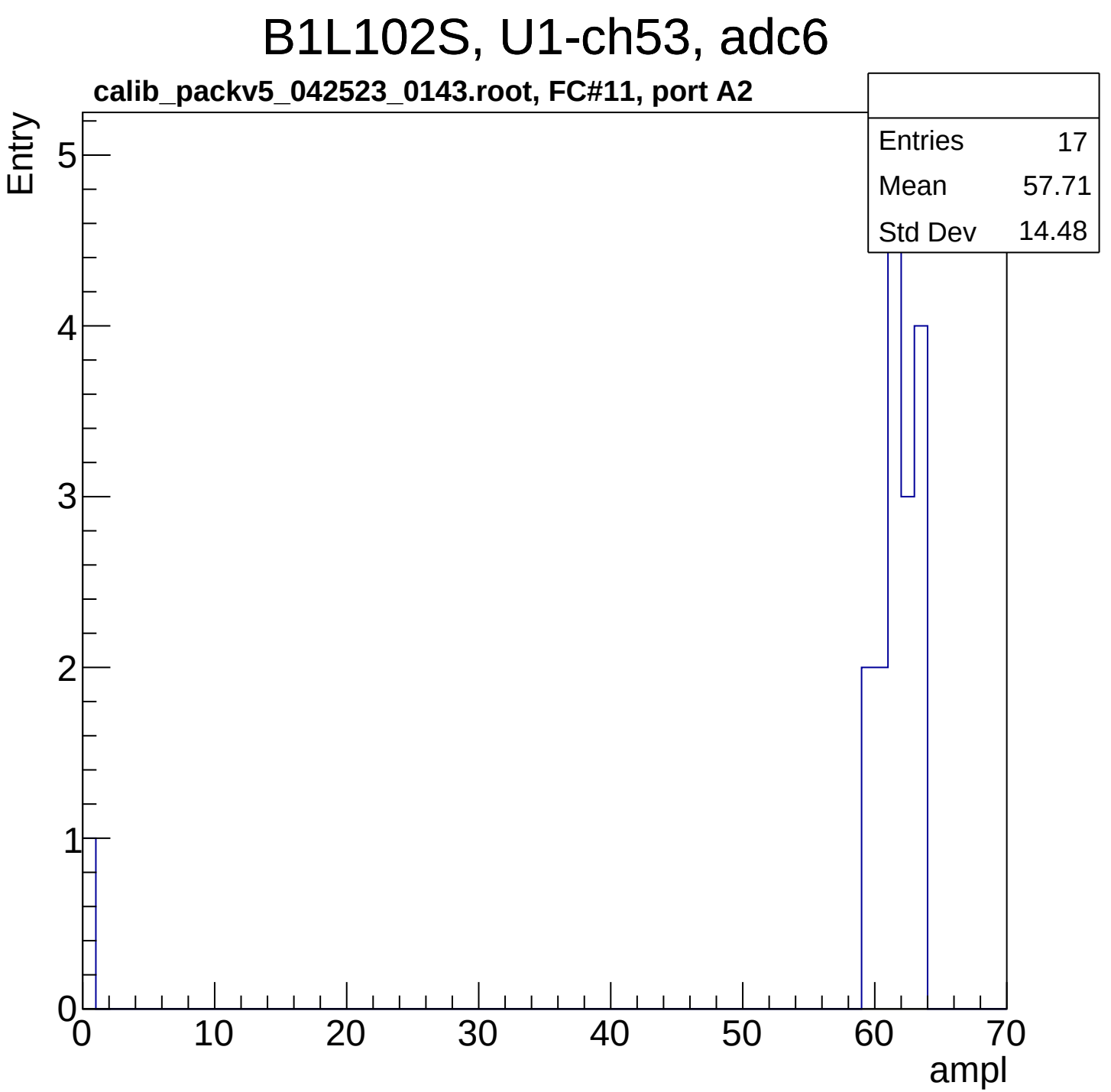
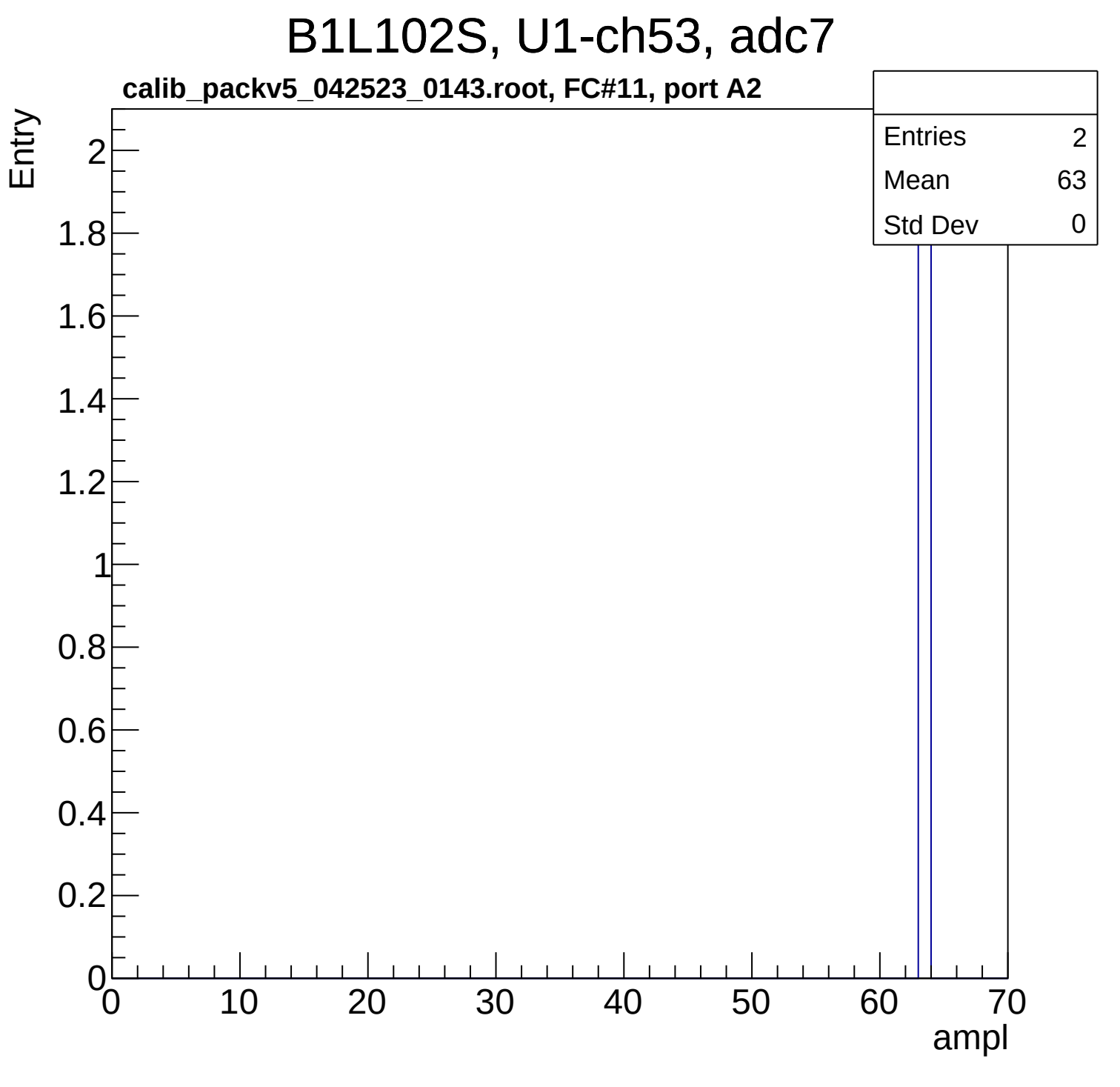

B1L102S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 40.56
Std Dev 3.264
Gaus mean : 41.1482
Gaus Width: 3.9559
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

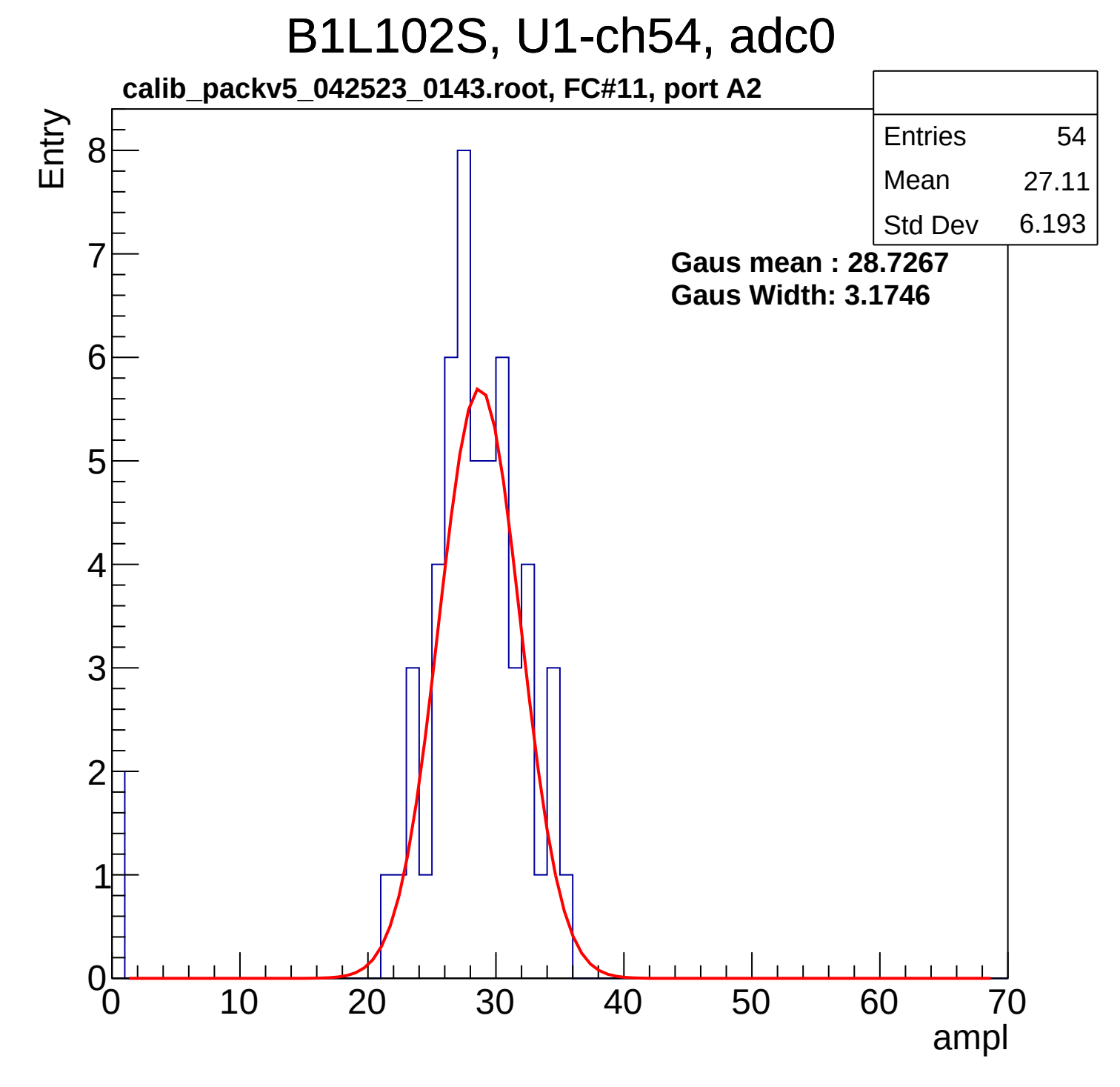

B1L102S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 47.79
Std Dev 4.008
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

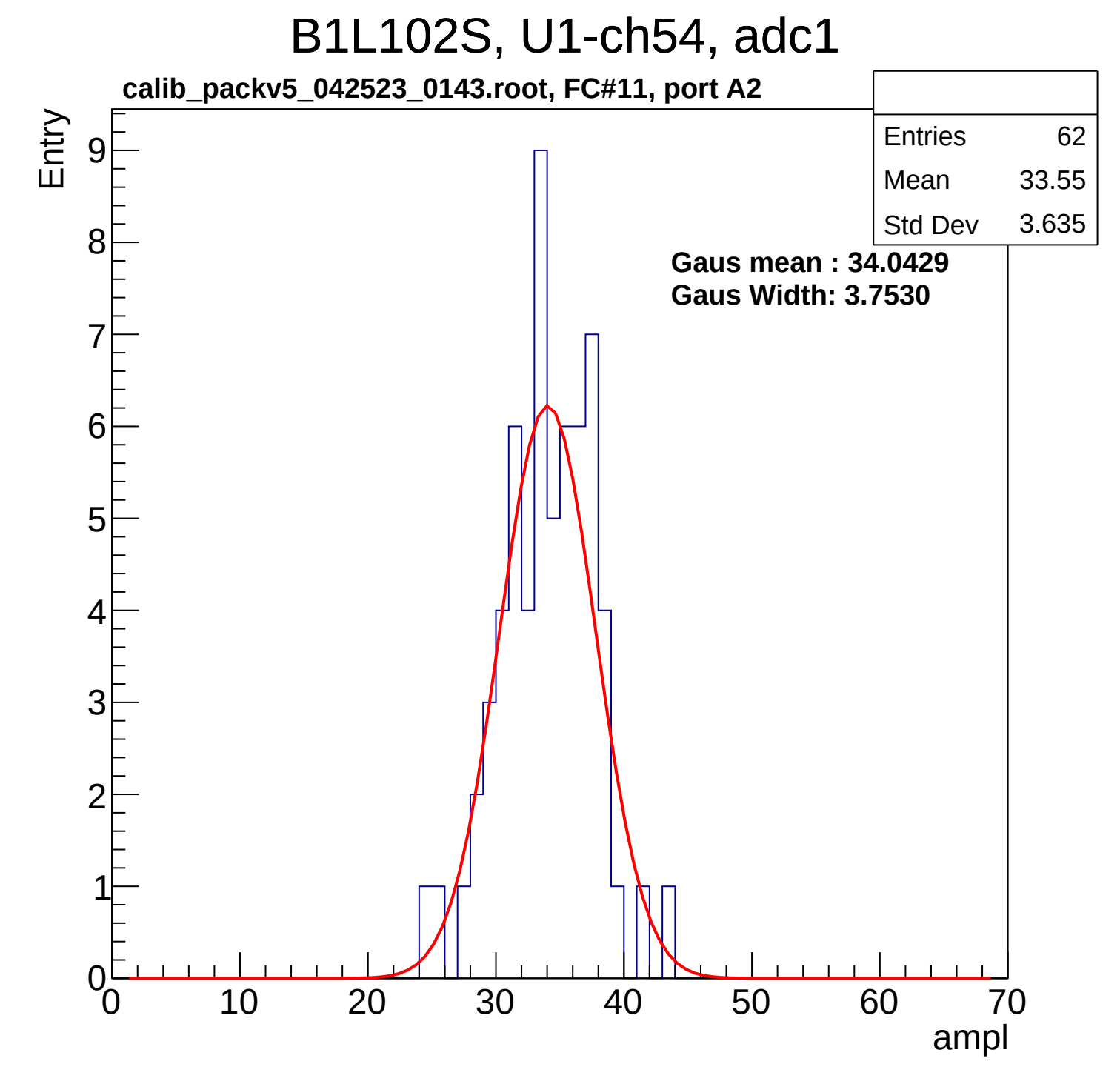

B1L102S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 54.12
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

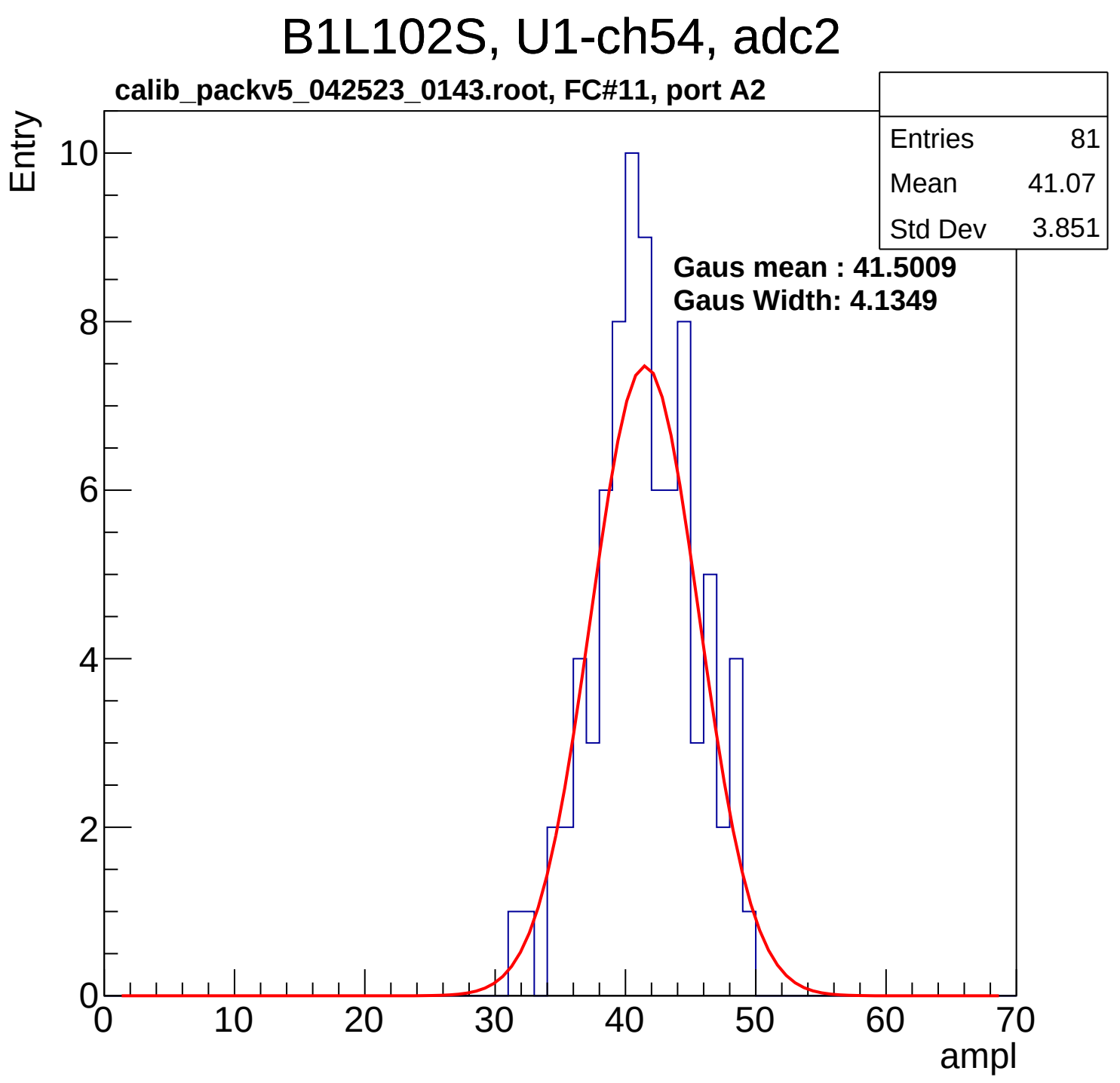

B1L102S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 59.02
Std Dev 2.748
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

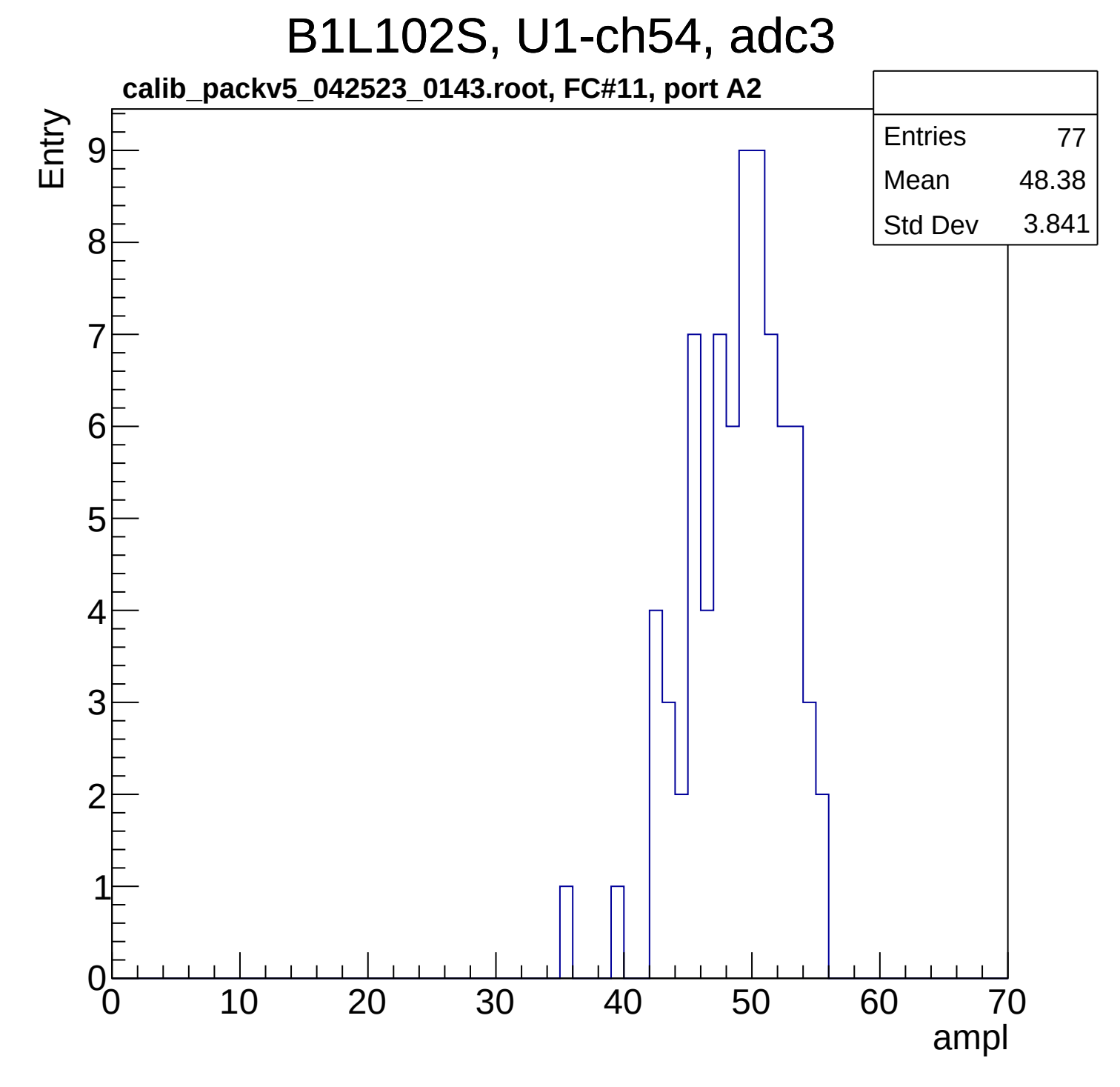

B1L102S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 17
Mean 57.71
Std Dev 14.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 27.11
Std Dev 6.193
Gaus mean : 28.7267
Gaus Width: 3.1746
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 33.55
Std Dev 3.635
Gaus mean : 34.0429
Gaus Width: 3.7530
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 41.07
Std Dev 3.851
Gaus mean : 41.5009
Gaus Width: 4.1349
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 48.38
Std Dev 3.841
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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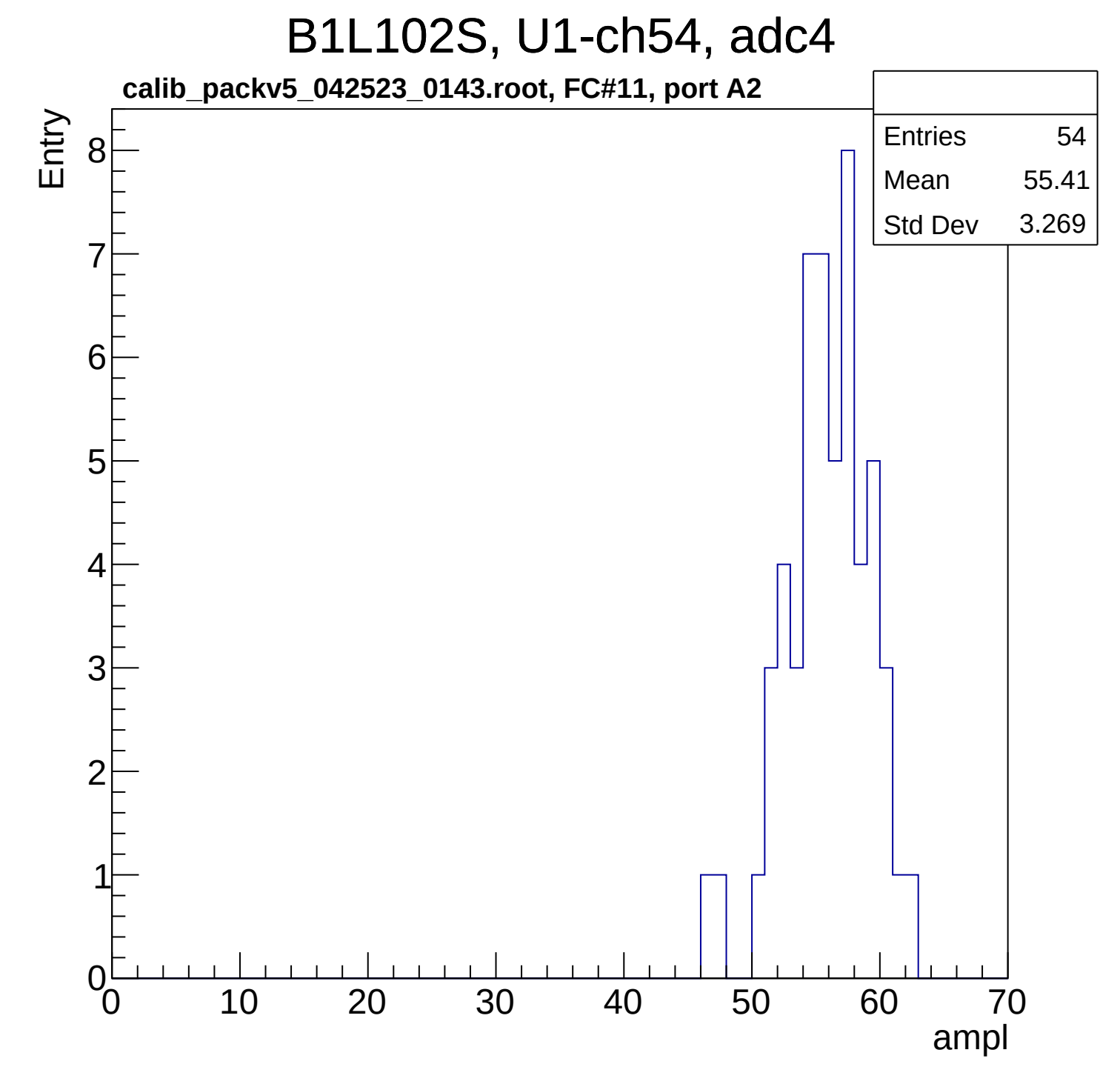
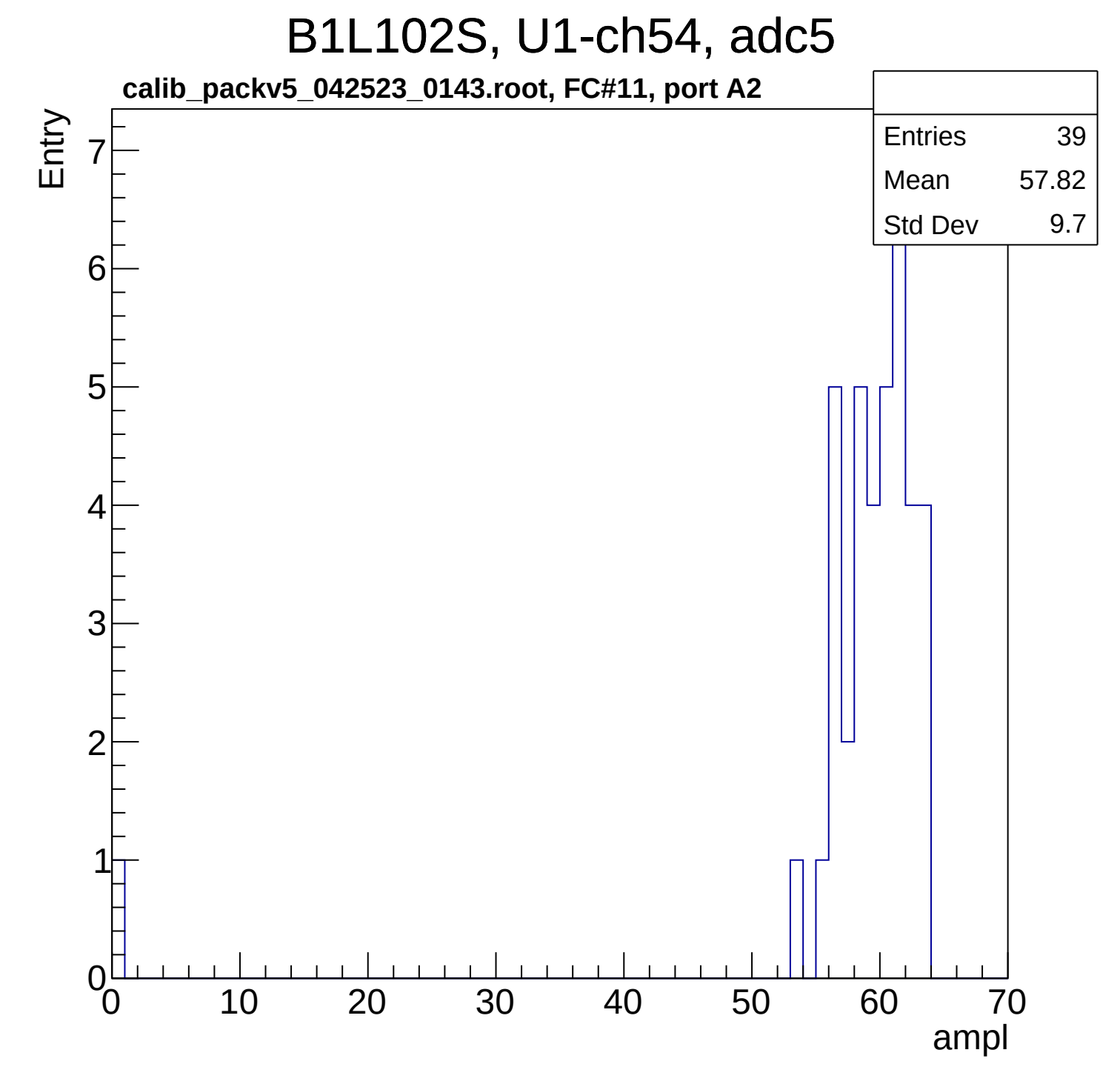
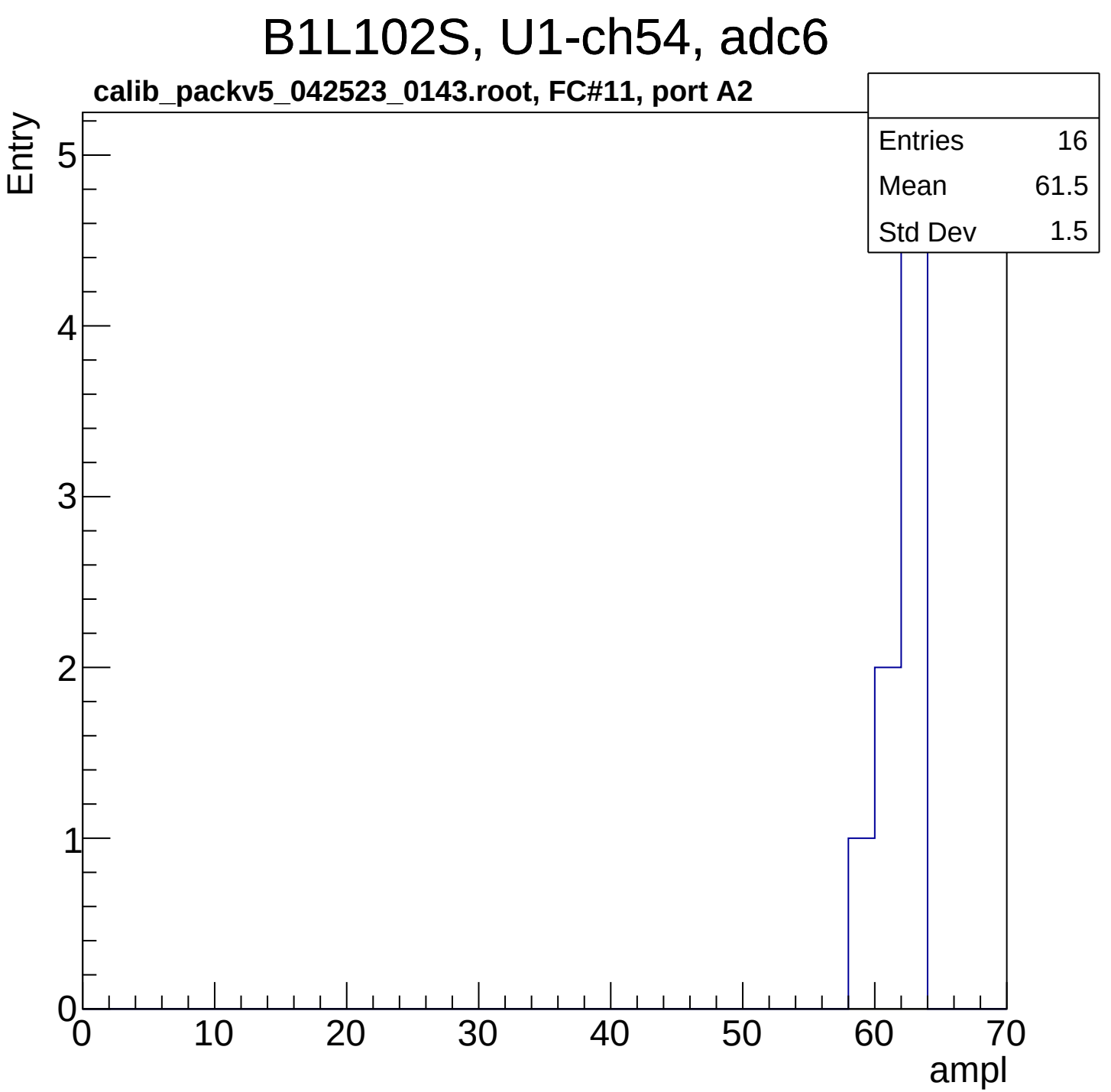
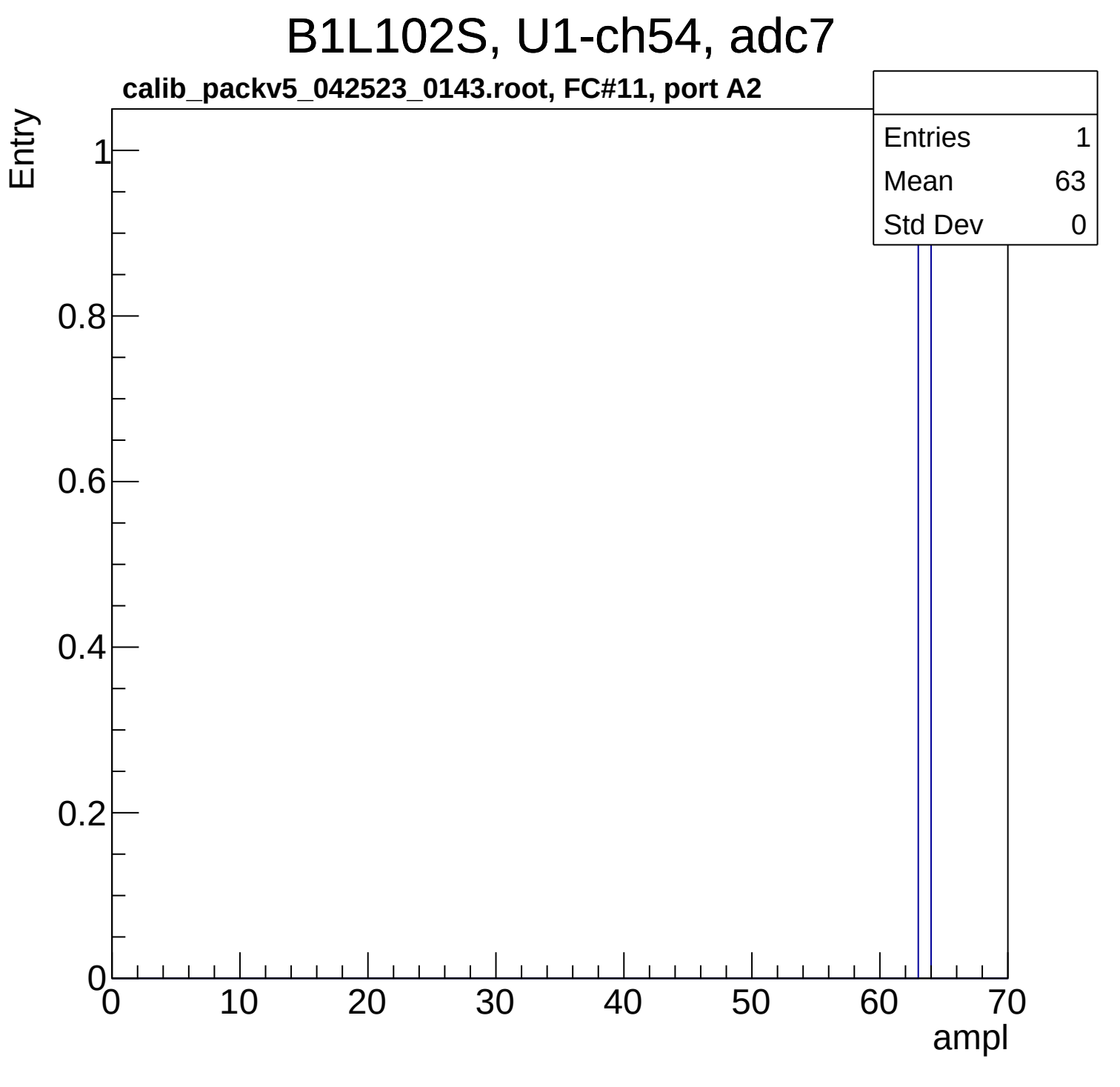
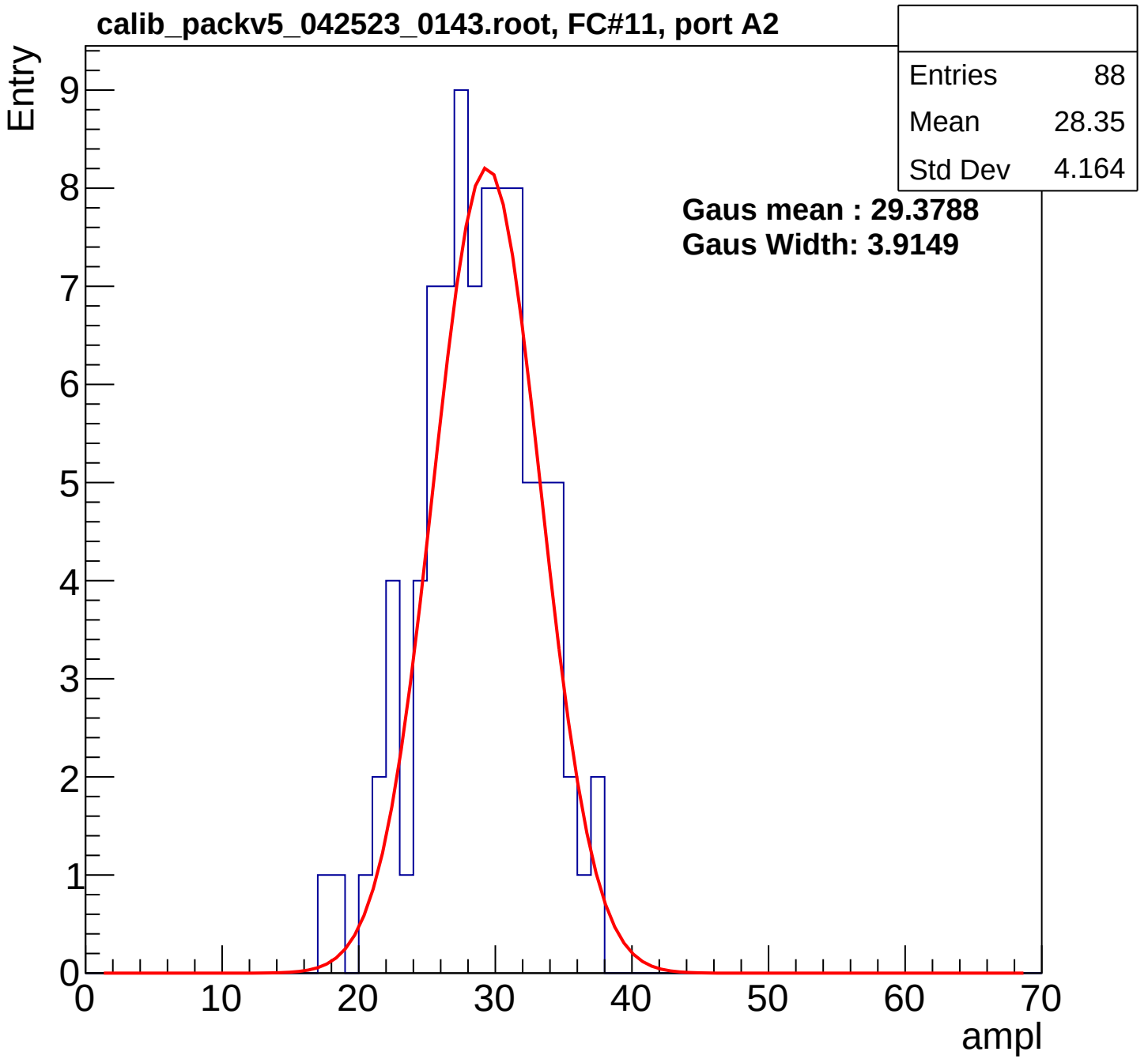

B1L102S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 55.41
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 39
Mean 57.82
Std Dev 9.7
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 61.5
Std Dev 1.5
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 28.35
Std Dev 4.164
Gaus mean : 29.3788
Gaus Width: 3.9149
Entry
ampl
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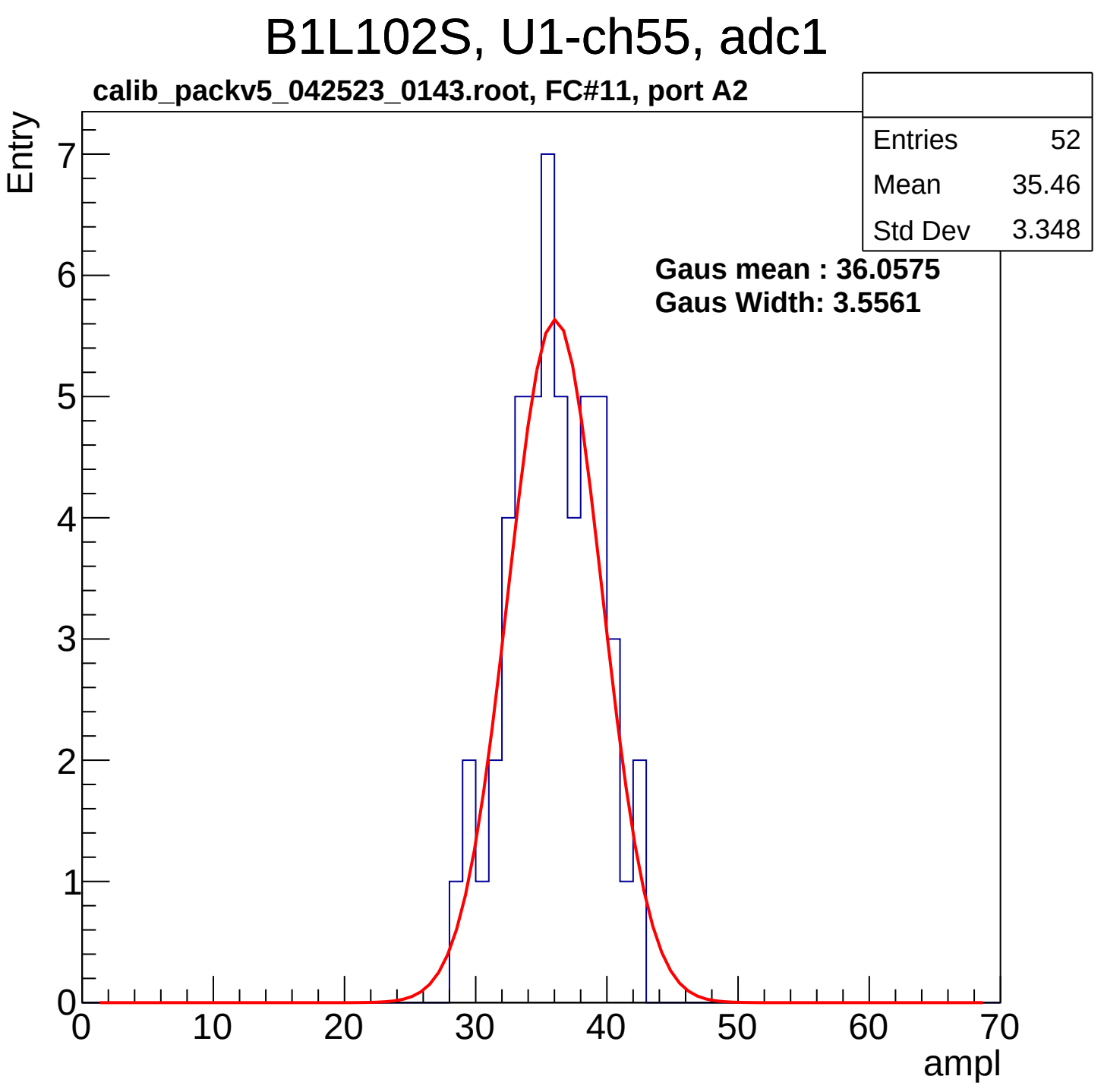
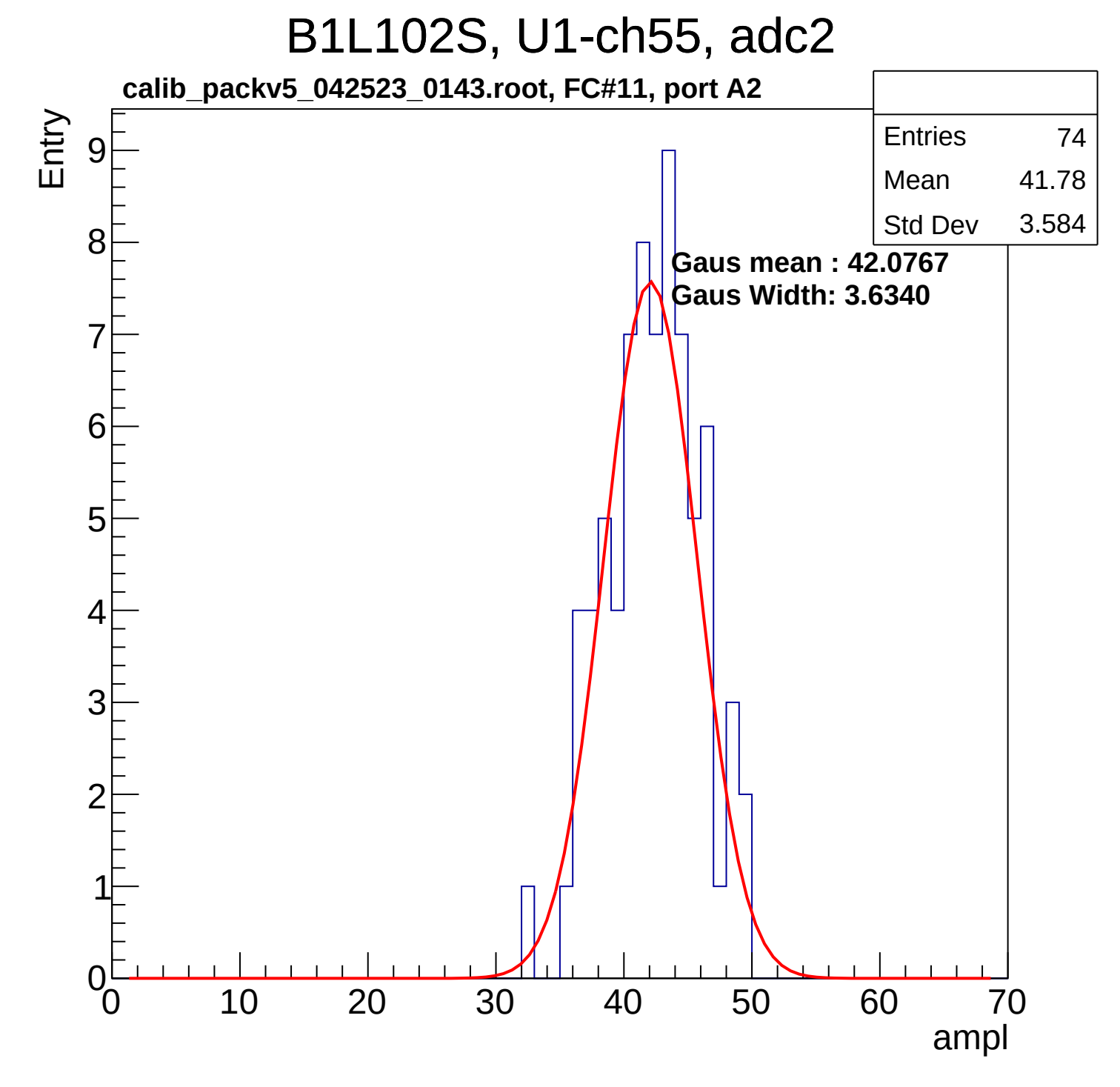
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 35.46
Std Dev 3.348
Gaus mean : 36.0575
Gaus Width: 3.5561
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 41.78
Std Dev 3.584
Gaus mean : 42.0767
Gaus Width: 3.6340
Entry
ampl
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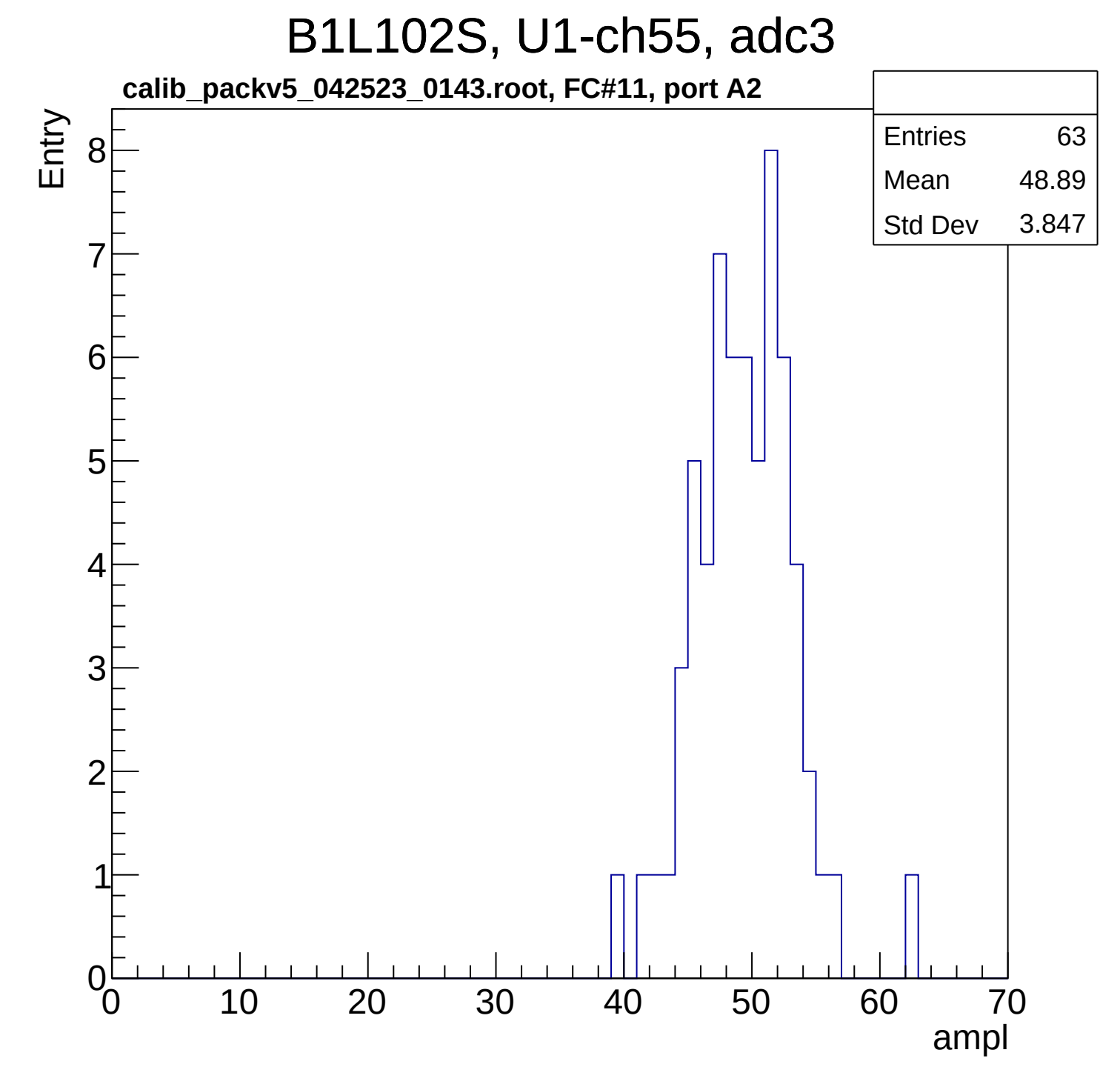
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 48.89
Std Dev 3.847
Entry
ampl
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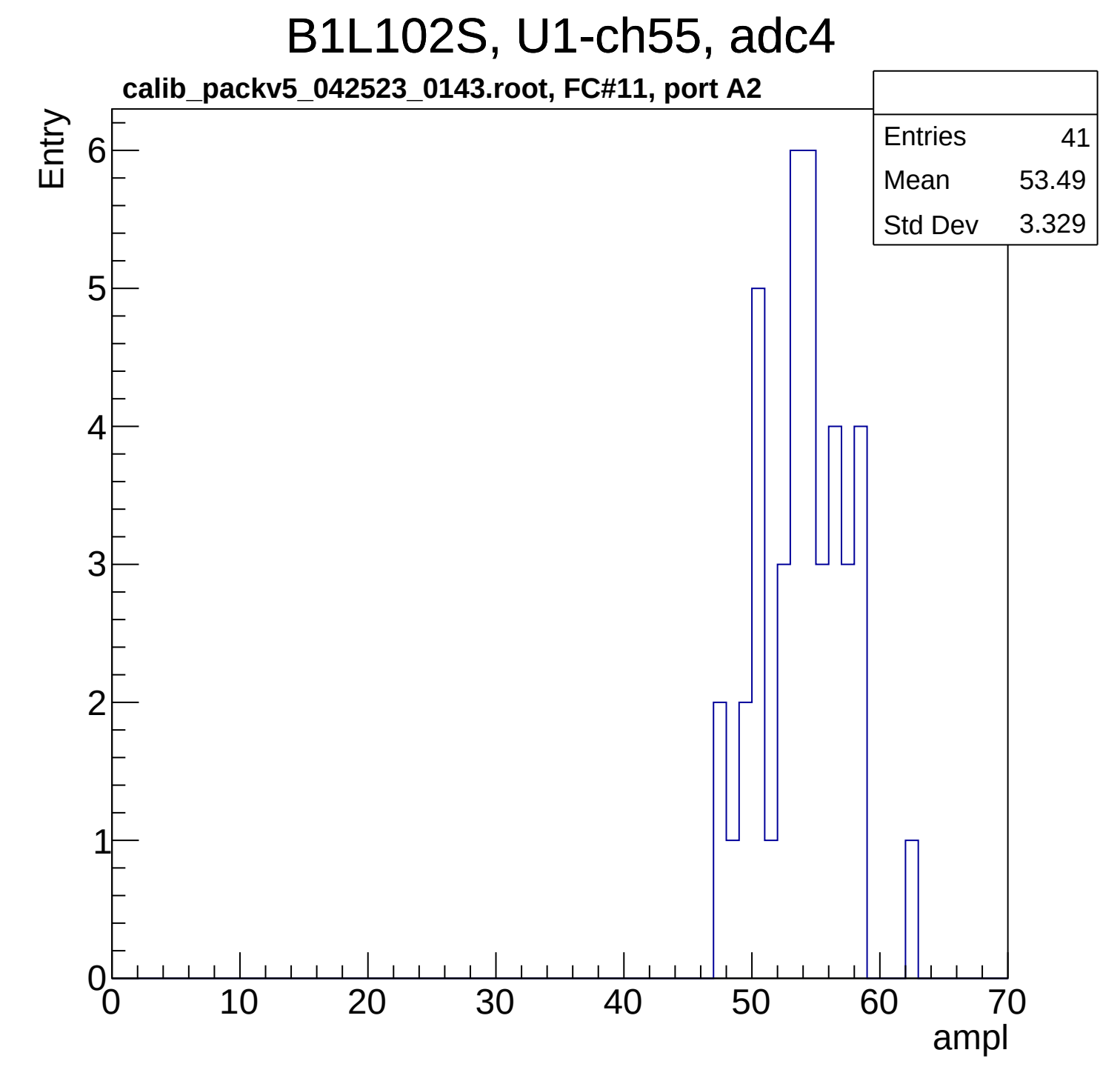
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 53.49
Std Dev 3.329
Entry
ampl
0
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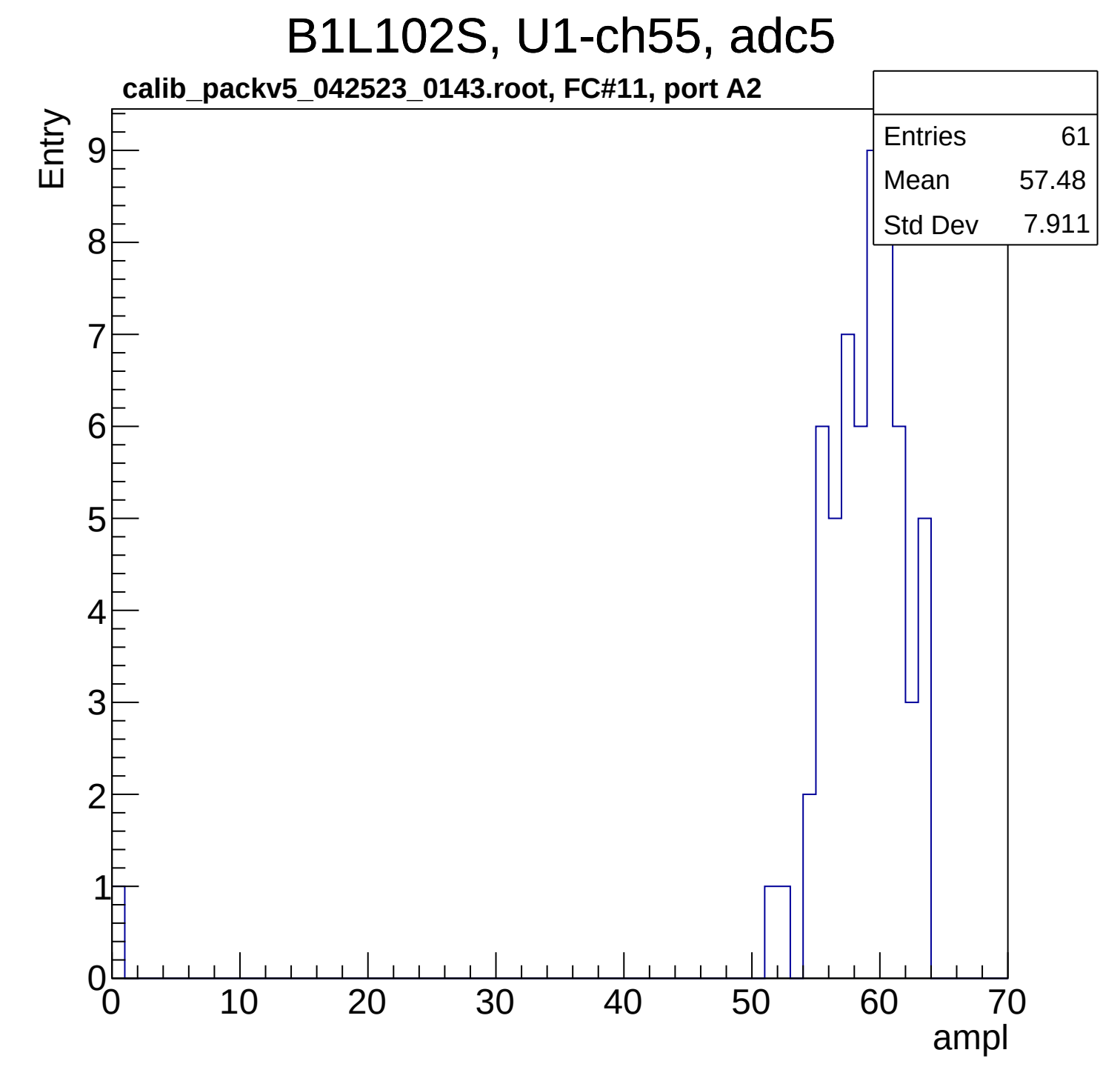
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 57.48
Std Dev 7.911
Entry
ampl
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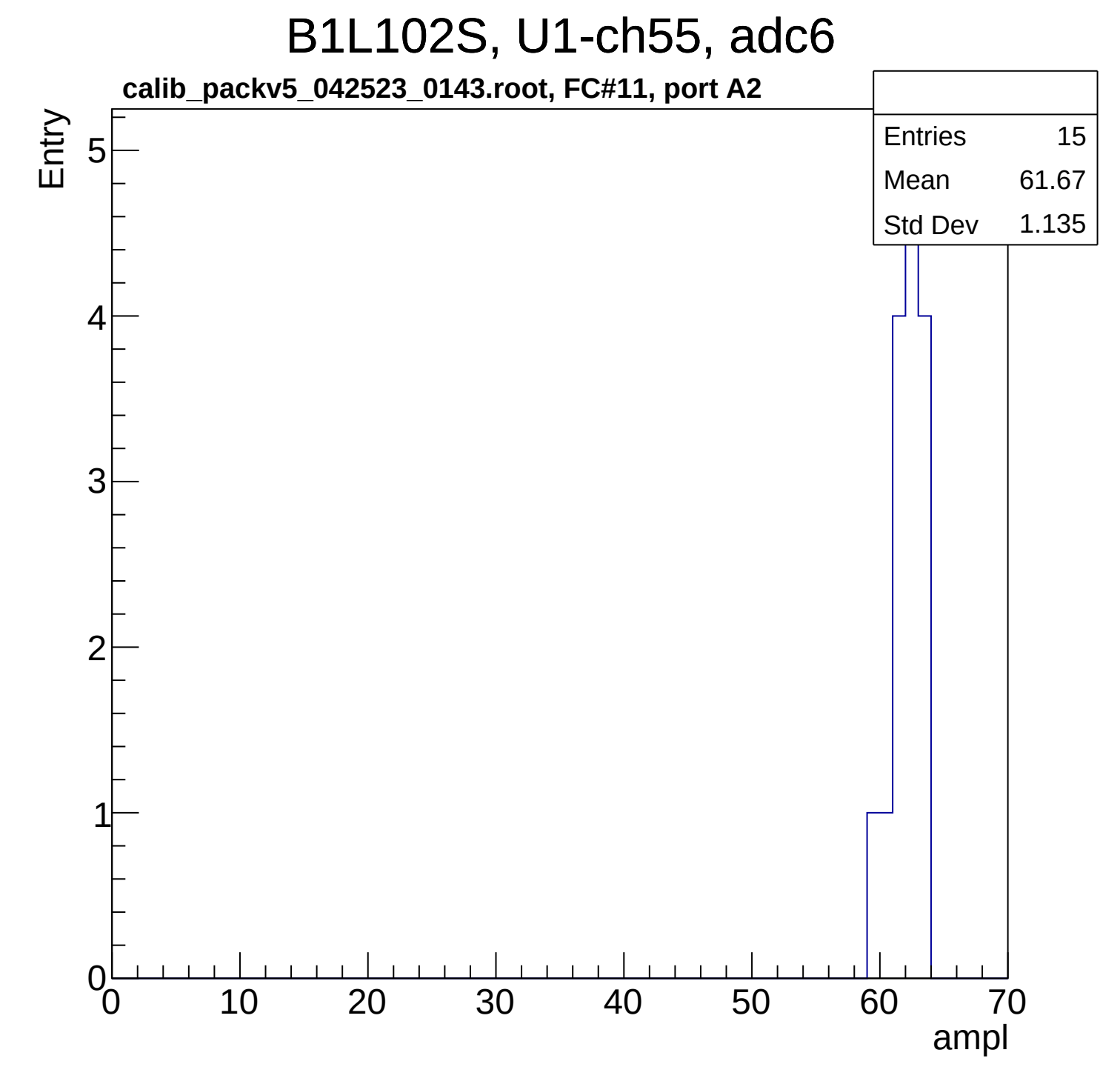
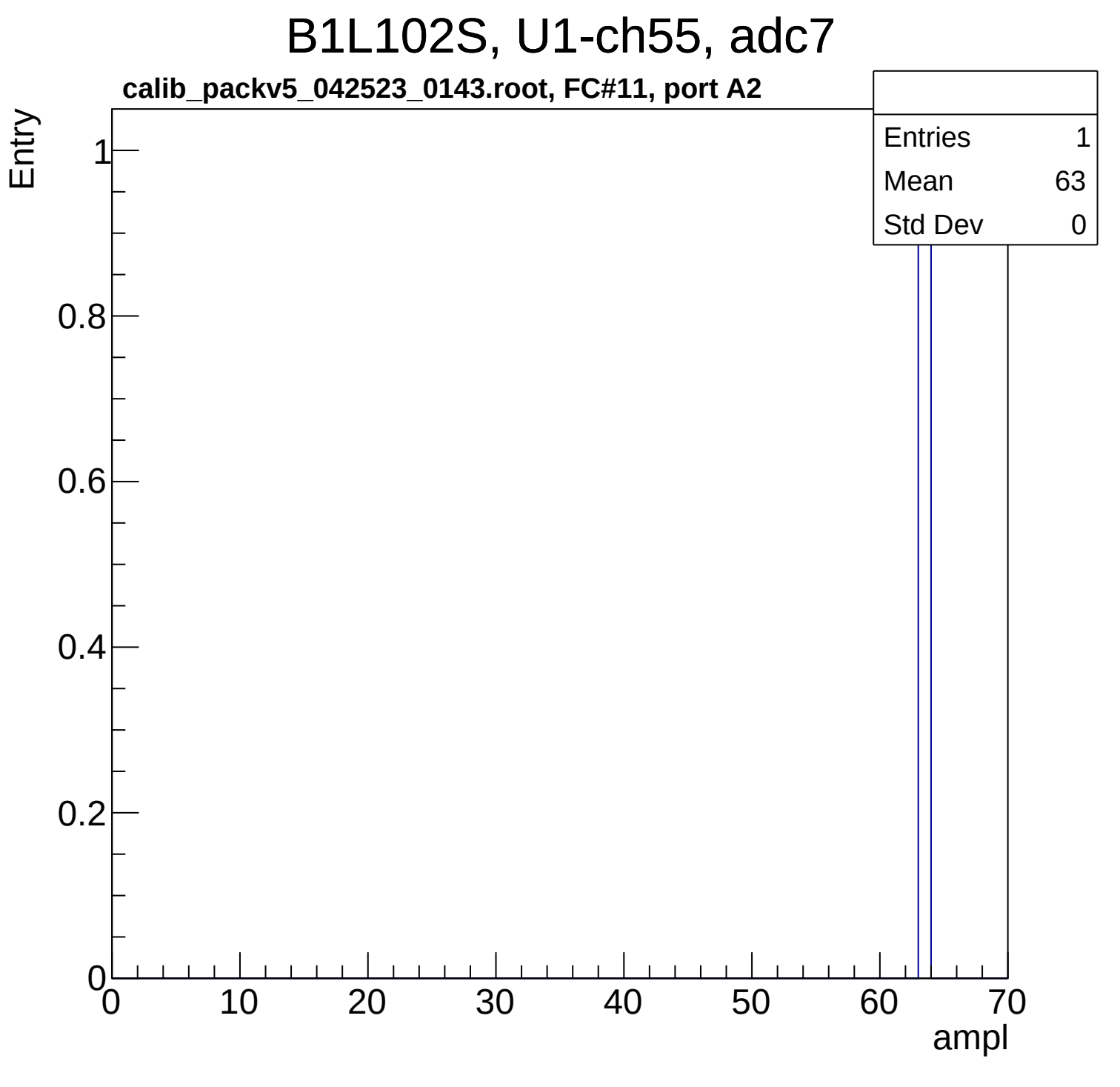
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 61.67
Std Dev 1.135
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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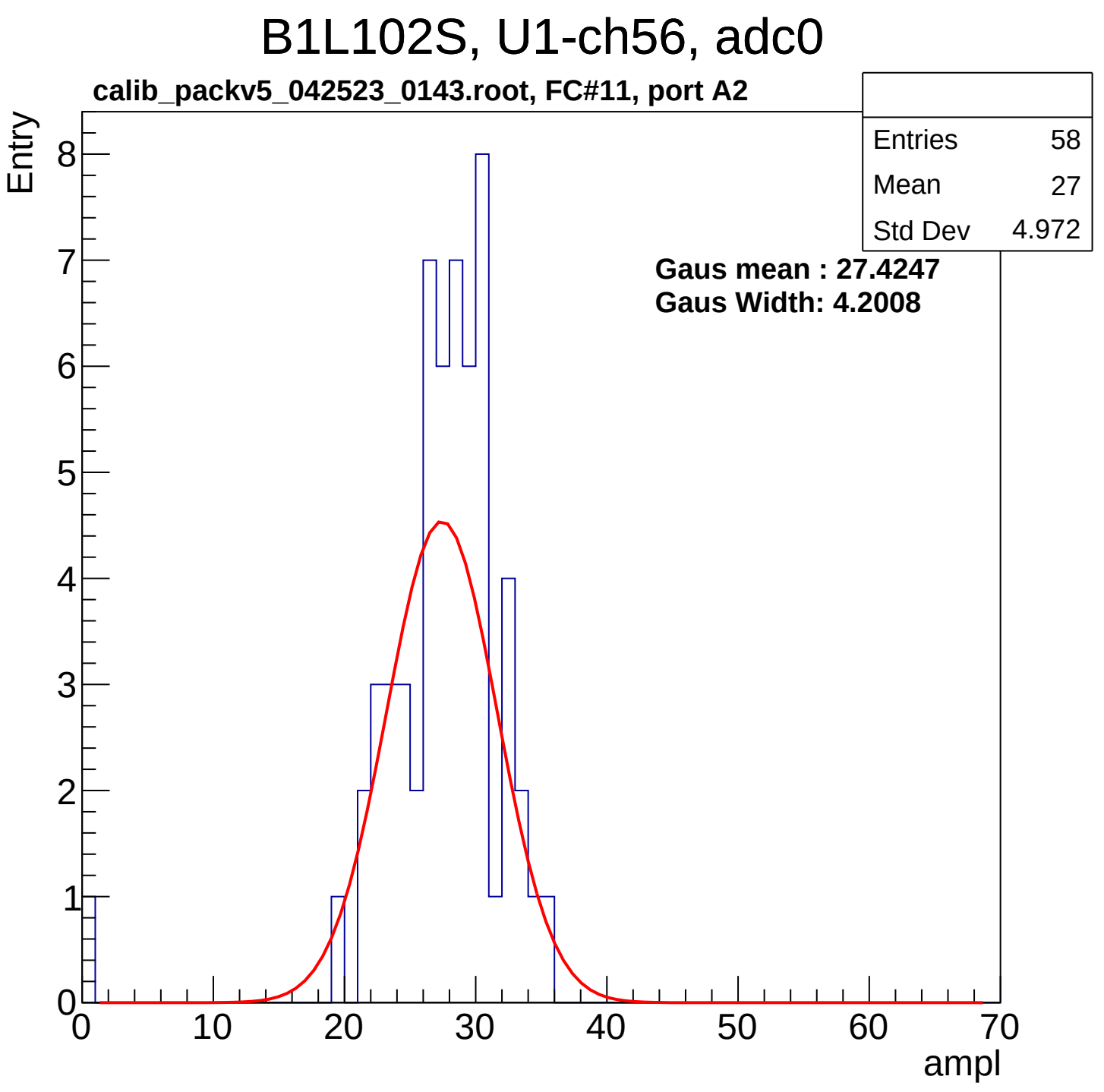
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 27
Std Dev 4.972
Gaus mean : 27.4247
Gaus Width: 4.2008
Entry
ampl
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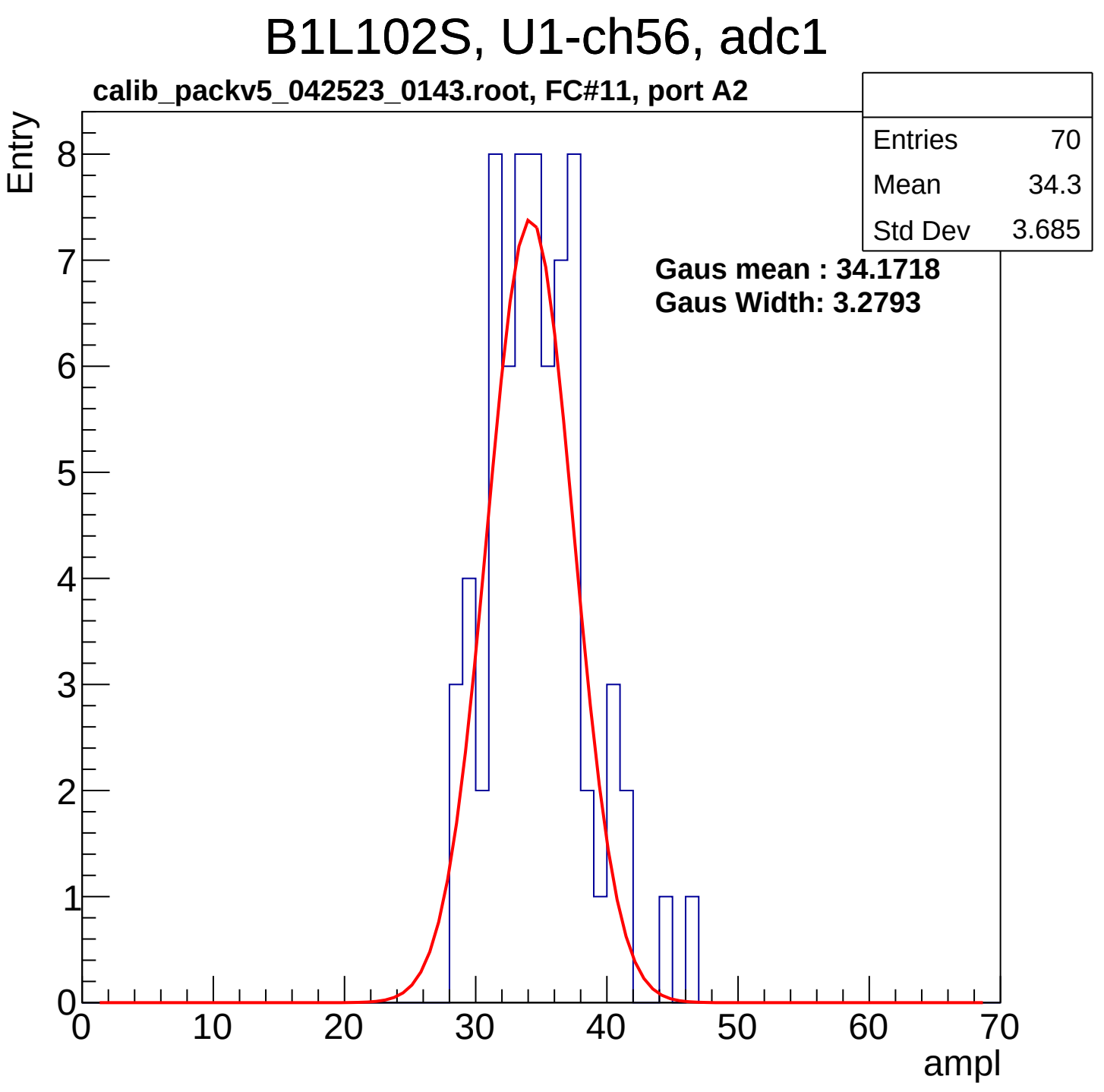
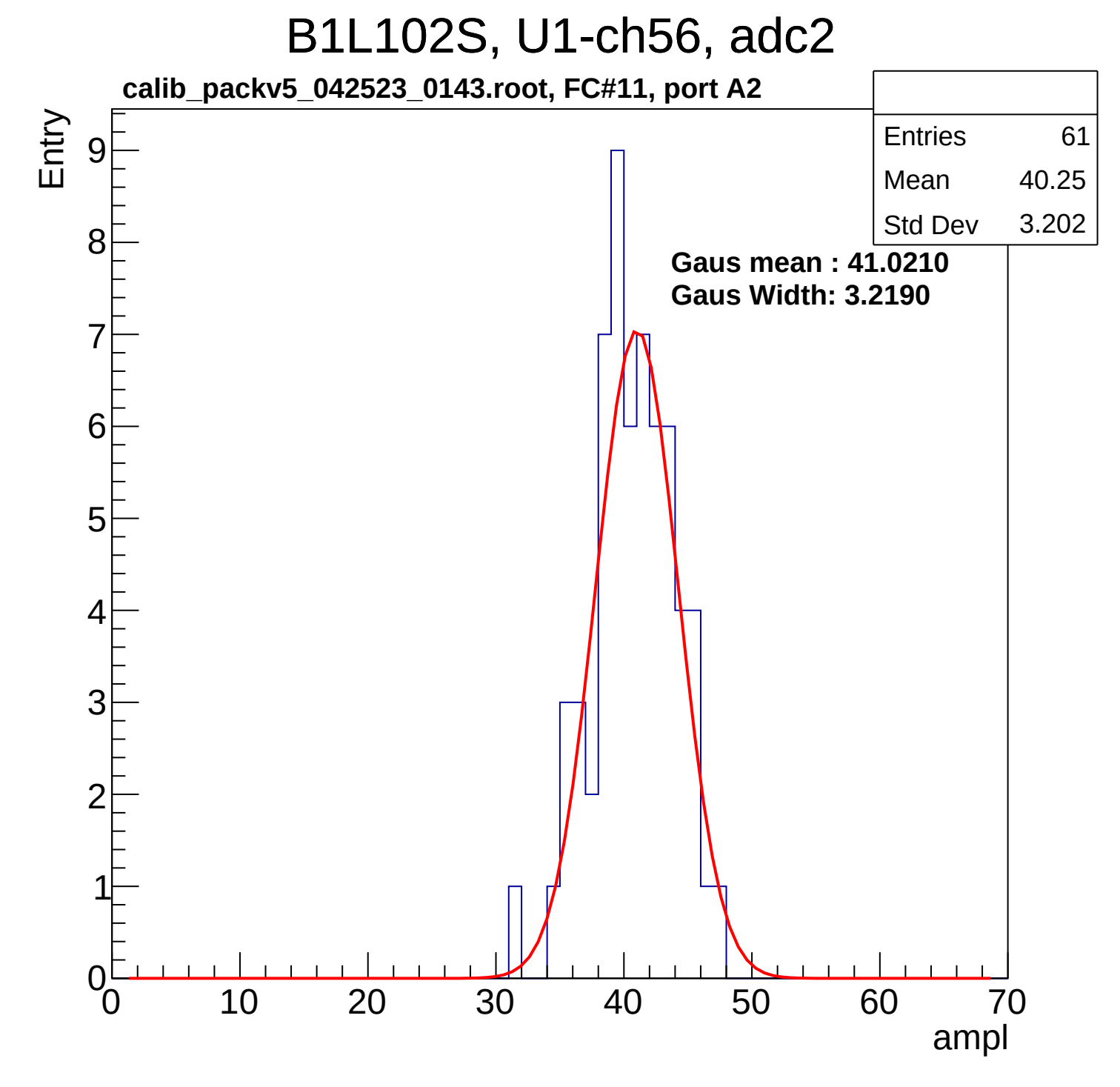
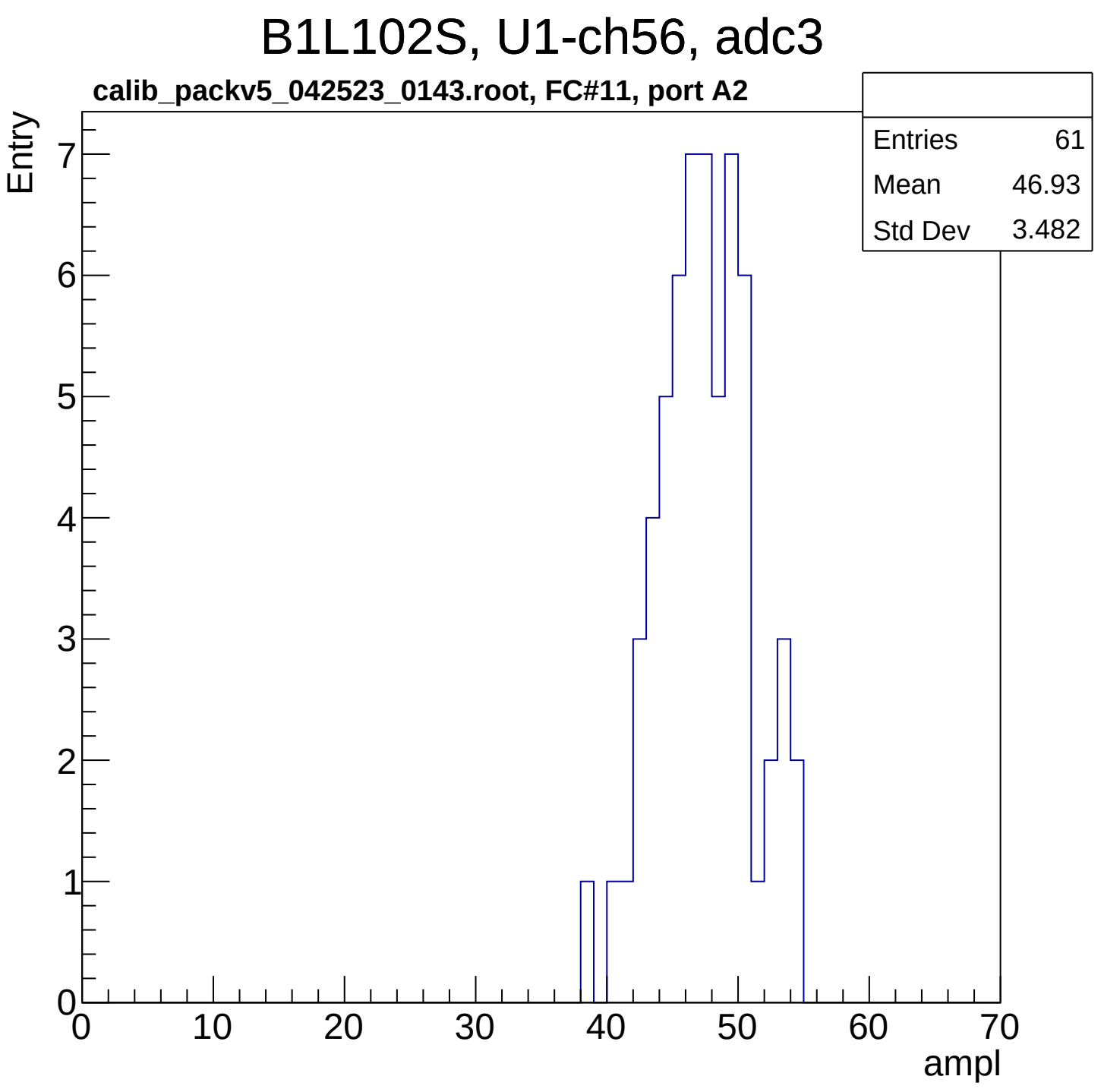
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.3
Std Dev 3.685
Gaus mean : 34.1718
Gaus Width: 3.2793
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 40.25
Std Dev 3.202
Gaus mean : 41.0210
Gaus Width: 3.2190
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 46.93
Std Dev 3.482
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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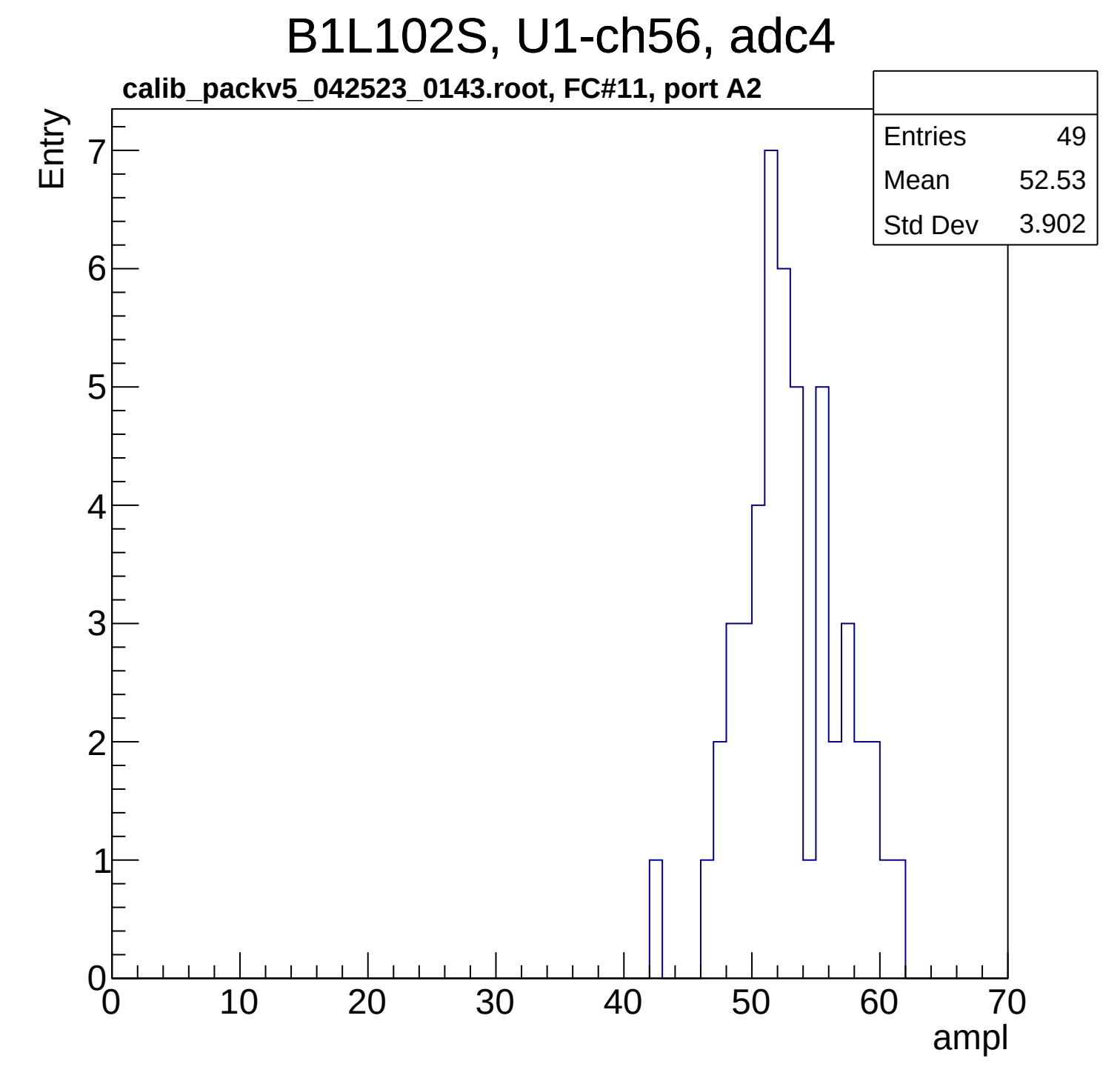
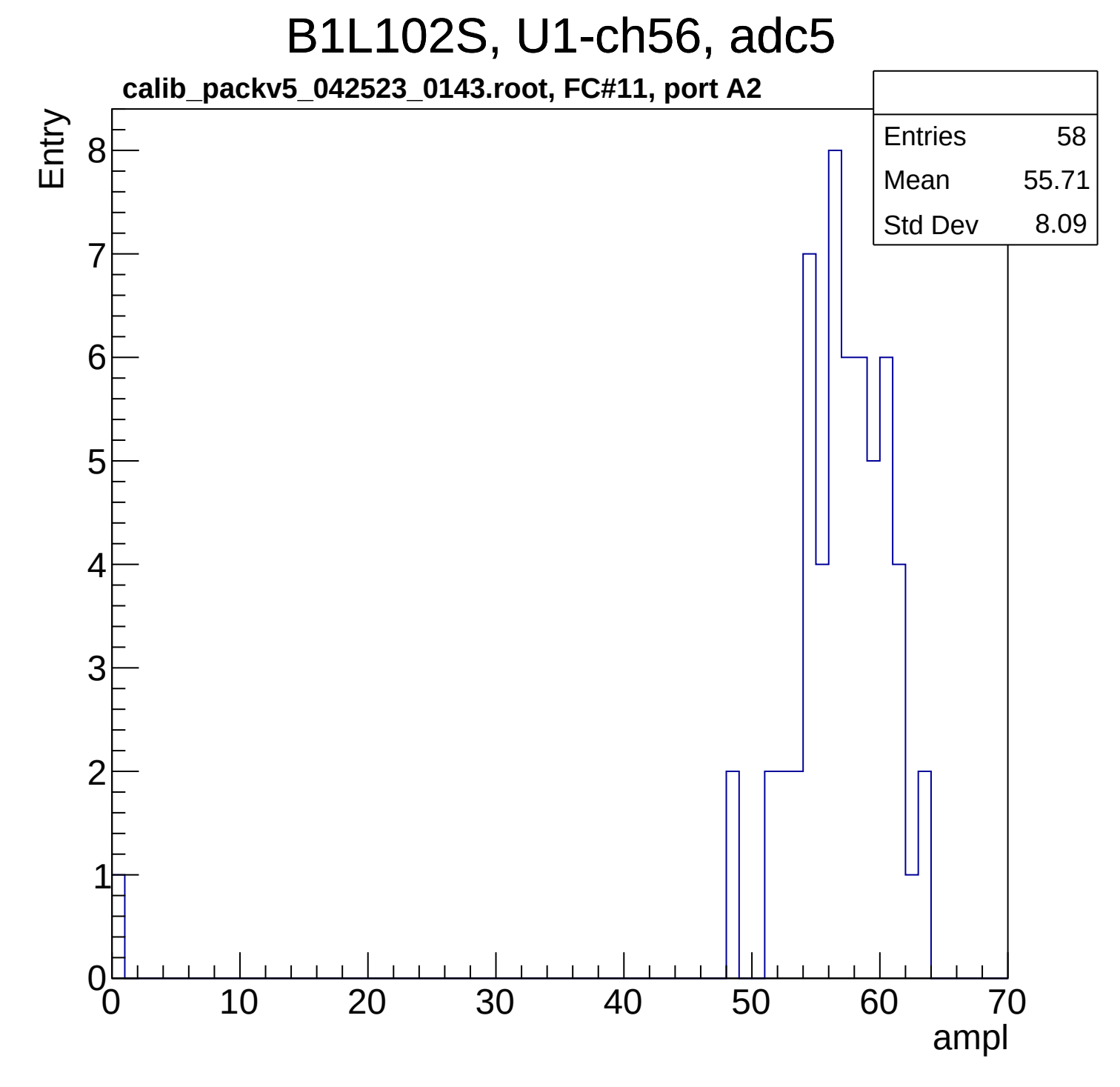

B1L102S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 52.53
Std Dev 3.902
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.71
Std Dev 8.09
Entry
ampl
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1
2
3
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5
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7
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0
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40
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60
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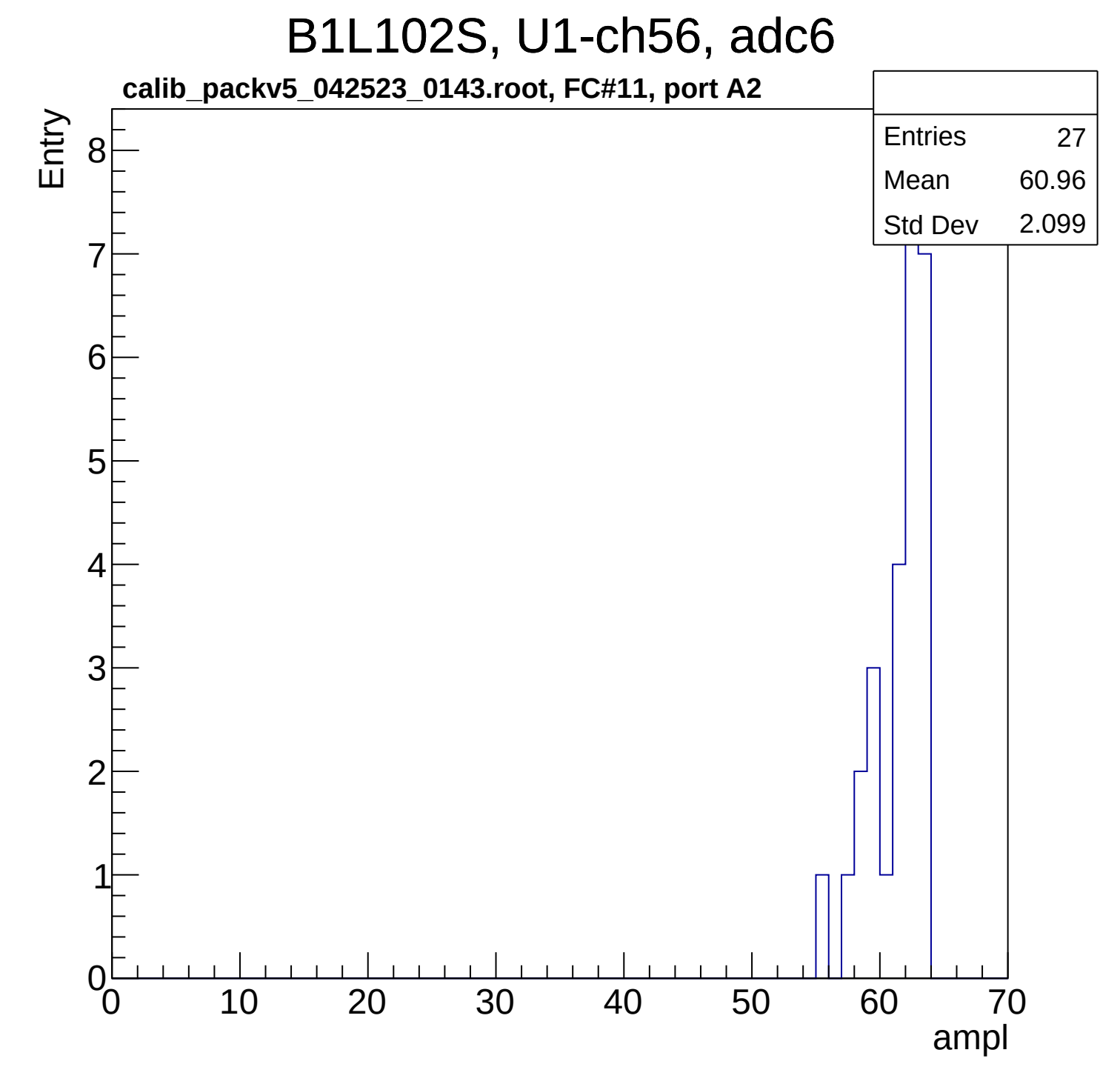
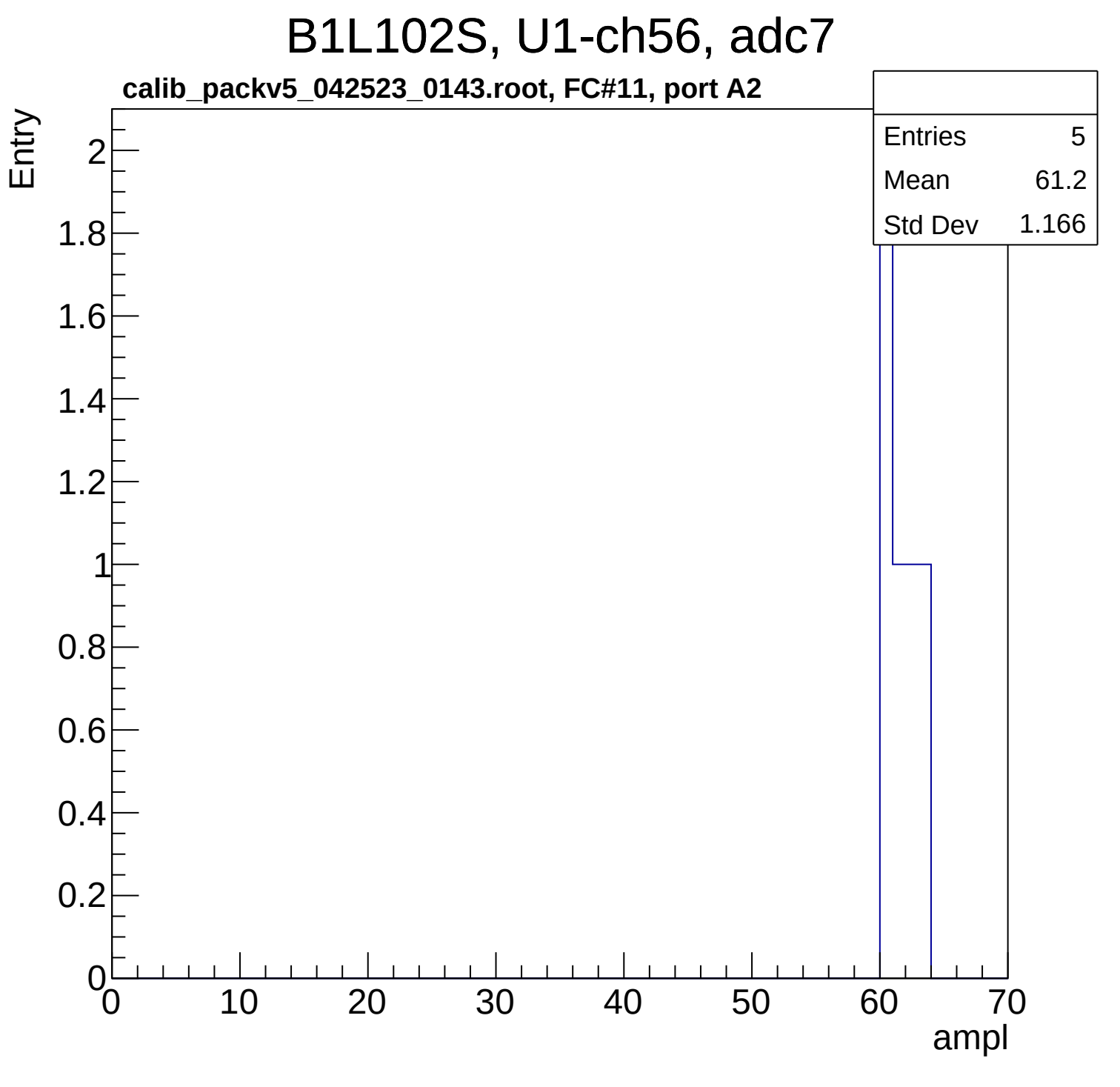
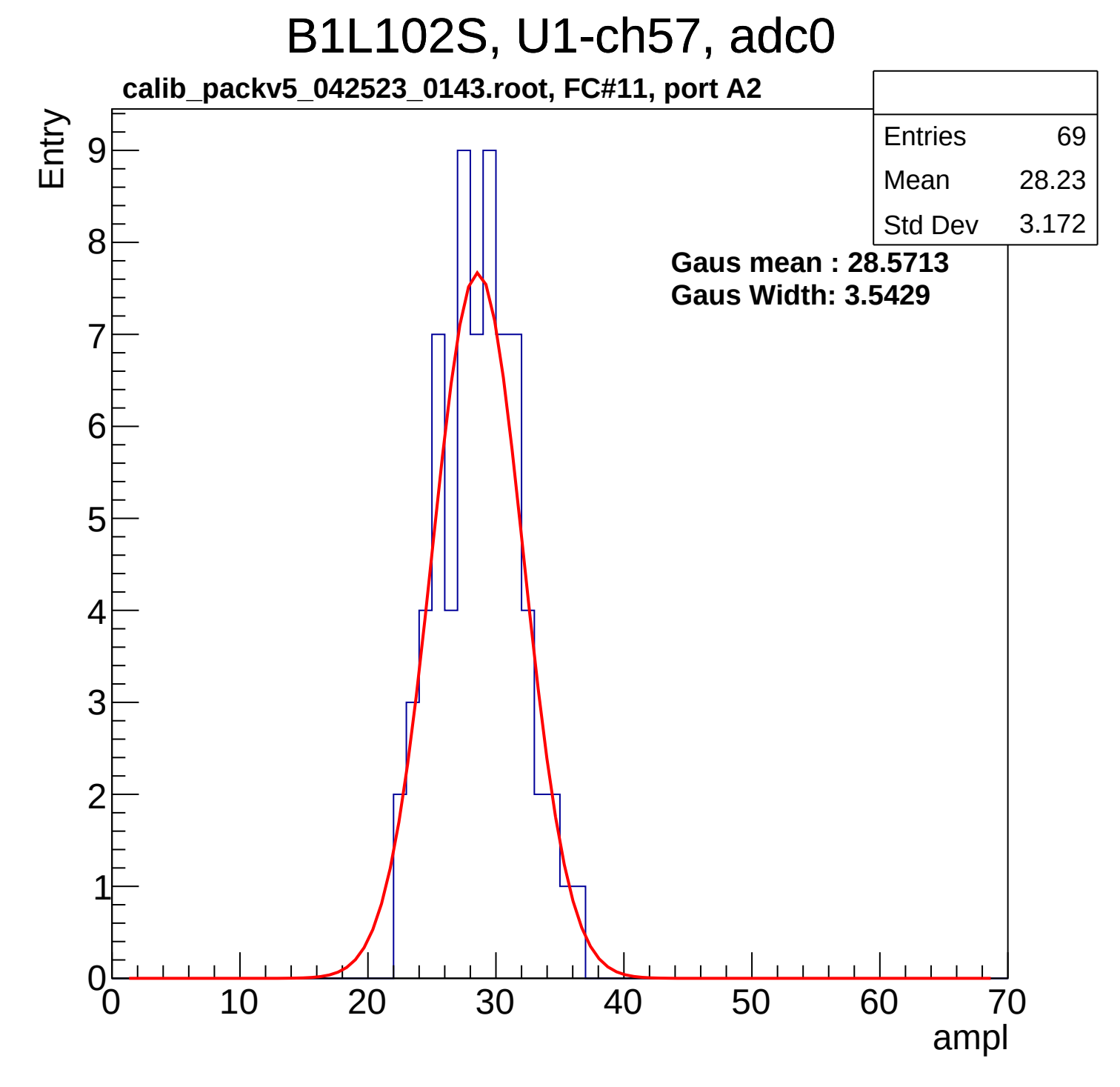
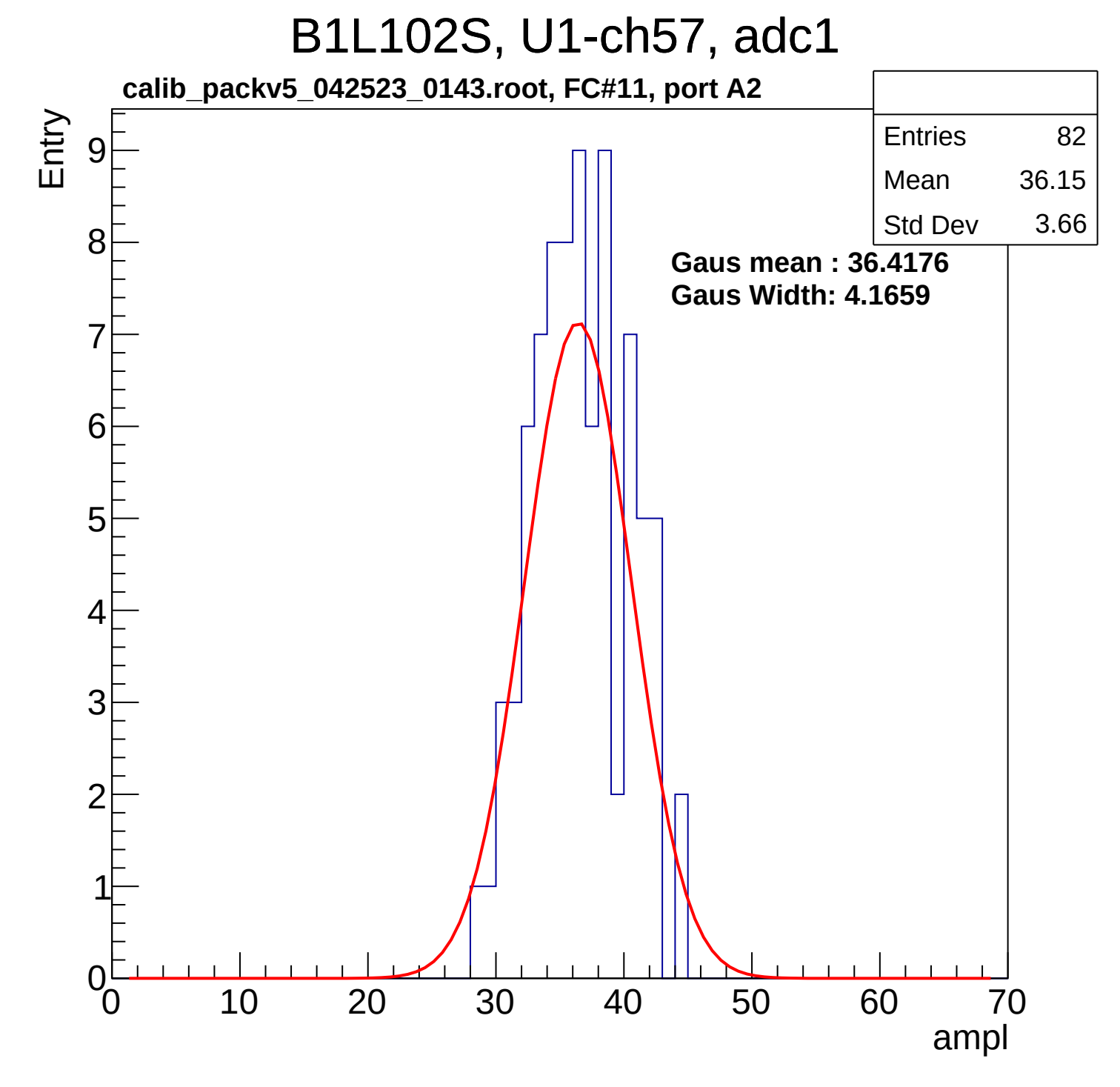
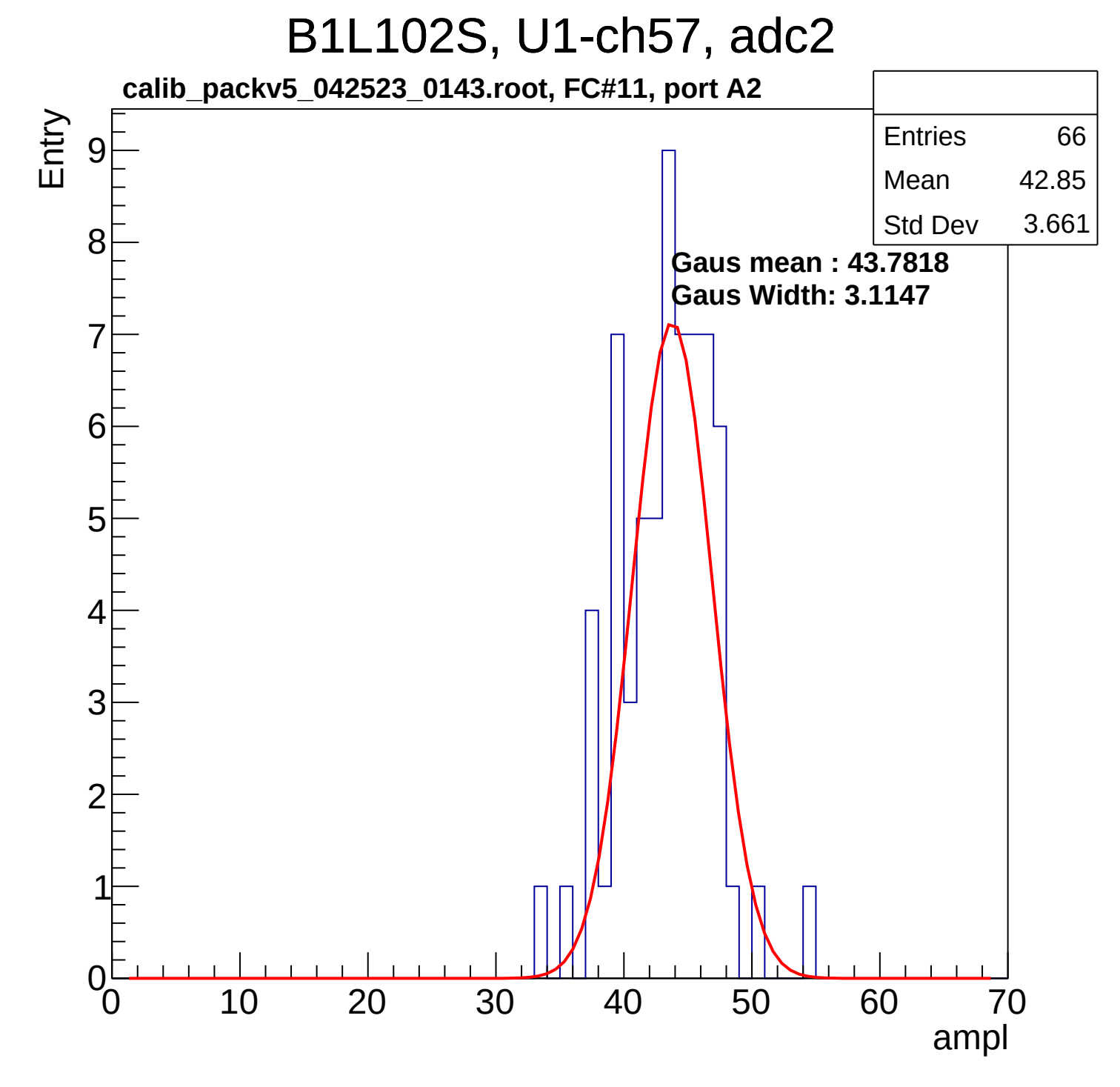
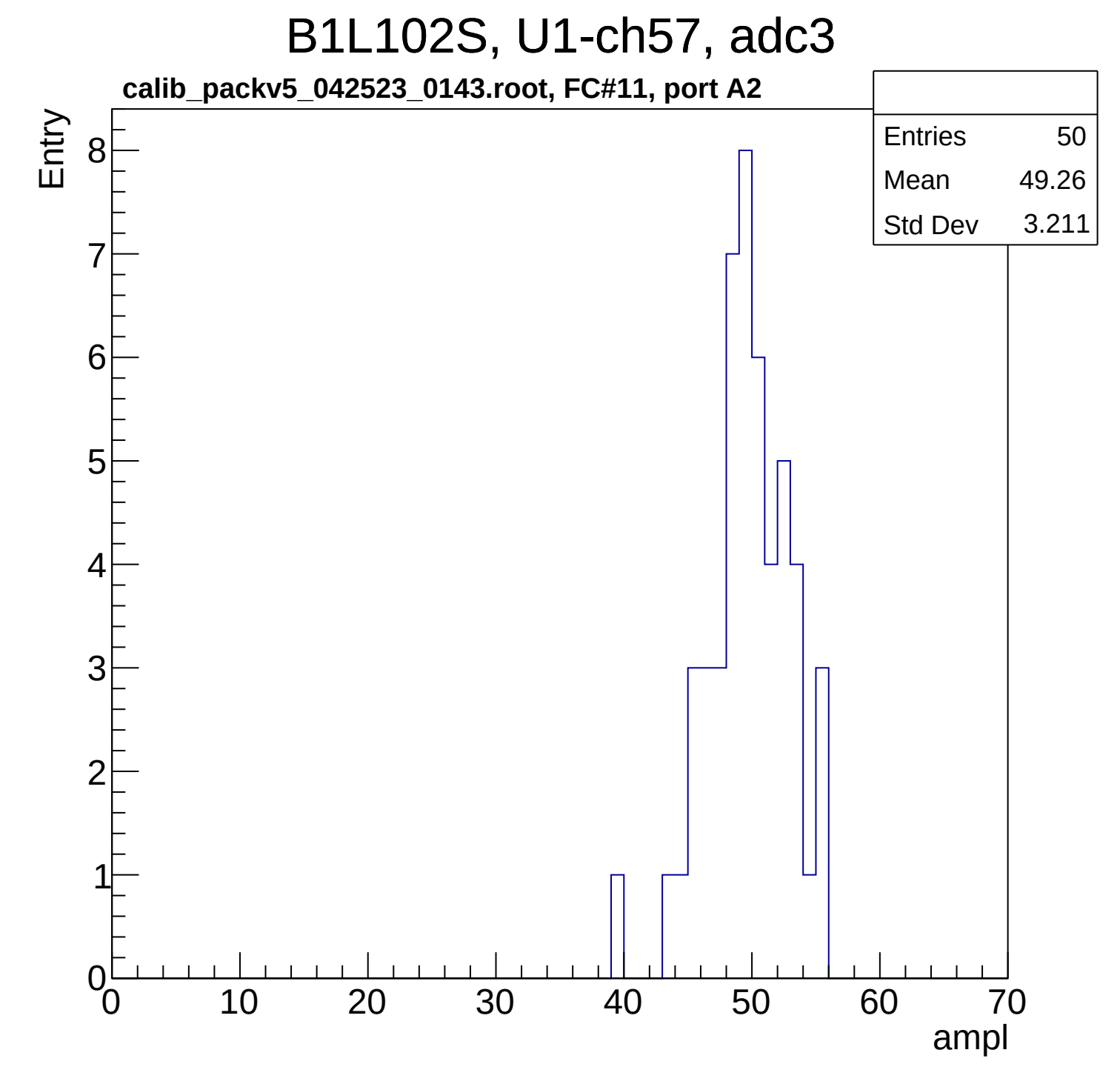

B1L102S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 27
Mean 60.96
Std Dev 2.099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

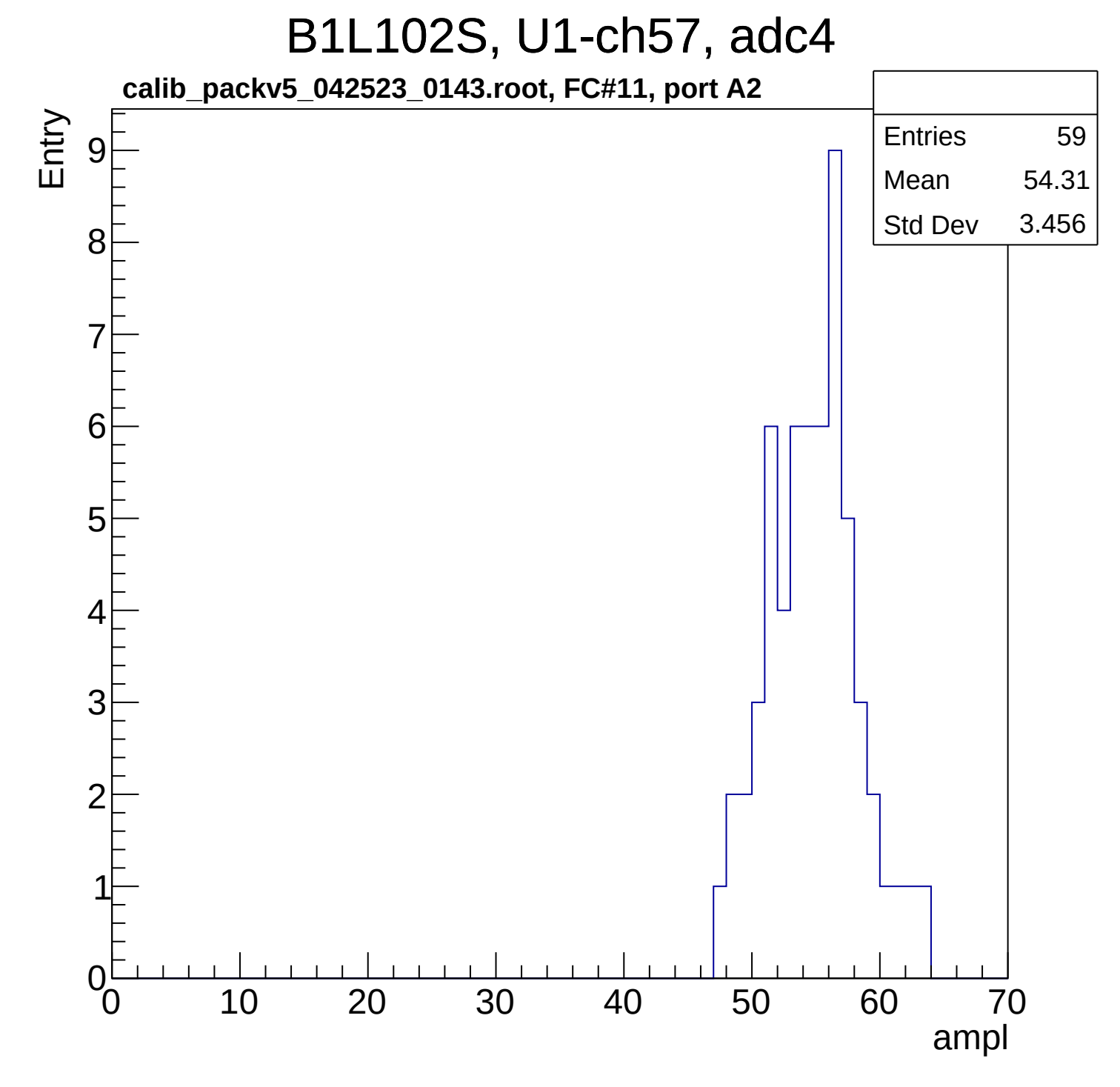

B1L102S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 61.2
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

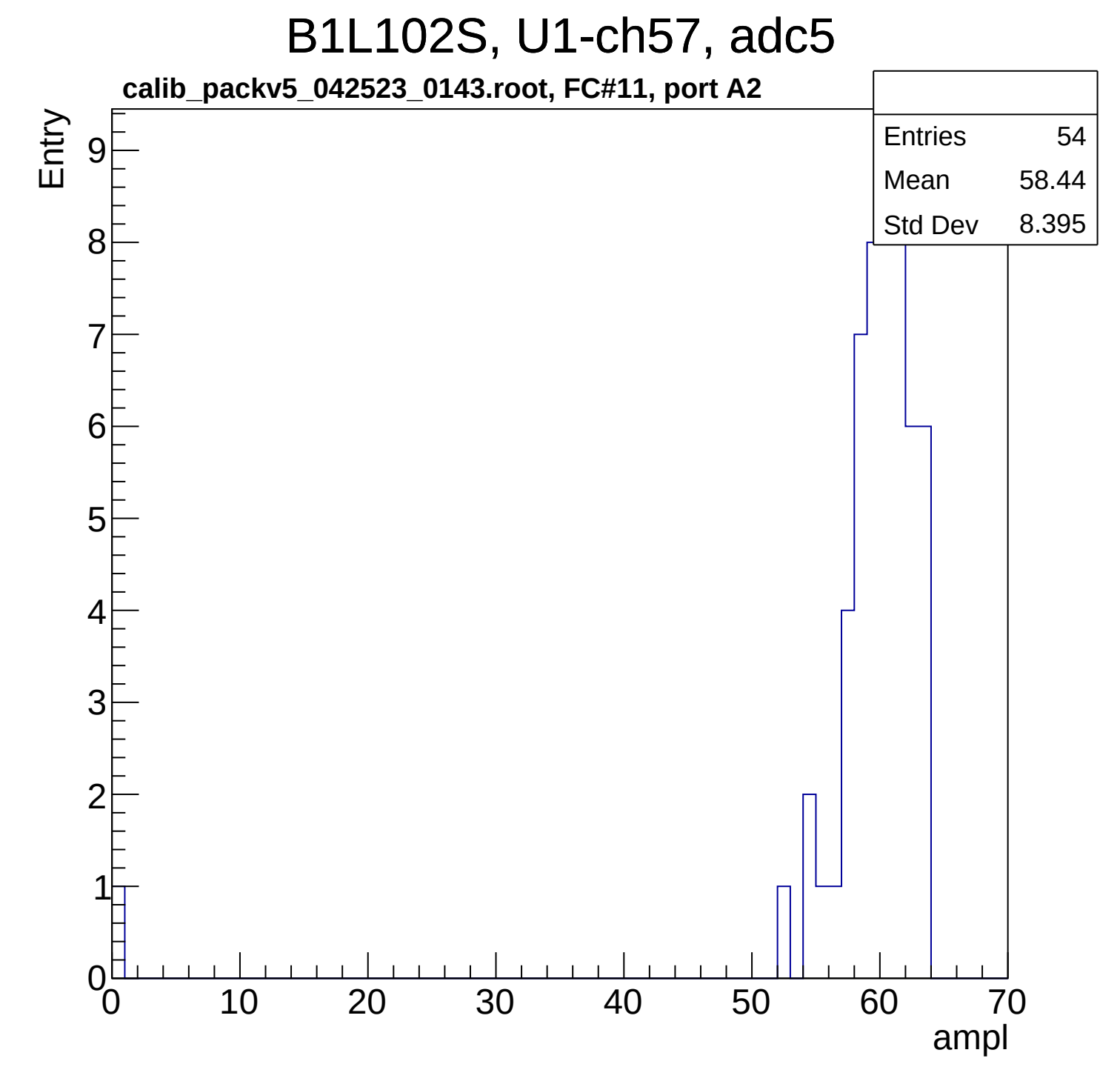

B1L102S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.23
Std Dev 3.172
Gaus mean : 28.5713
Gaus Width: 3.5429
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

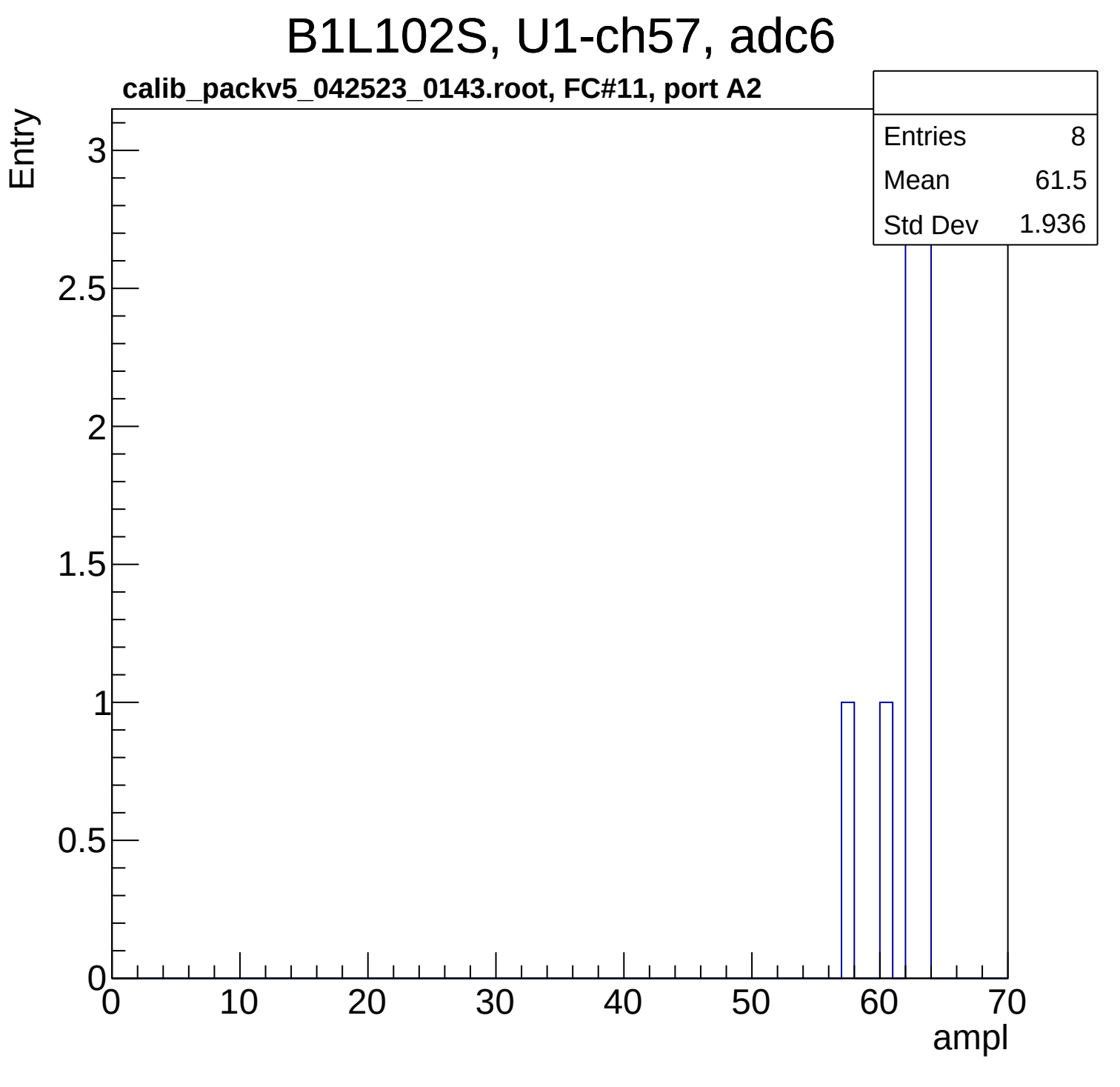

B1L102S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 36.15
Std Dev 3.66
Gaus mean : 36.4176
Gaus Width: 4.1659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

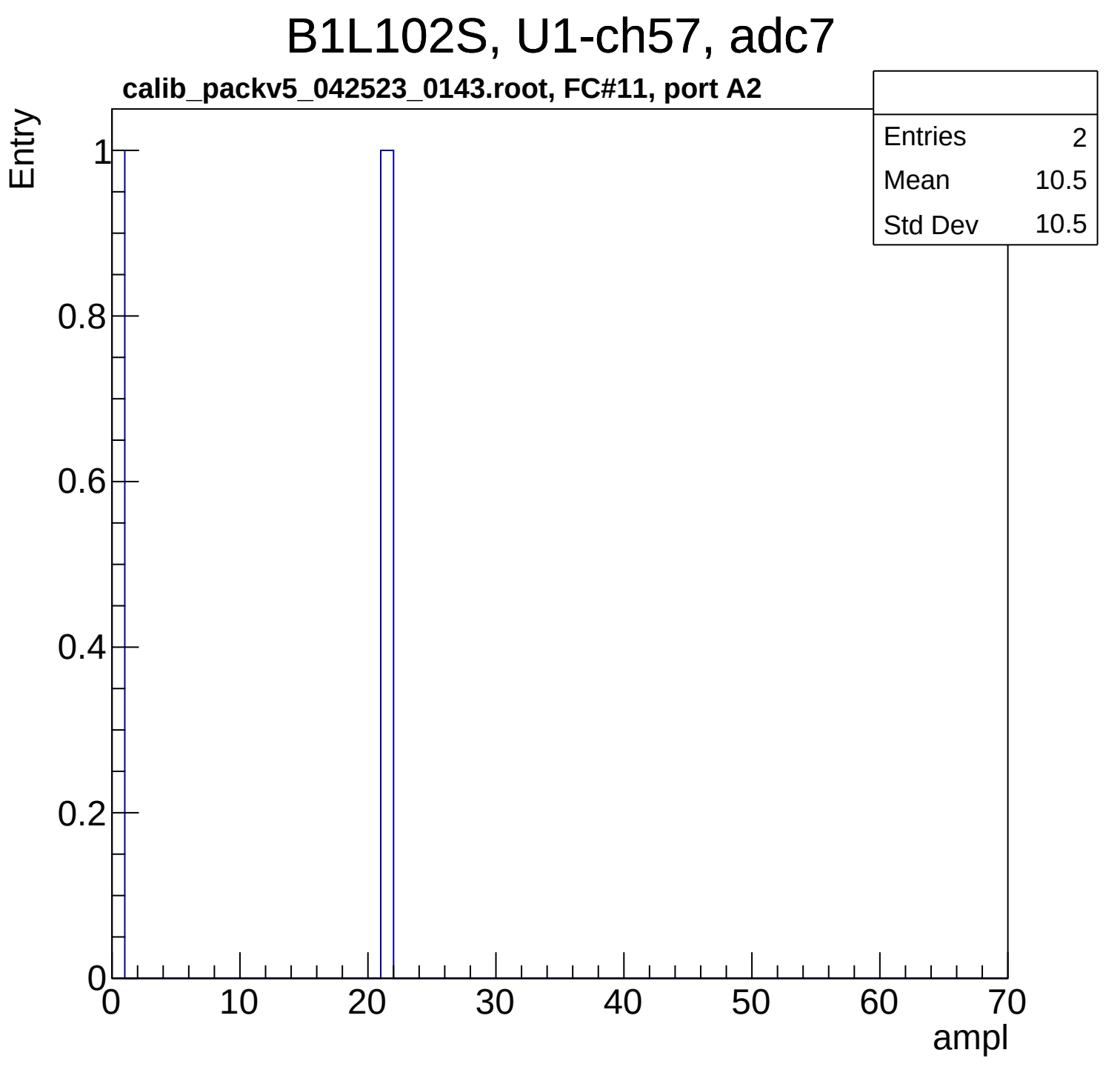

B1L102S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 42.85
Std Dev 3.661
Gaus mean : 43.7818
Gaus Width: 3.1147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

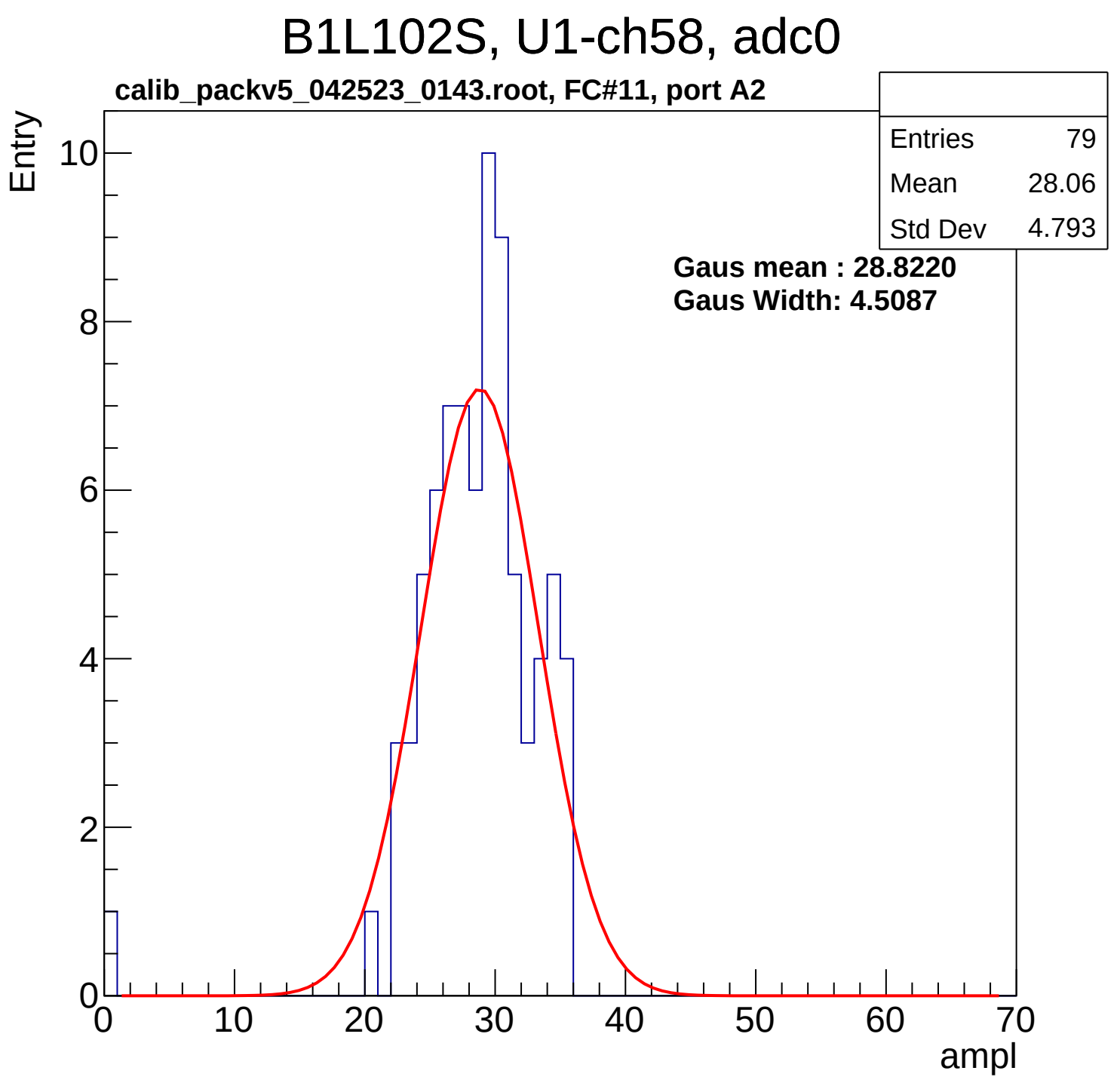

B1L102S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 49.26
Std Dev 3.211
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

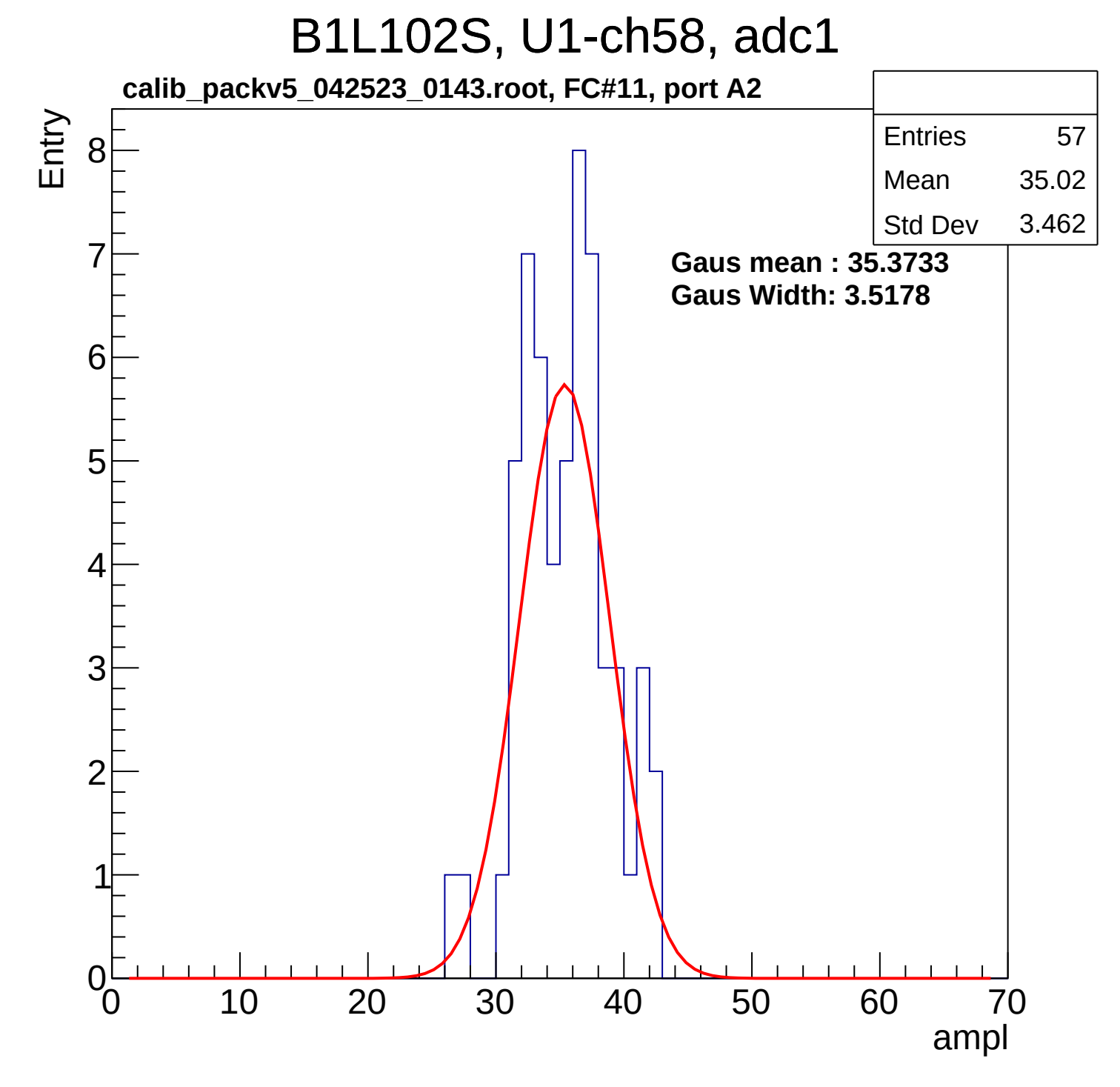

B1L102S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 54.31
Std Dev 3.456
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

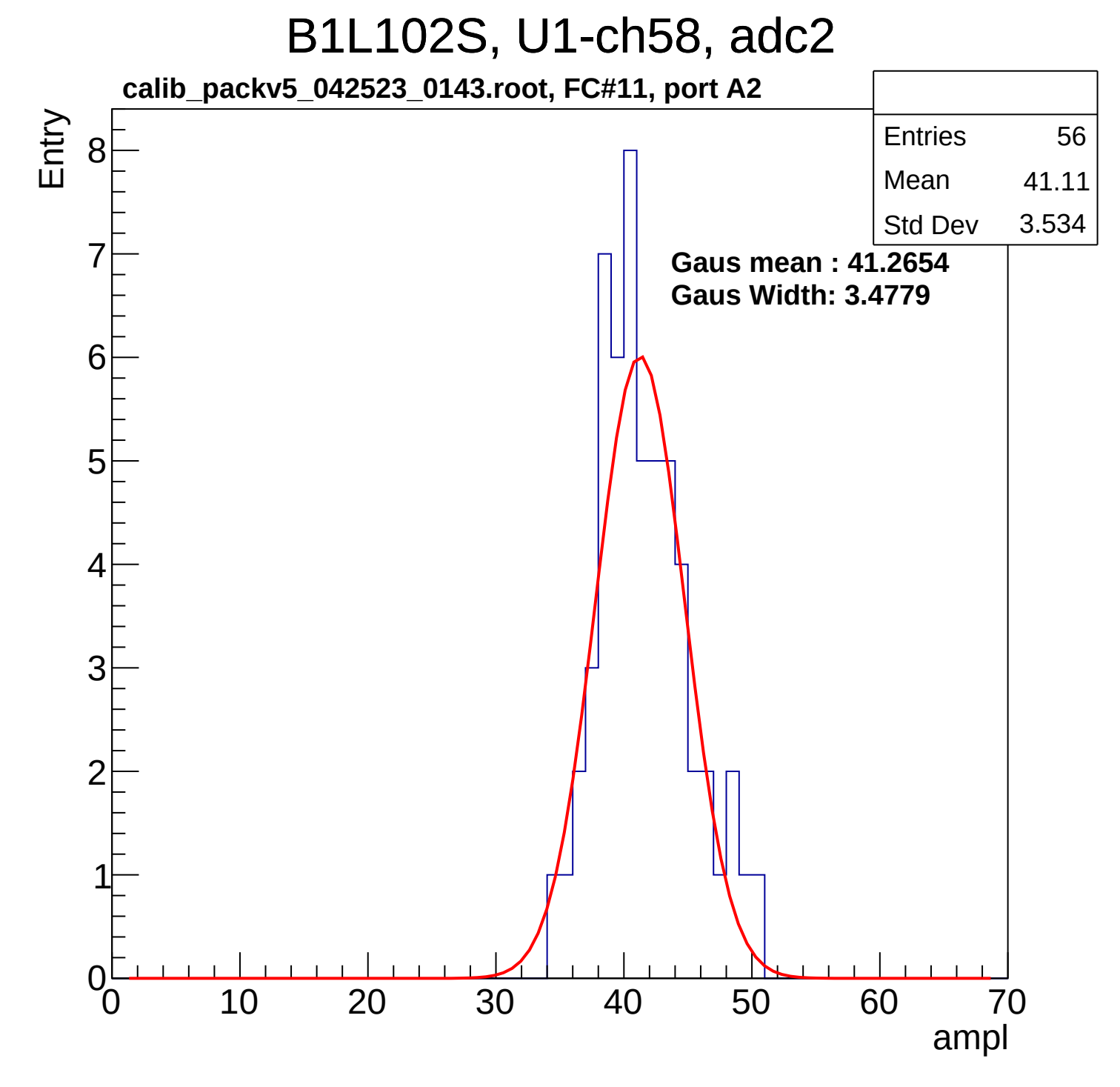

B1L102S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 58.44
Std Dev 8.395
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

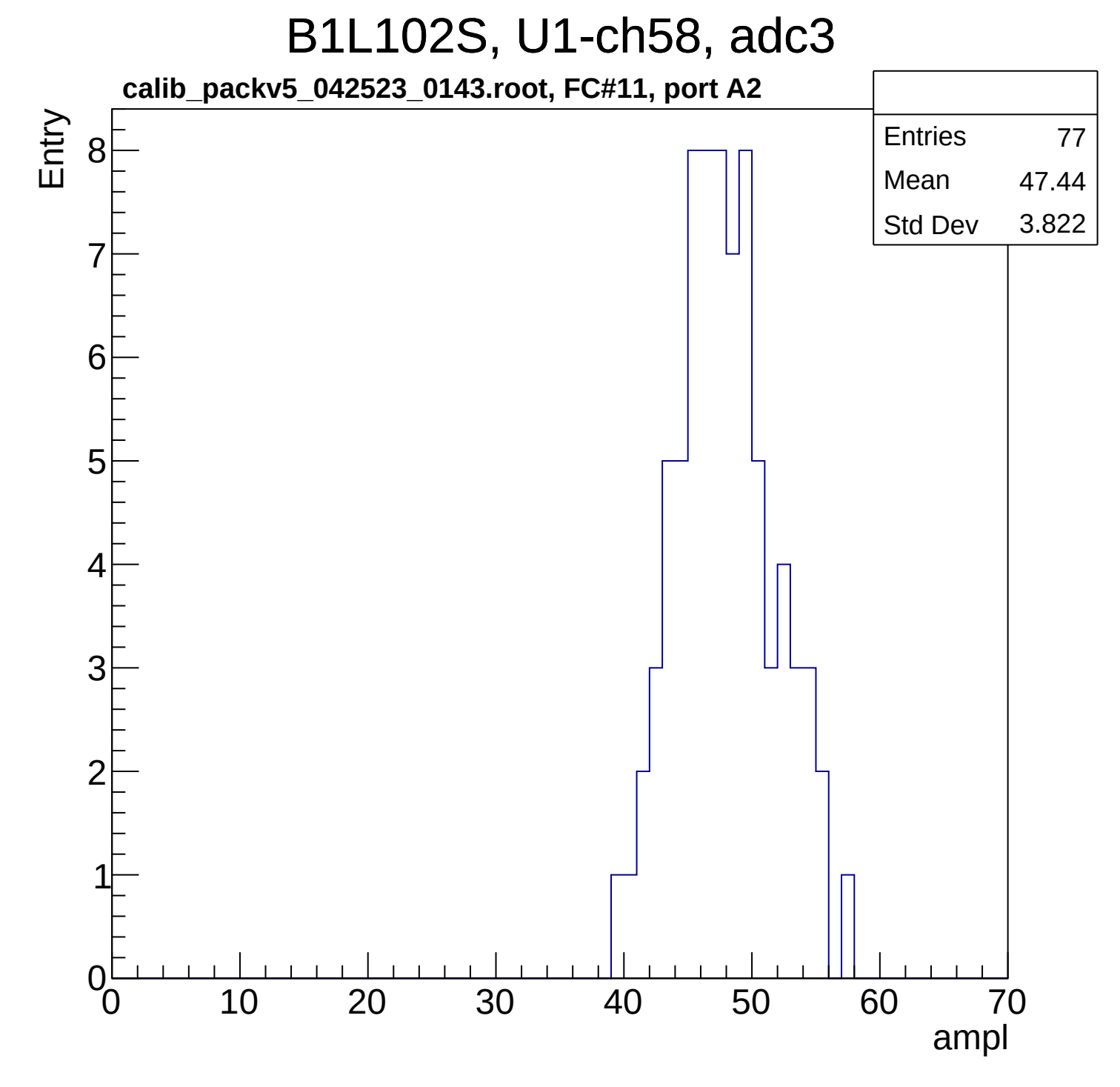

B1L102S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.5
Std Dev 1.936
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

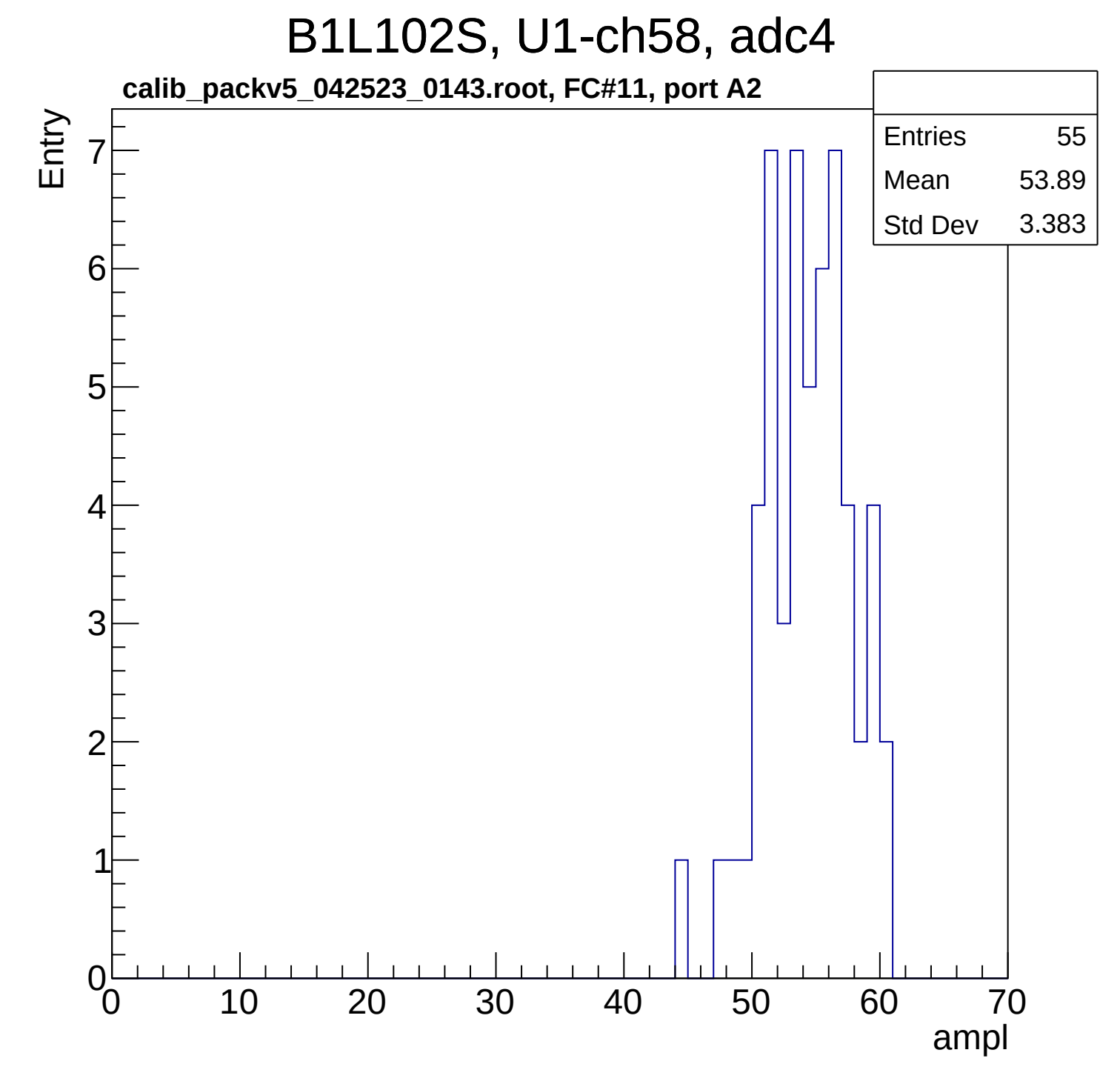

B1L102S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

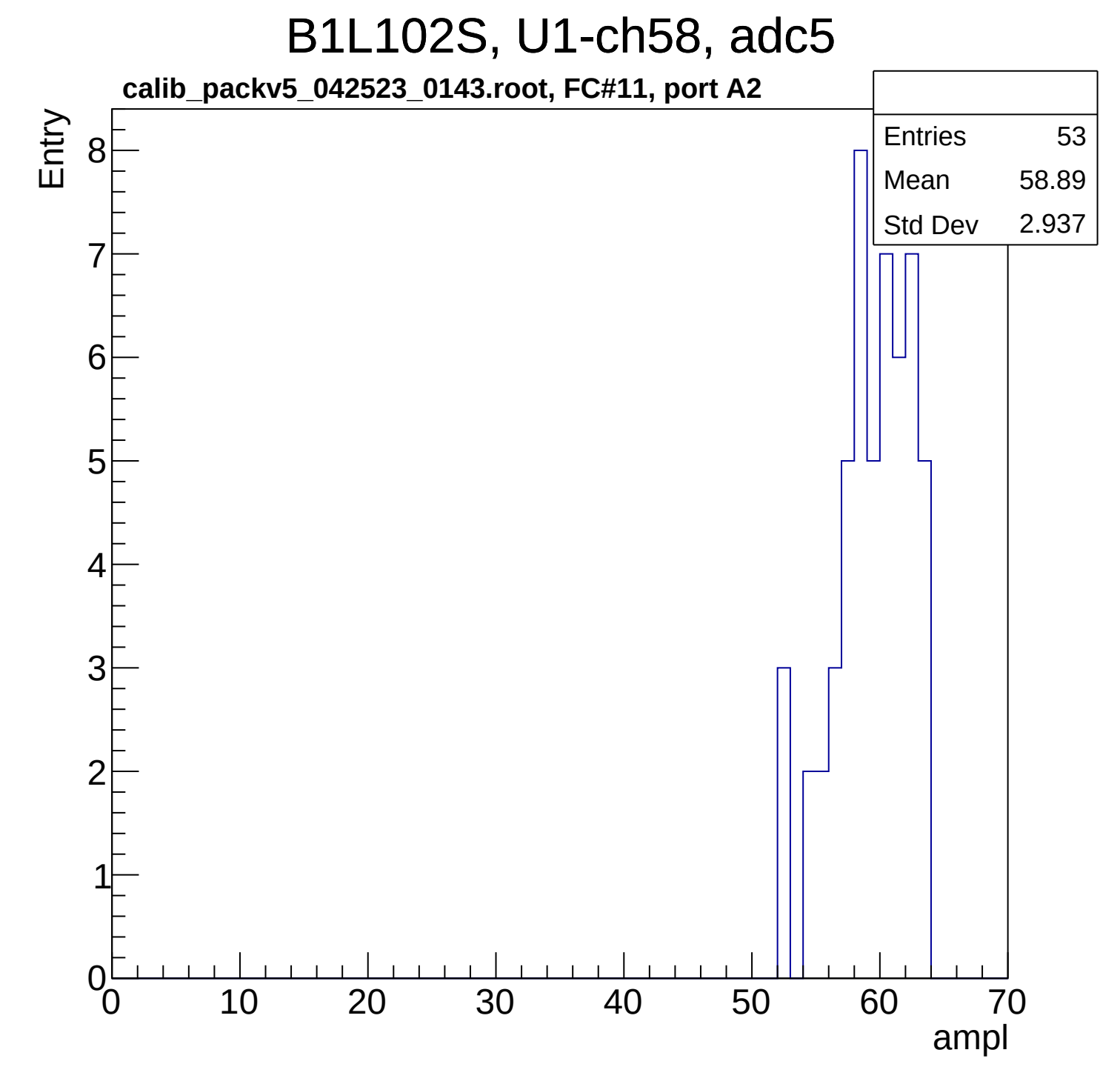

B1L102S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 28.06
Std Dev 4.793
Gaus mean : 28.8220
Gaus Width: 4.5087
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 35.02
Std Dev 3.462
Gaus mean : 35.3733
Gaus Width: 3.5178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 41.11
Std Dev 3.534
Gaus mean : 41.2654
Gaus Width: 3.4779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 47.44
Std Dev 3.822
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.89
Std Dev 3.383
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 58.89
Std Dev 2.937
Entry
ampl
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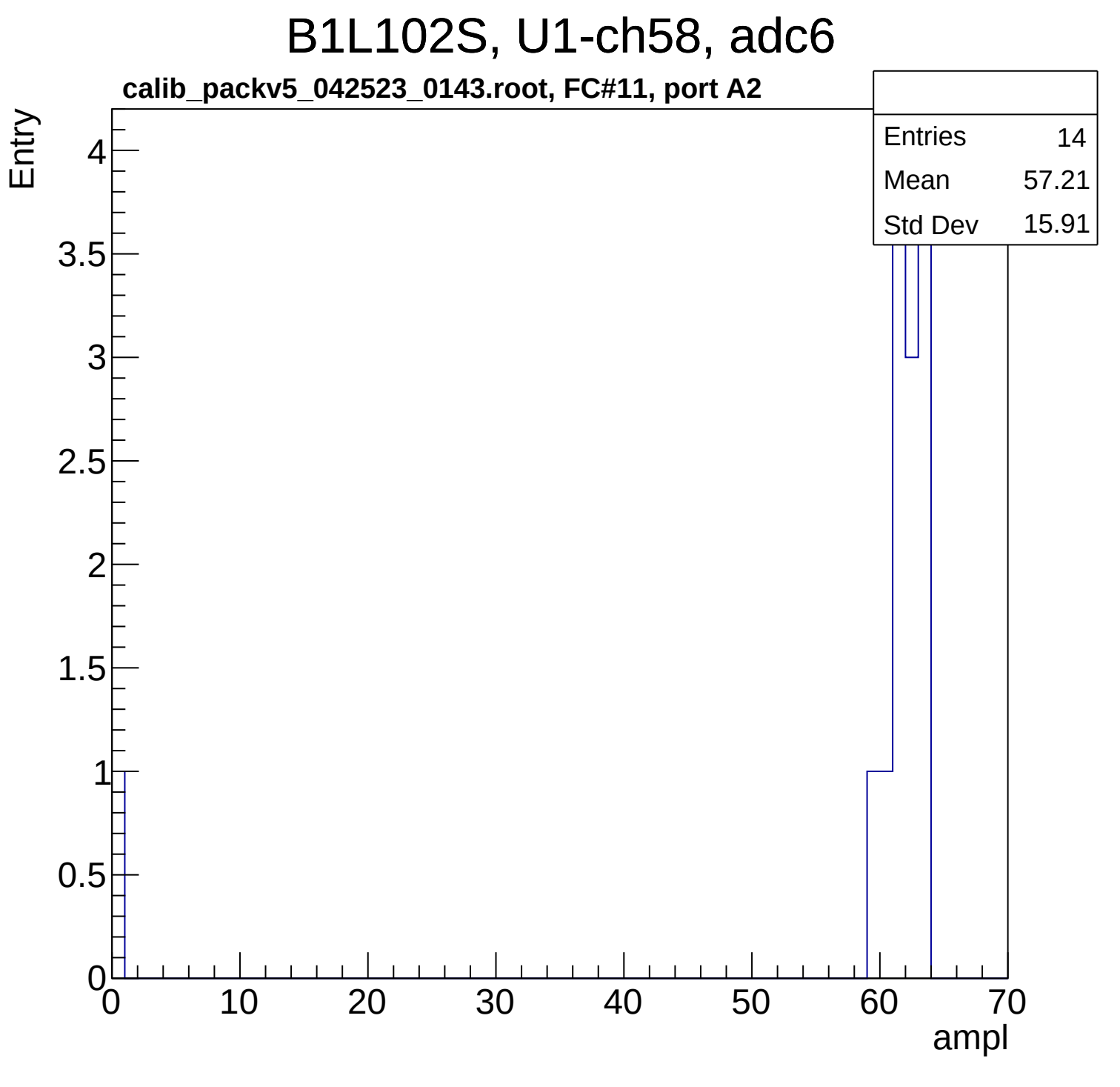
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 57.21
Std Dev 15.91
Entry
ampl
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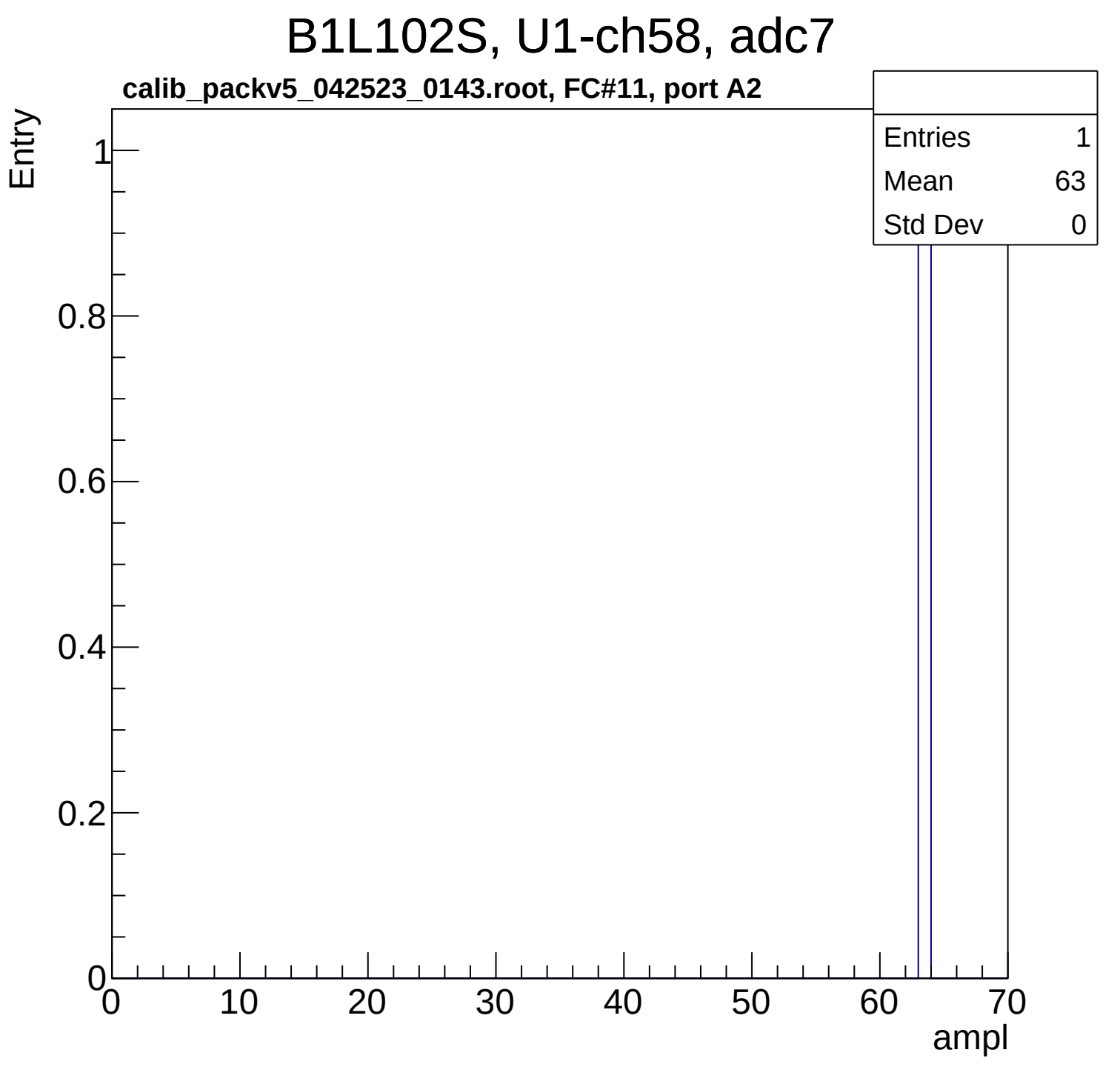
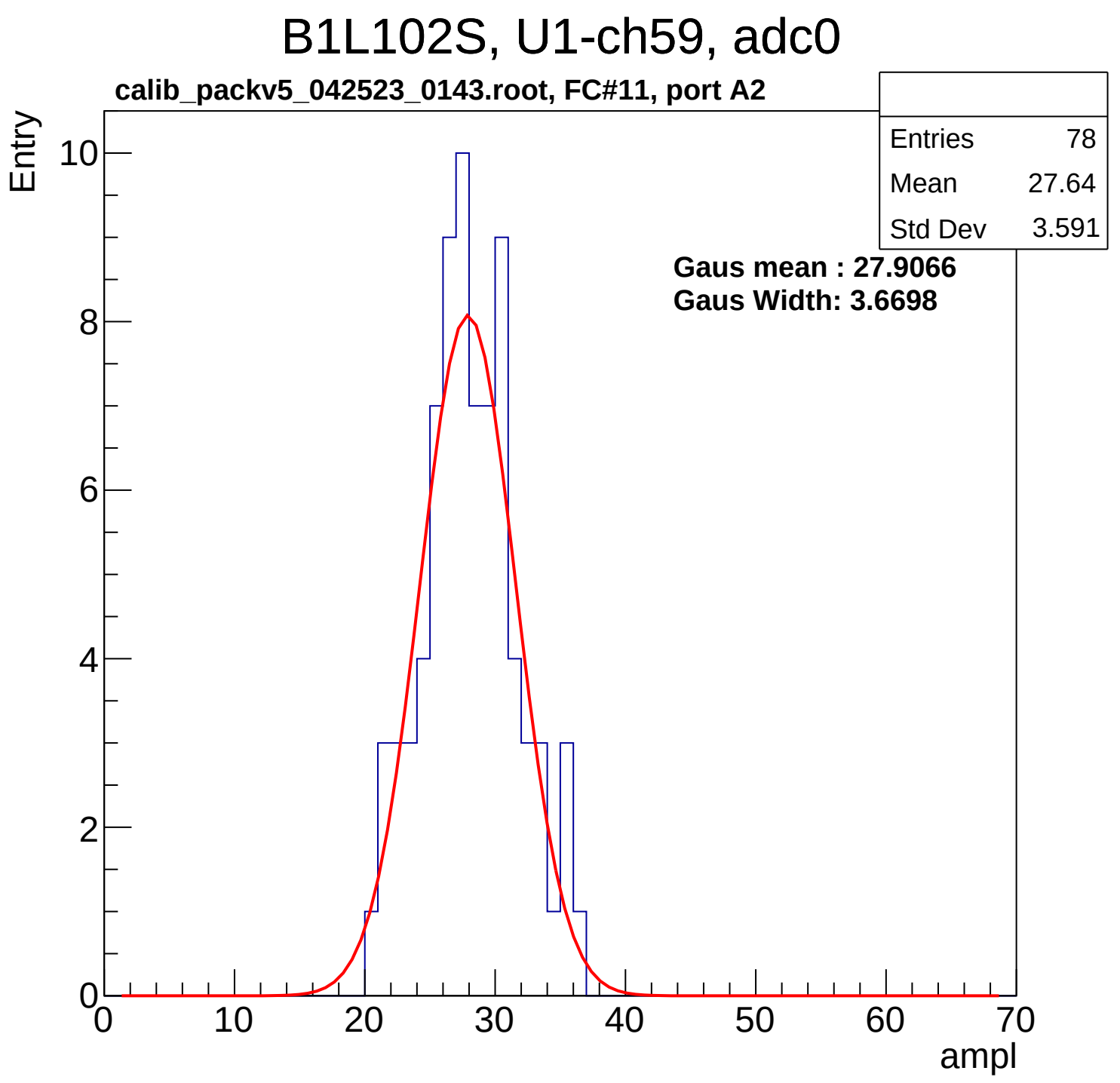
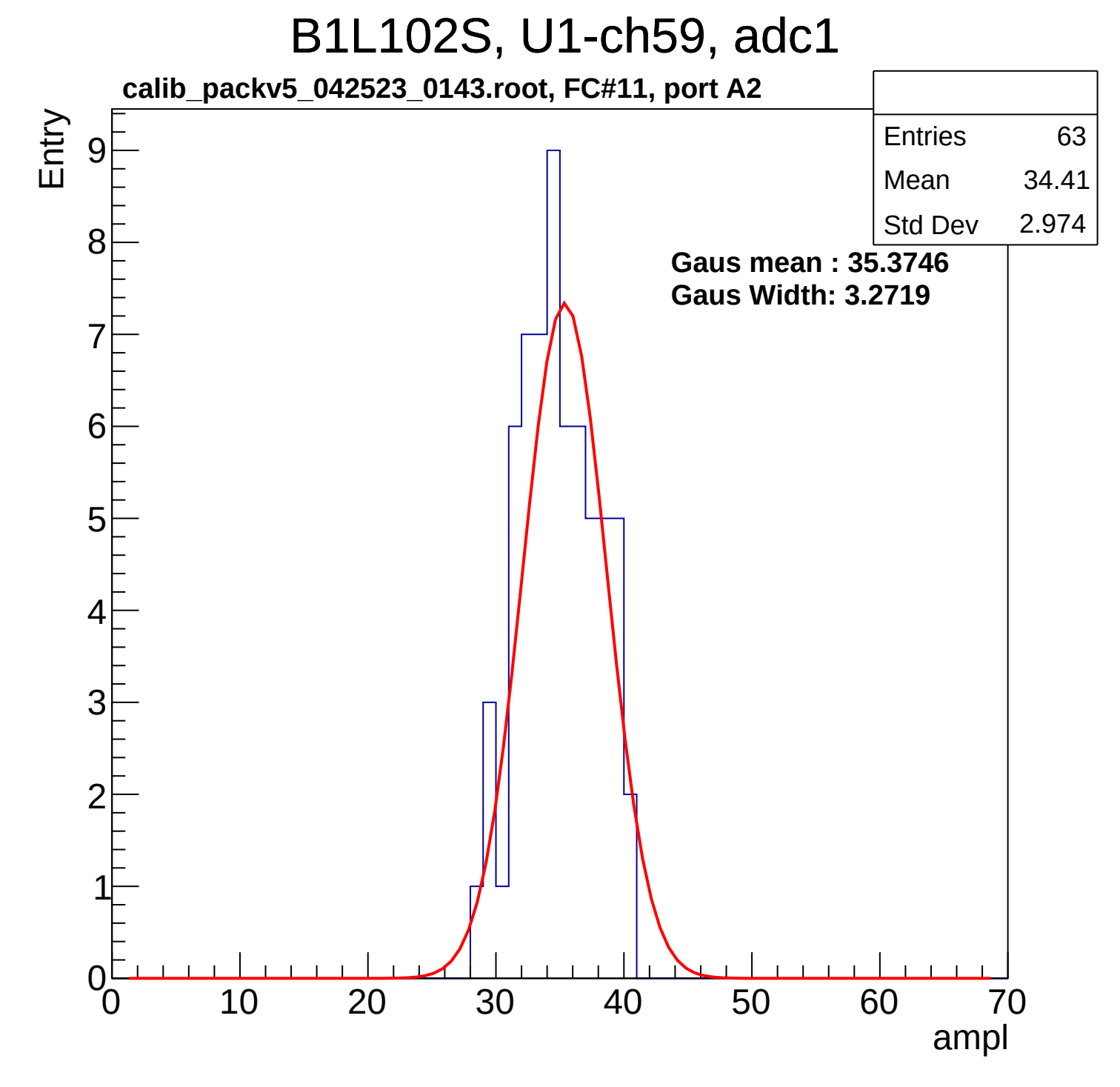
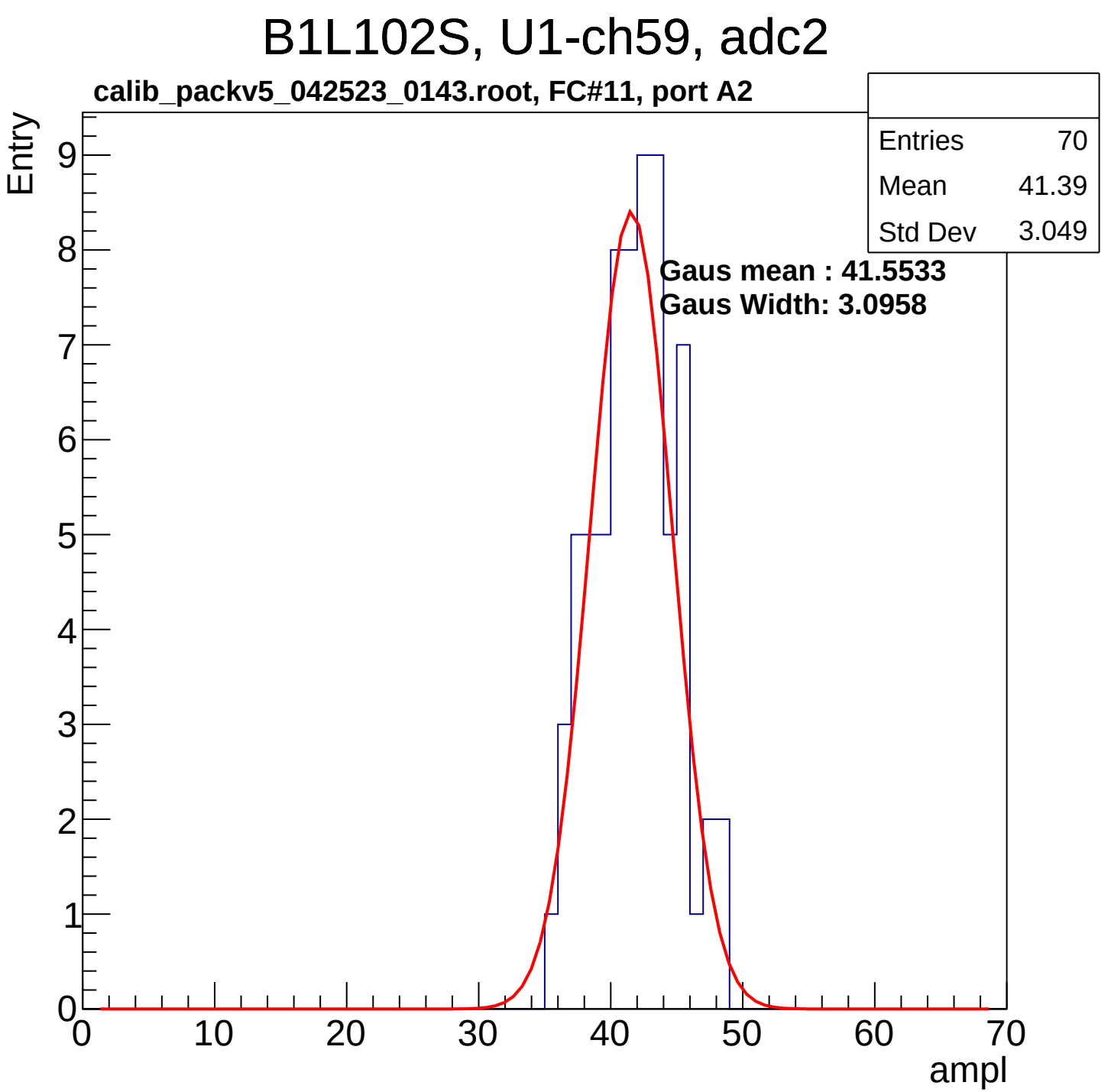
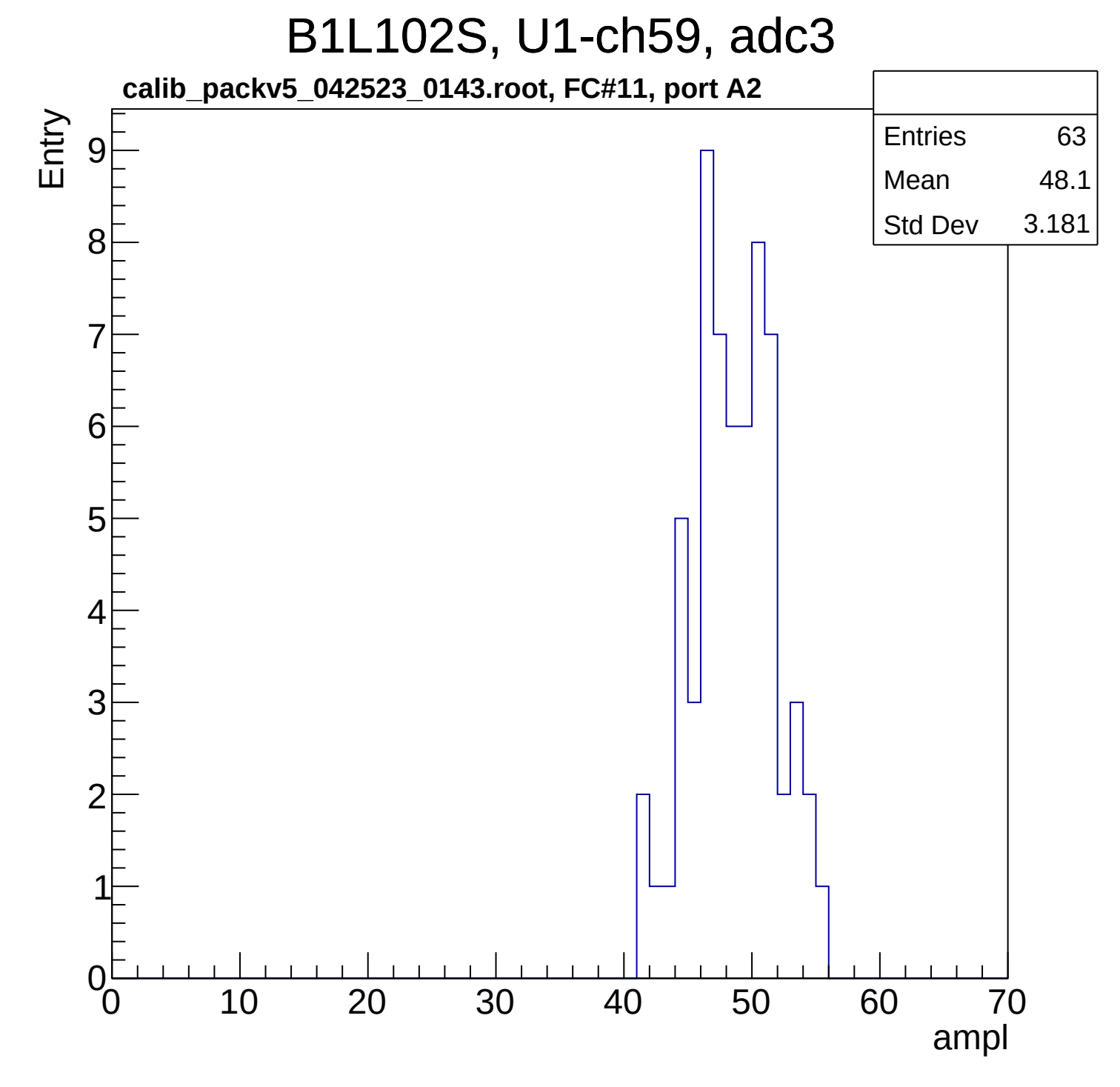
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 27.64
Std Dev 3.591
Gaus mean : 27.9066
Gaus Width: 3.6698
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 34.41
Std Dev 2.974
Gaus mean : 35.3746
Gaus Width: 3.2719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 41.39
Std Dev 3.049
Gaus mean : 41.5533
Gaus Width: 3.0958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 48.1
Std Dev 3.181
Entry
ampl
0
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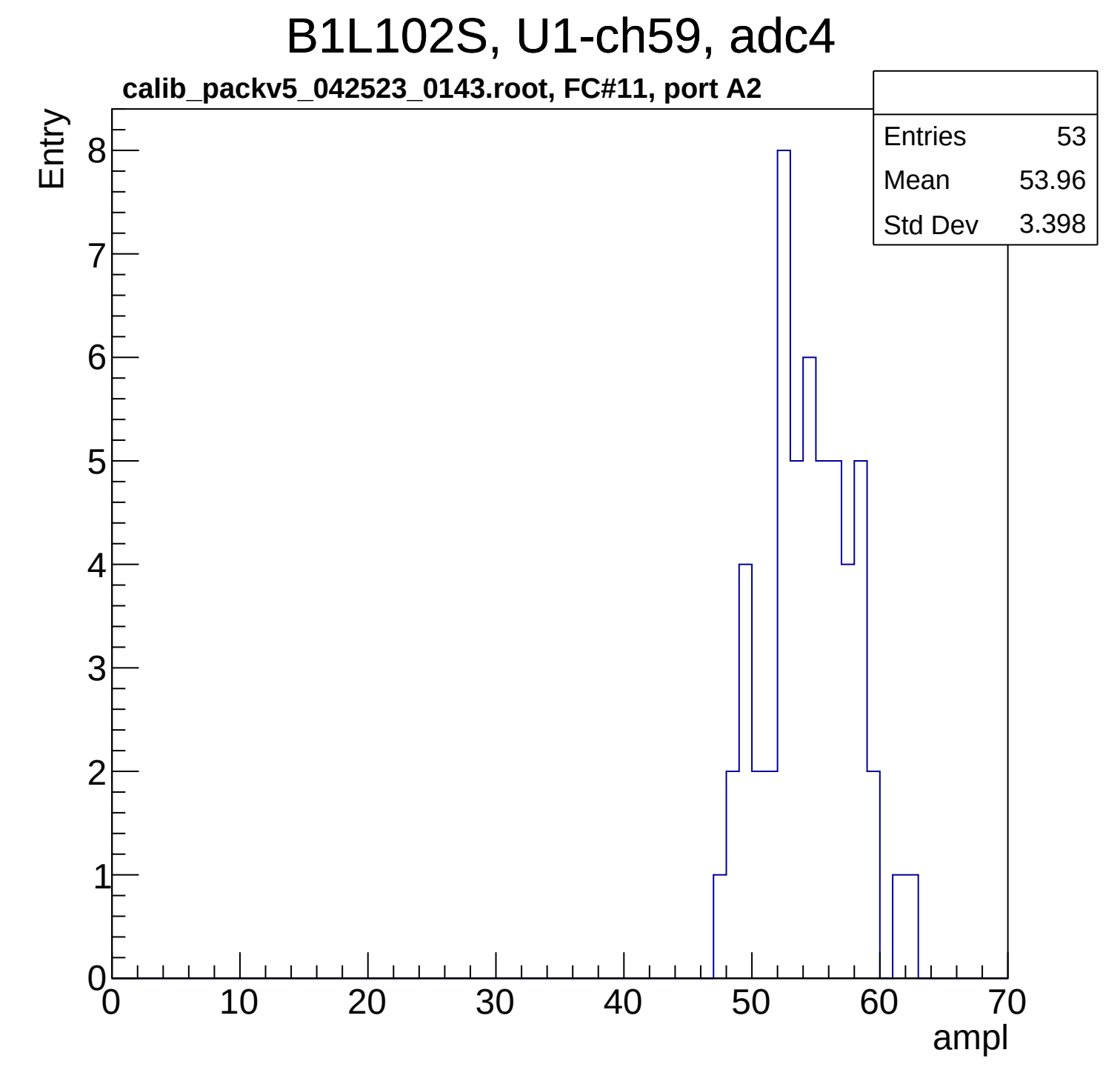
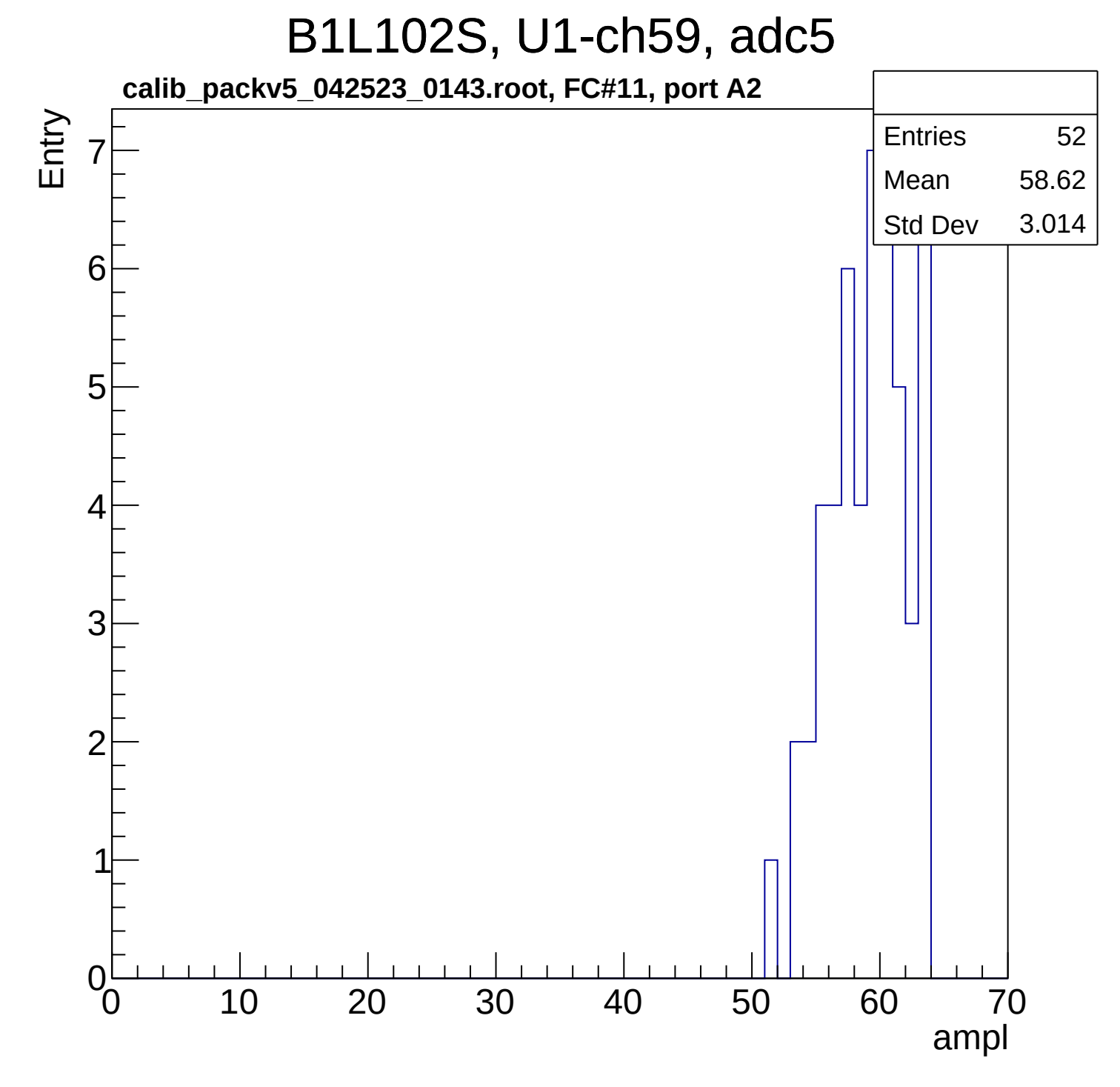
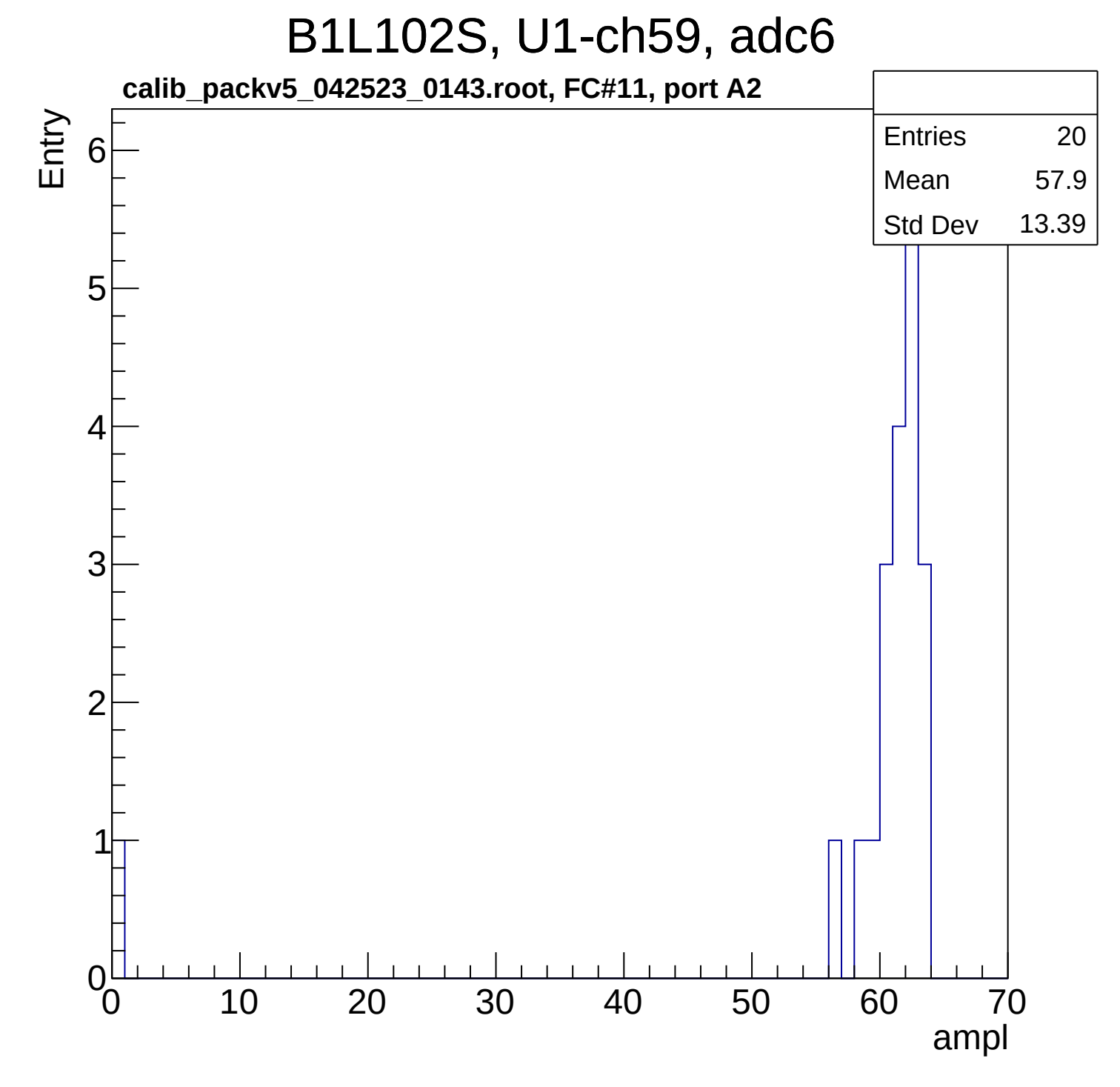
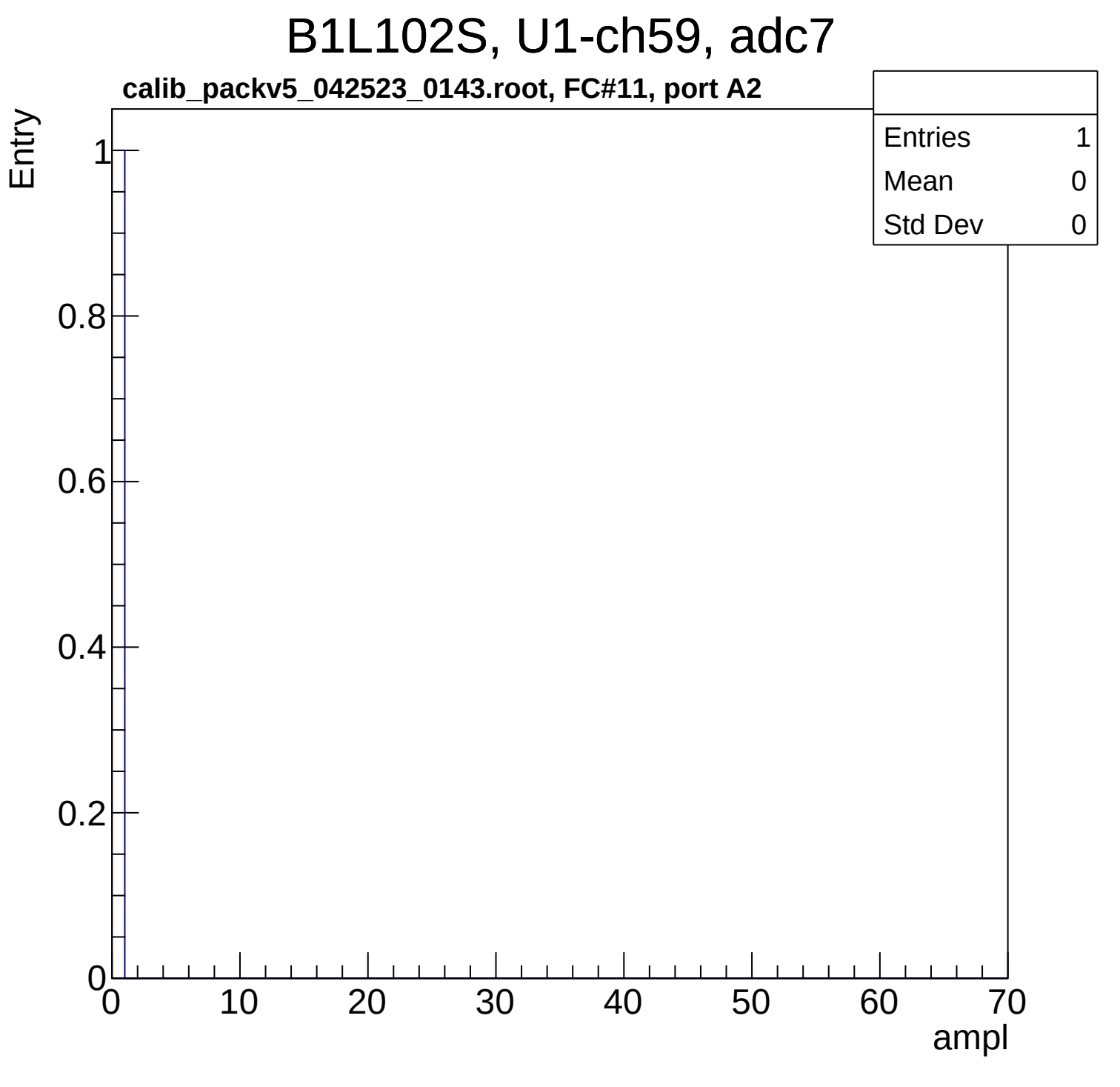
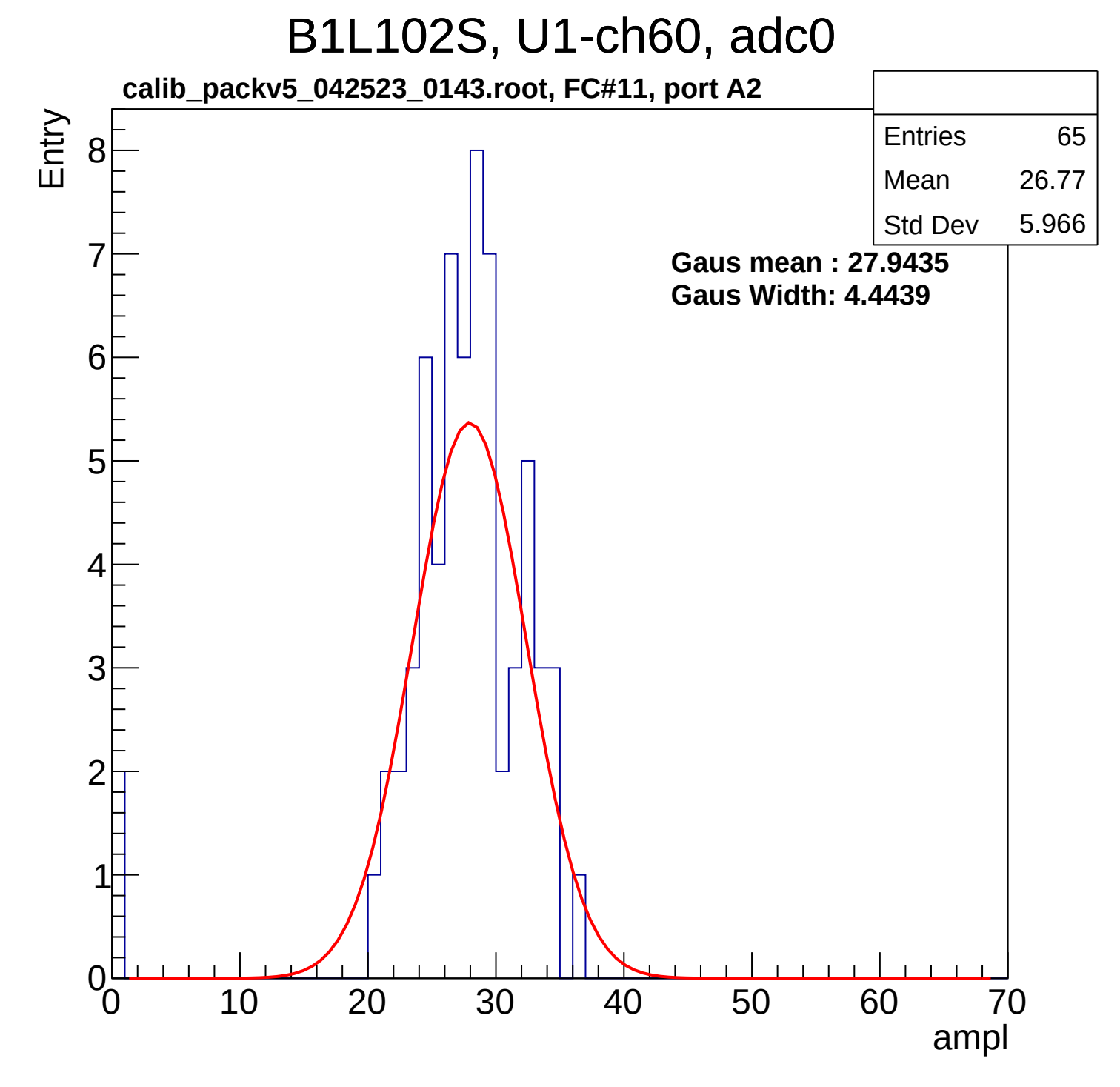
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

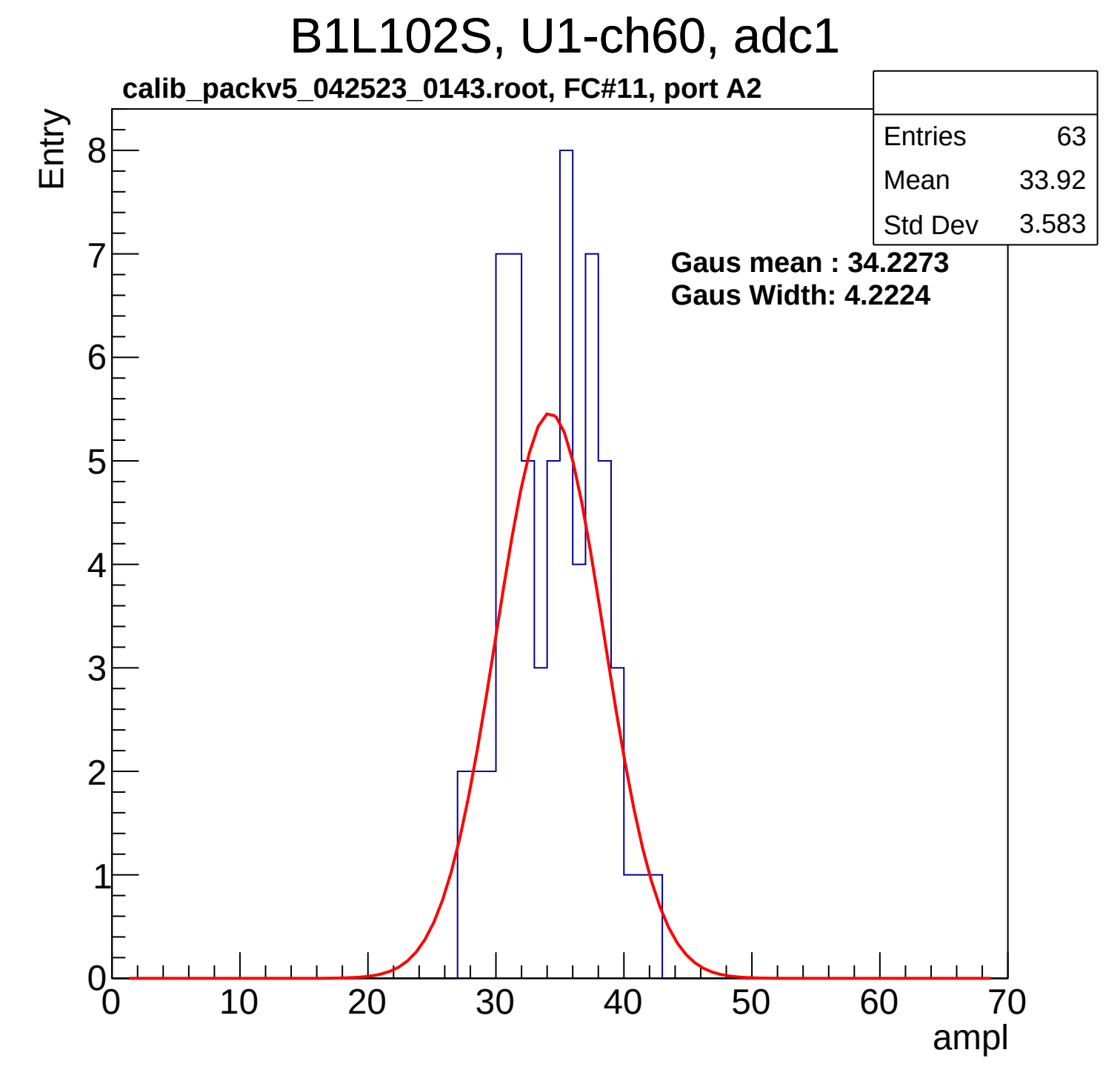

B1L102S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 53.96
Std Dev 3.398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

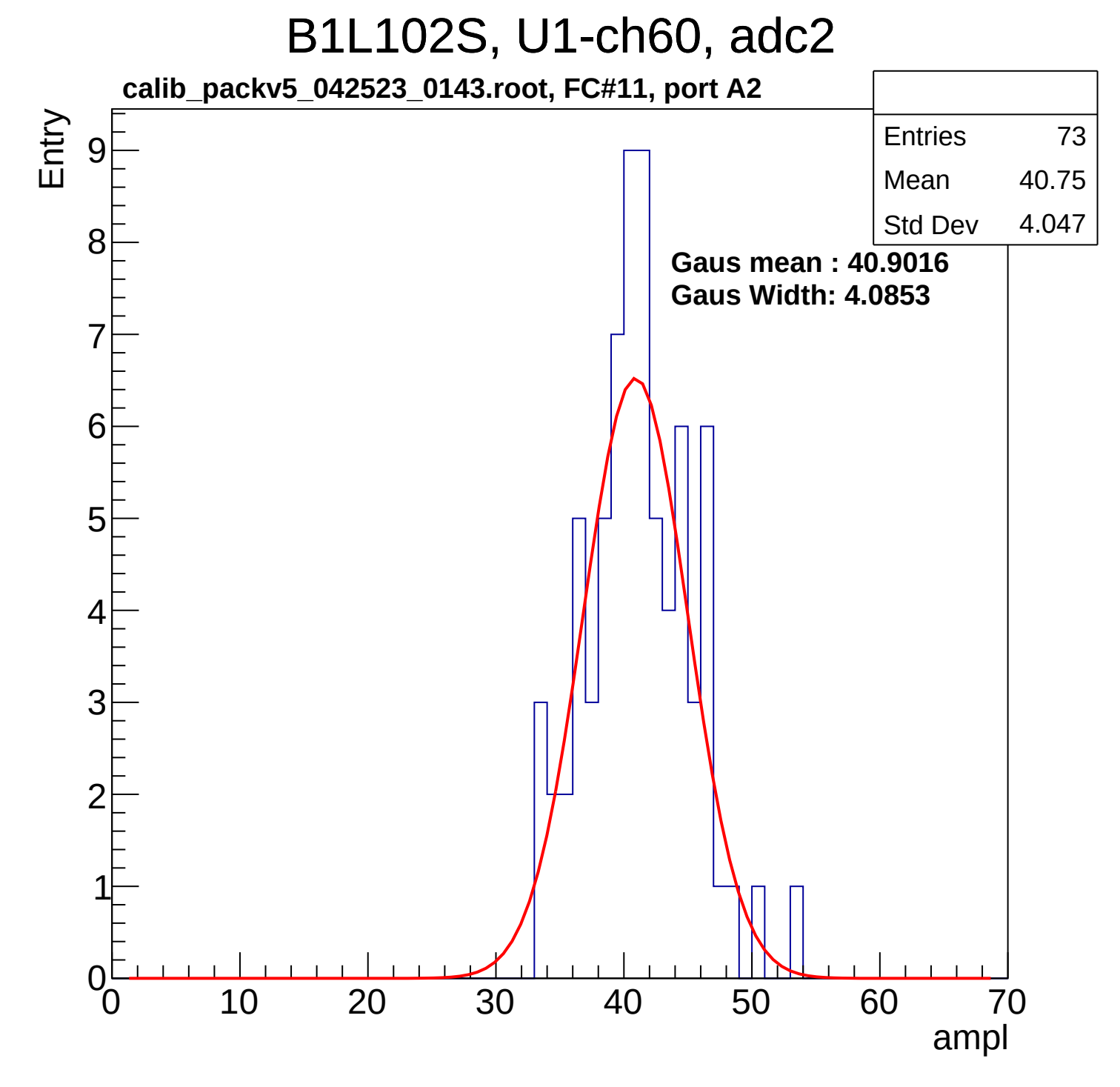

B1L102S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.62
Std Dev 3.014
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 20
Mean 57.9
Std Dev 13.39
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 26.77
Std Dev 5.966
Gaus mean : 27.9435
Gaus Width: 4.4439
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 33.92
Std Dev 3.583
Gaus mean : 34.2273
Gaus Width: 4.2224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 40.75
Std Dev 4.047
Gaus mean : 40.9016
Gaus Width: 4.0853
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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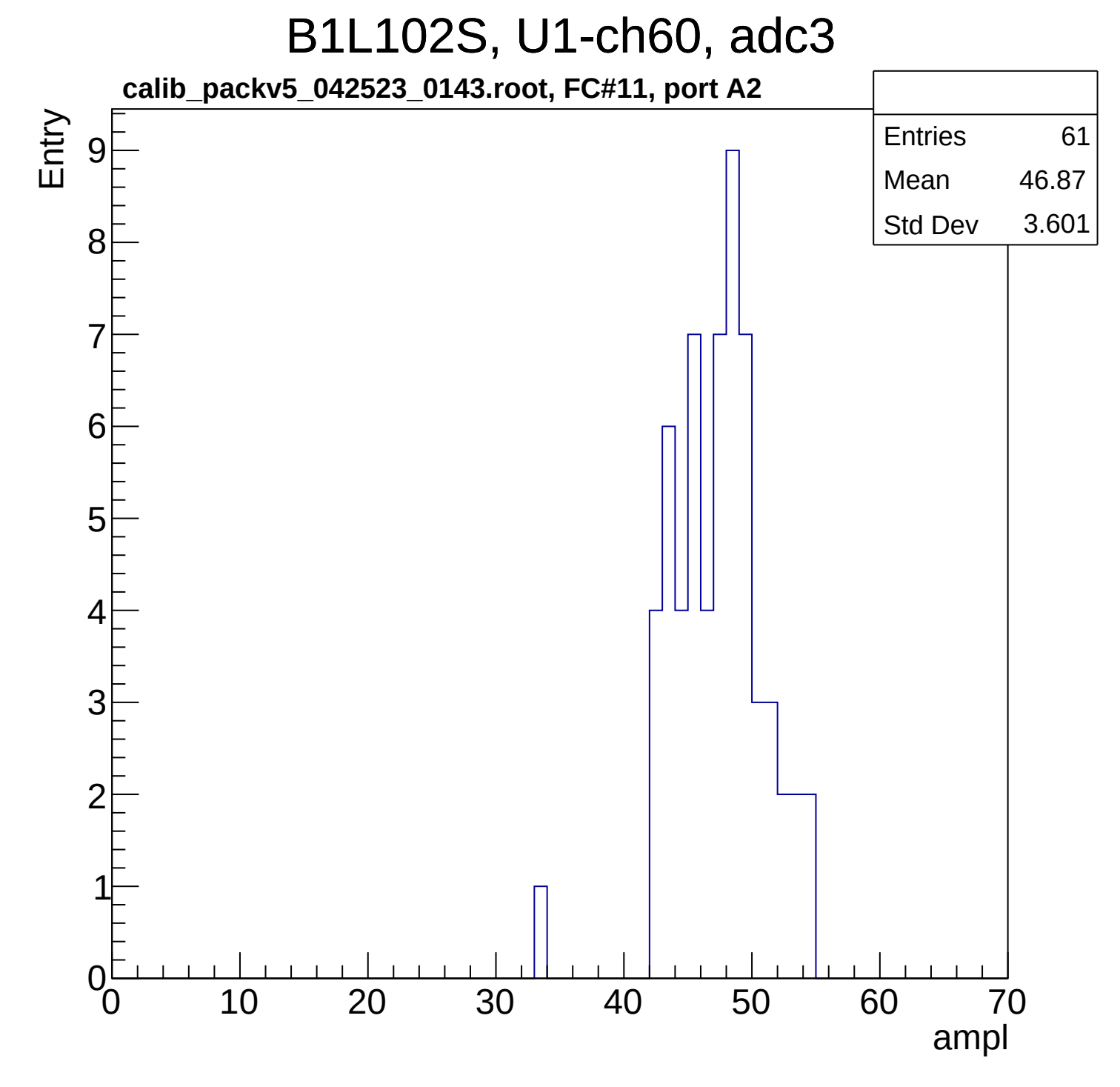
B1L102S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 46.87
Std Dev 3.601
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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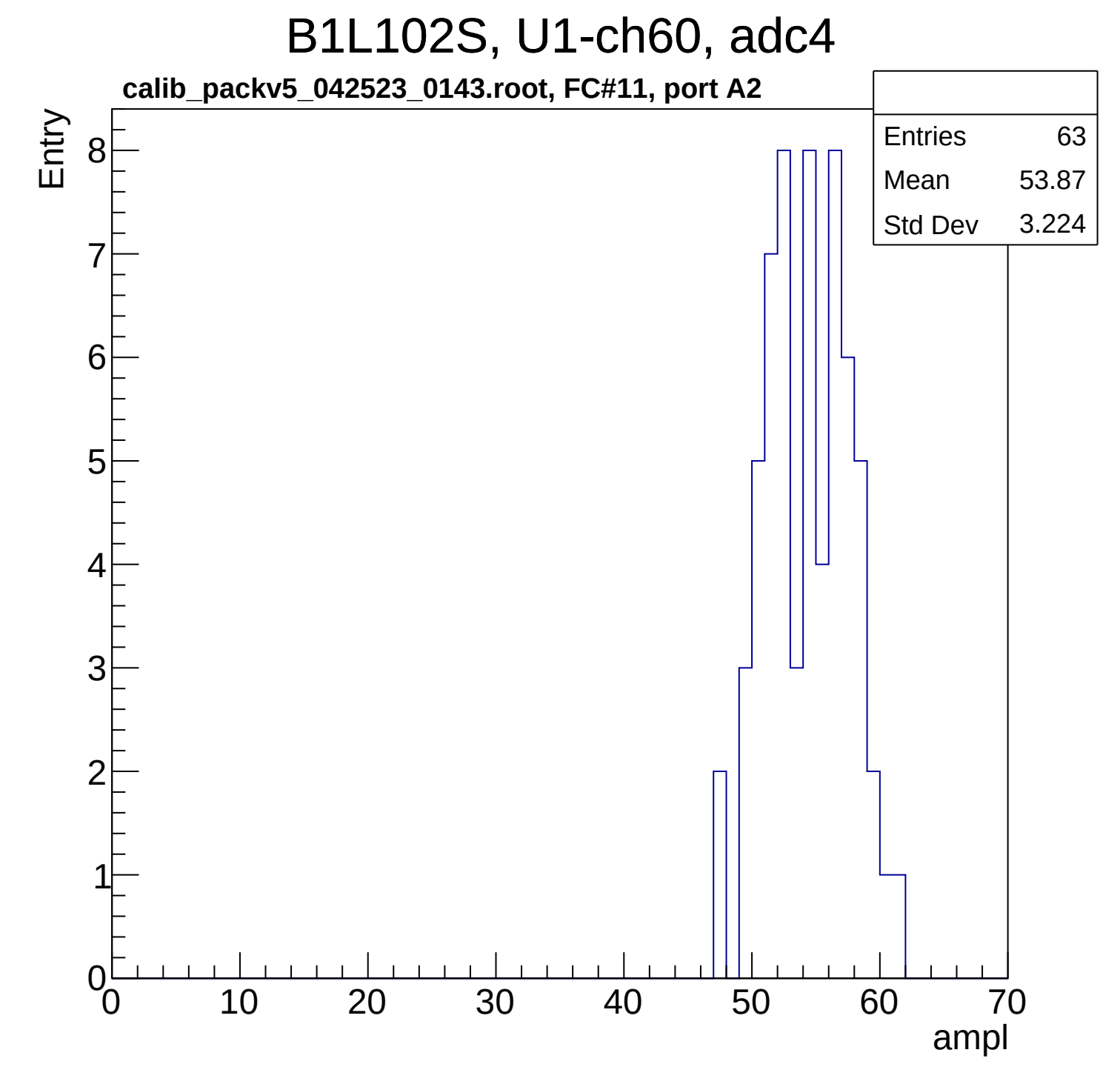
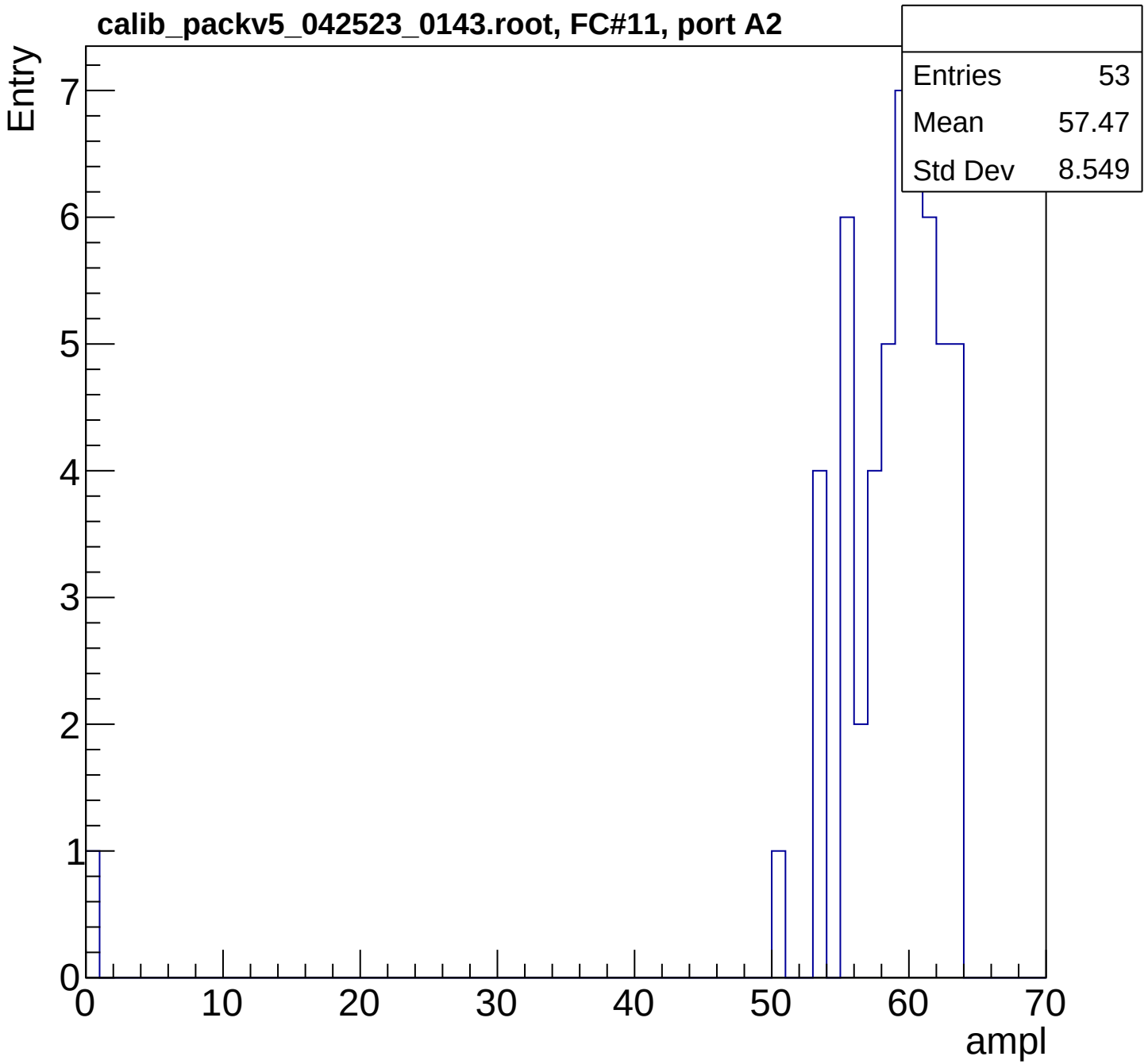

B1L102S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 53.87
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 57.47
Std Dev 8.549
Entry
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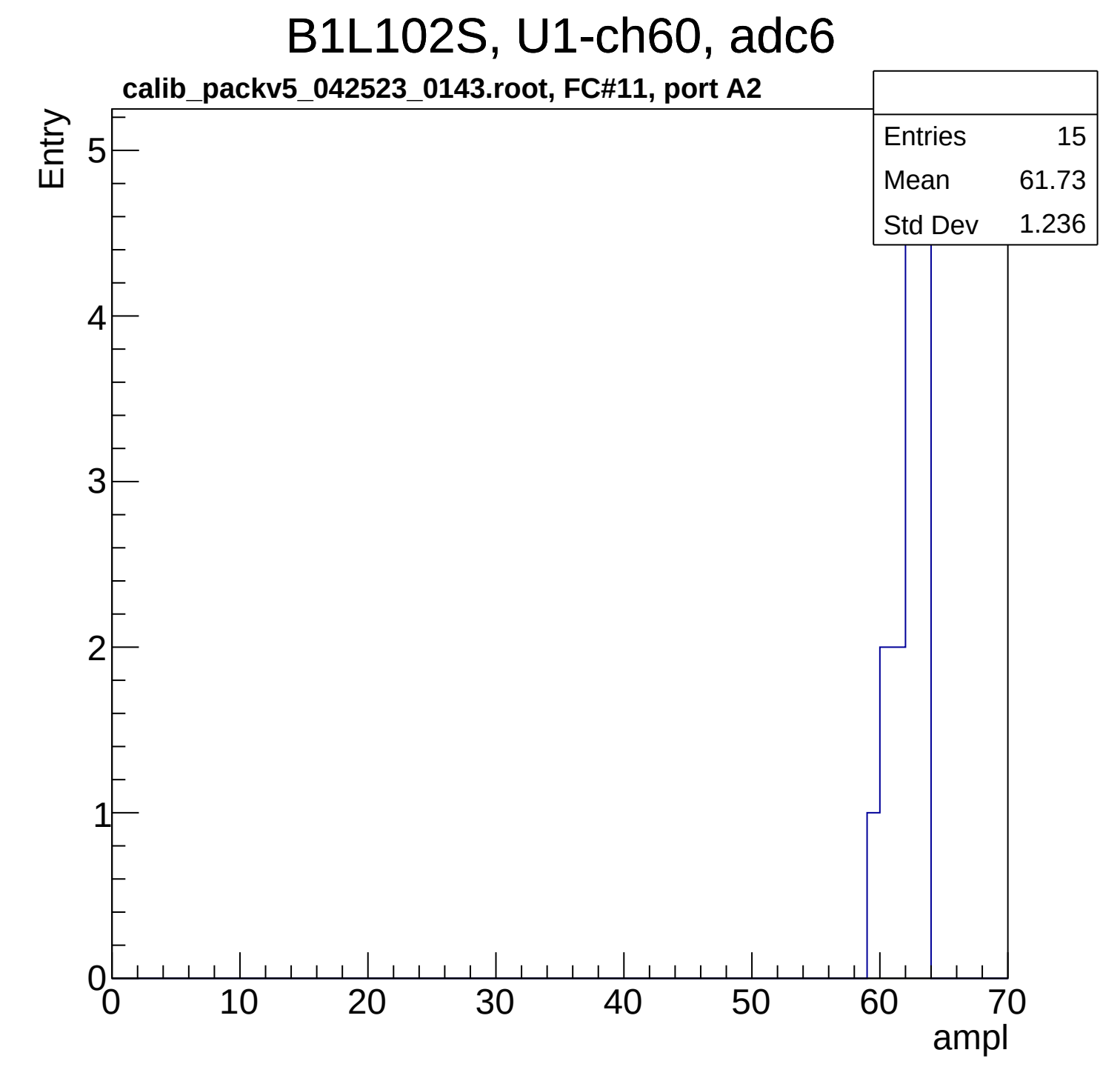
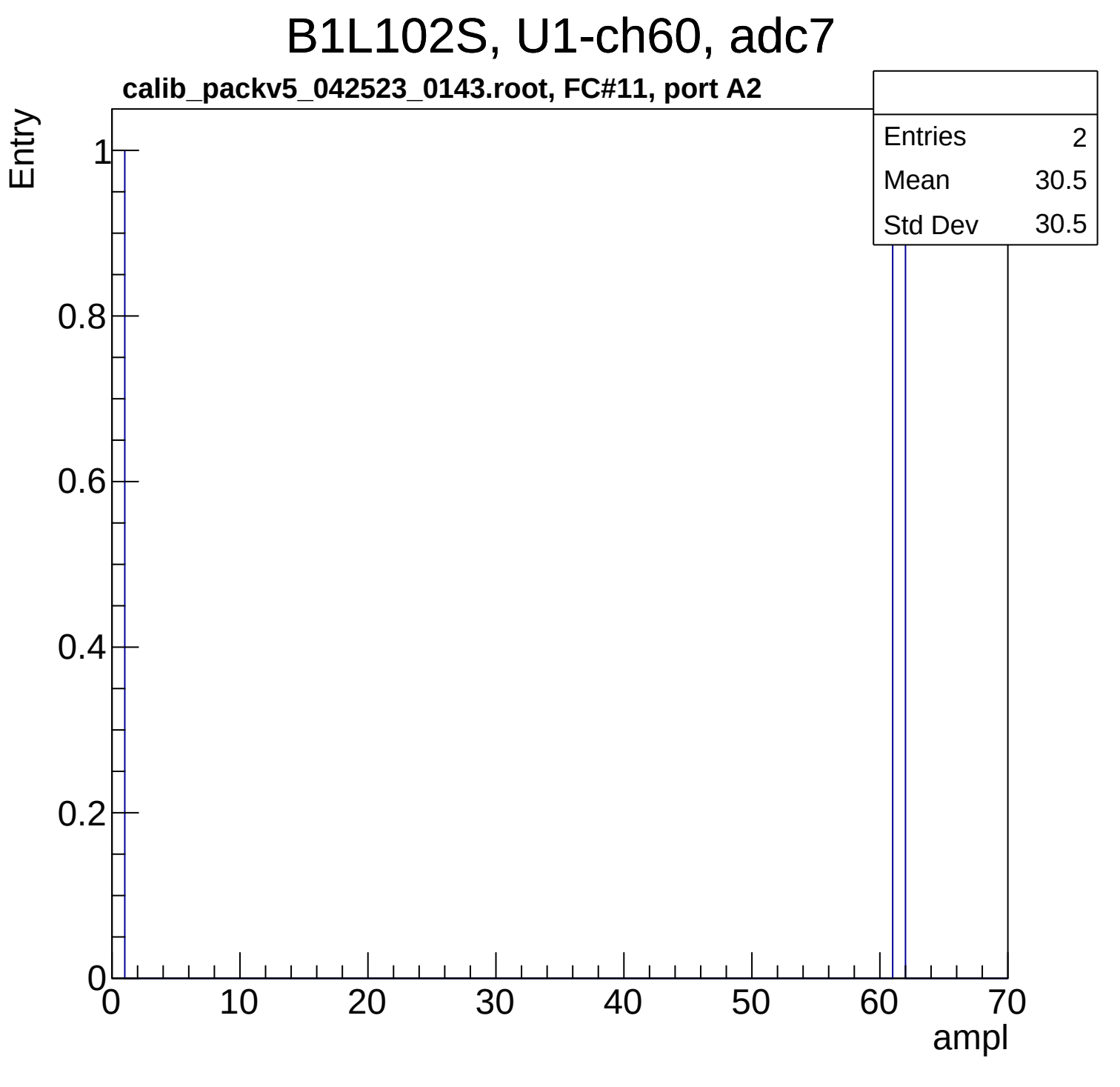
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 61.73
Std Dev 1.236
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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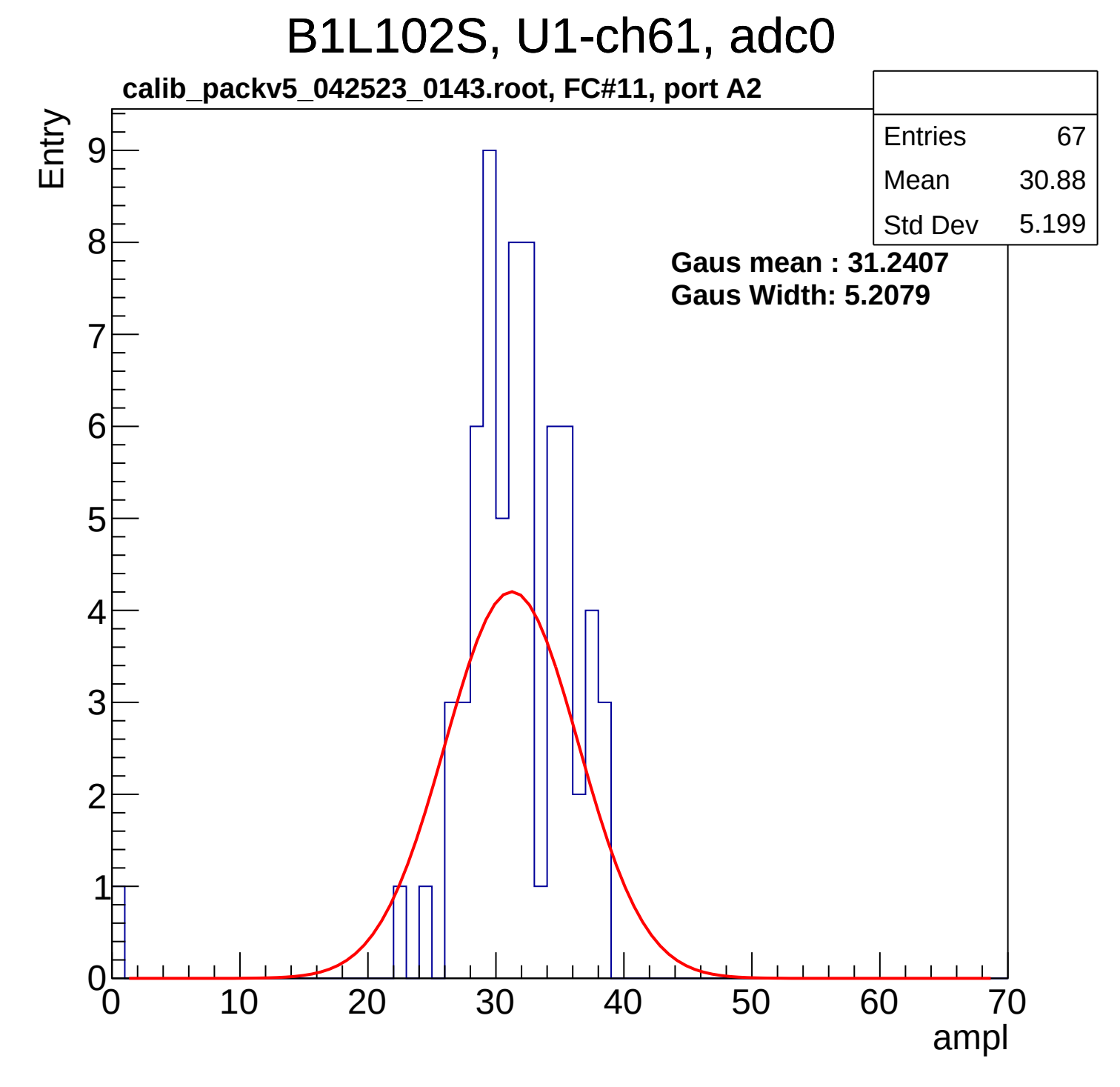
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 30.88
Std Dev 5.199
Gaus mean : 31.2407
Gaus Width: 5.2079
Entry
ampl
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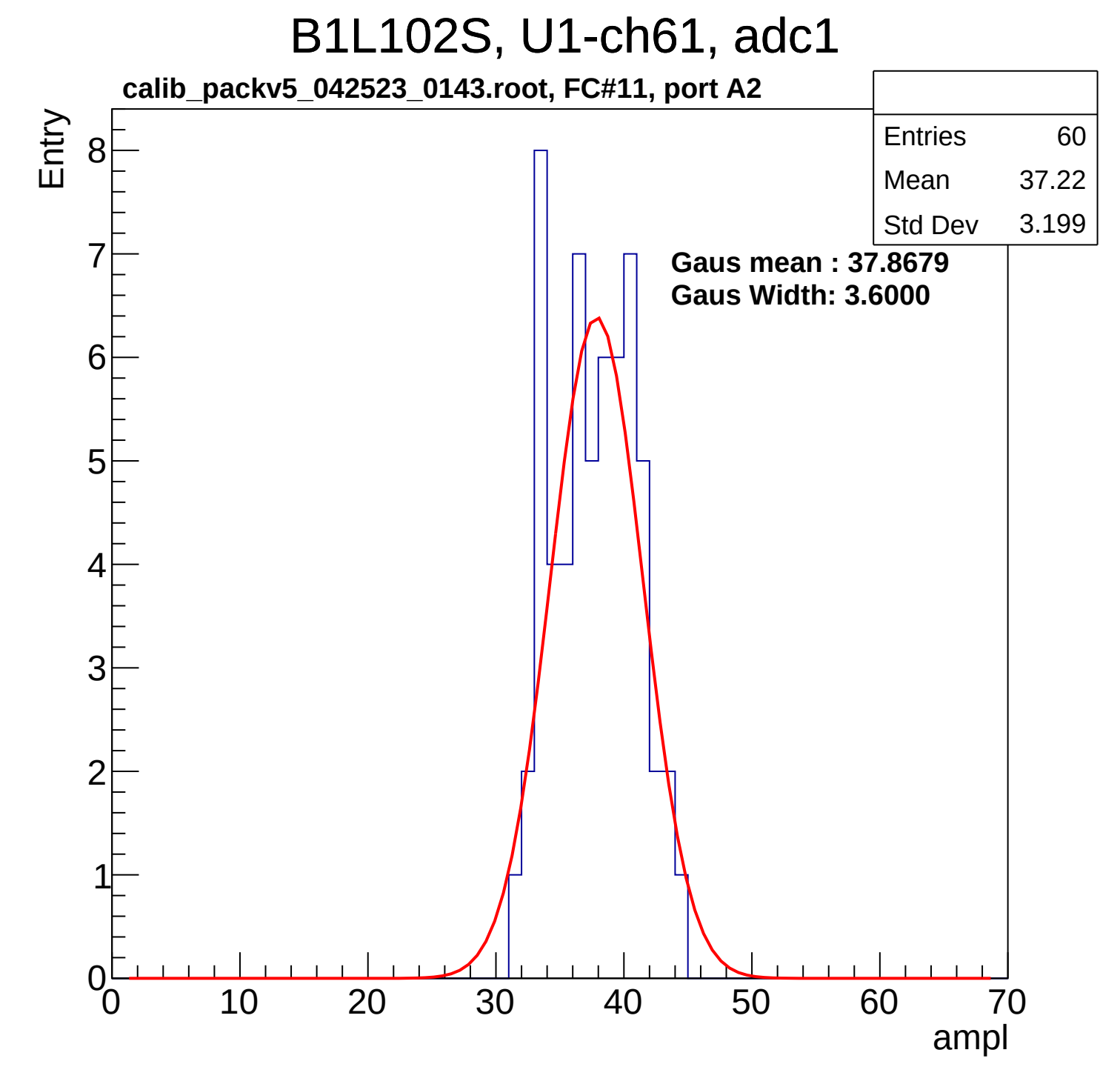
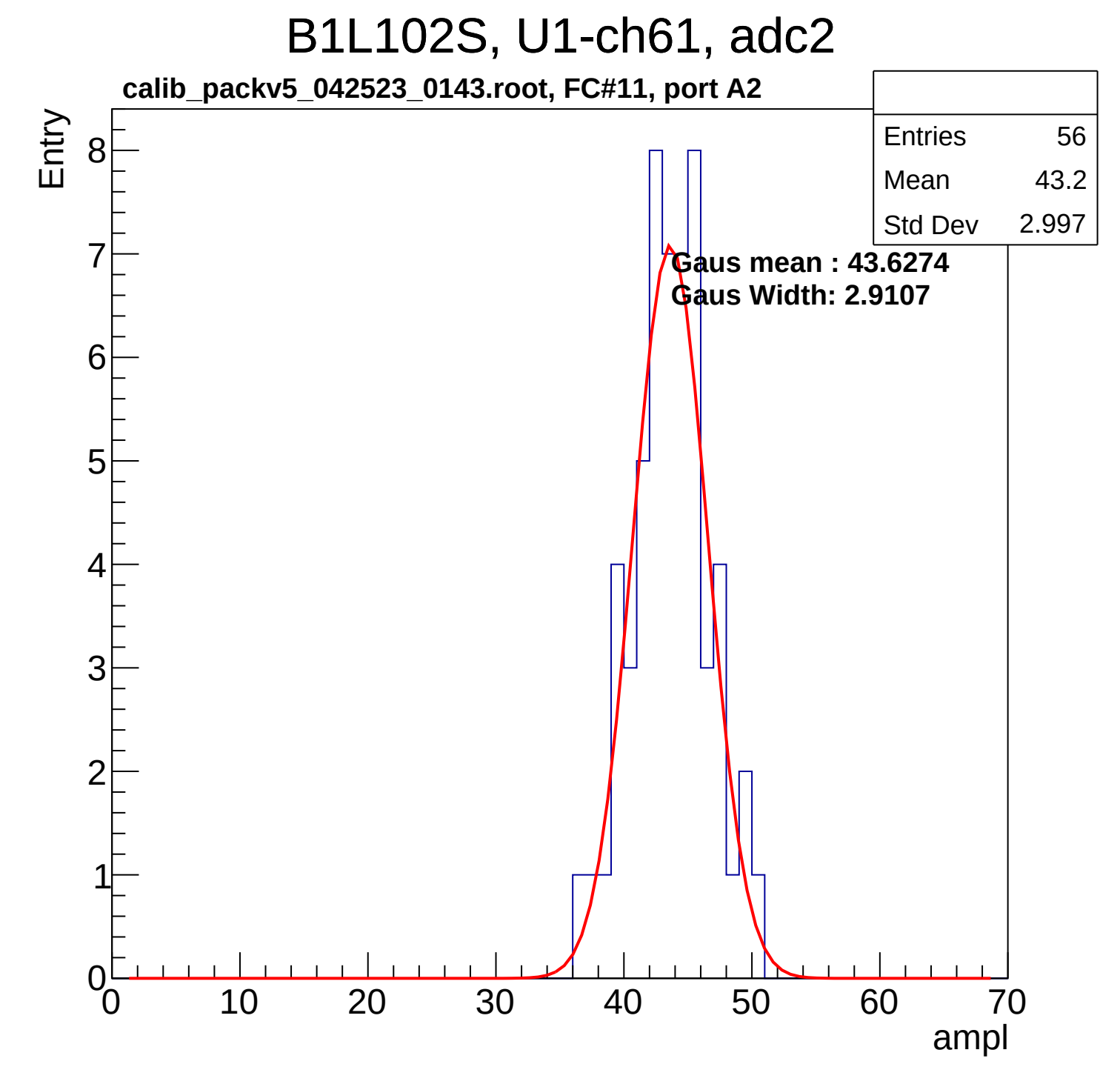
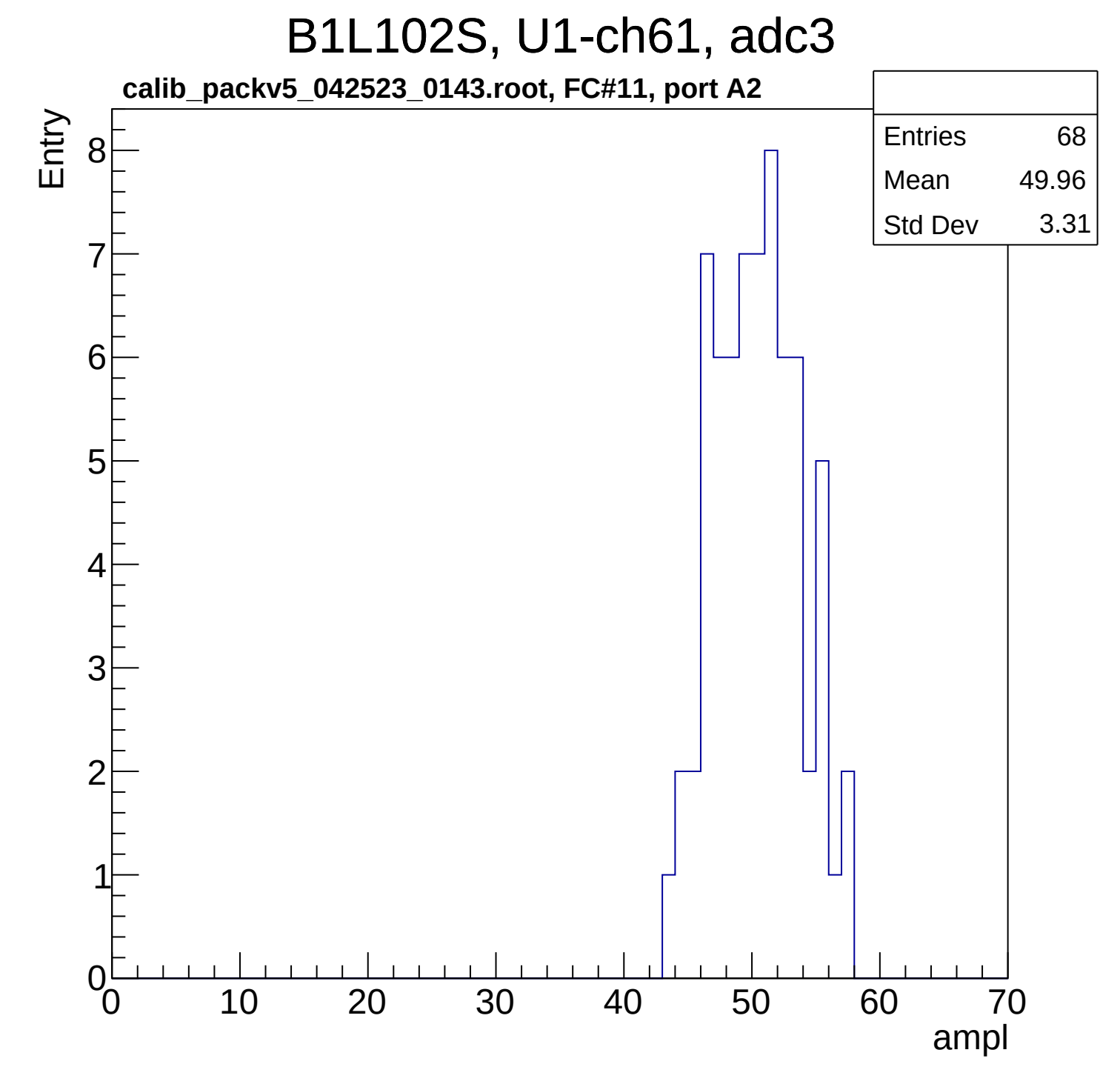
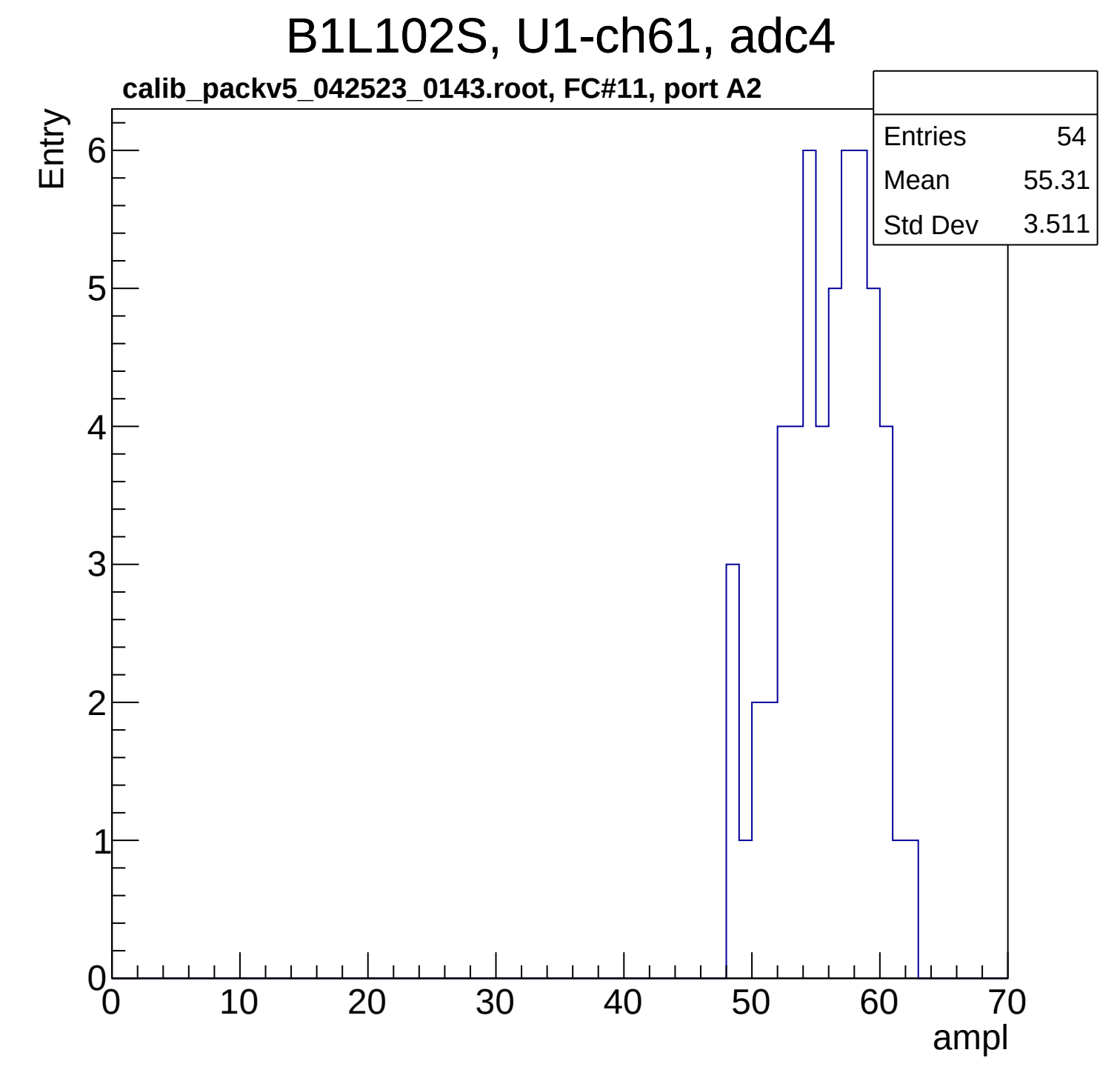
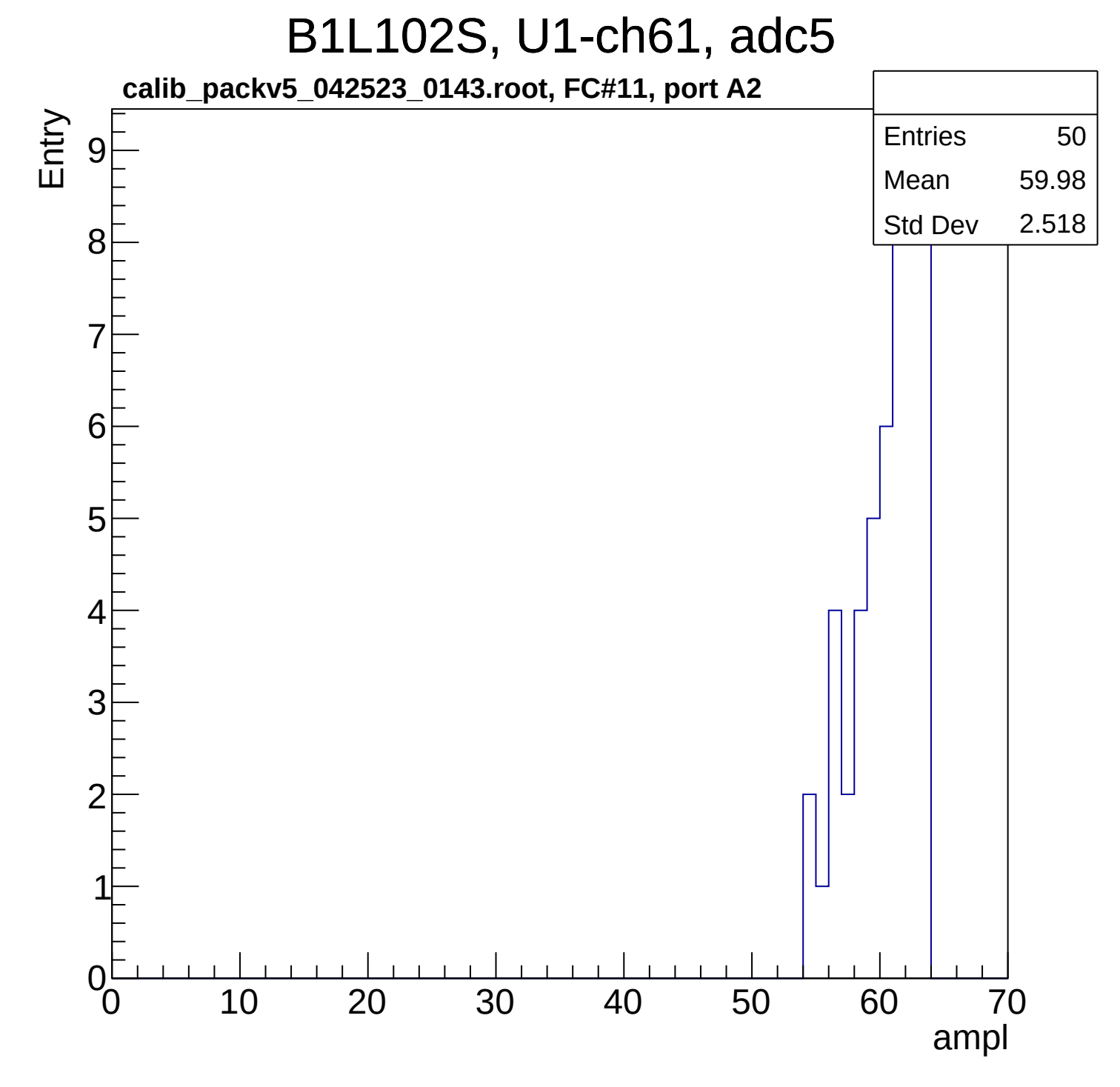
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

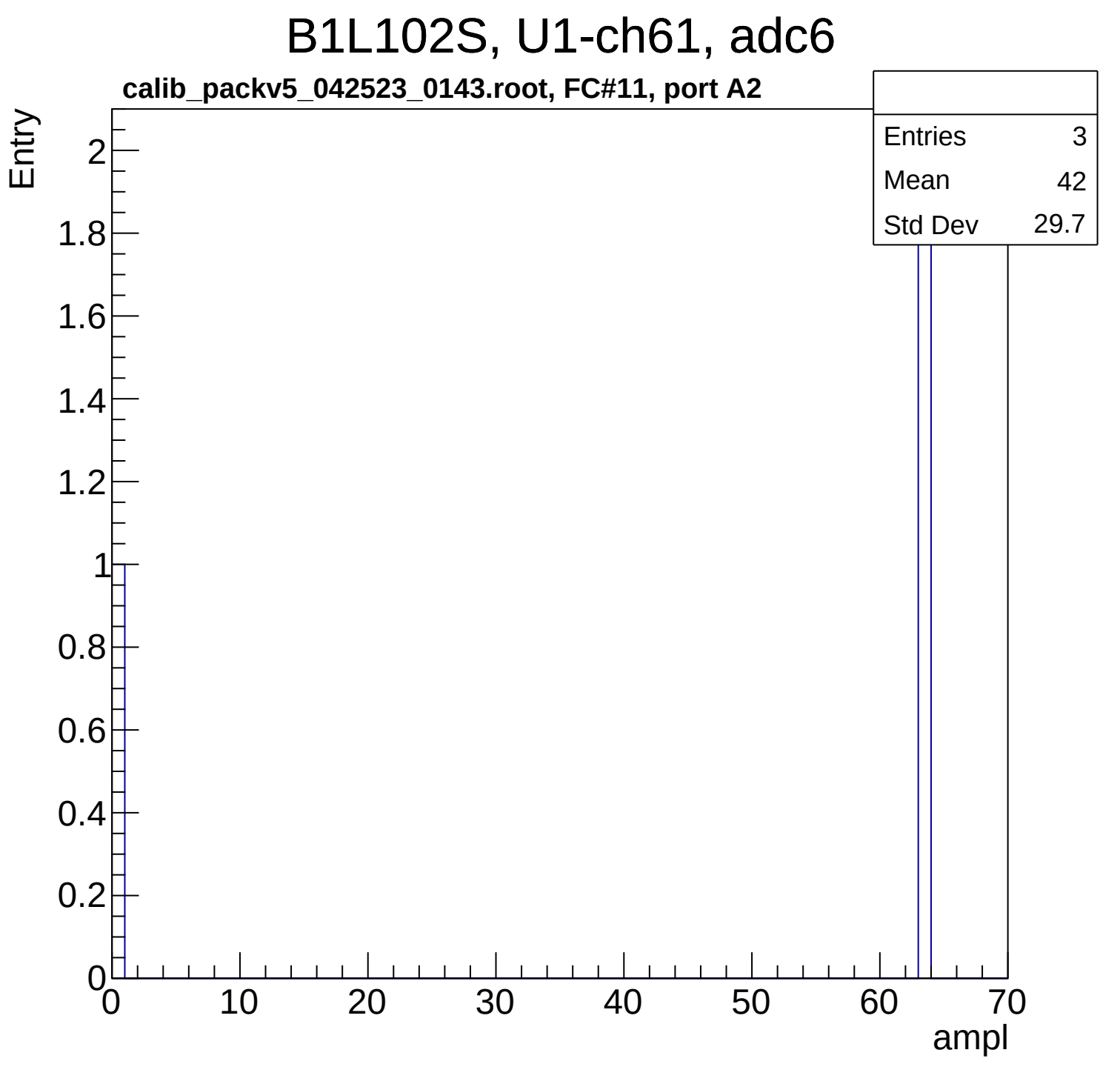

B1L102S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 37.22
Std Dev 3.199
Gaus mean : 37.8679
Gaus Width: 3.6000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

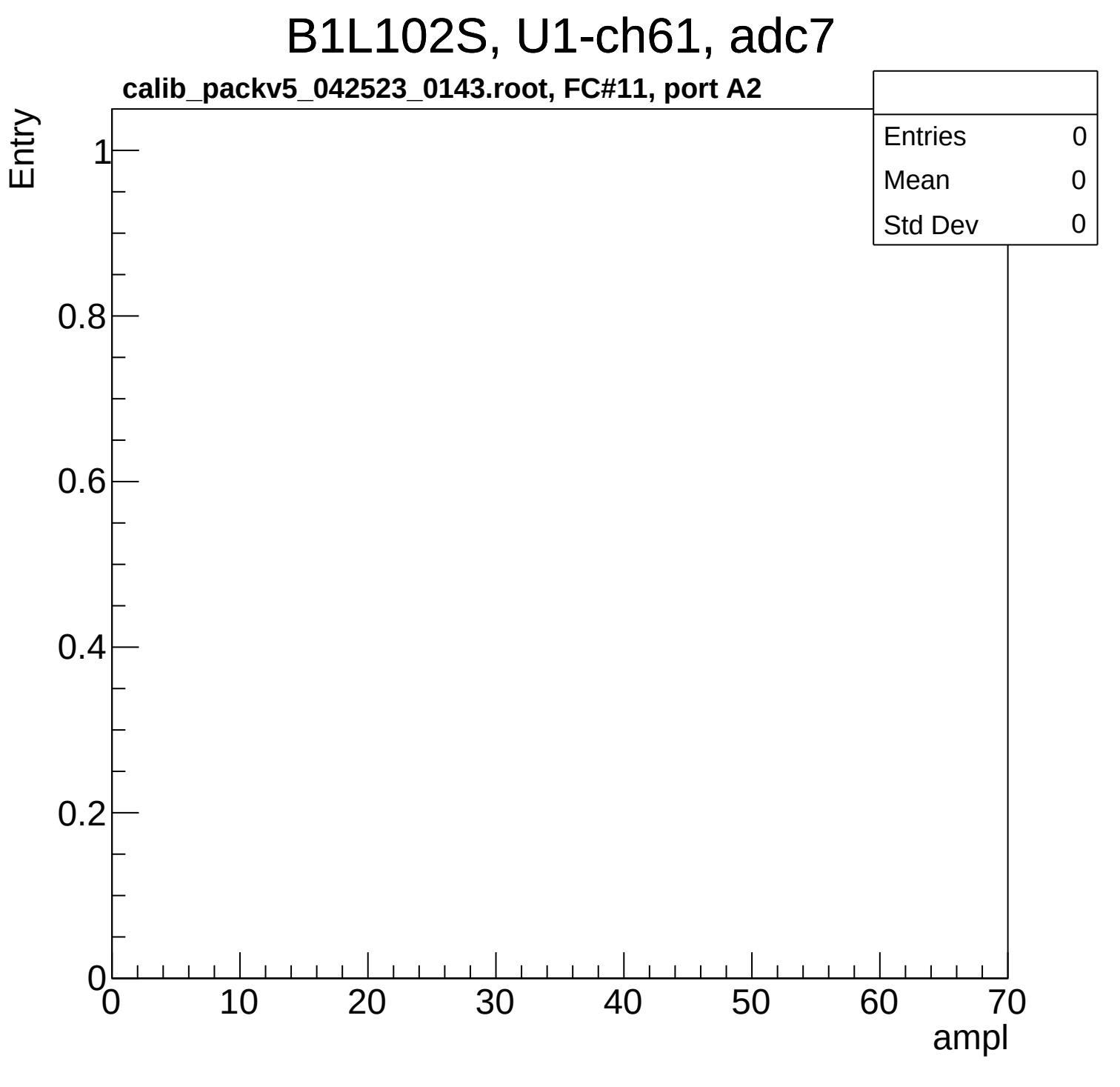

B1L102S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 43.2
Std Dev 2.997
Gaus mean : 43.6274
Gaus Width: 2.9107
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

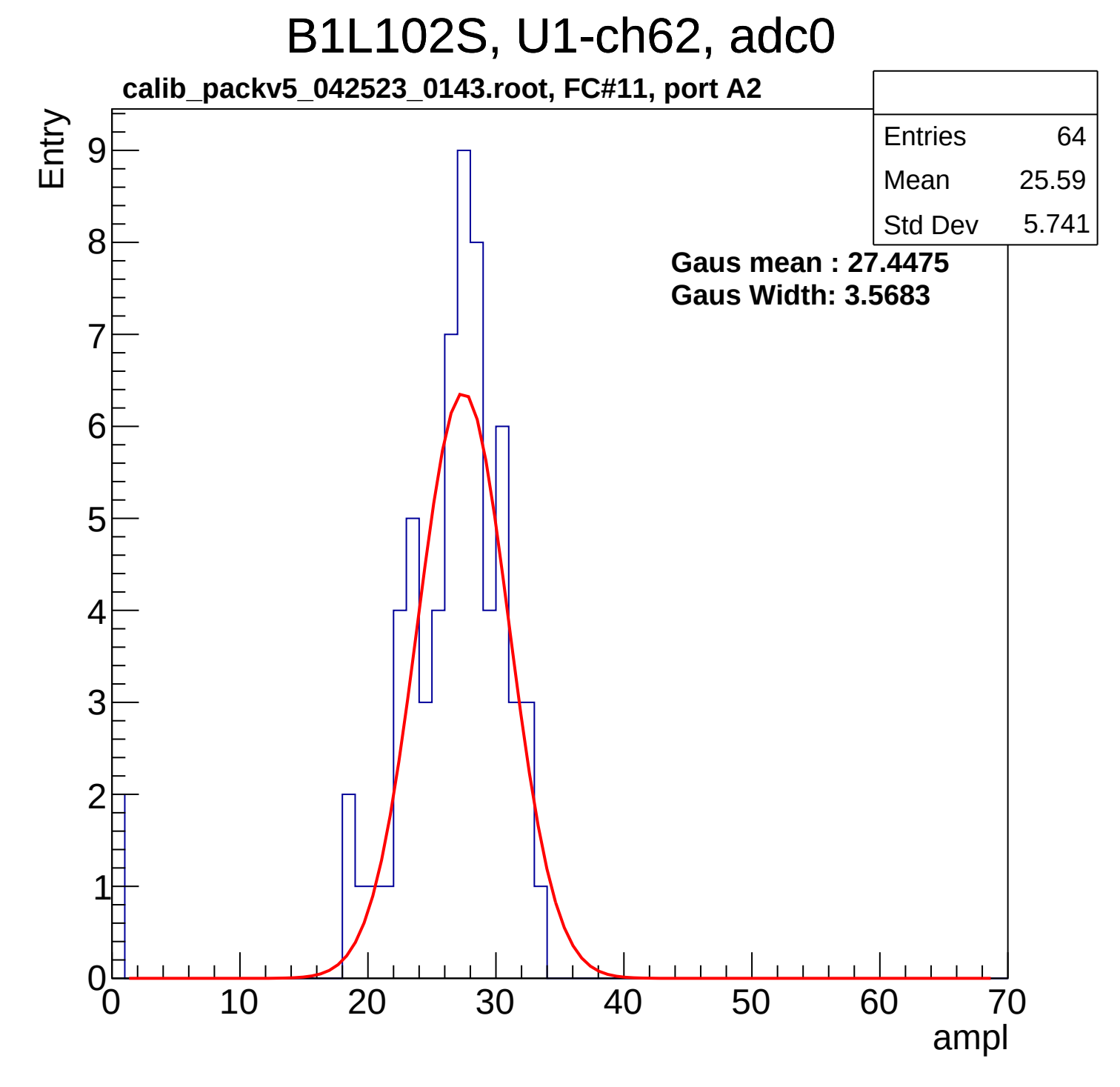

B1L102S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 49.96
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

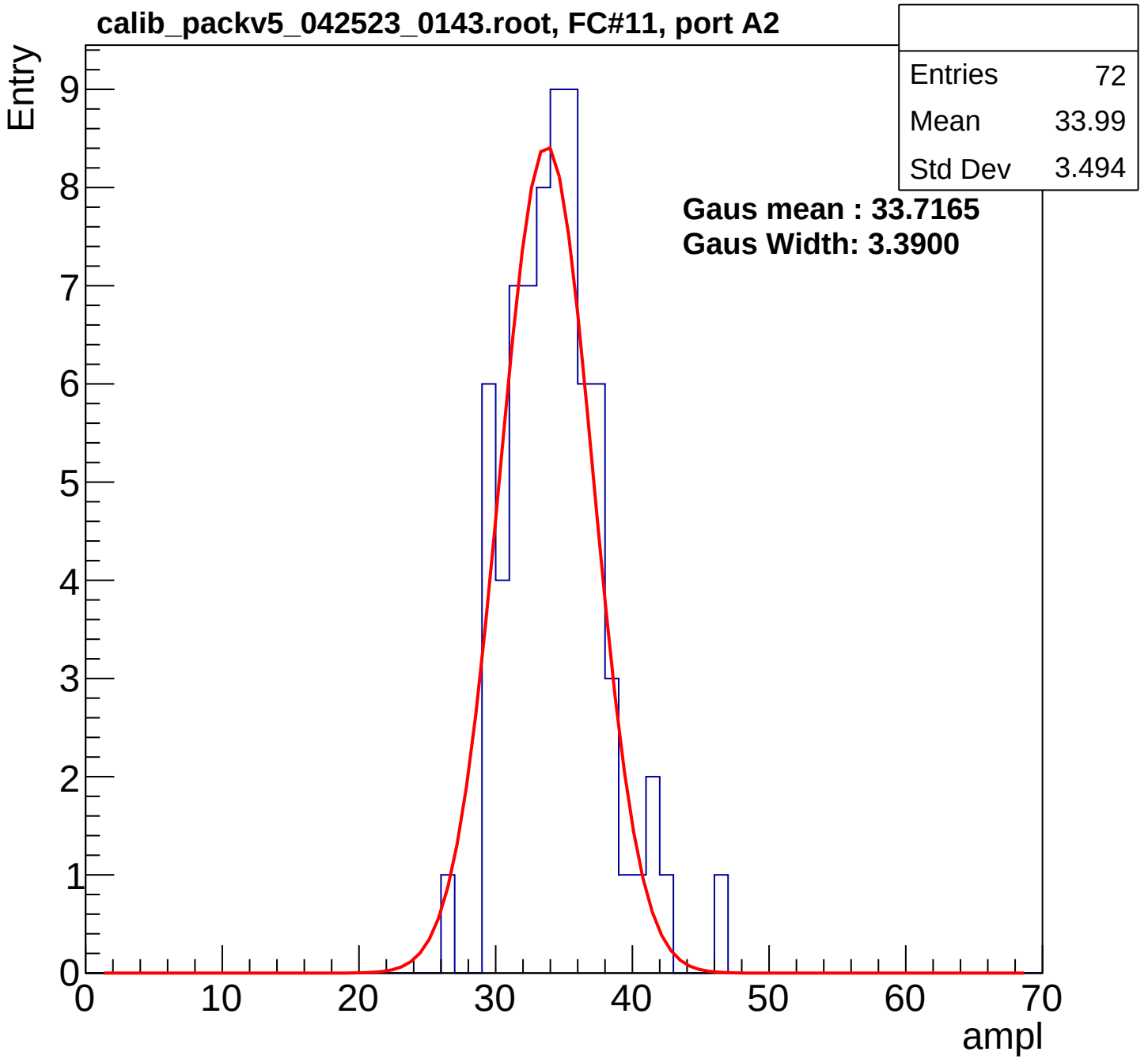

B1L102S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 55.31
Std Dev 3.511
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 59.98
Std Dev 2.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 25.59
Std Dev 5.741
Gaus mean : 27.4475
Gaus Width: 3.5683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 33.99
Std Dev 3.494
Gaus mean : 33.7165
Gaus Width: 3.3900
Entry
ampl
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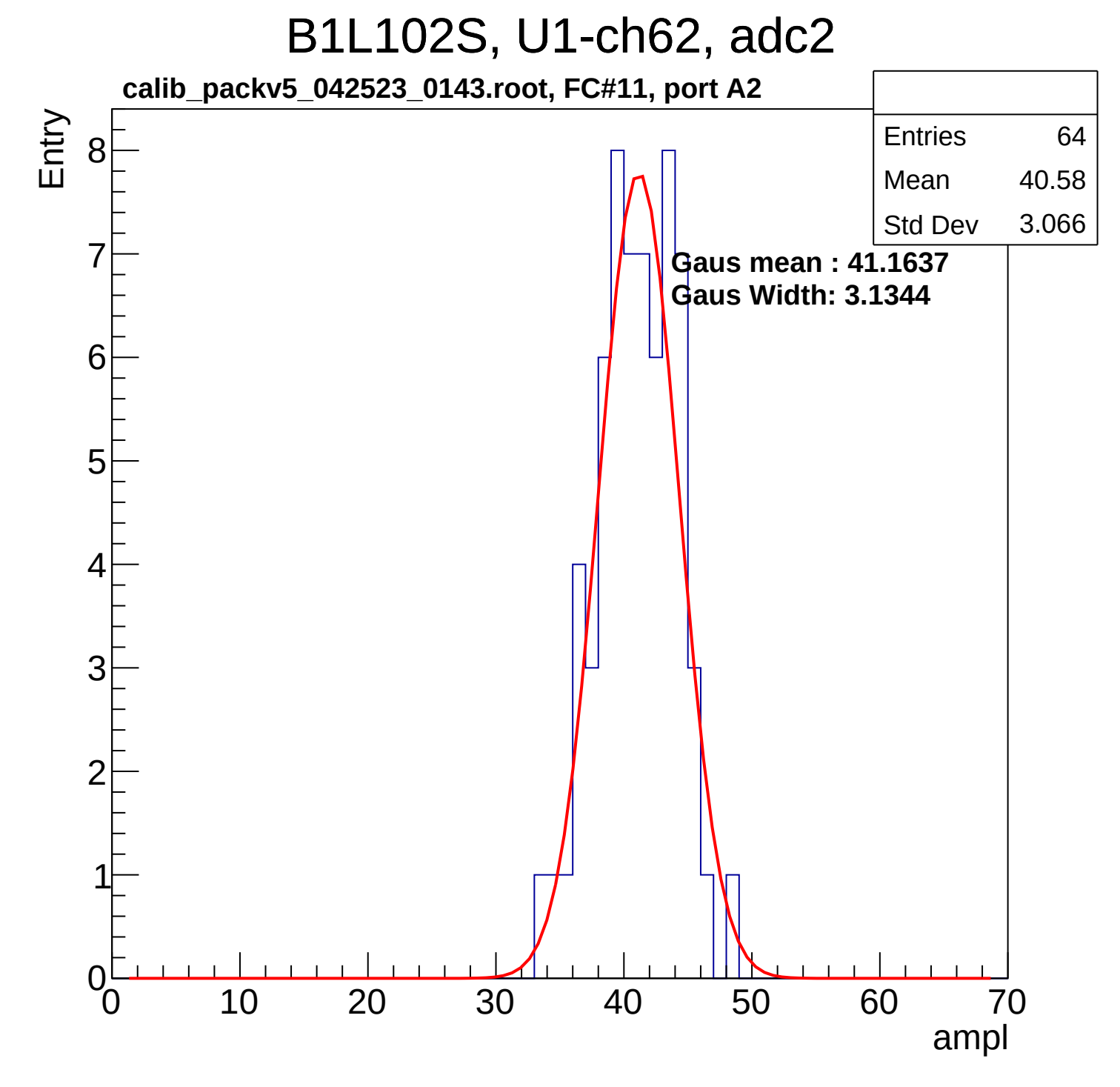
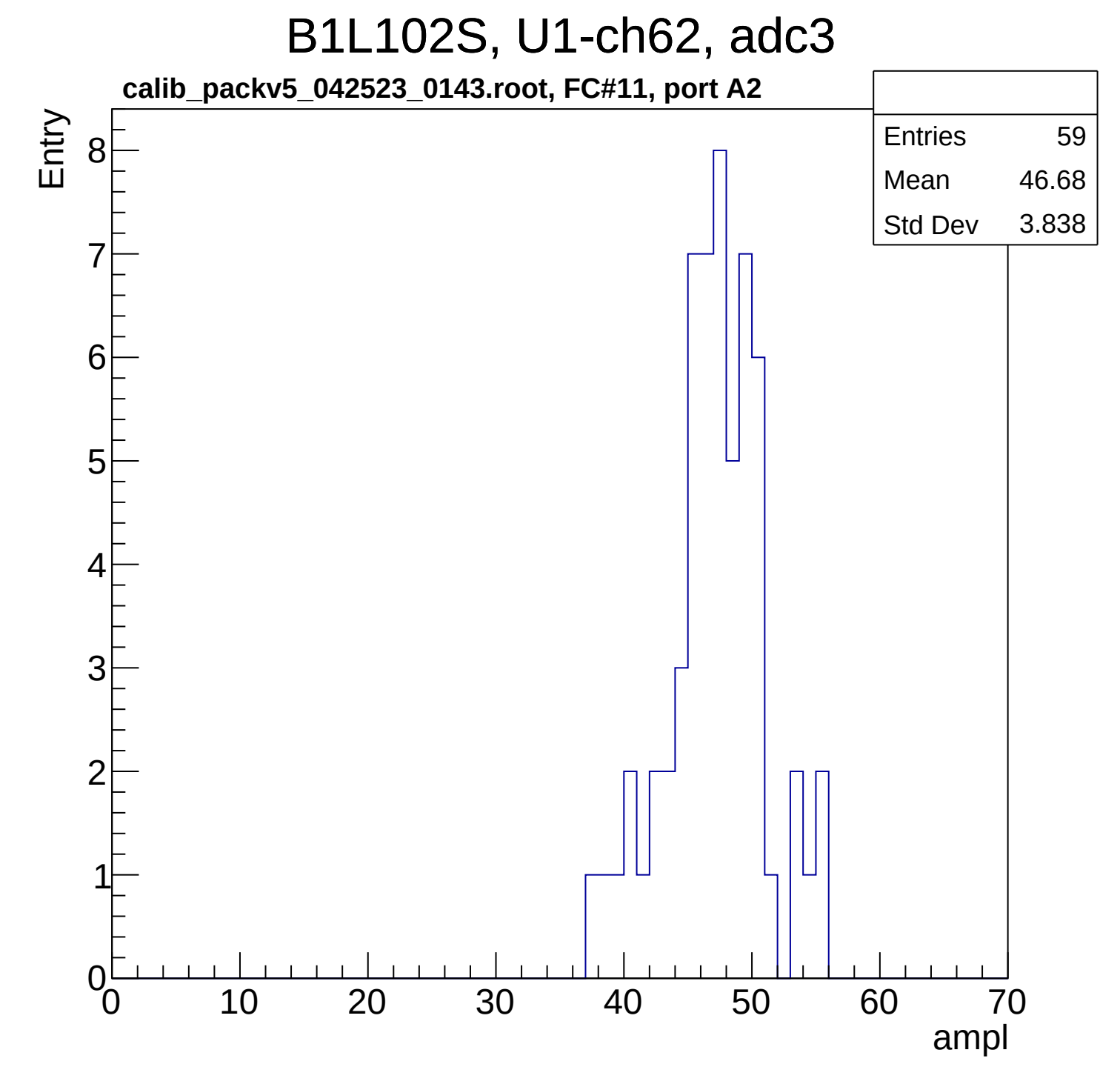
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 40.58
Std Dev 3.066
Gaus mean : 41.1637
Gaus Width: 3.1344
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 46.68
Std Dev 3.838
Entry
ampl
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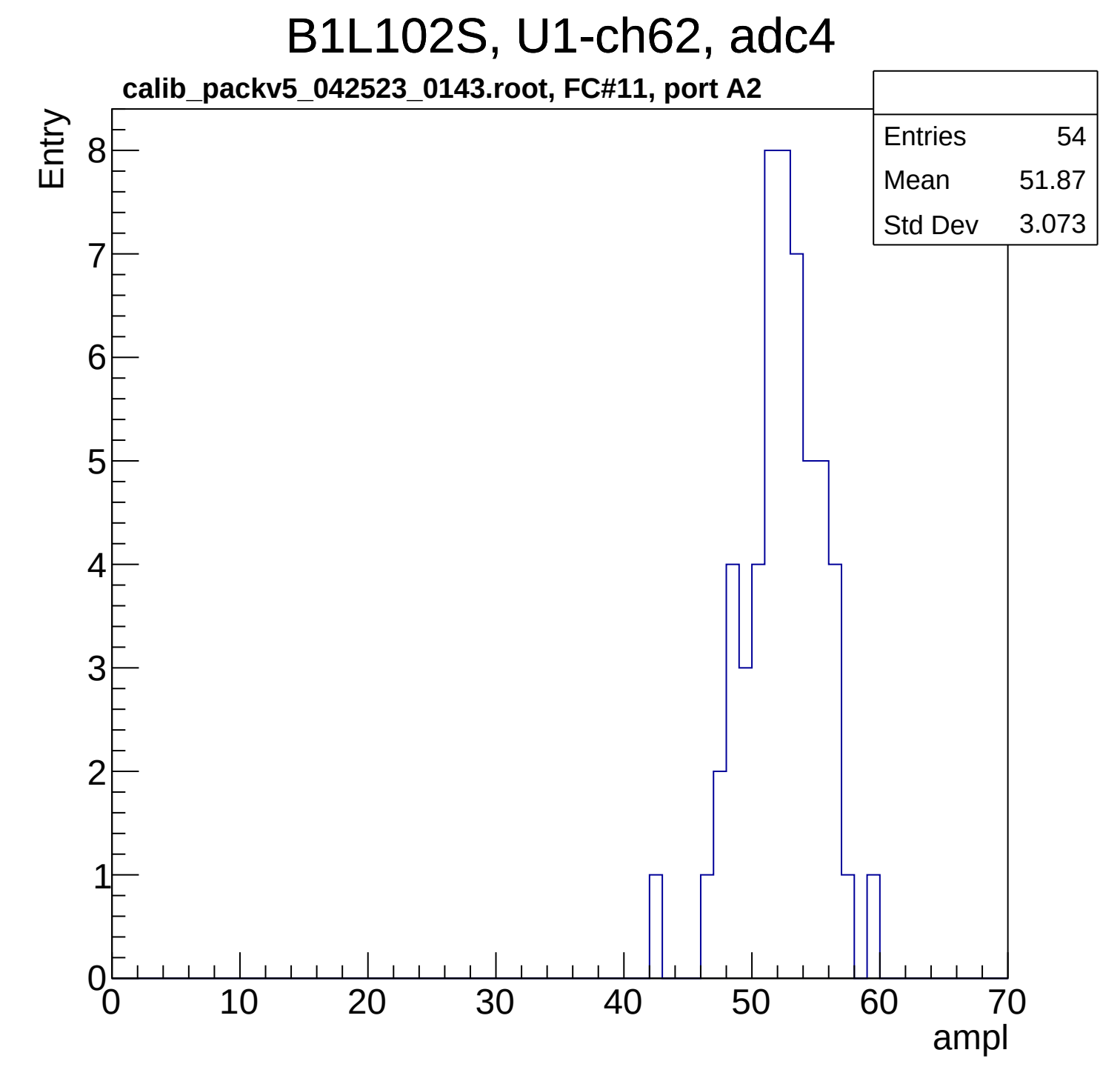
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 51.87
Std Dev 3.073
Entry
ampl
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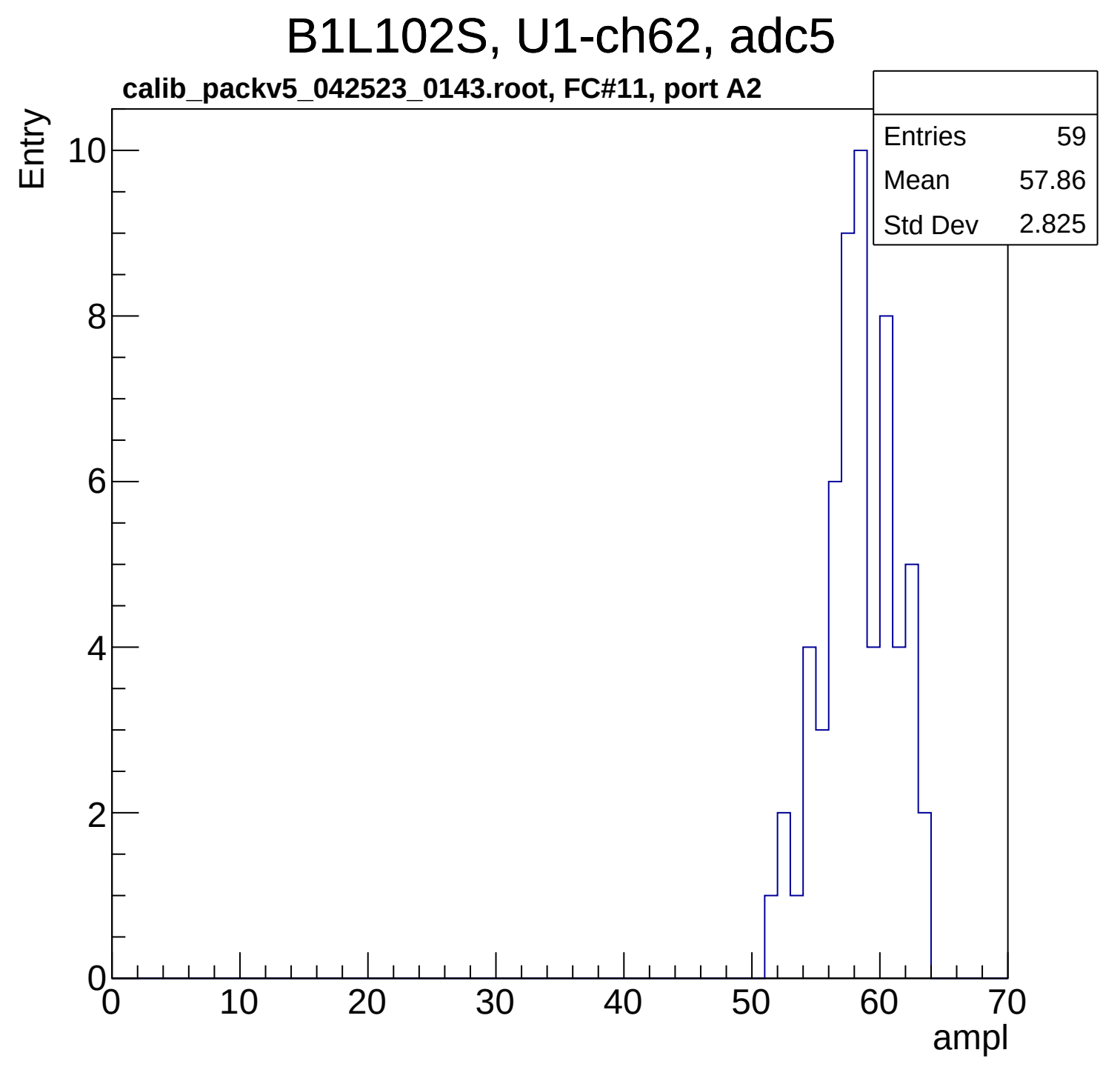
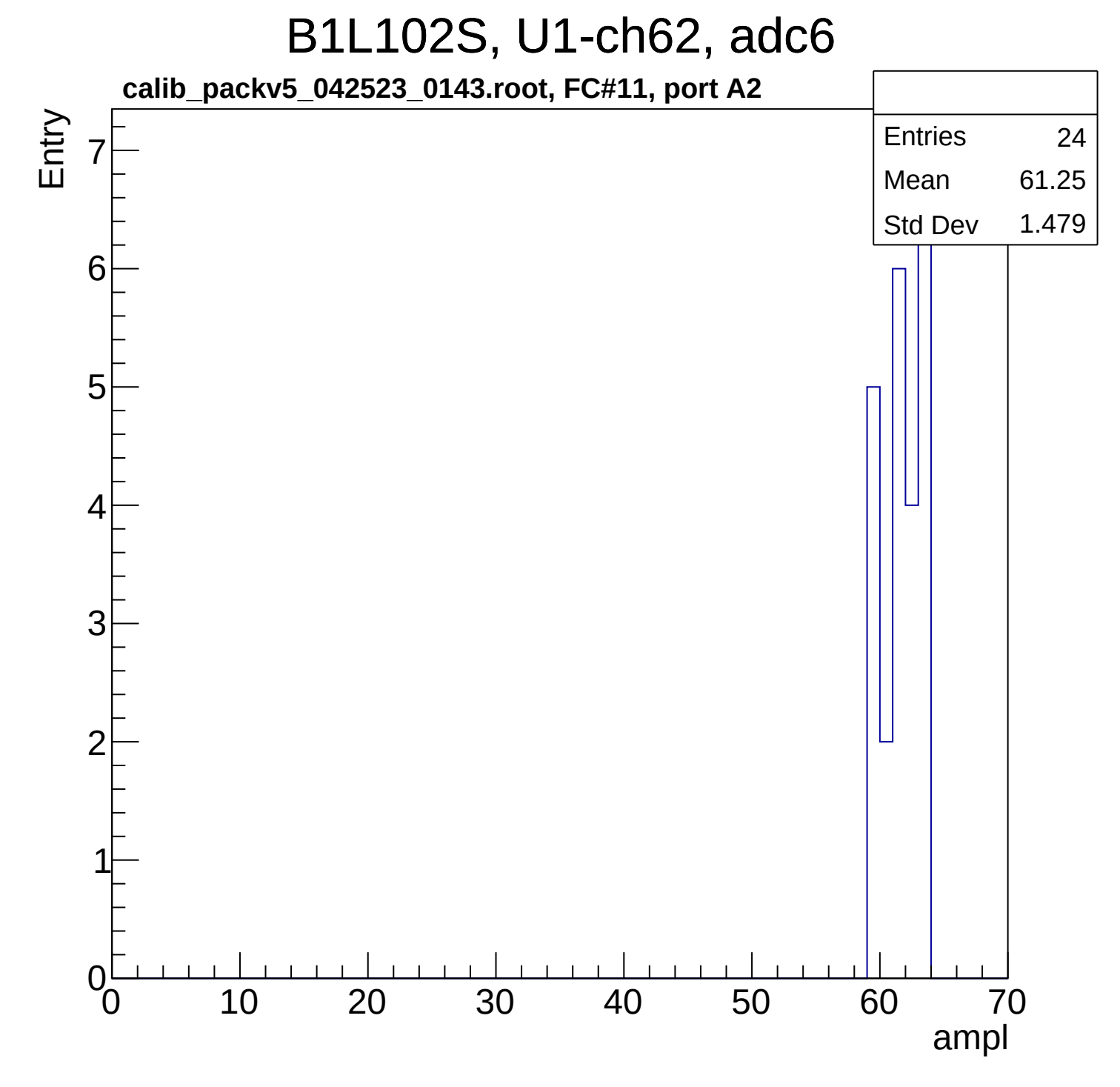
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 57.86
Std Dev 2.825
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 24
Mean 61.25
Std Dev 1.479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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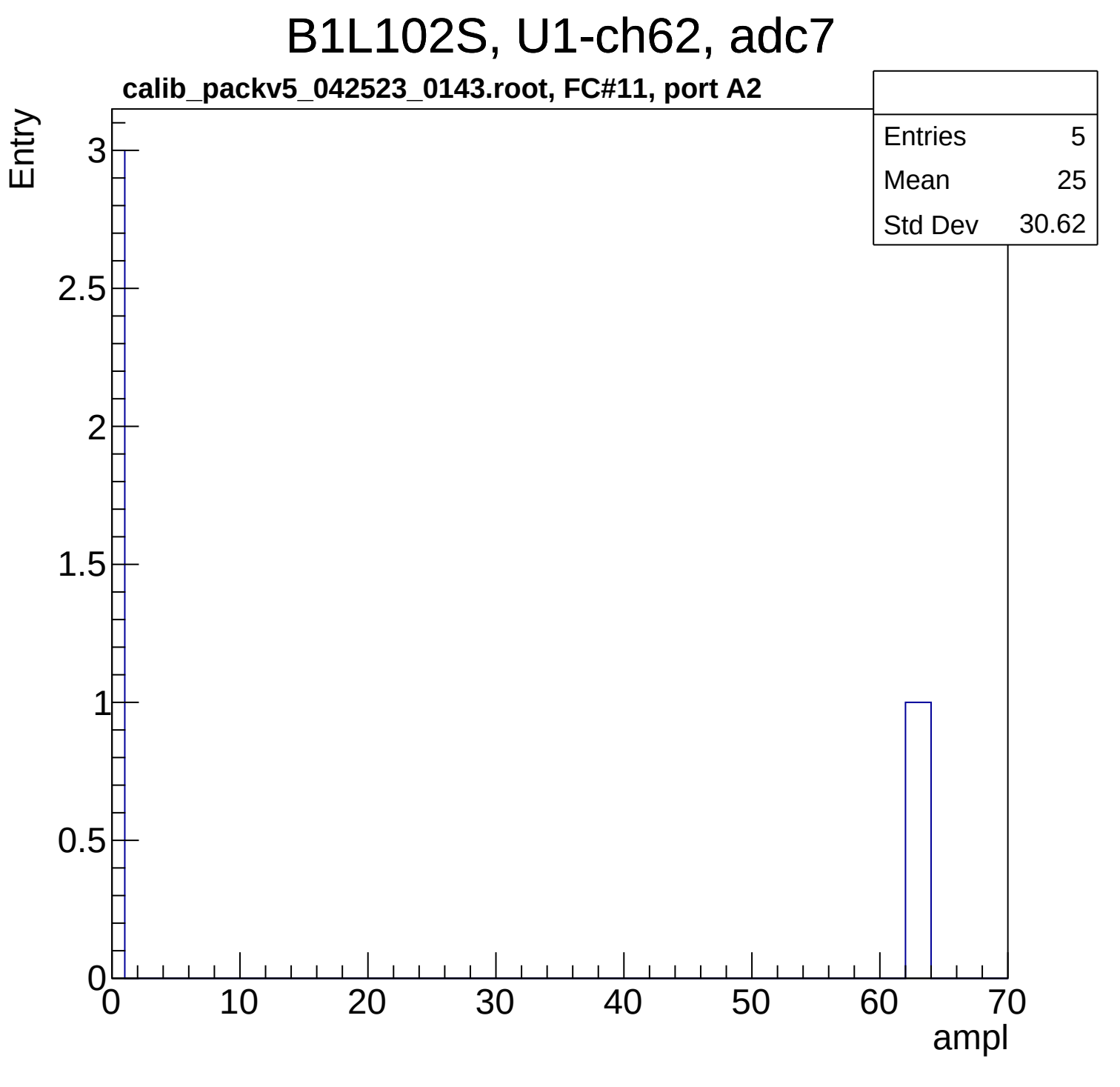
40
50
60
70


B1L102S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 25
Std Dev 30.62
Entry
ampl
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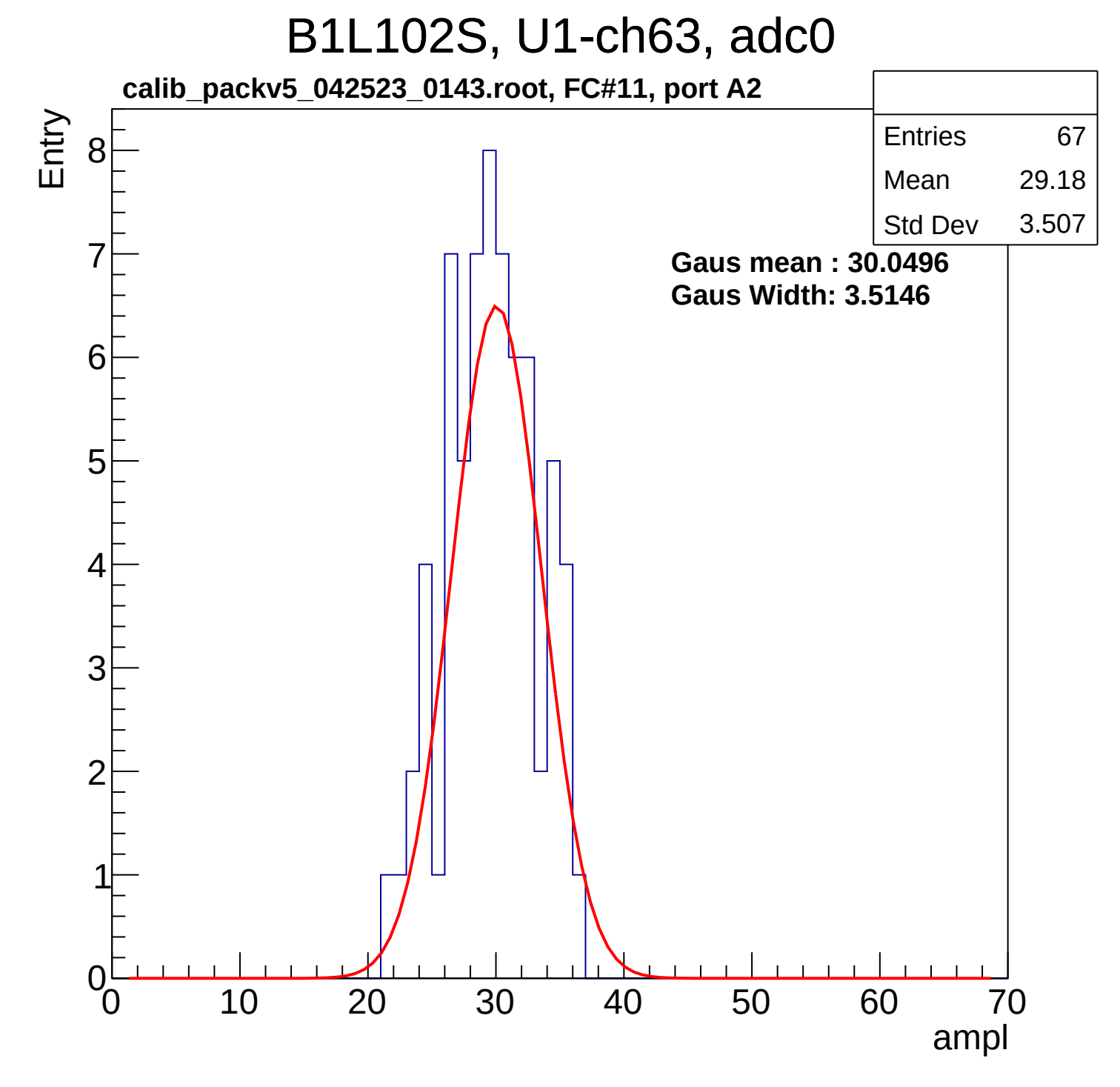
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 29.18
Std Dev 3.507
Gaus mean : 30.0496
Gaus Width: 3.5146
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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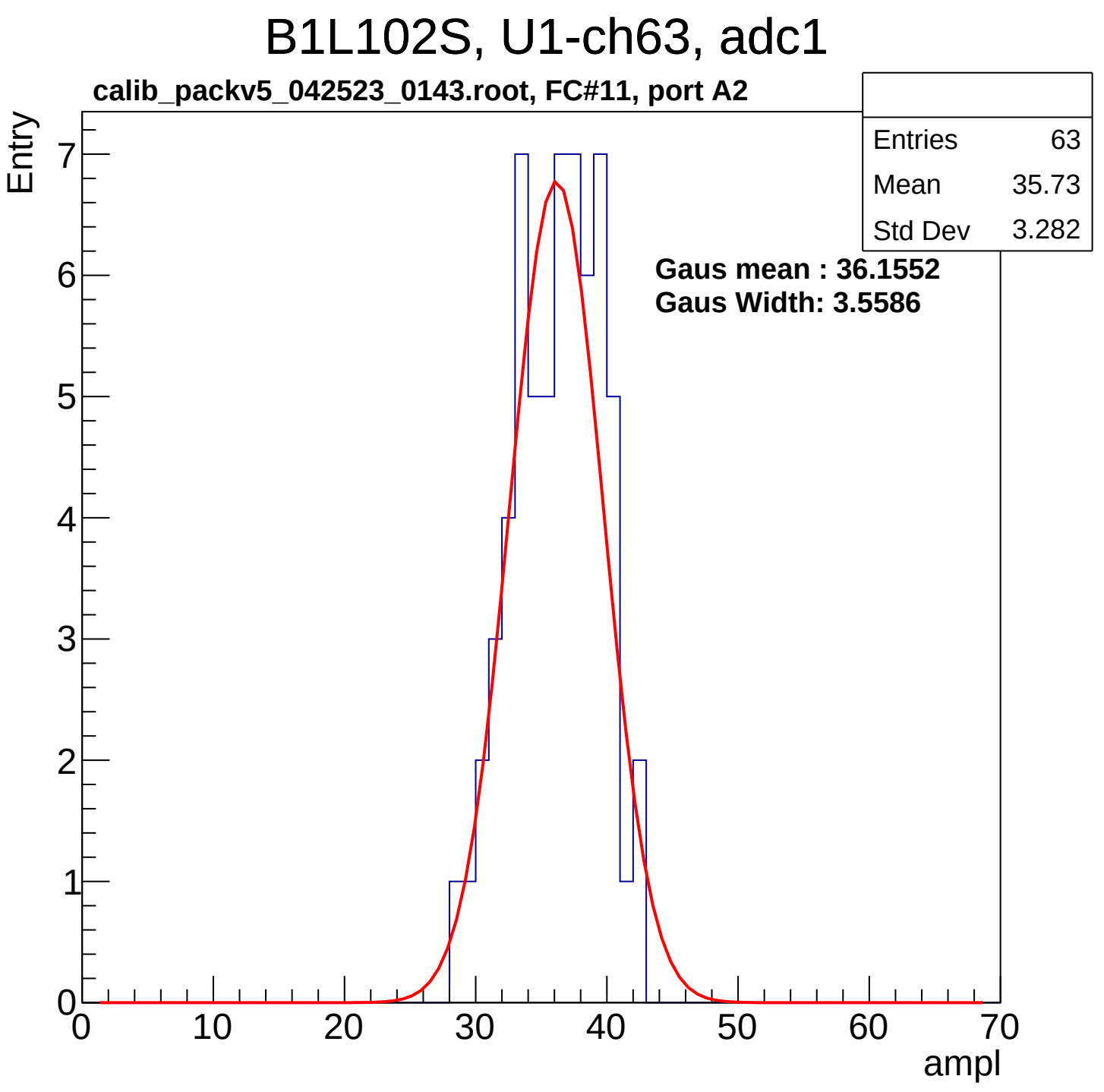
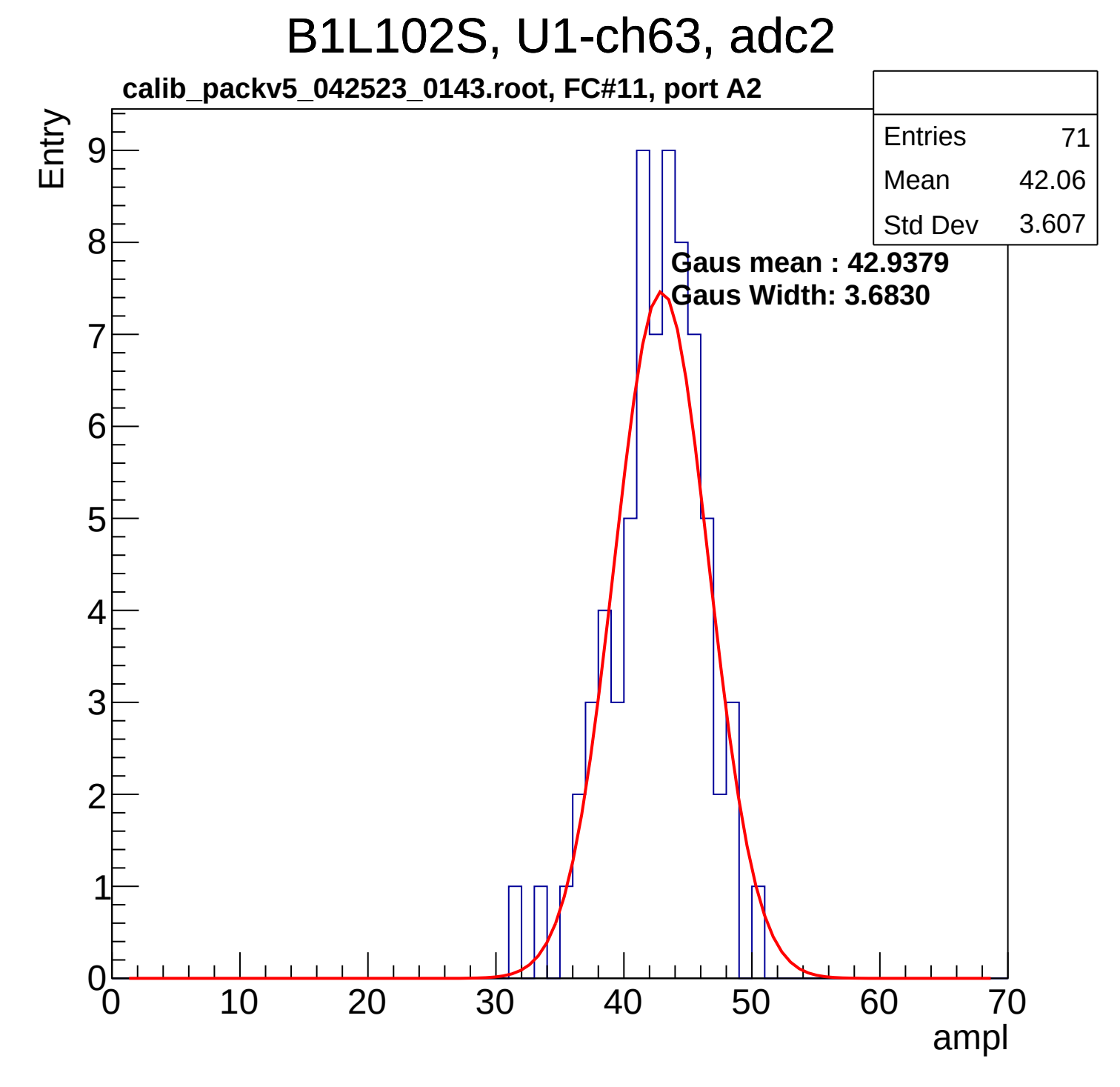

B1L102S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 35.73
Std Dev 3.282
Gaus mean : 36.1552
Gaus Width: 3.5586
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.06
Std Dev 3.607
Gaus mean : 42.9379
Gaus Width: 3.6830
Entry
ampl
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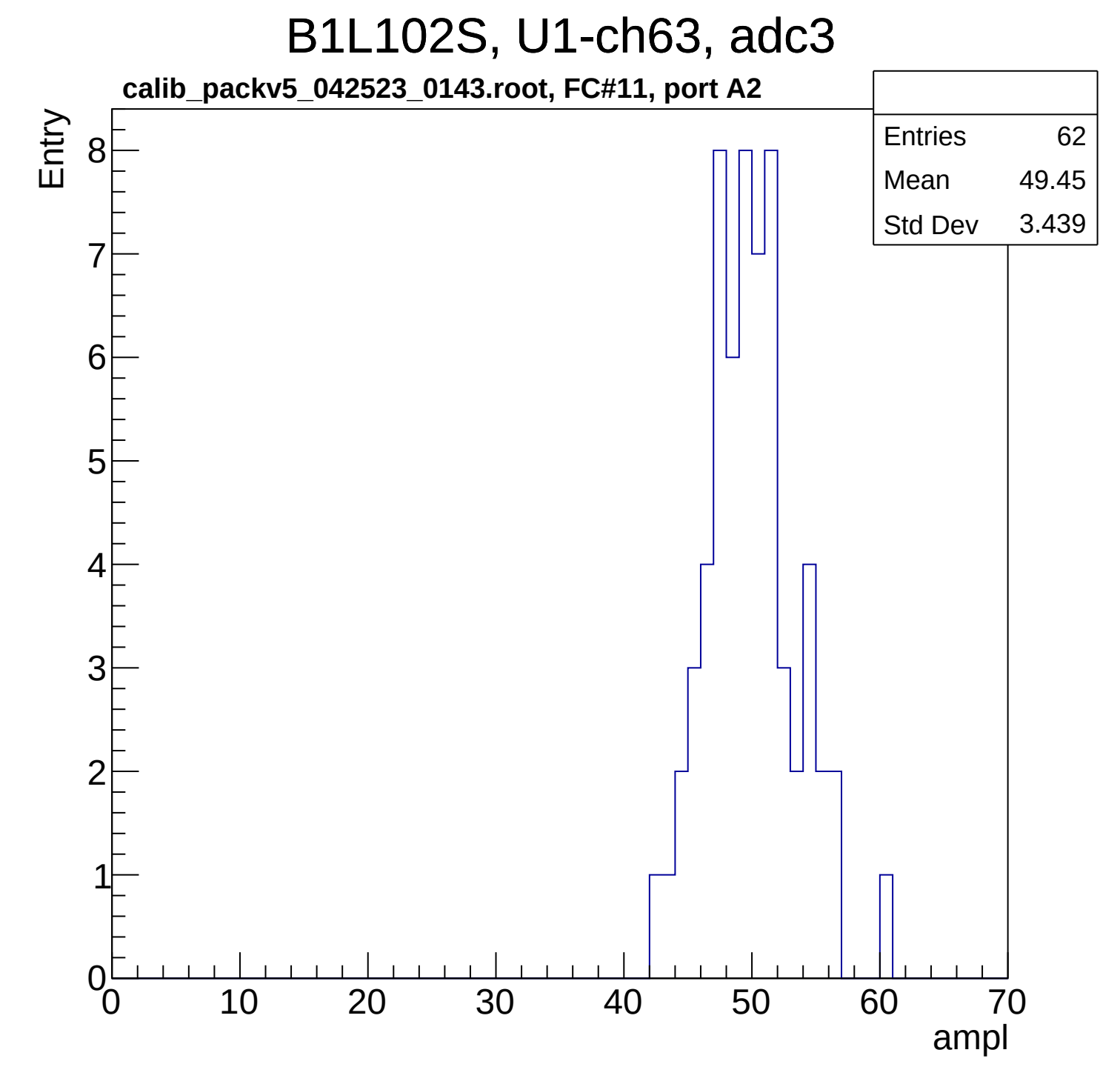
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 49.45
Std Dev 3.439
Entry
ampl
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2
3
4
5
6
7
8
0
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20
30
40
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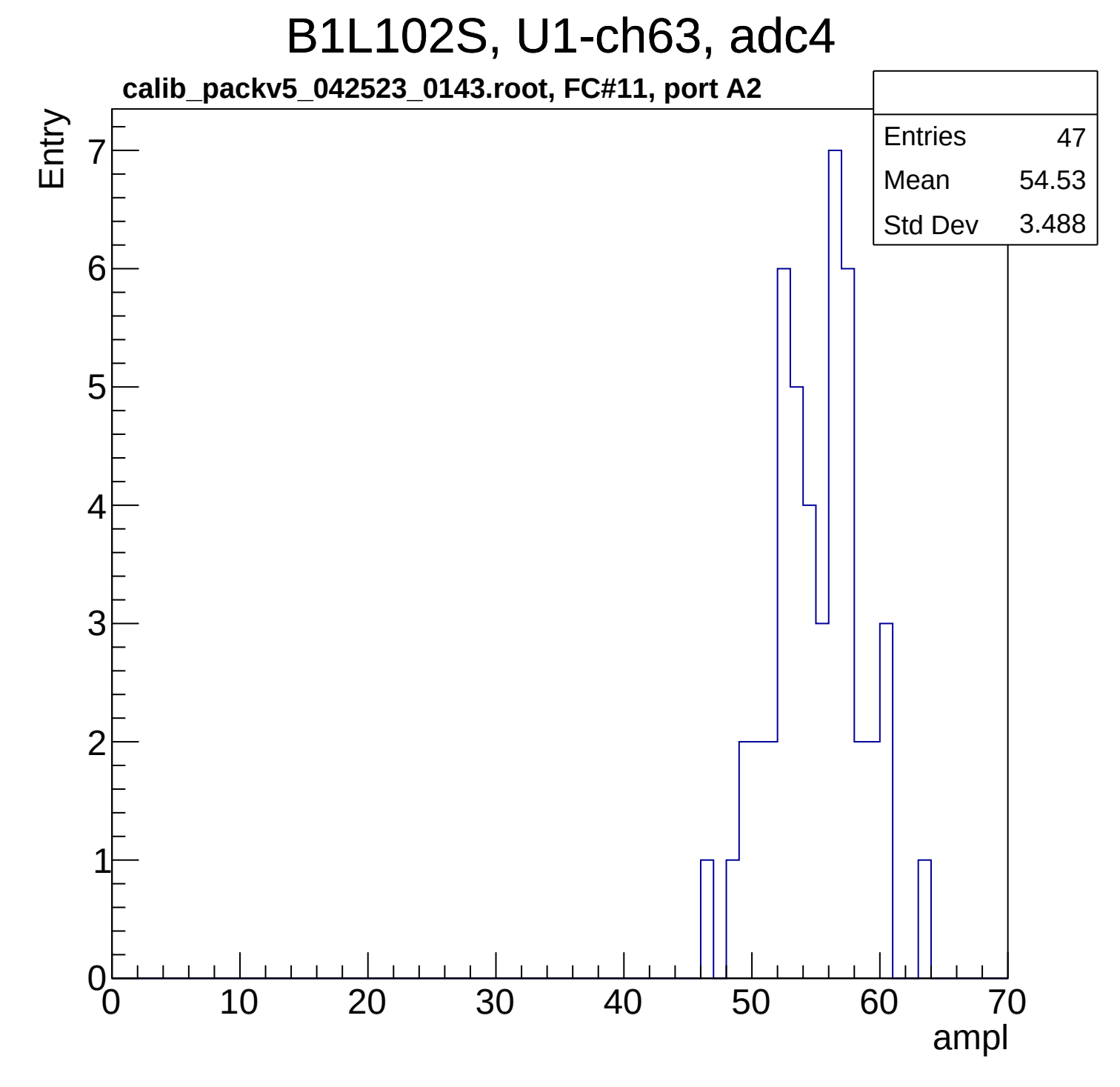
B1L102S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 54.53
Std Dev 3.488
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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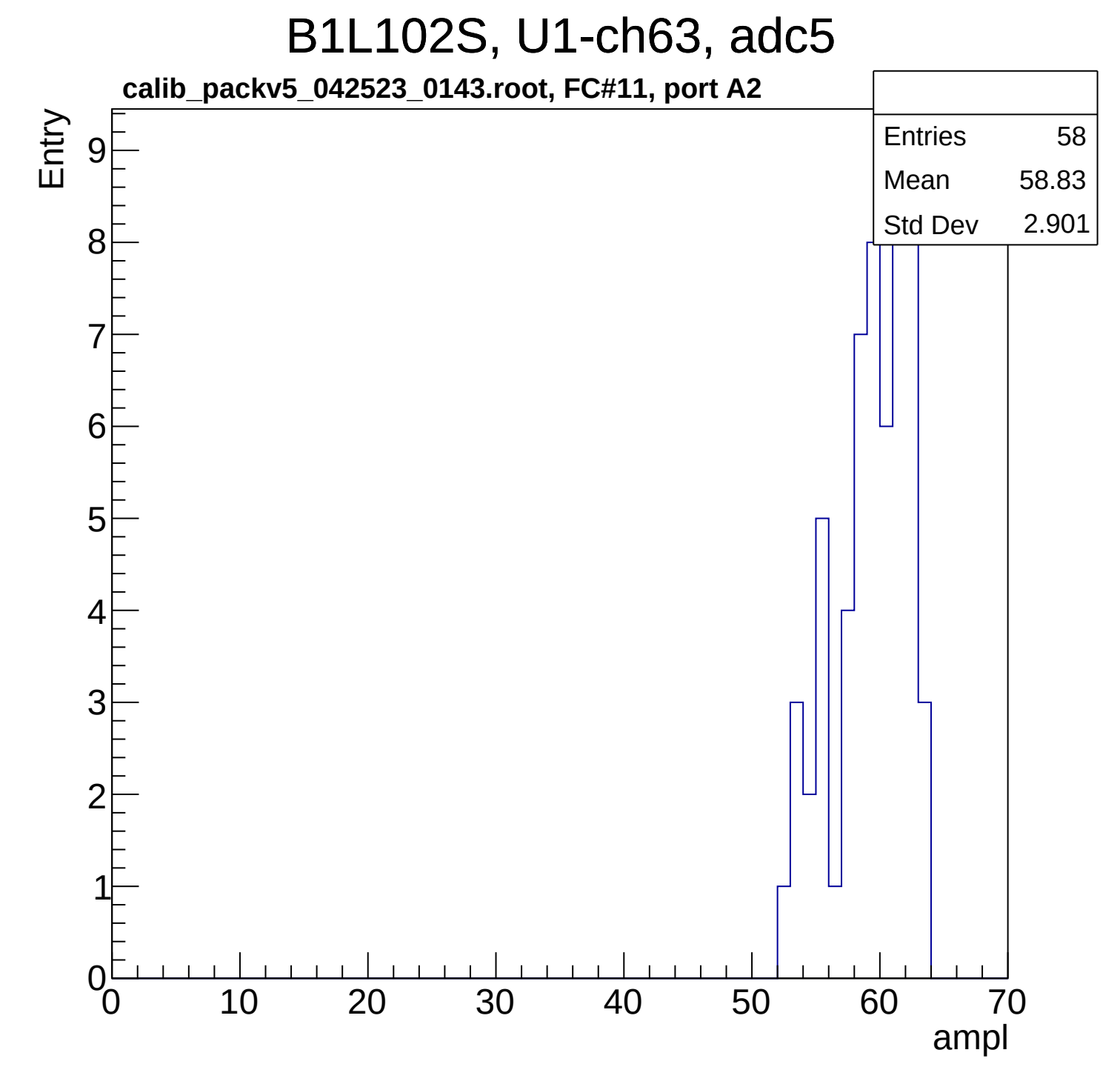
B1L102S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 58.83
Std Dev 2.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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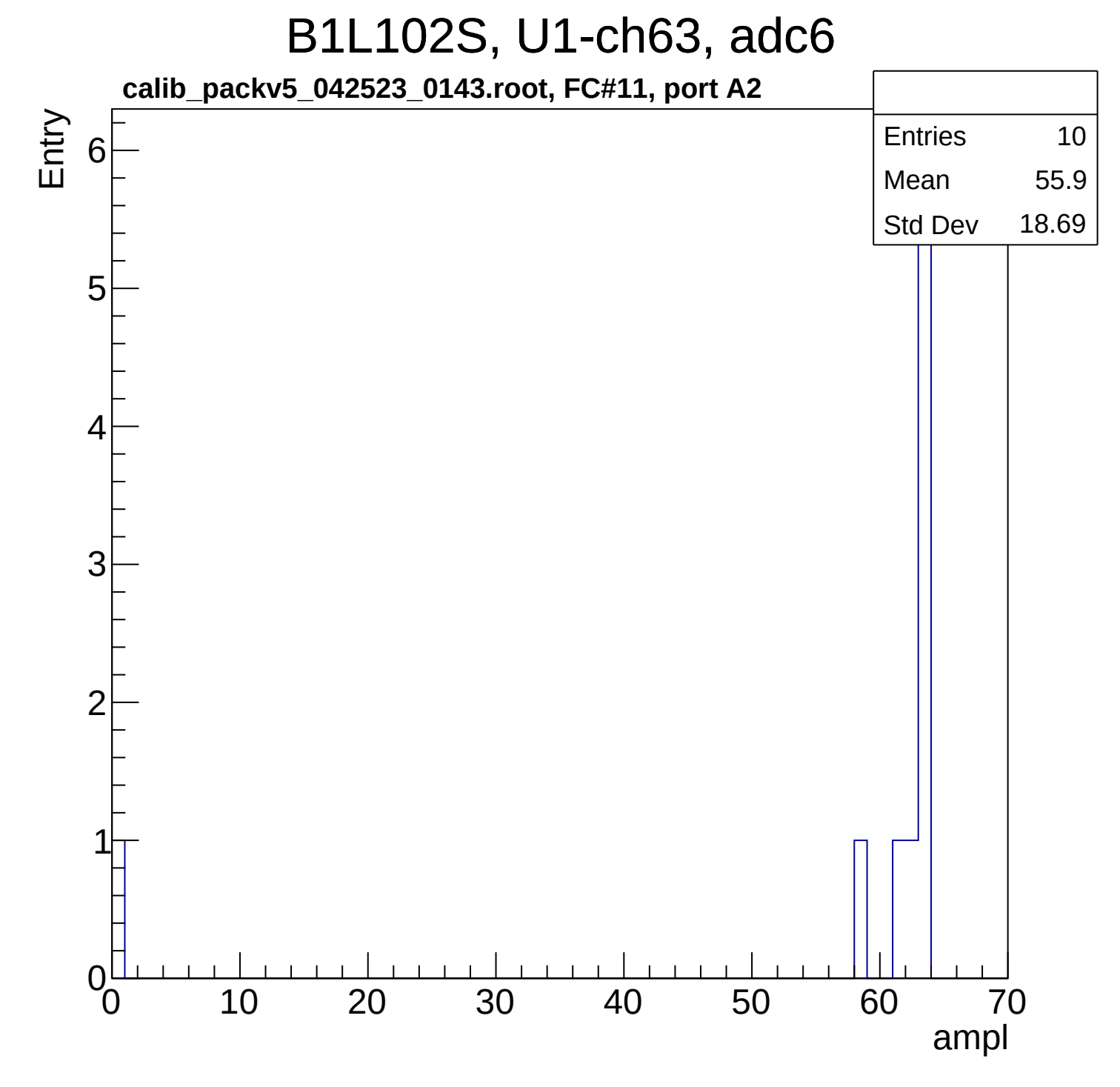
B1L102S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 55.9
Std Dev 18.69
Entry
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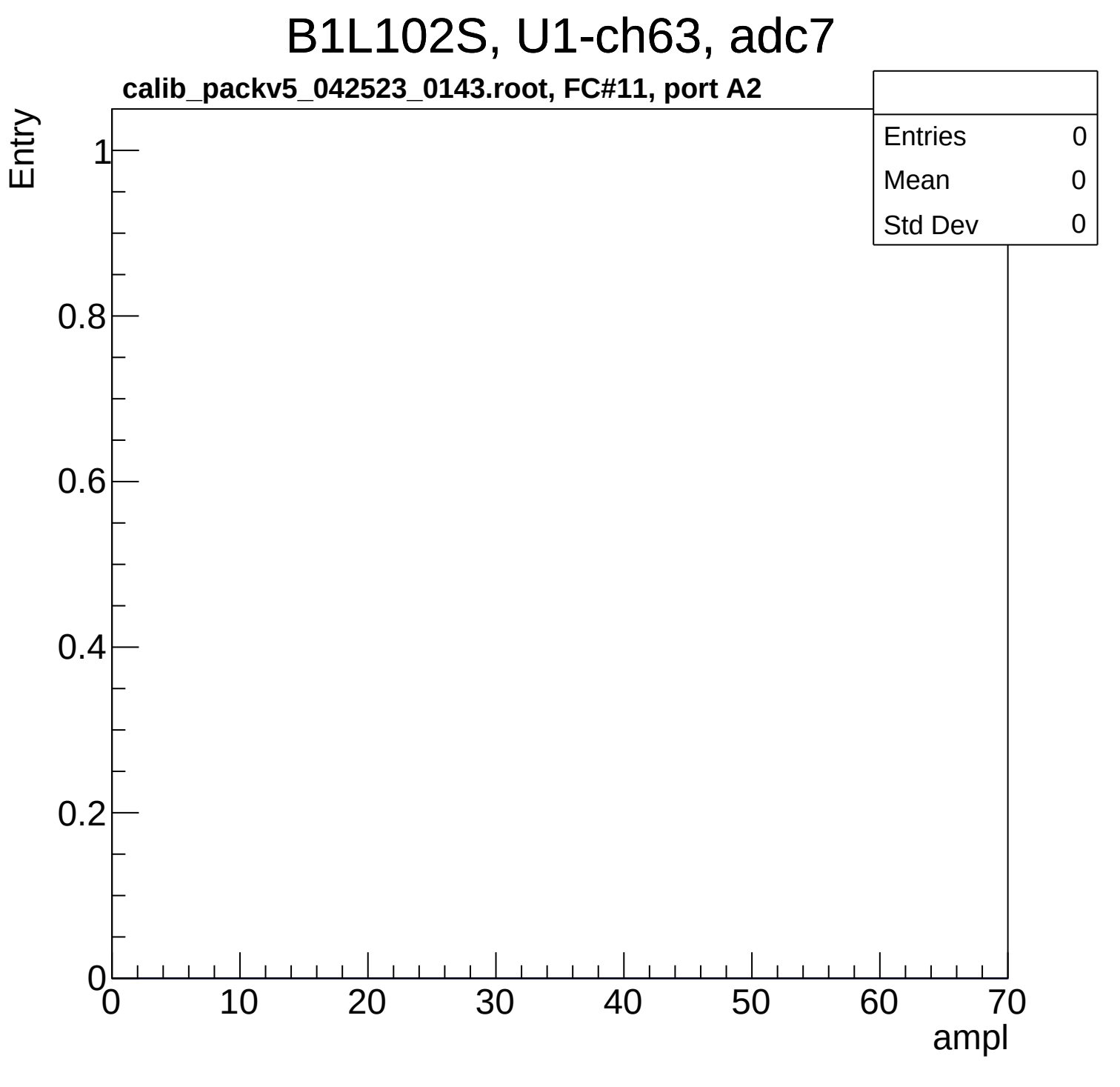
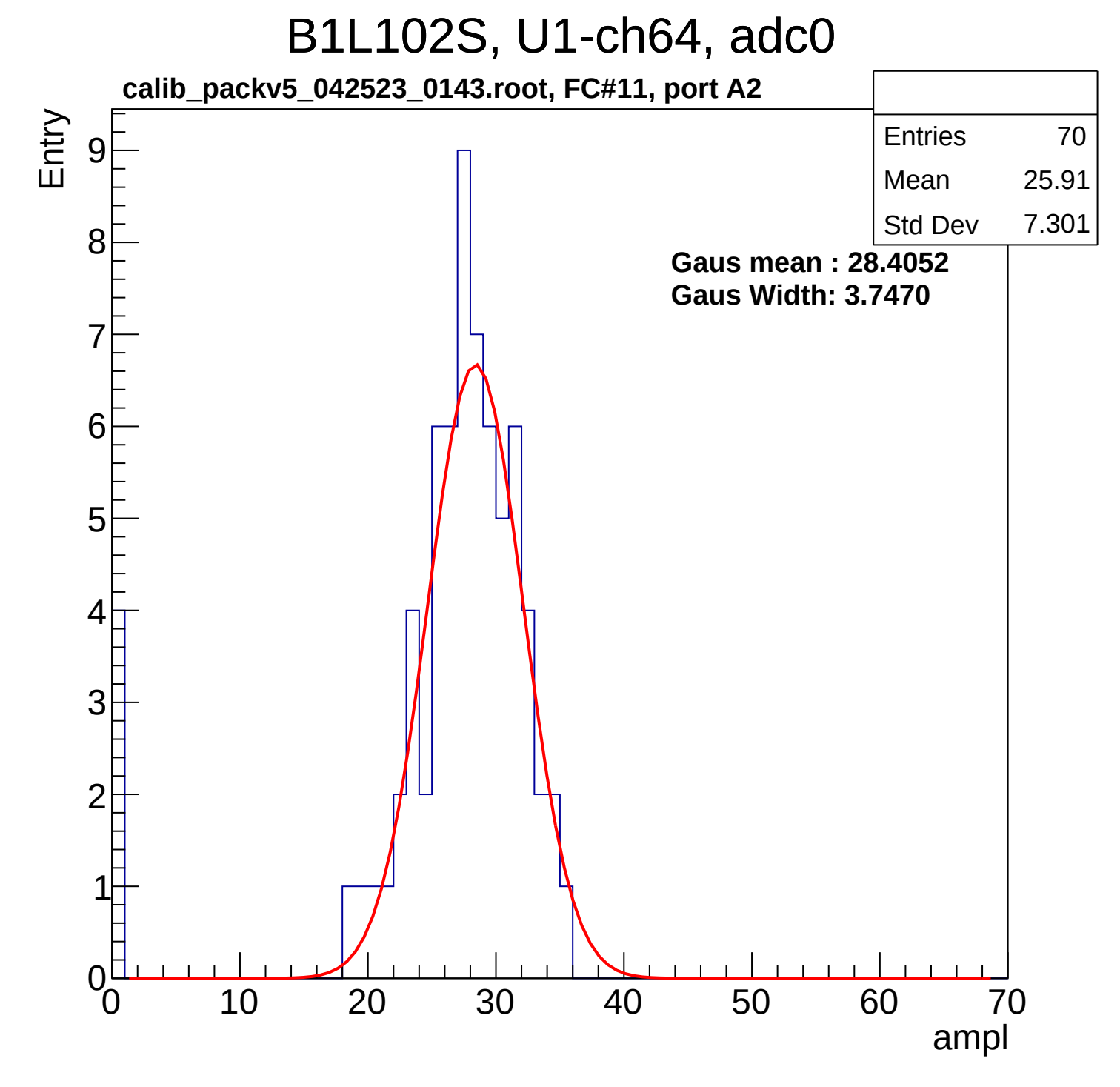
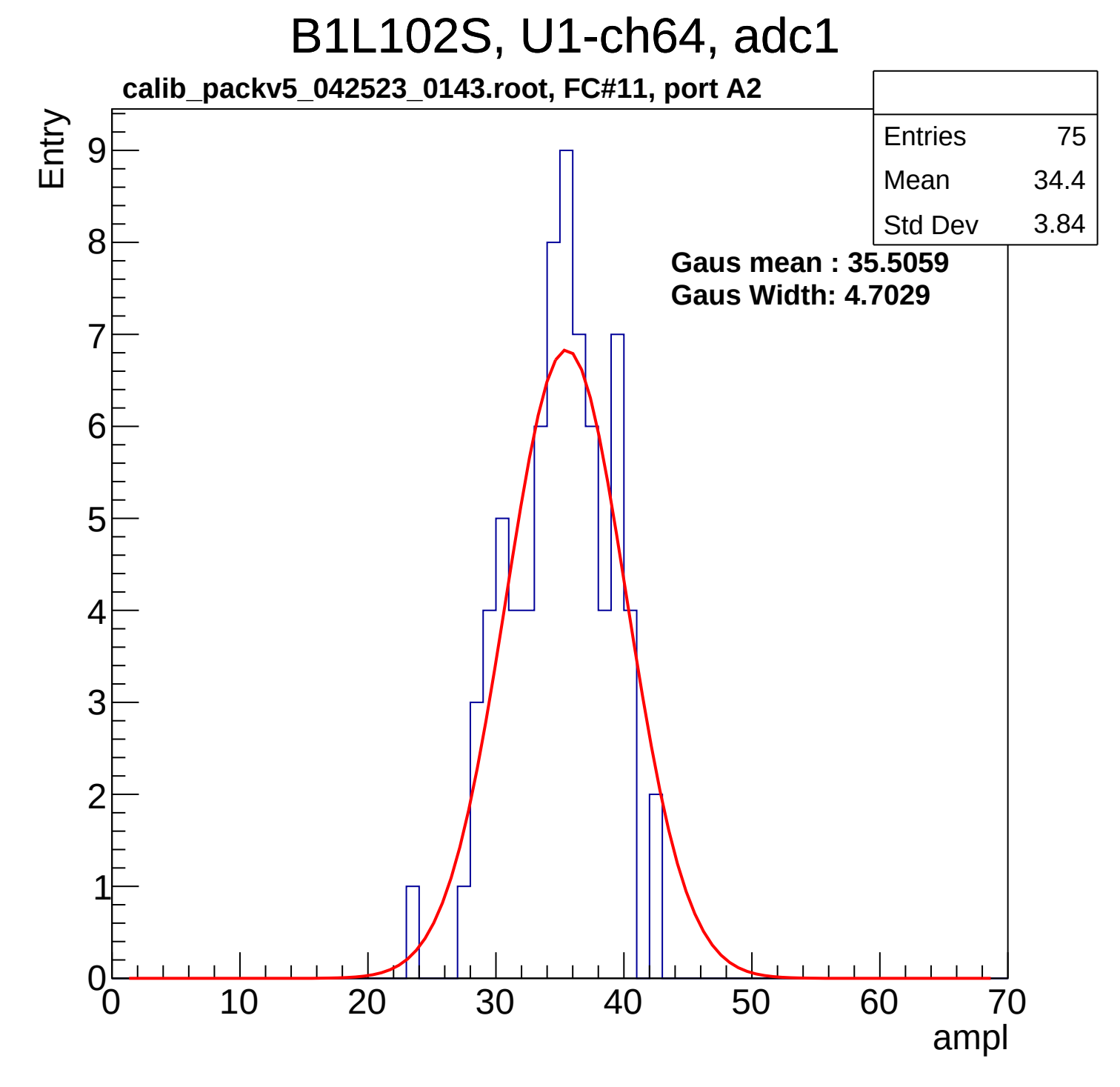
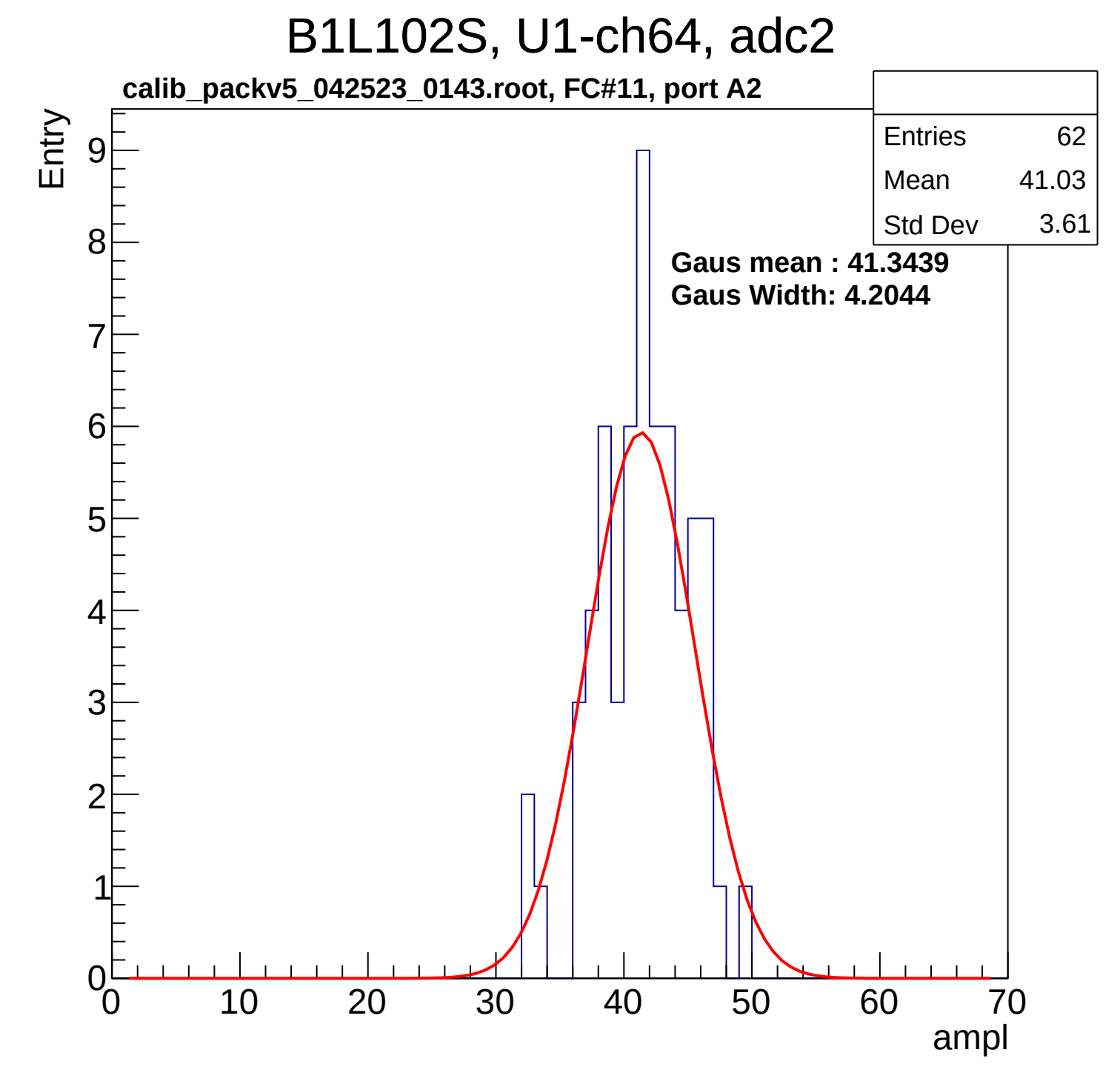
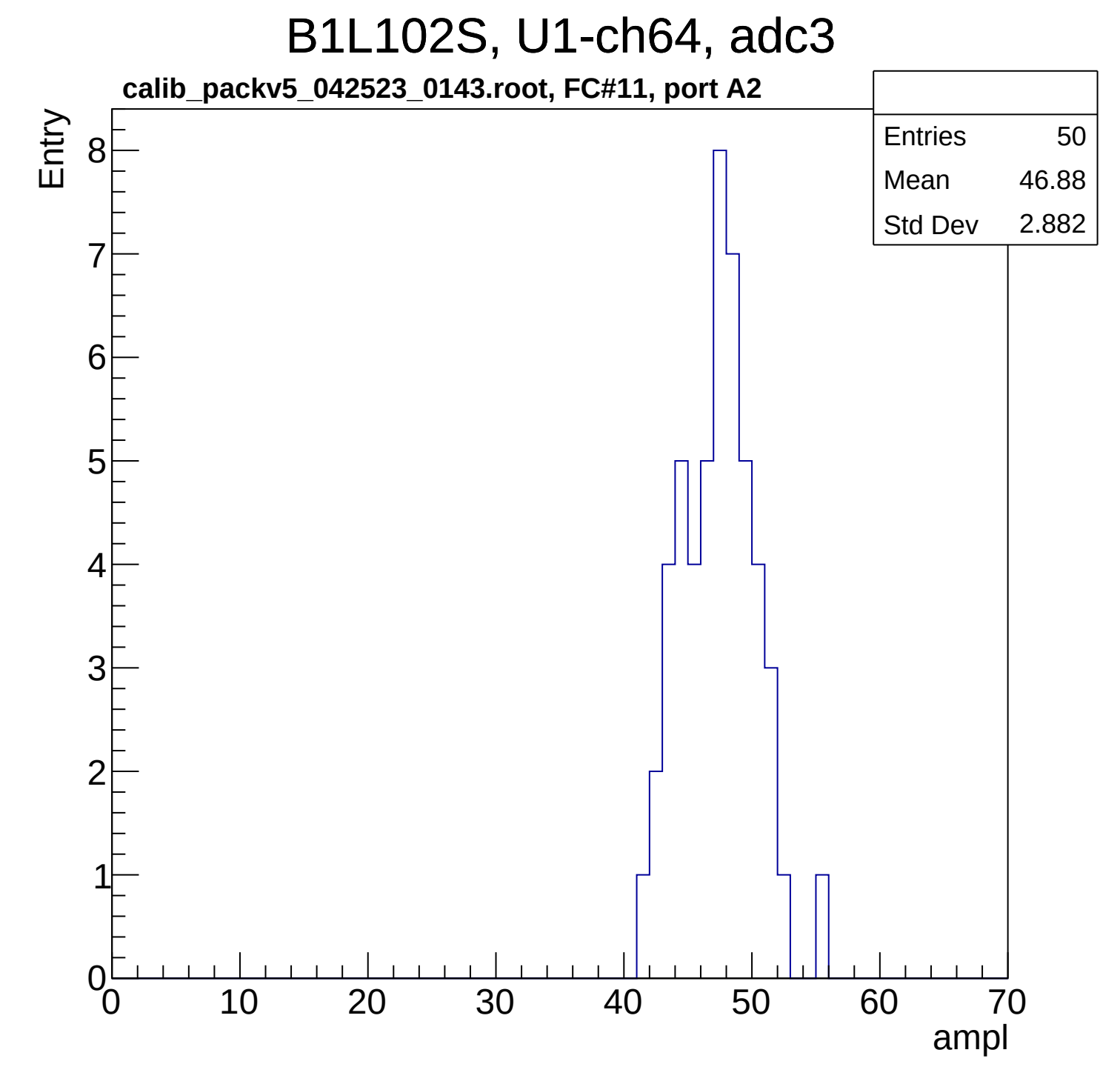
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 25.91
Std Dev 7.301
Gaus mean : 28.4052
Gaus Width: 3.7470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 34.4
Std Dev 3.84
Gaus mean : 35.5059
Gaus Width: 4.7029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 41.03
Std Dev 3.61
Gaus mean : 41.3439
Gaus Width: 4.2044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 46.88
Std Dev 2.882
Entry
ampl
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2
3
4
5
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7
8
0
10
20
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40
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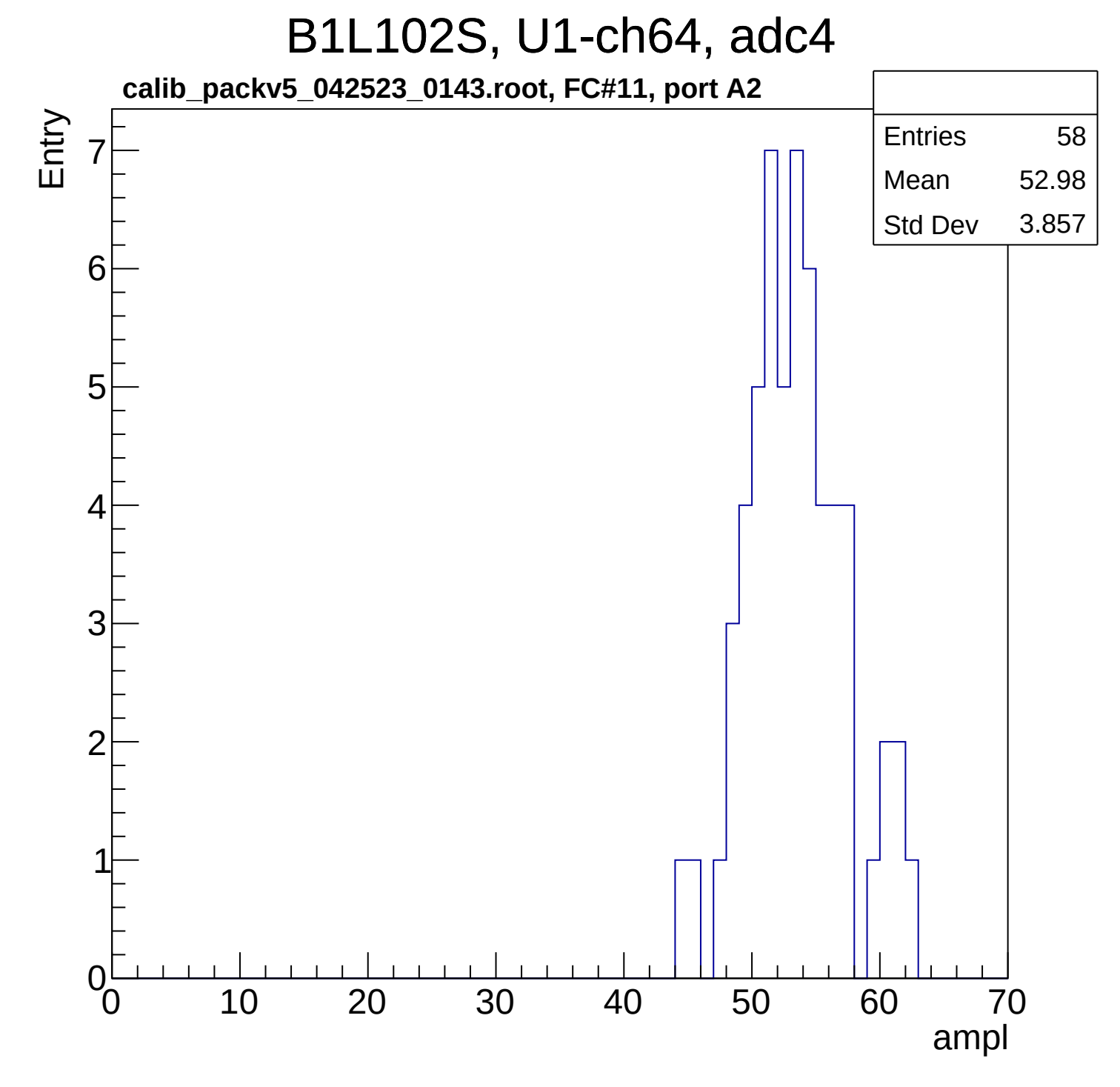
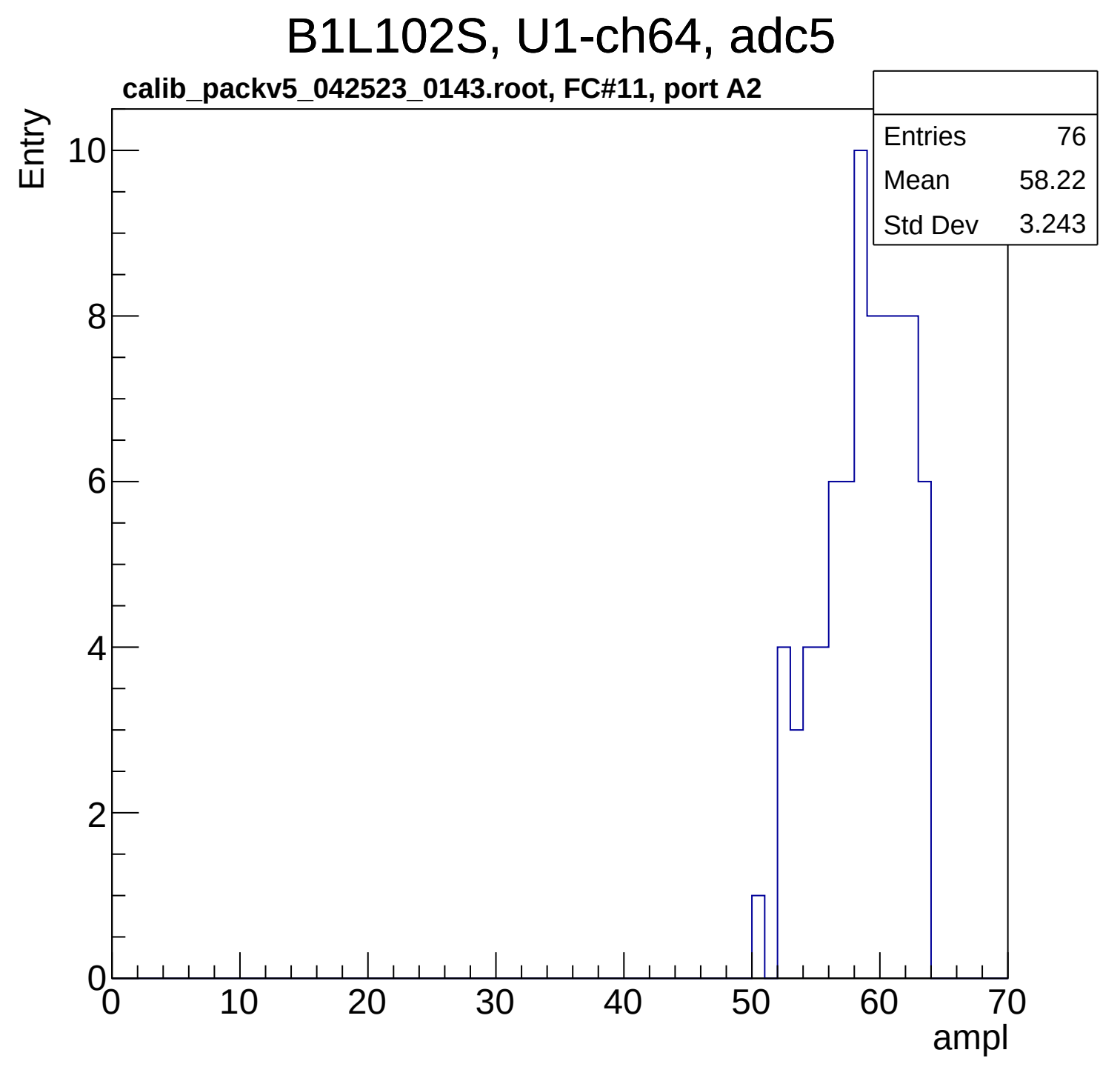
60
70


B1L102S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 52.98
Std Dev 3.857
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 58.22
Std Dev 3.243
Entry
ampl
0
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4
6
8
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0
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30
40
50
60
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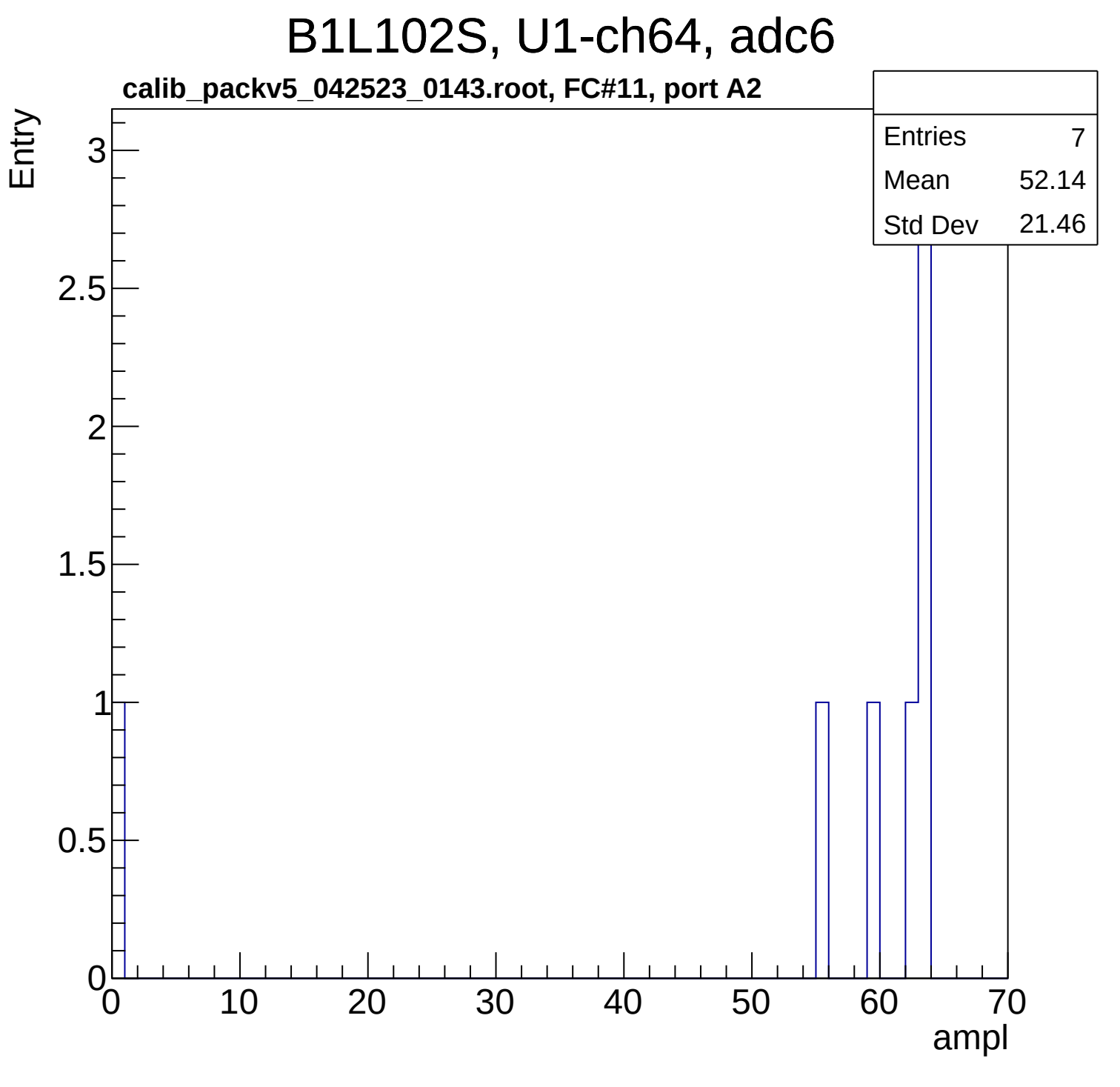
B1L102S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 52.14
Std Dev 21.46
Entry
ampl
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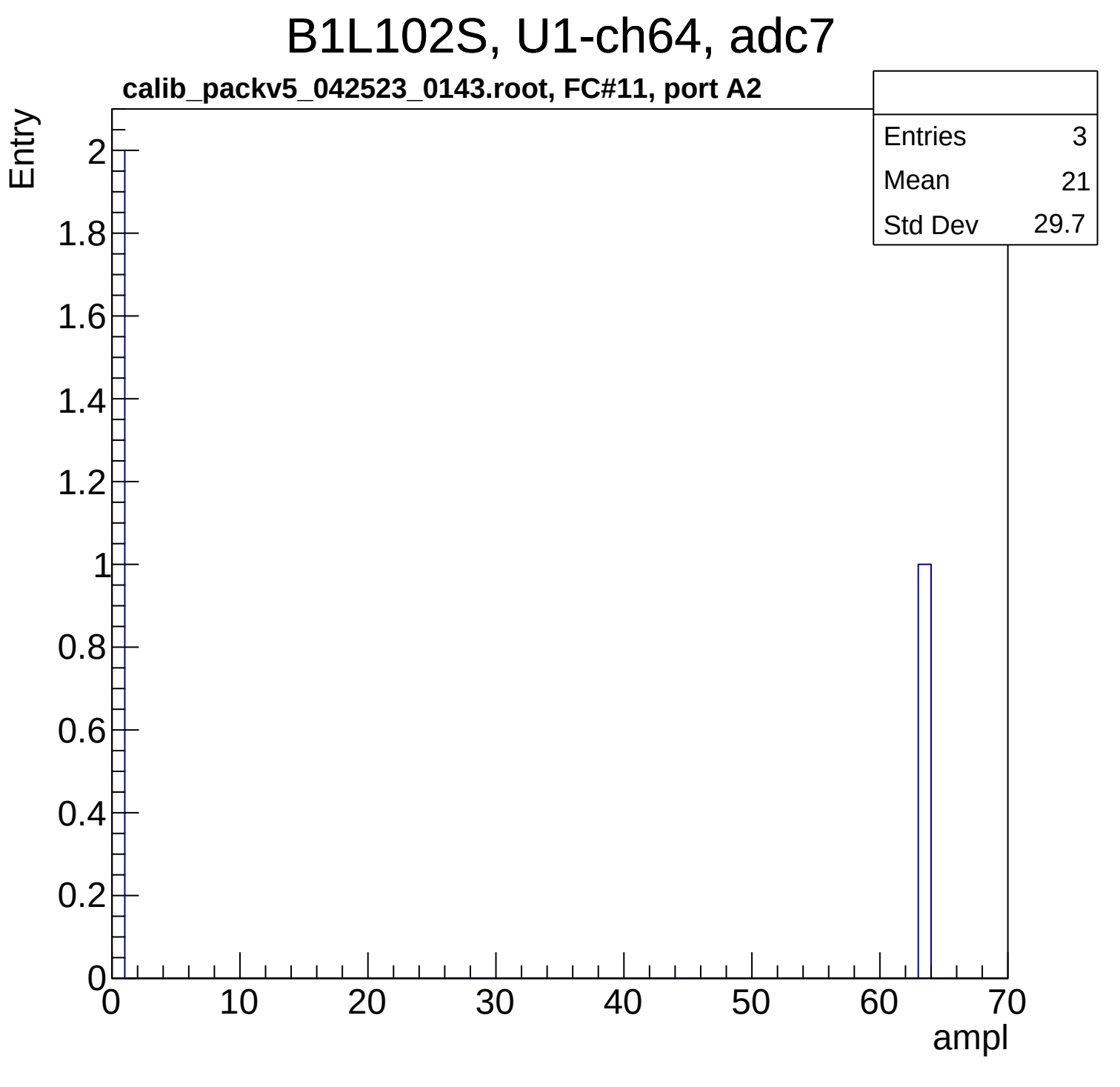
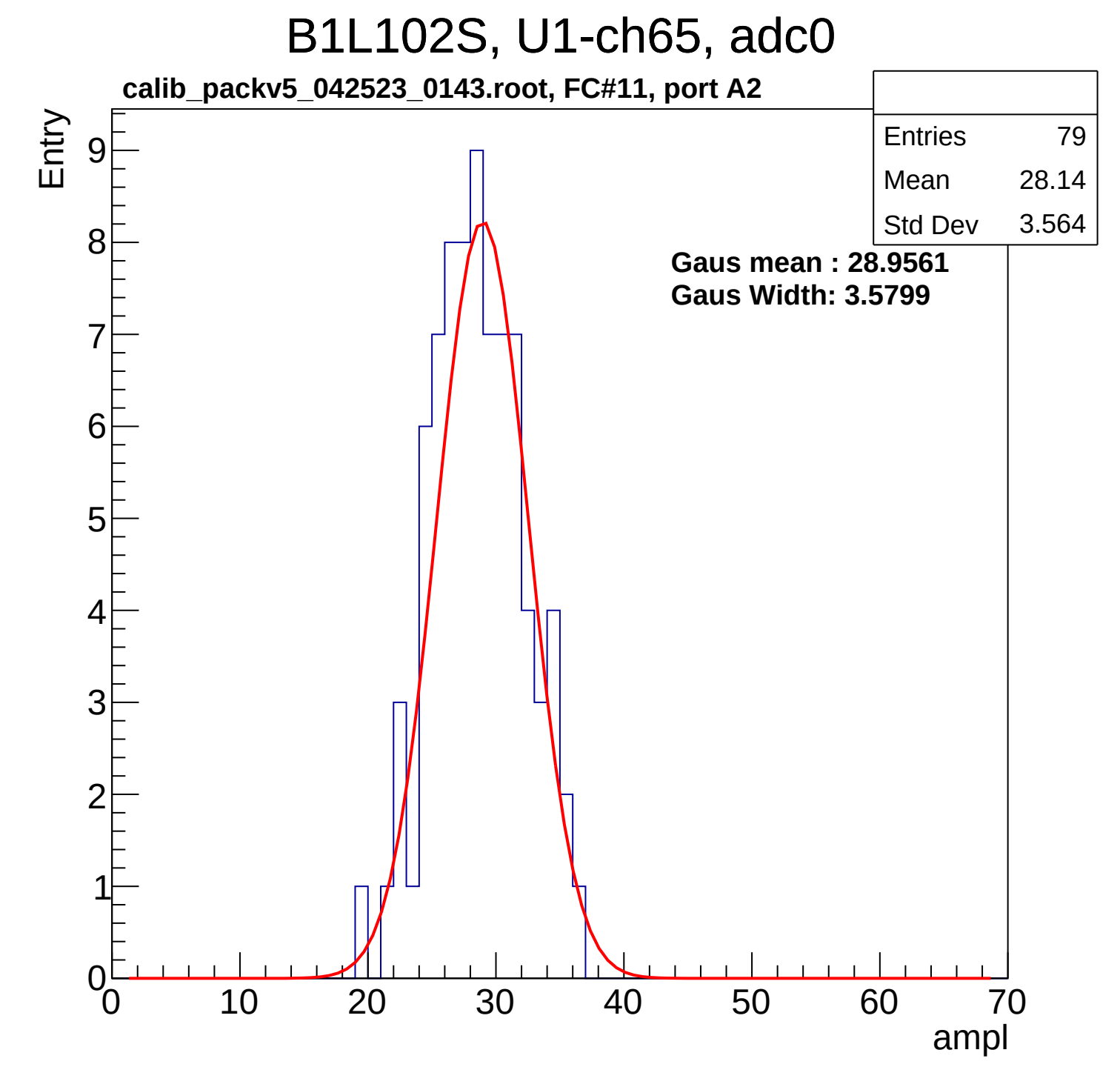
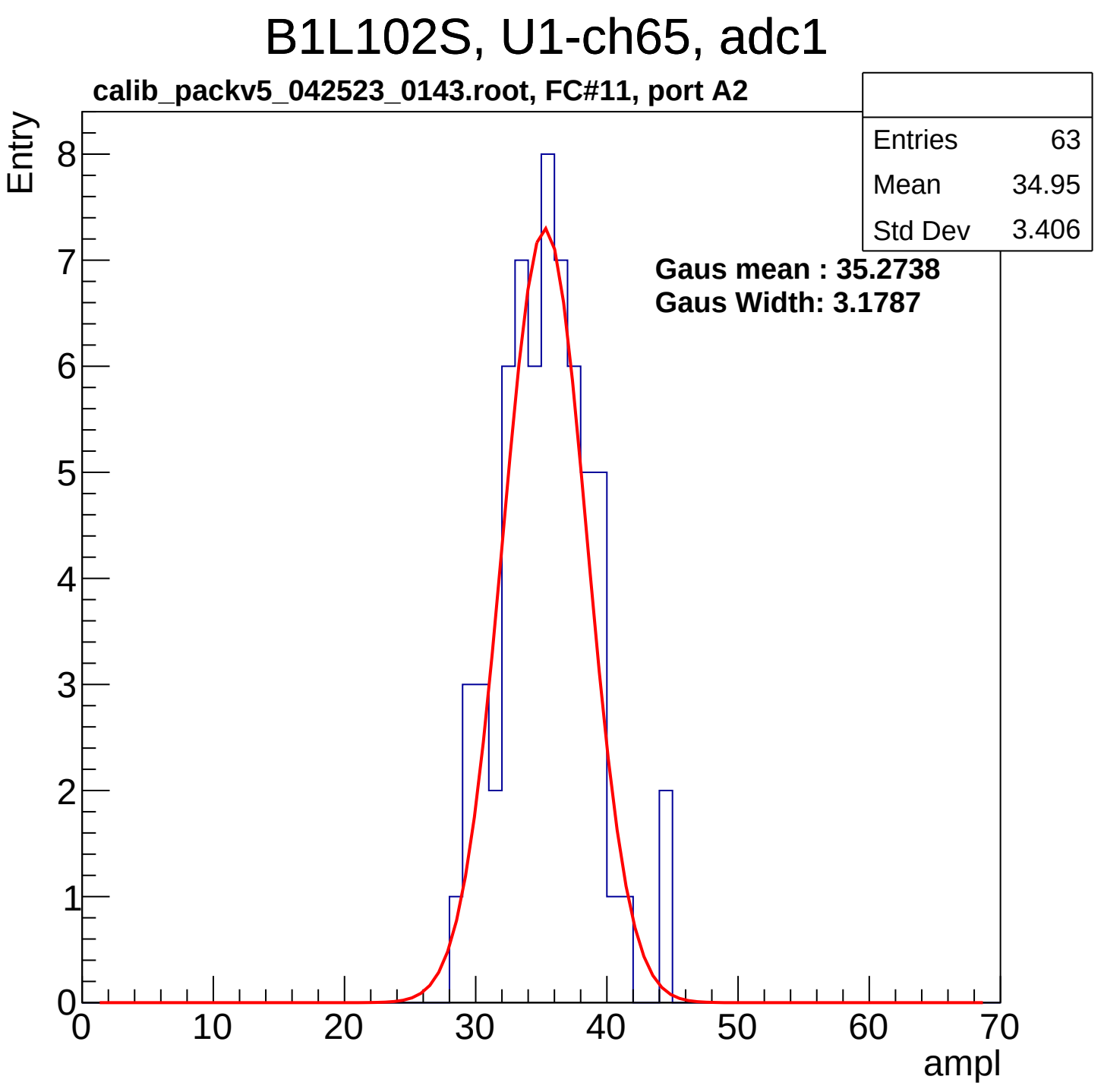
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 28.14
Std Dev 3.564
Gaus mean : 28.9561
Gaus Width: 3.5799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 34.95
Std Dev 3.406
Gaus mean : 35.2738
Gaus Width: 3.1787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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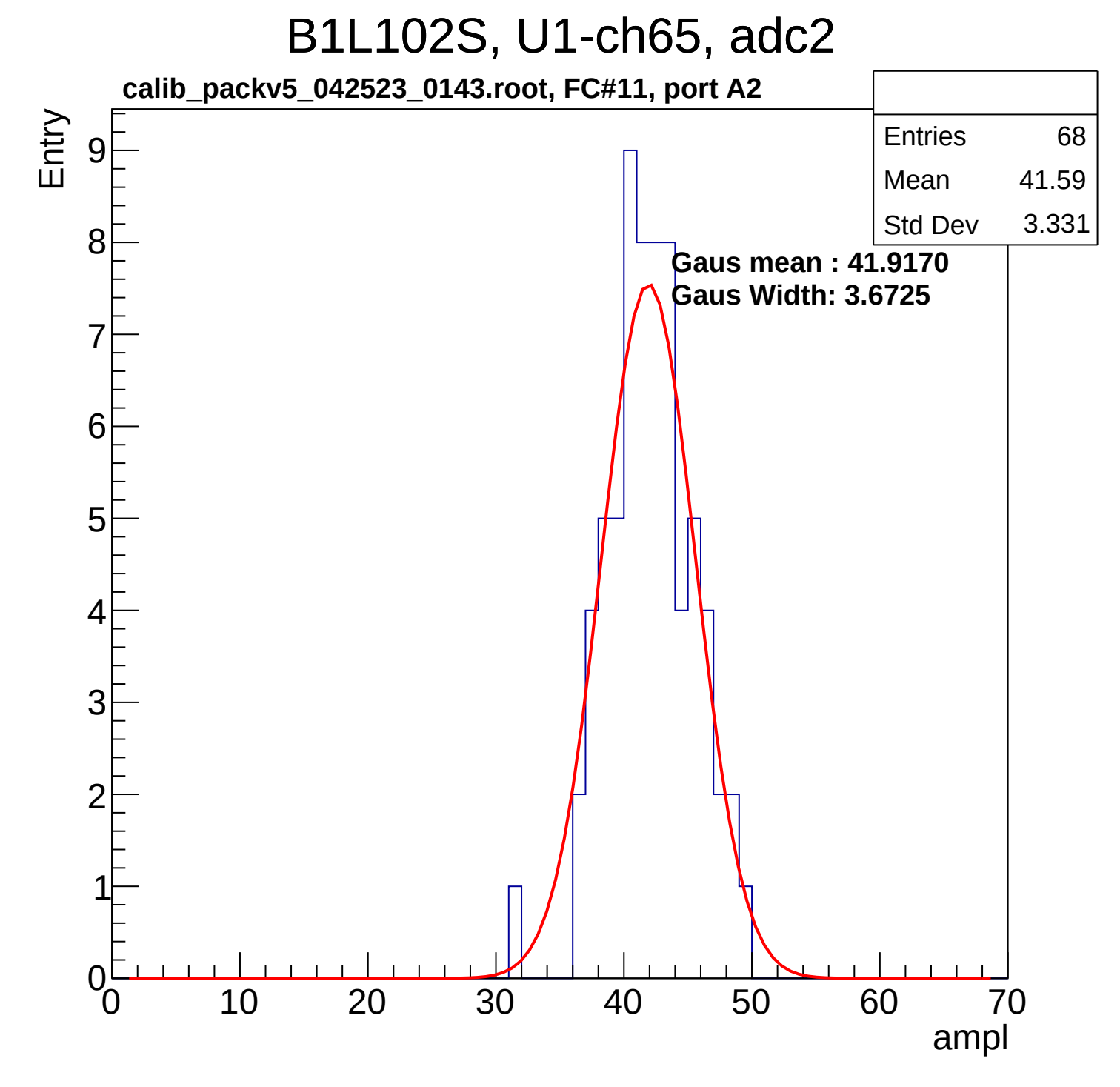
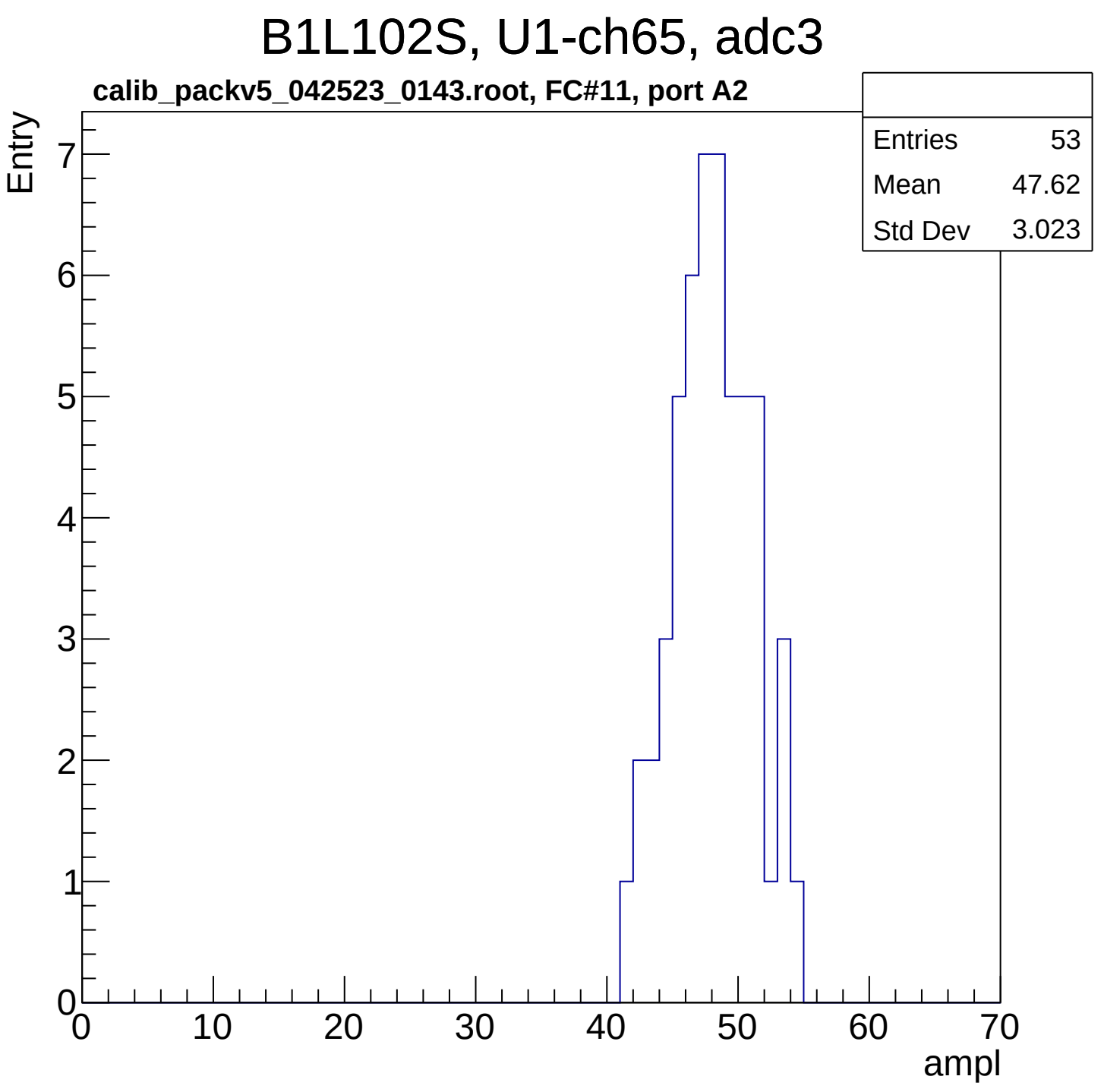
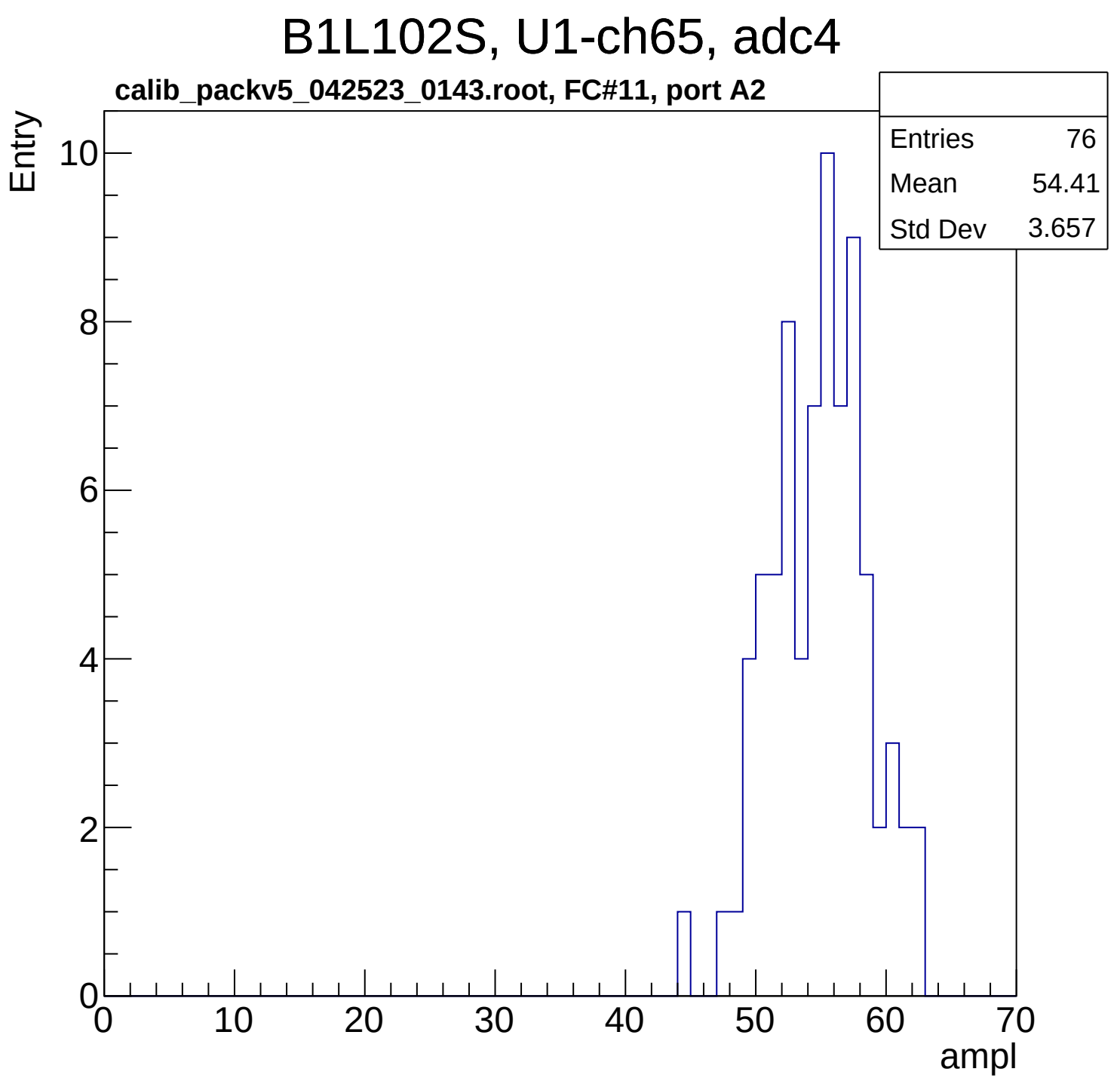

B1L102S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 41.59
Std Dev 3.331
Gaus mean : 41.9170
Gaus Width: 3.6725
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 47.62
Std Dev 3.023
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 54.41
Std Dev 3.657
Entry
ampl
0
2
4
6
8
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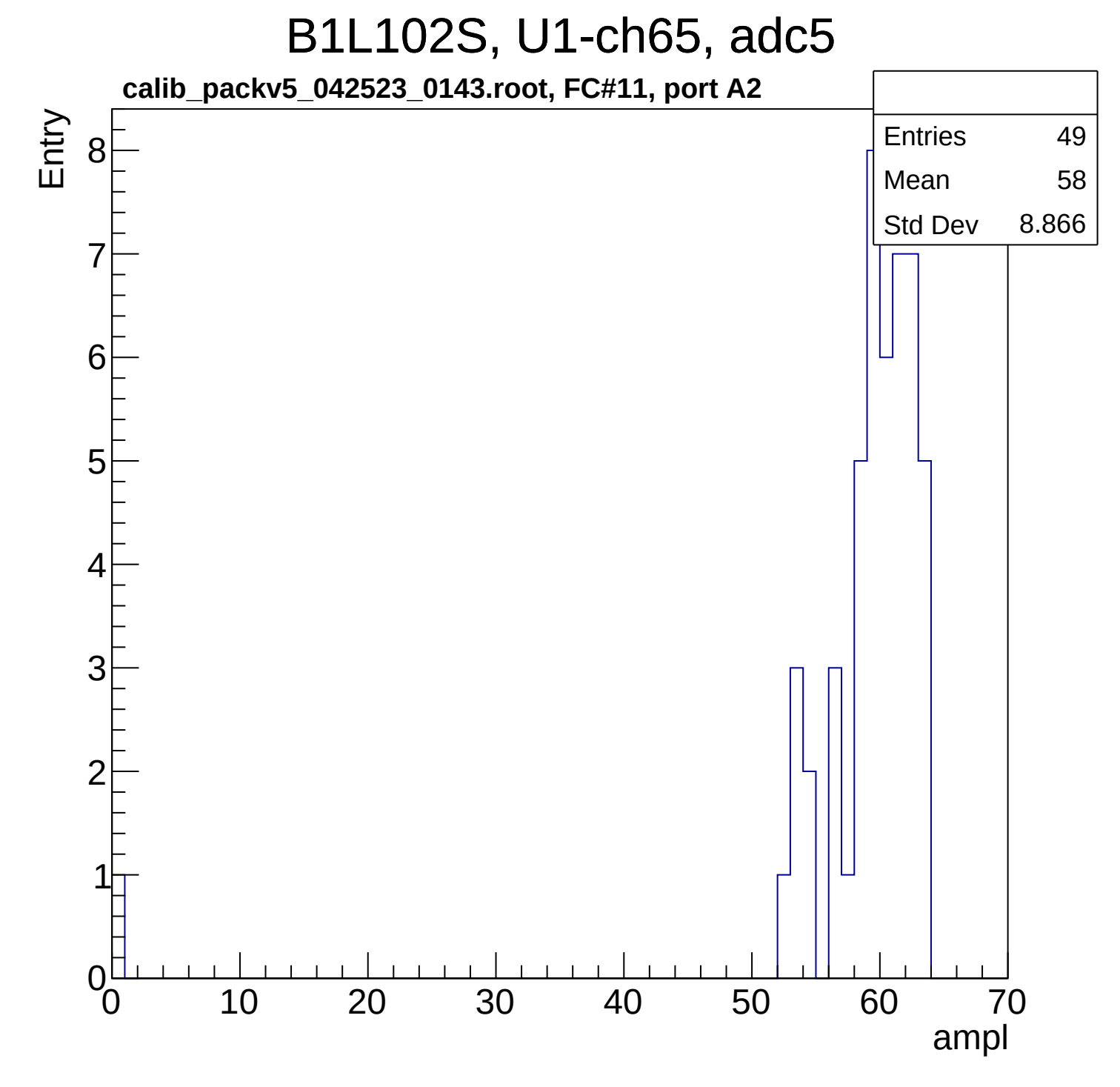
0
10
20
30
40
50
60
70


B1L102S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58
Std Dev 8.866
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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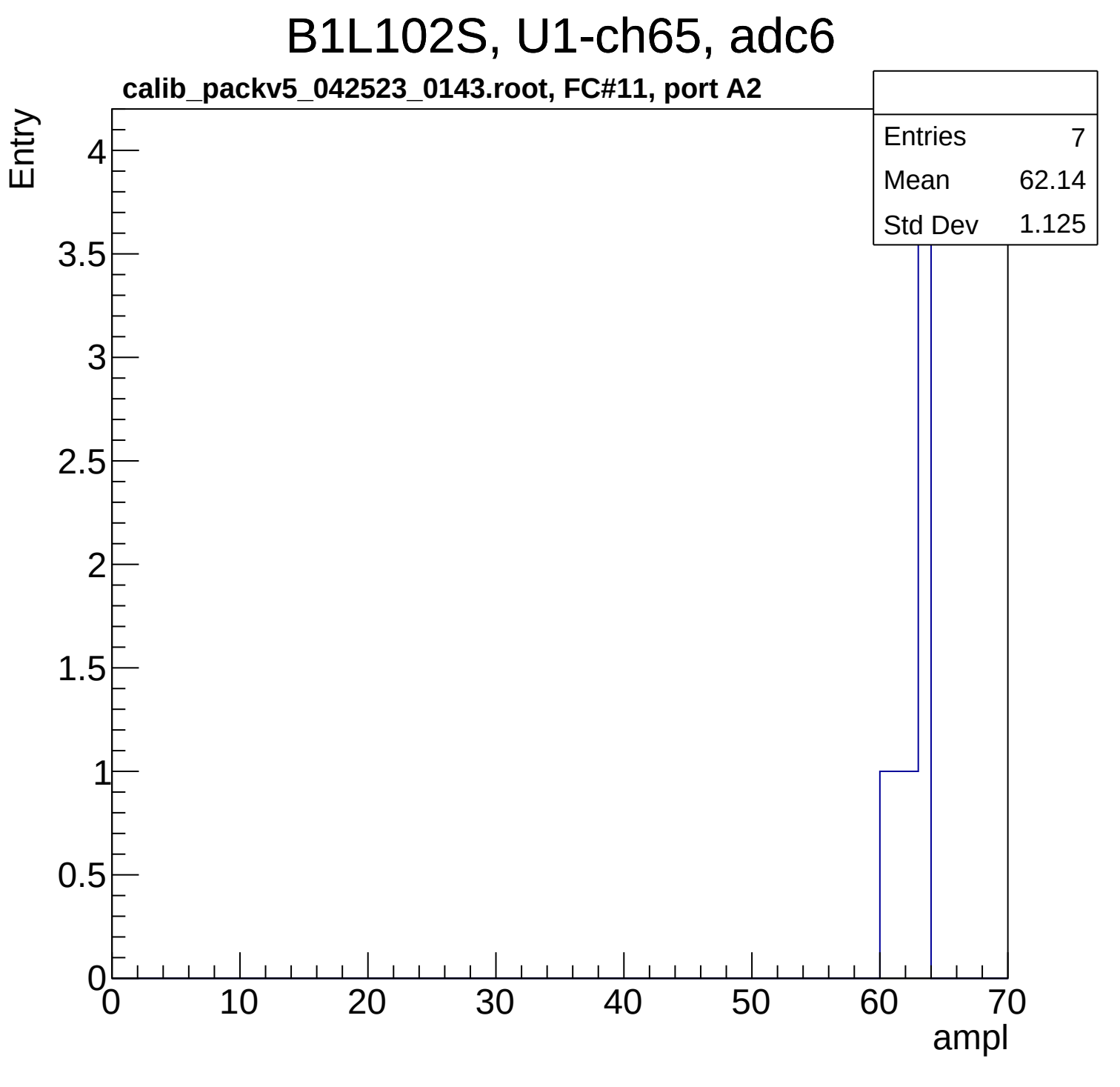
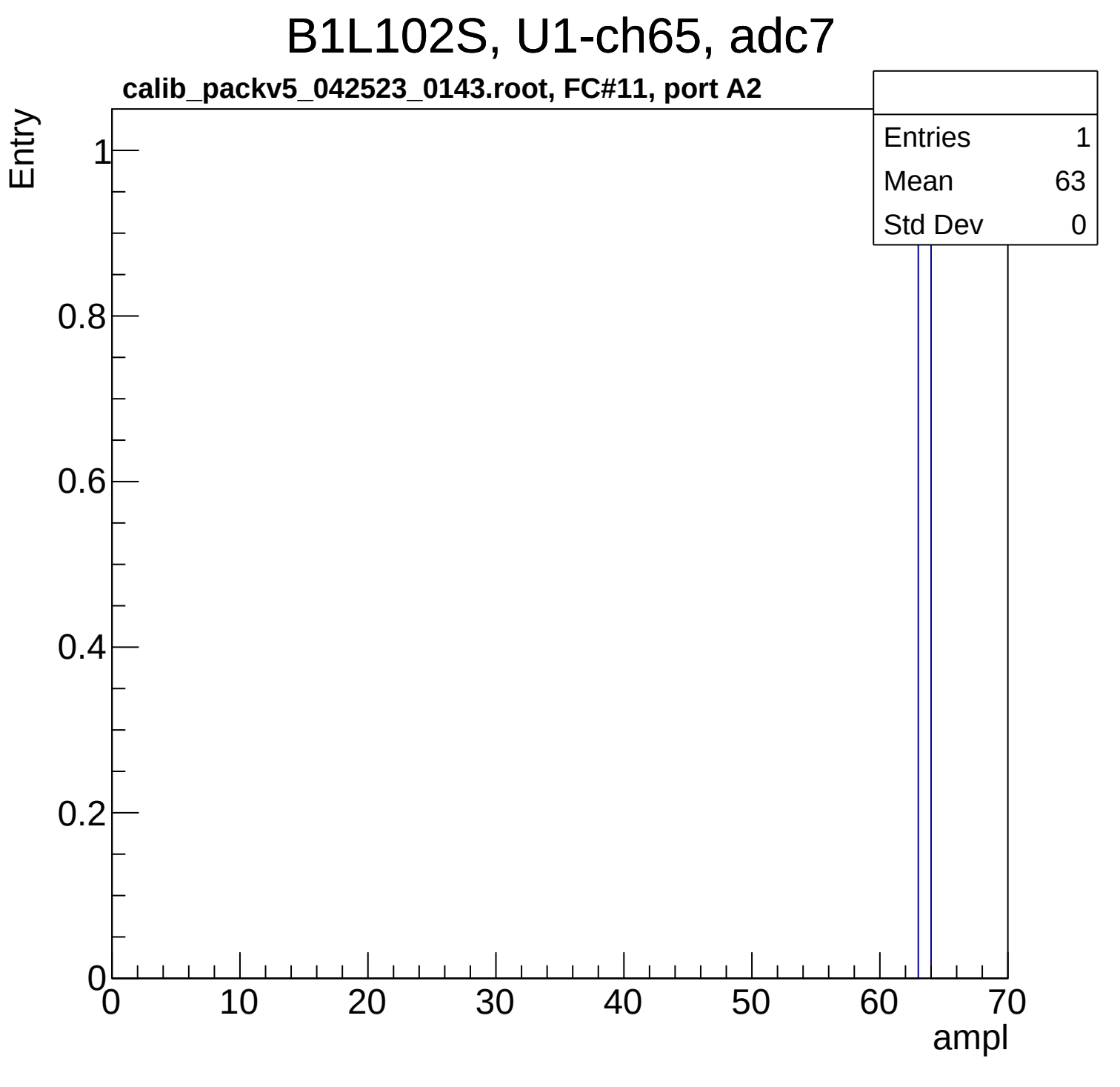

B1L102S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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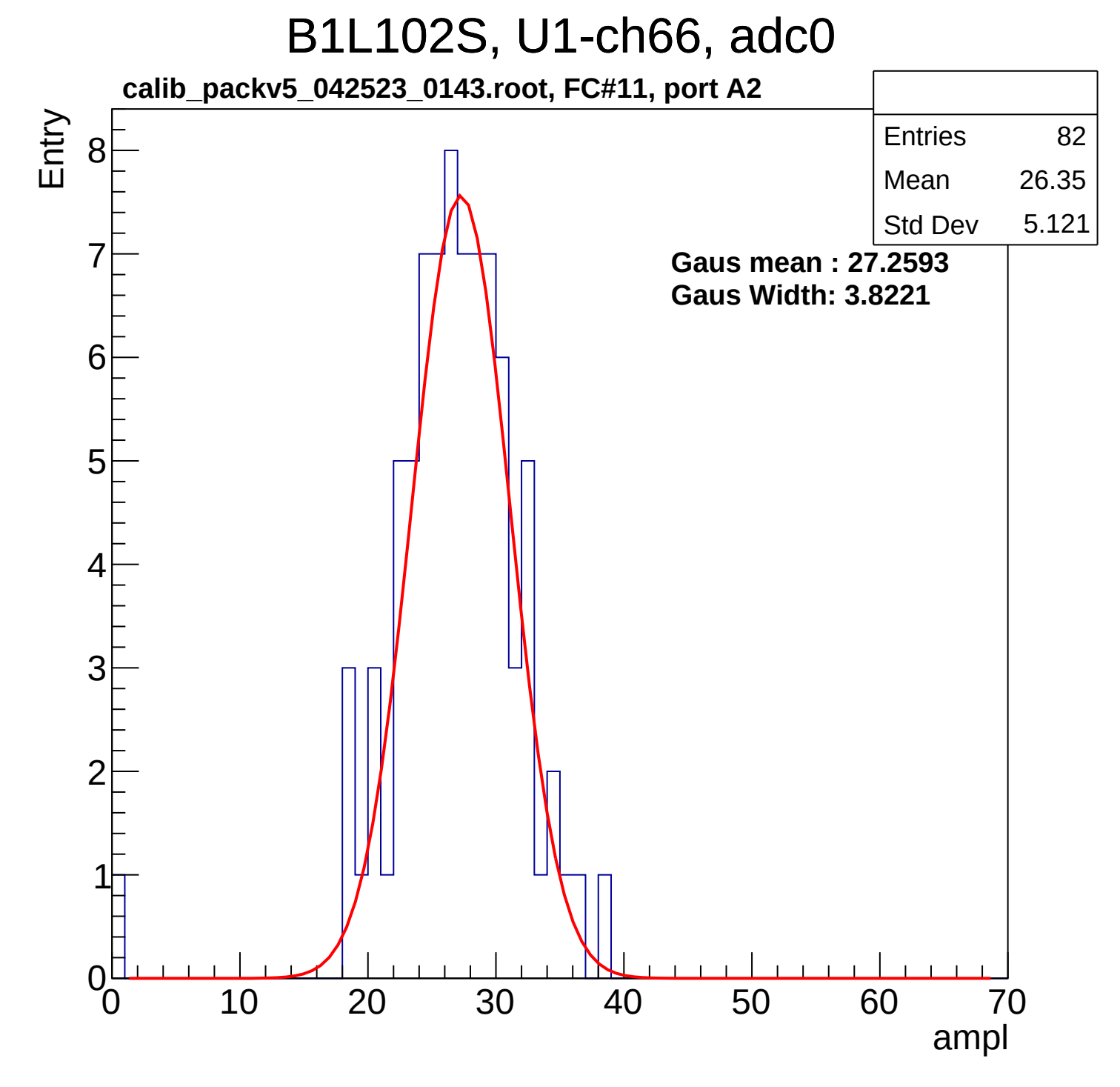
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 26.35
Std Dev 5.121
Gaus mean : 27.2593
Gaus Width: 3.8221
Entry
ampl
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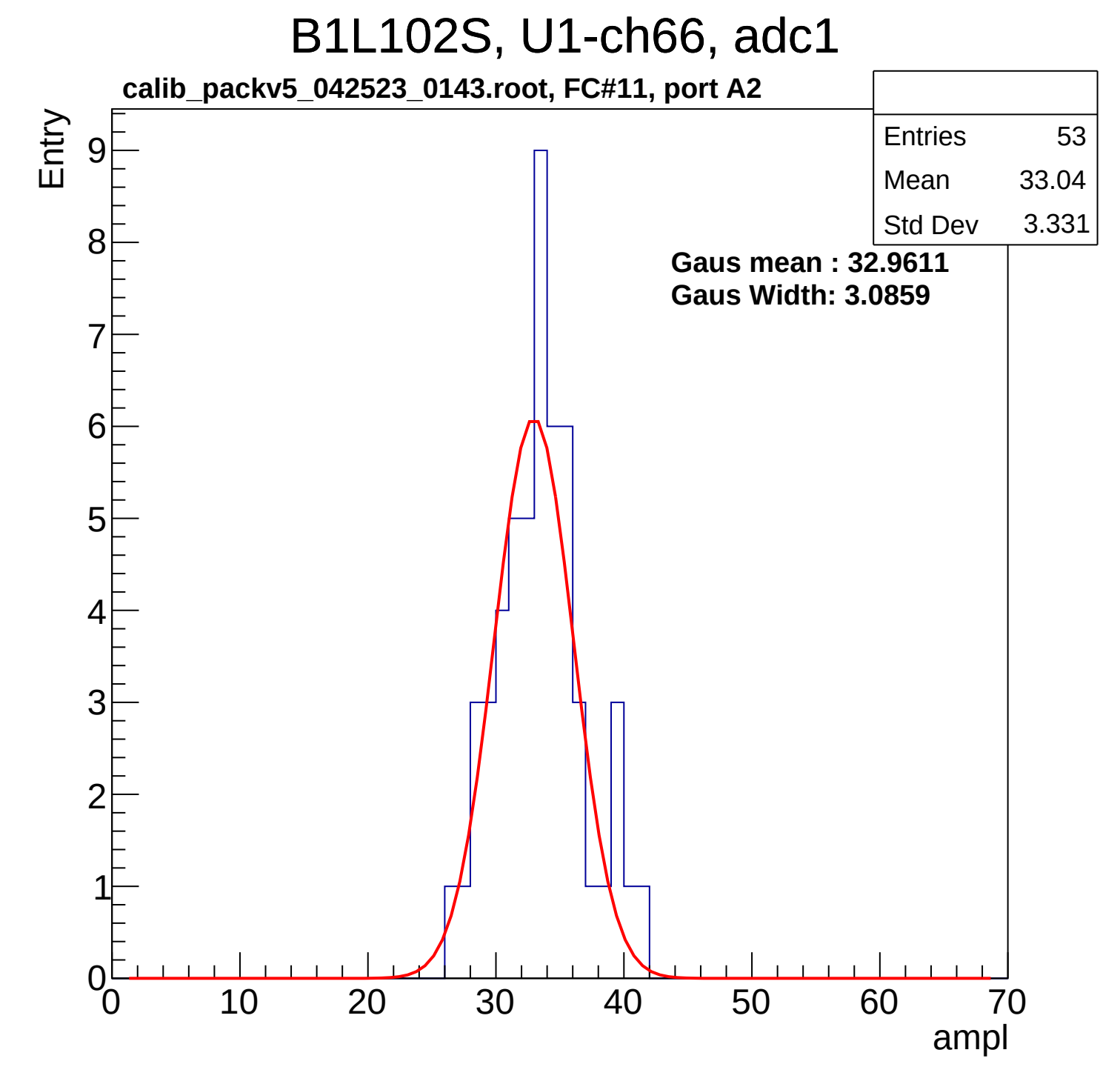
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 33.04
Std Dev 3.331
Gaus mean : 32.9611
Gaus Width: 3.0859
Entry
ampl
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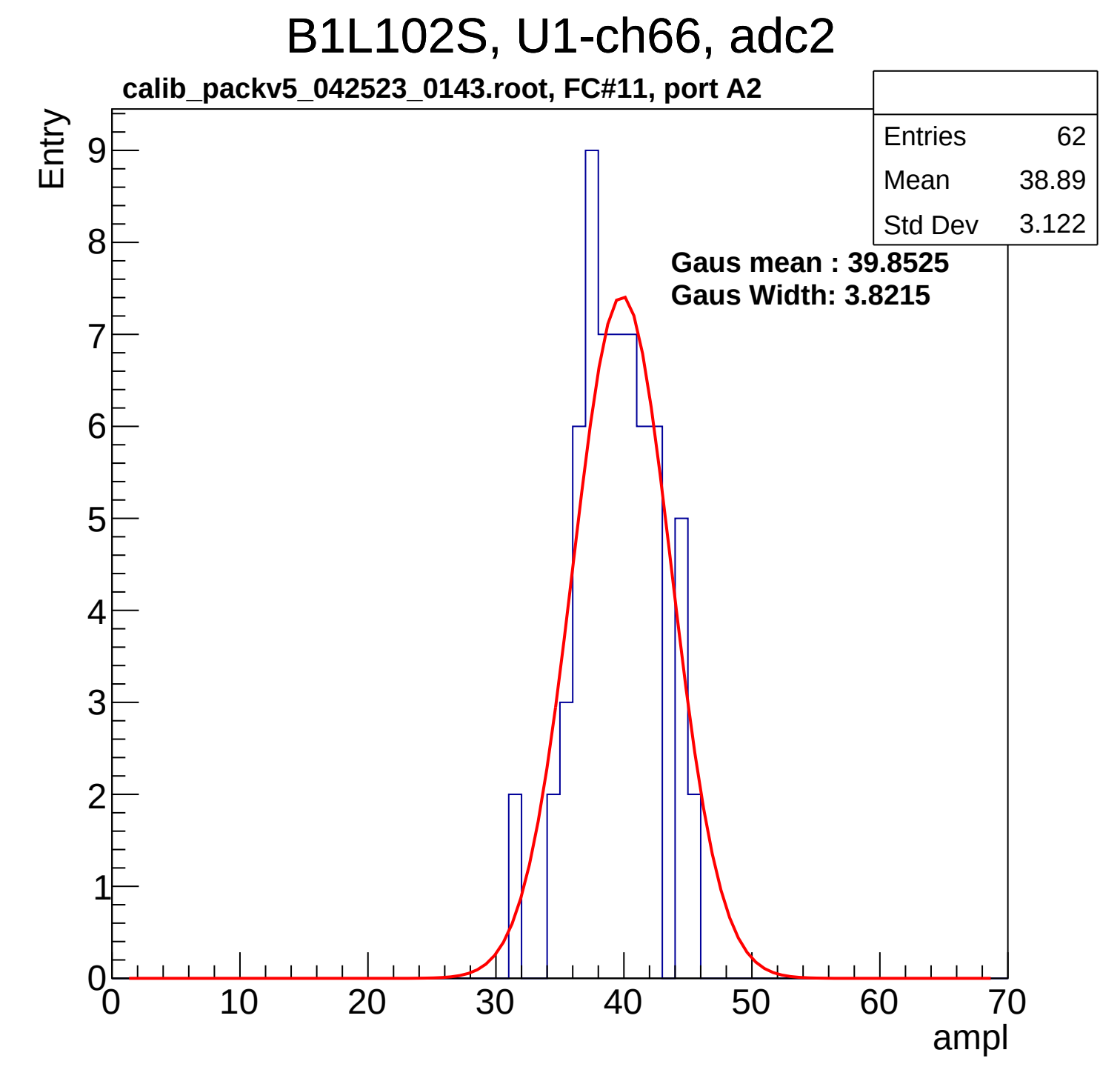
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 38.89
Std Dev 3.122
Gaus mean : 39.8525
Gaus Width: 3.8215
Entry
ampl
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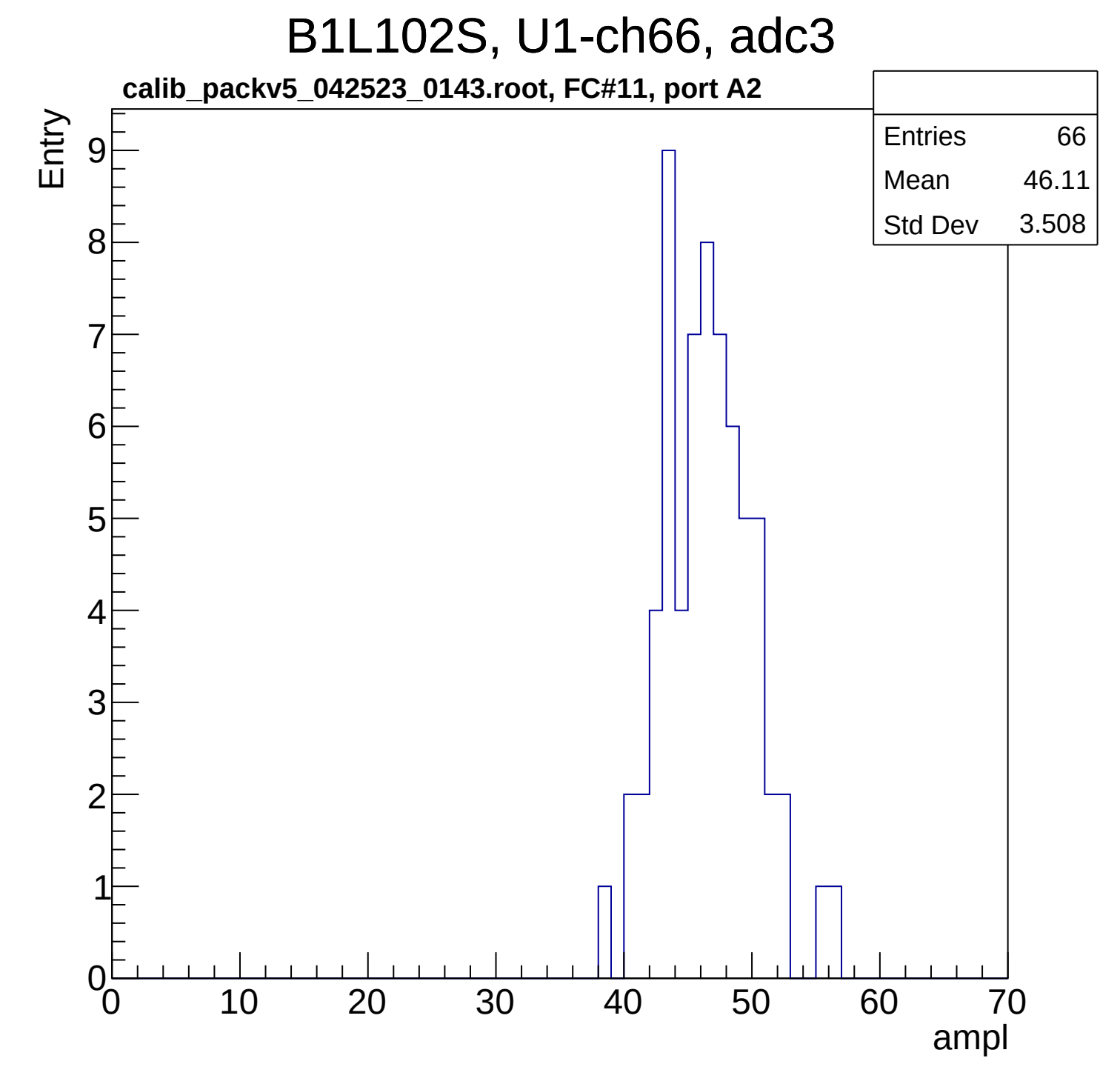
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 46.11
Std Dev 3.508
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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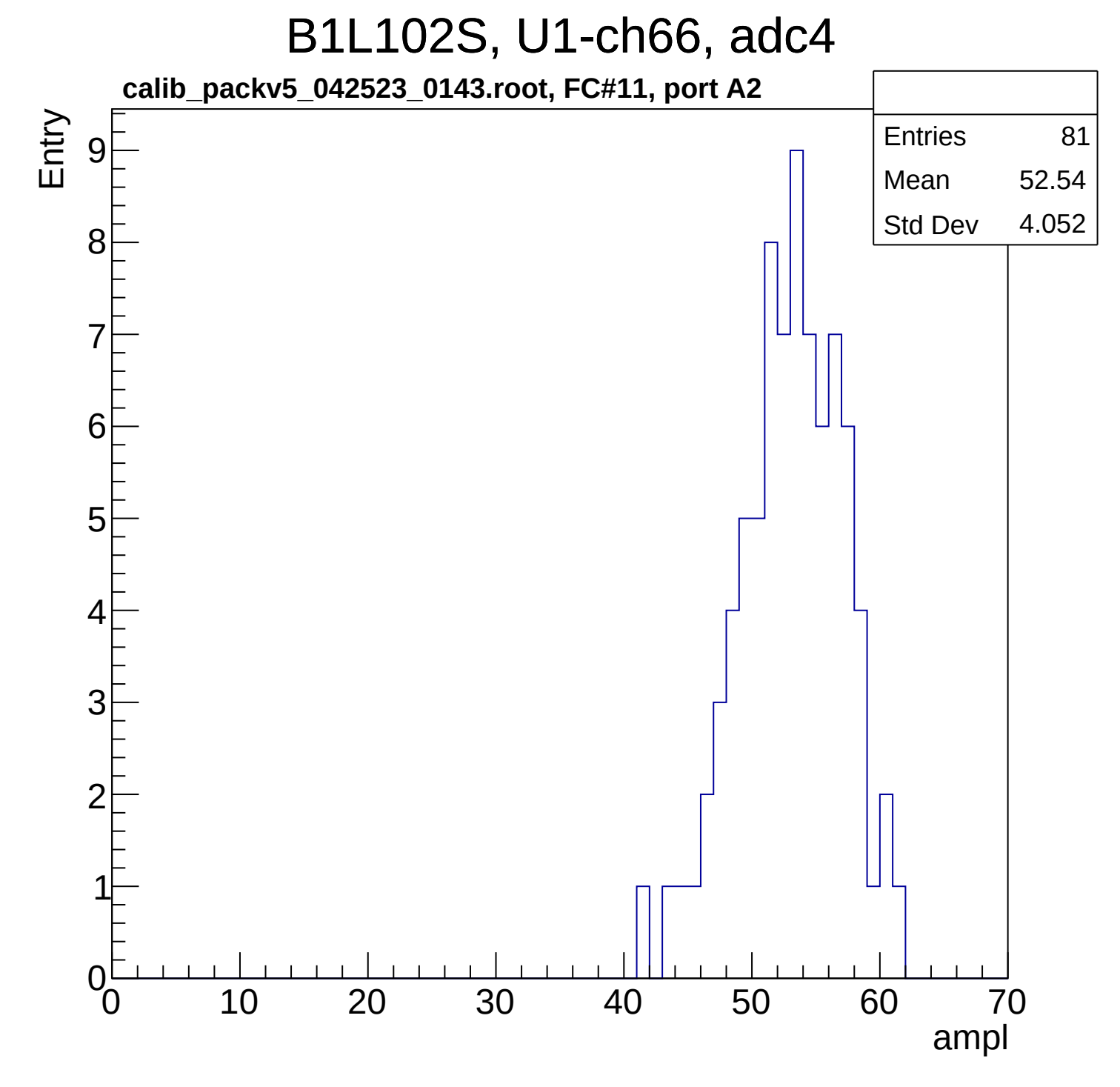
B1L102S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 52.54
Std Dev 4.052
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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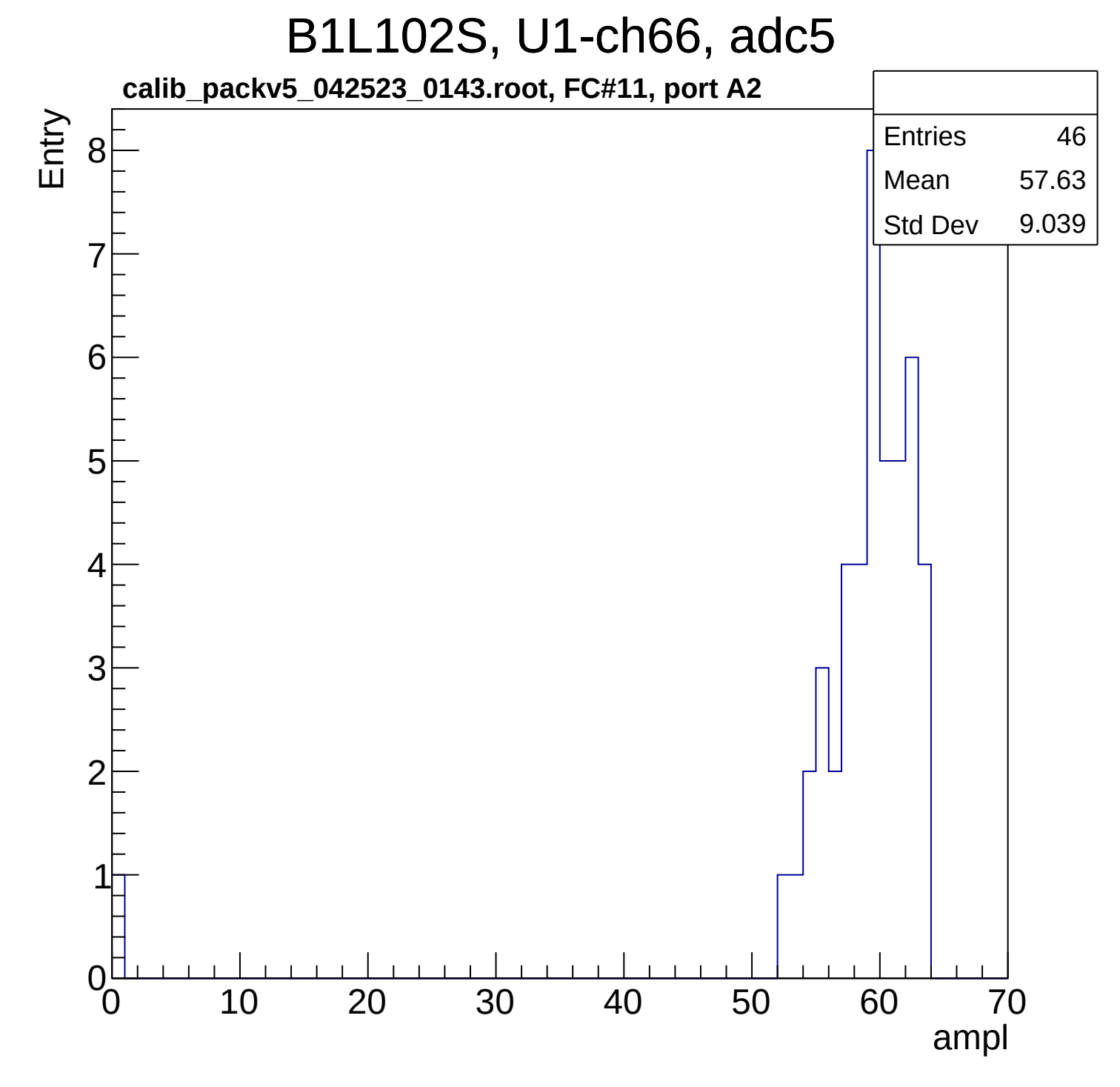
B1L102S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 57.63
Std Dev 9.039
Entry
ampl
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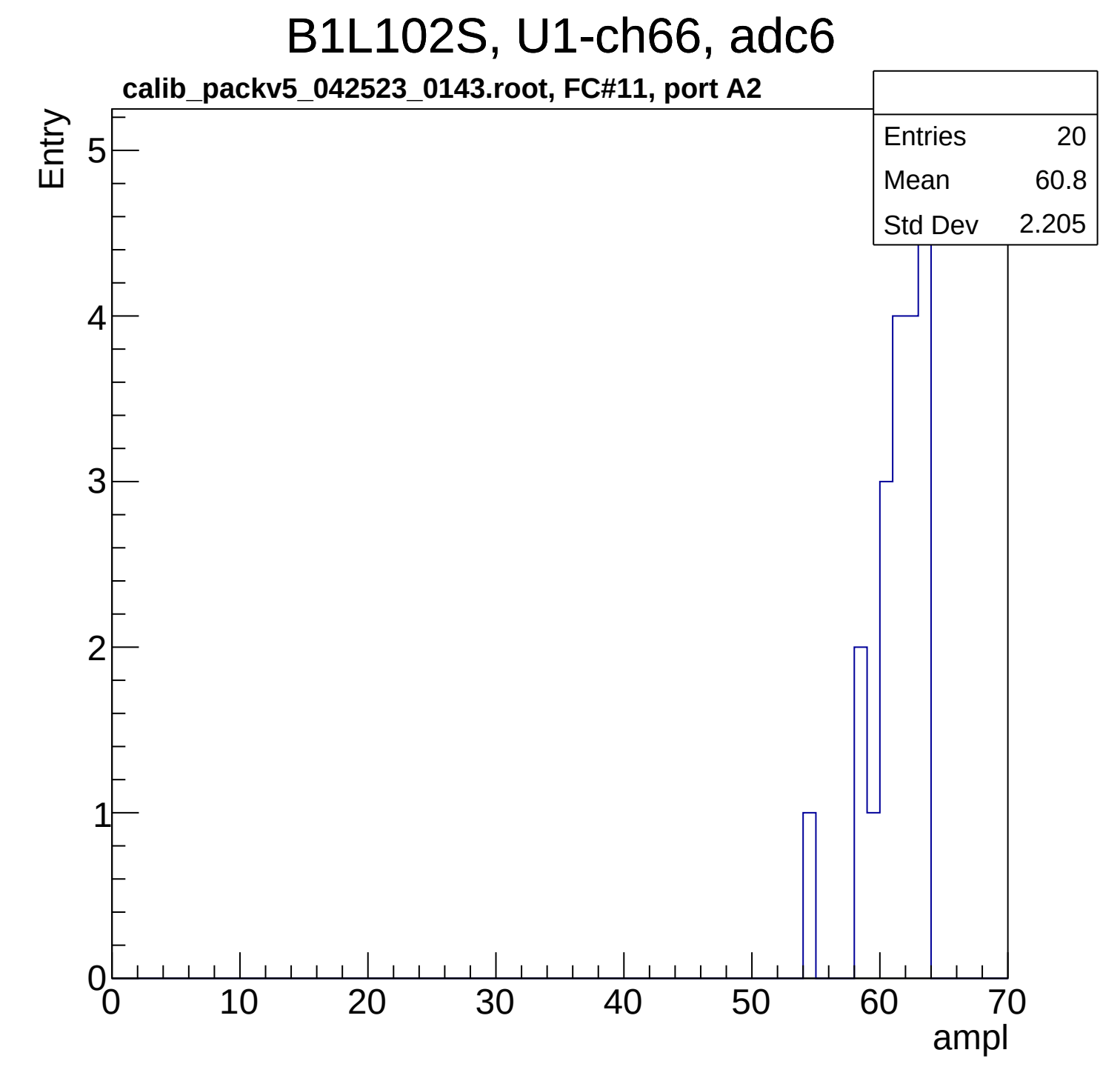
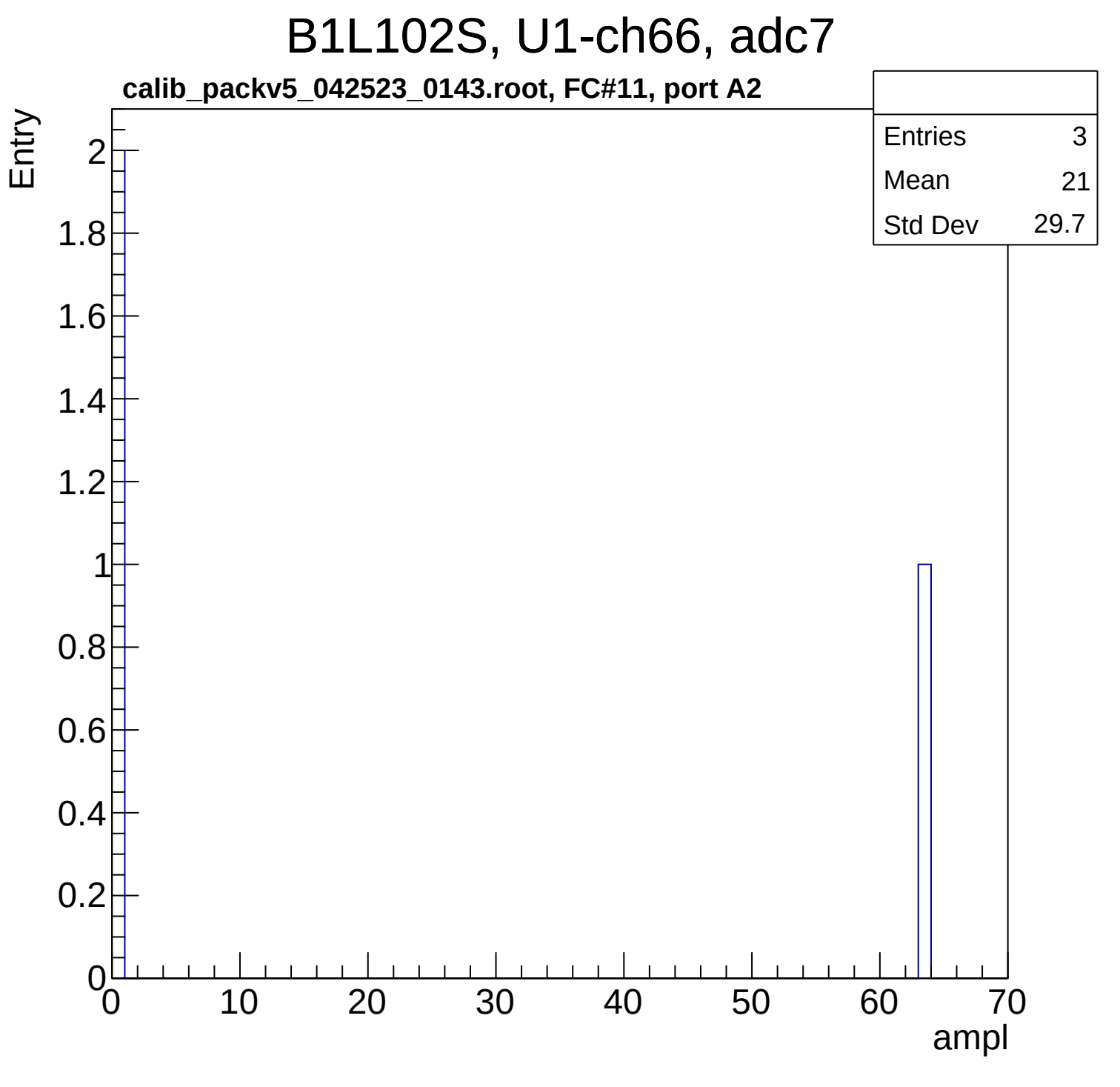
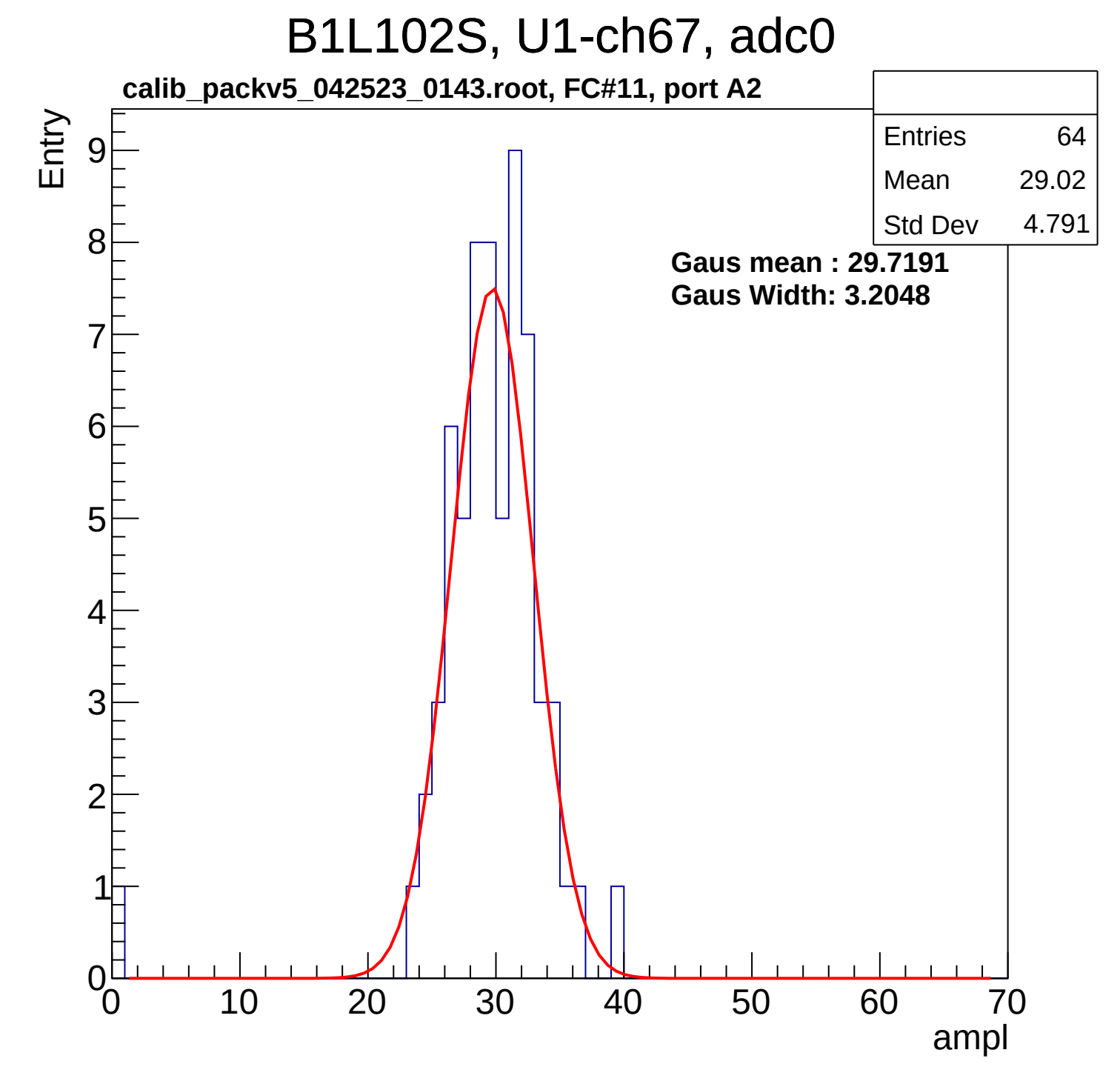
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 20
Mean 60.8
Std Dev 2.205
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 29.02
Std Dev 4.791
Gaus mean : 29.7191
Gaus Width: 3.2048
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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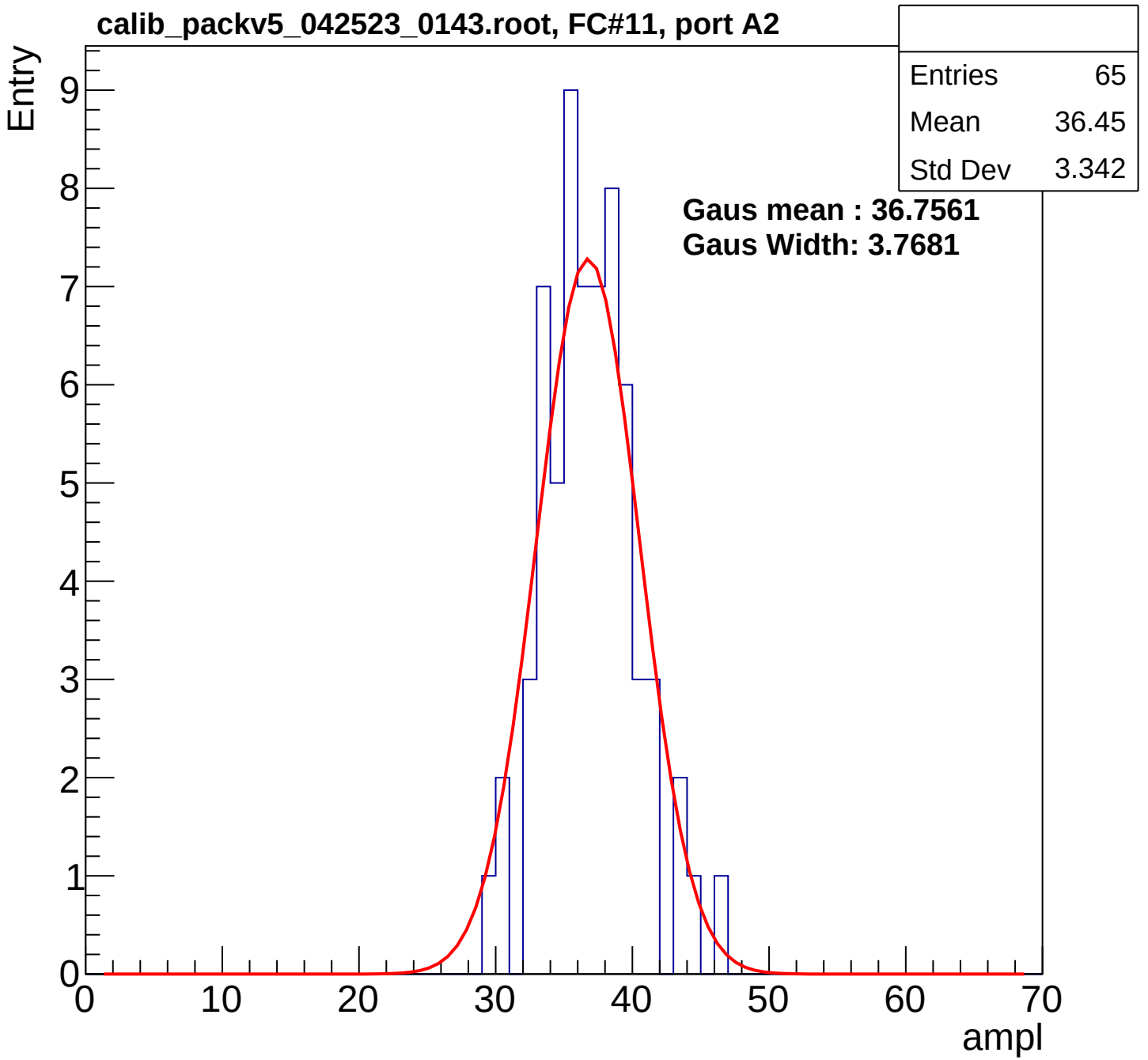
60
70


B1L102S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 36.45
Std Dev 3.342
Gaus mean : 36.7561
Gaus Width: 3.7681
Entry
ampl
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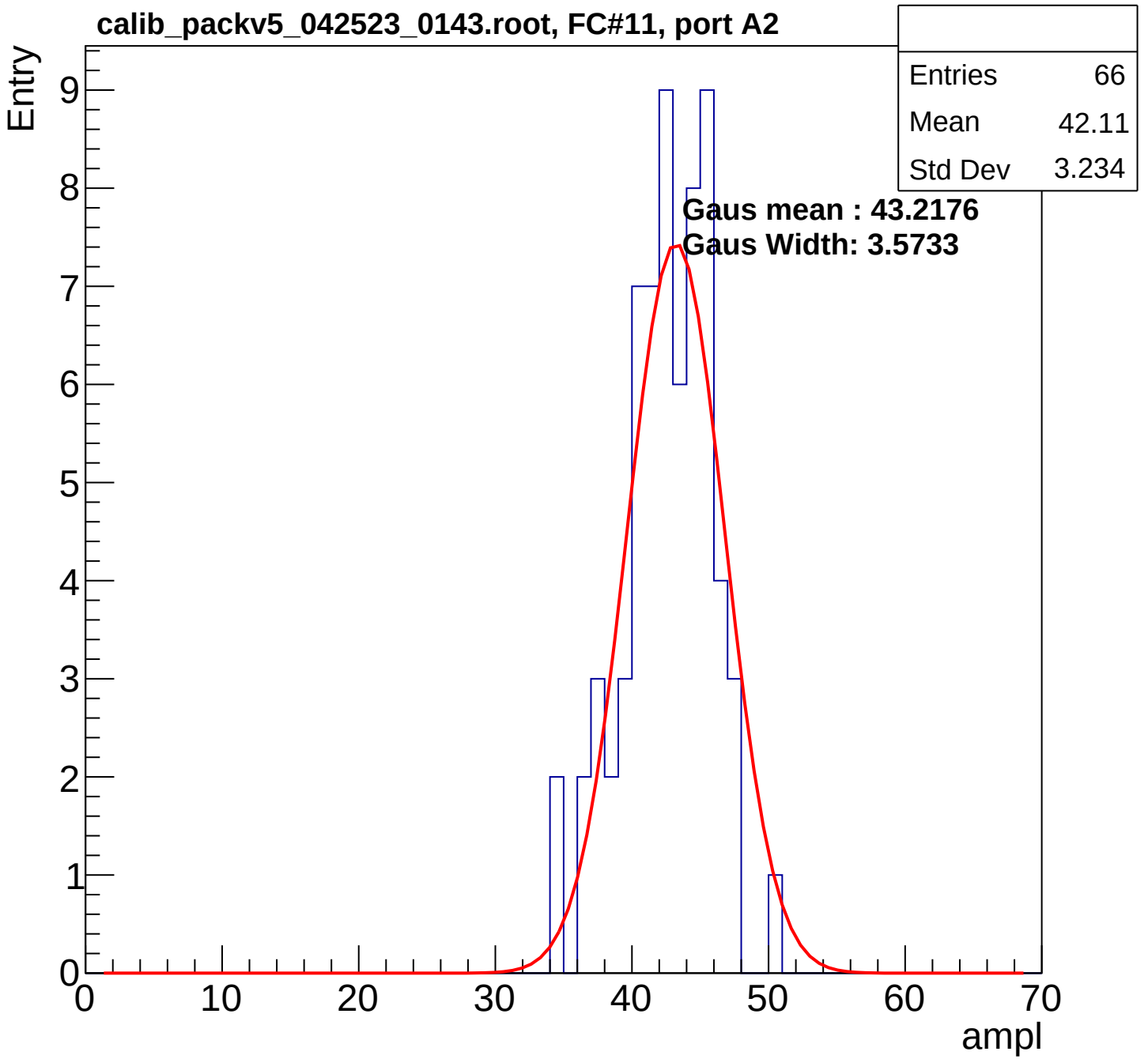
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 42.11
Std Dev 3.234
Gaus mean : 43.2176
Gaus Width: 3.5733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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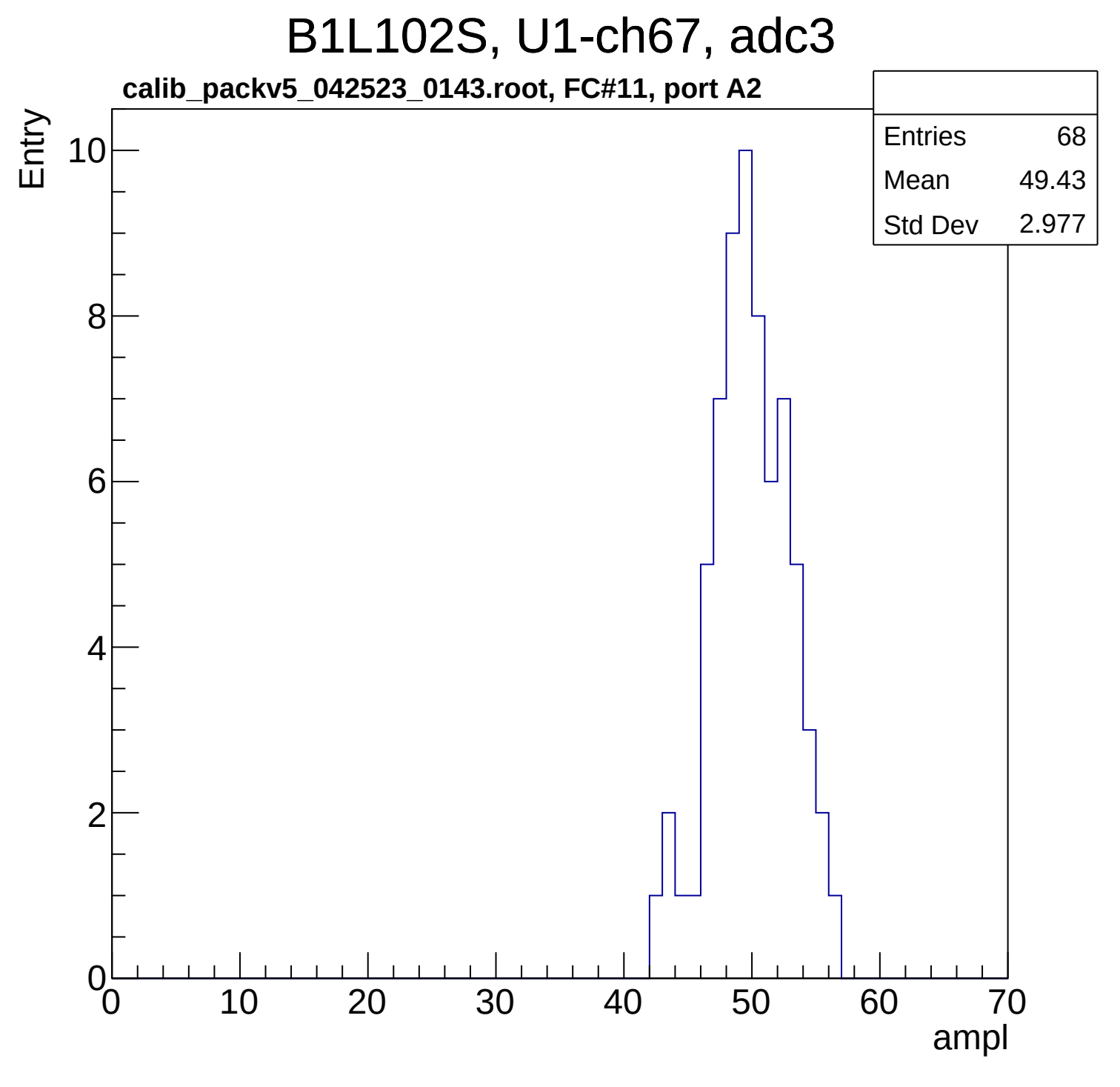
60
70


B1L102S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 49.43
Std Dev 2.977
Entry
ampl
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2
4
6
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0
10
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30
40
50
60
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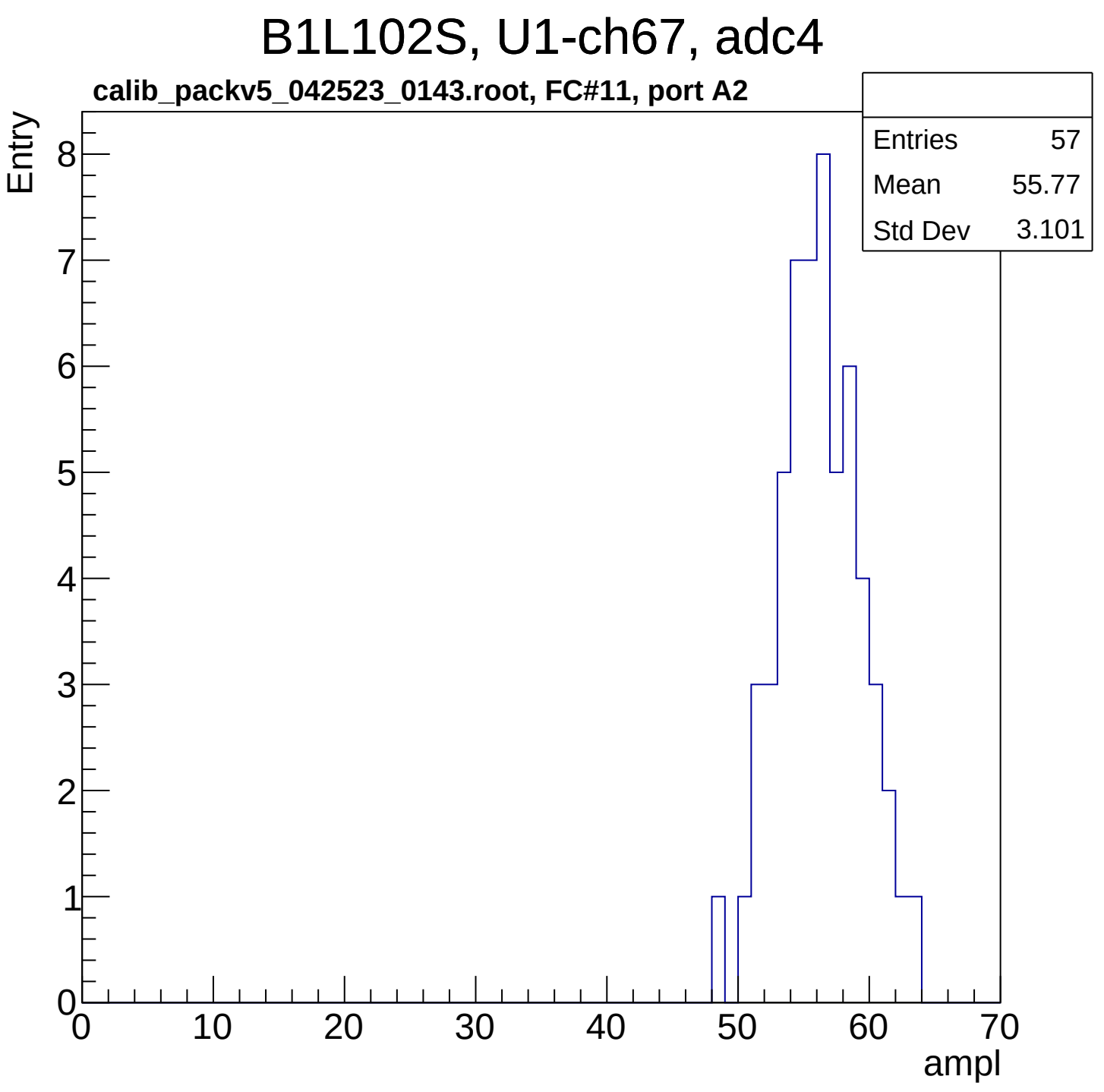
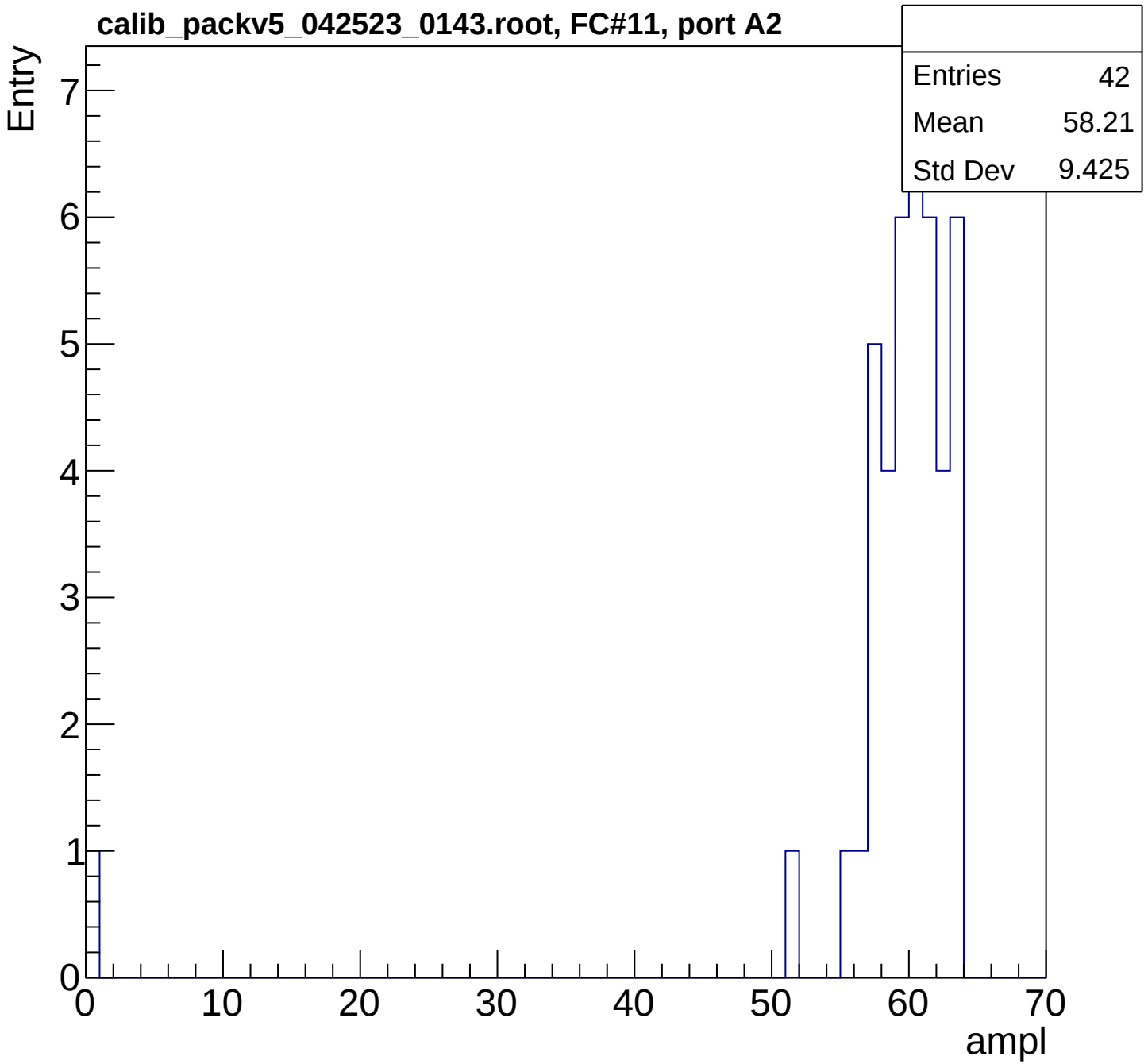
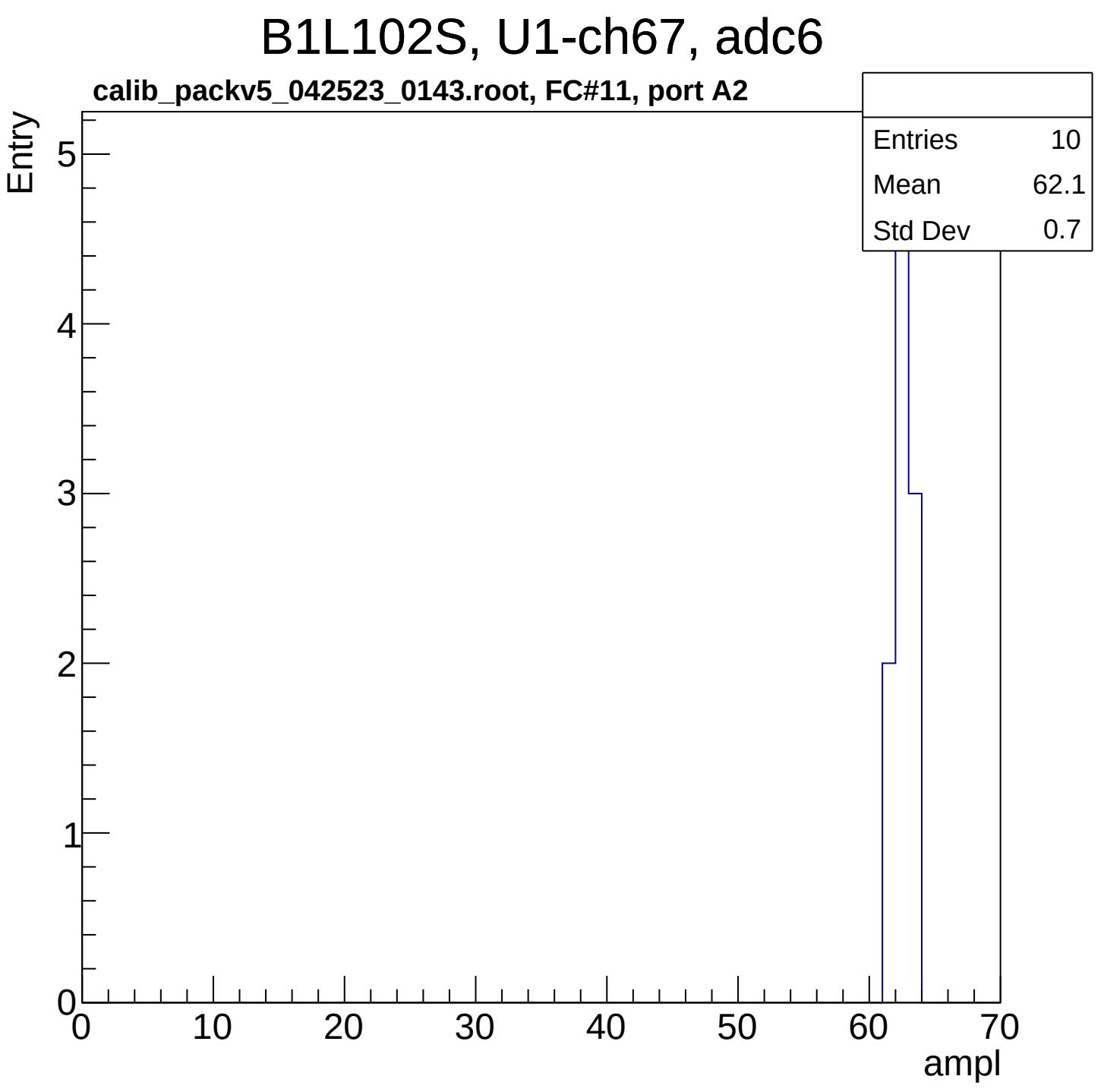
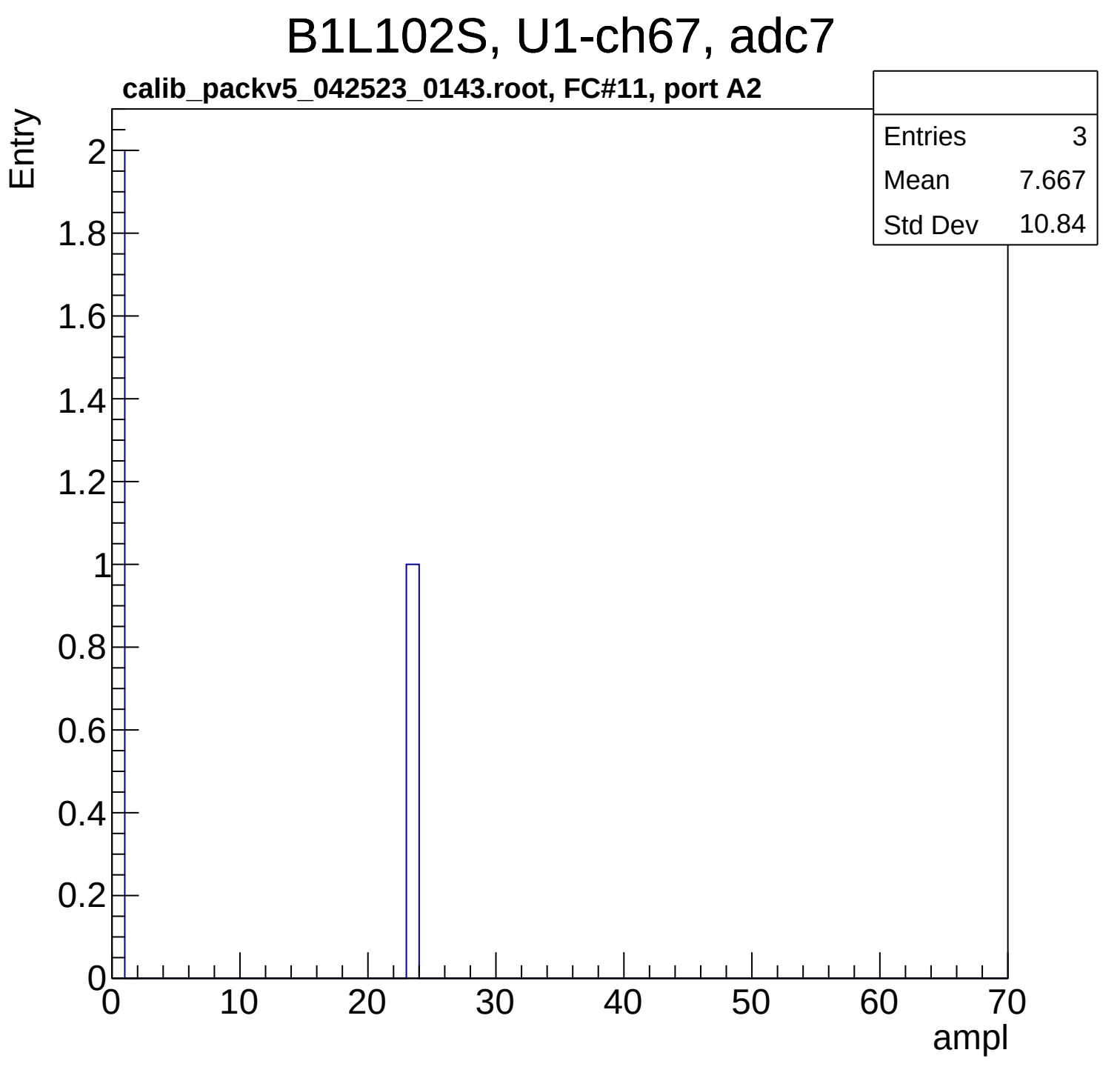
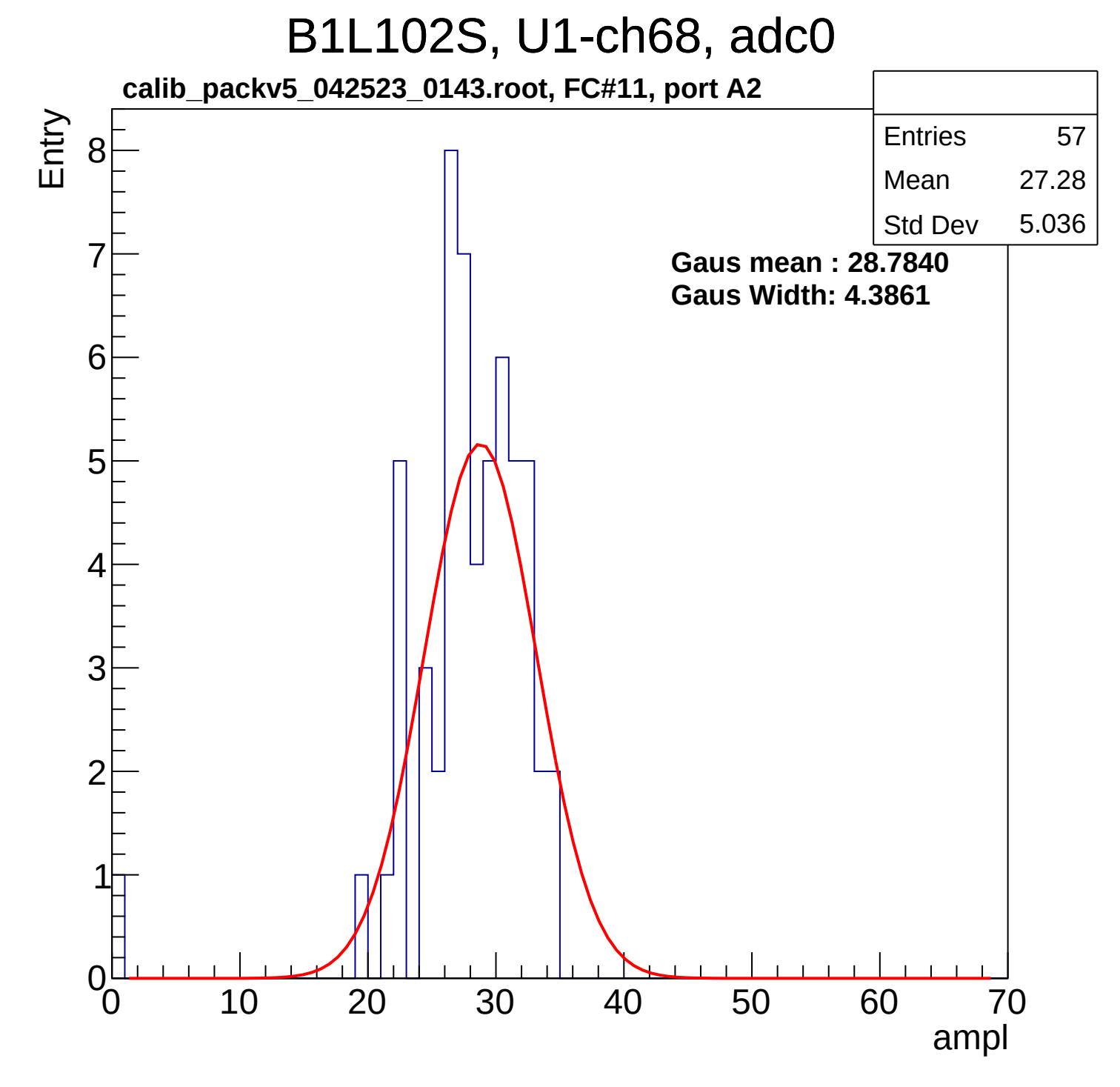
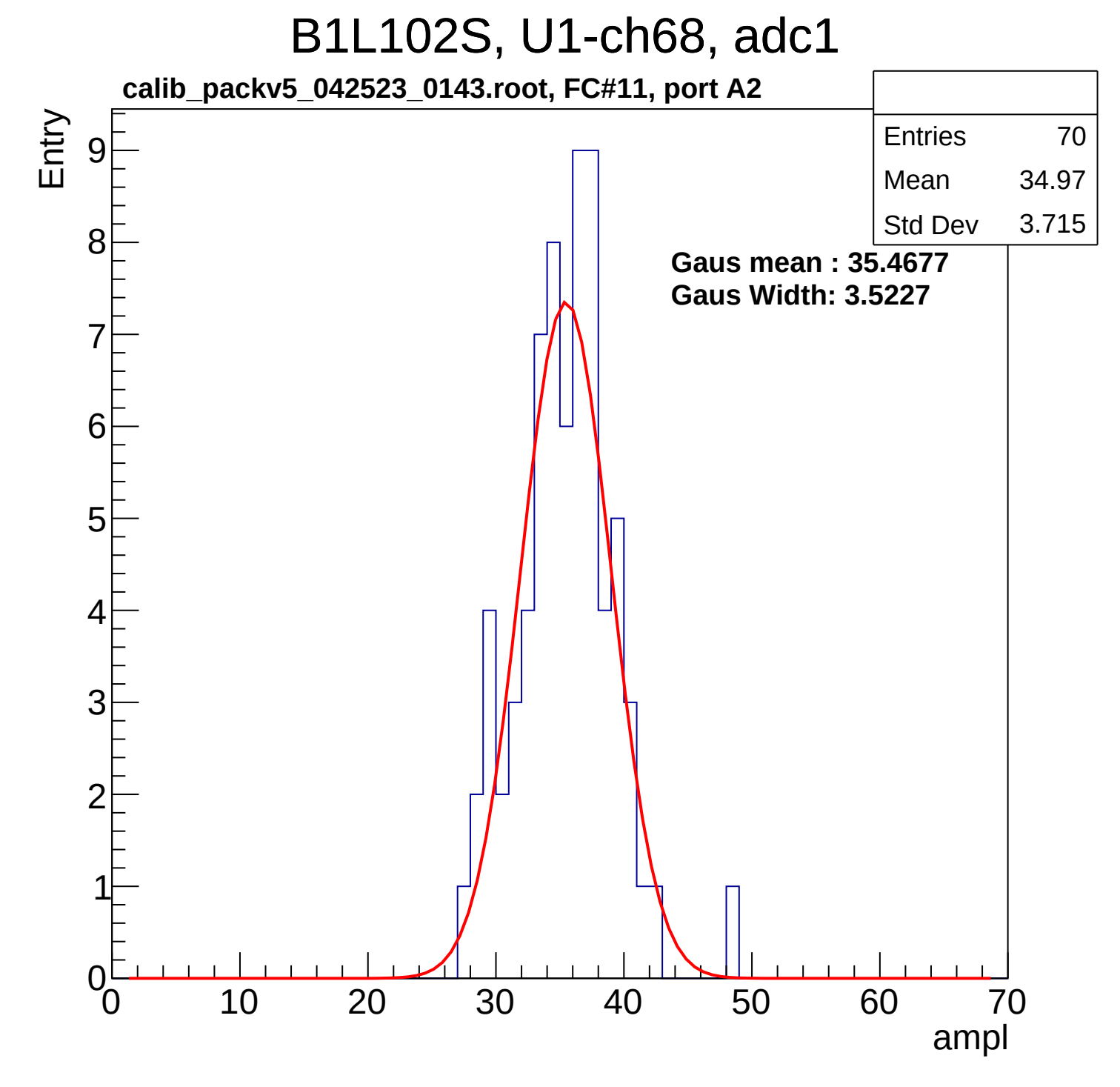

B1L102S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 55.77
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

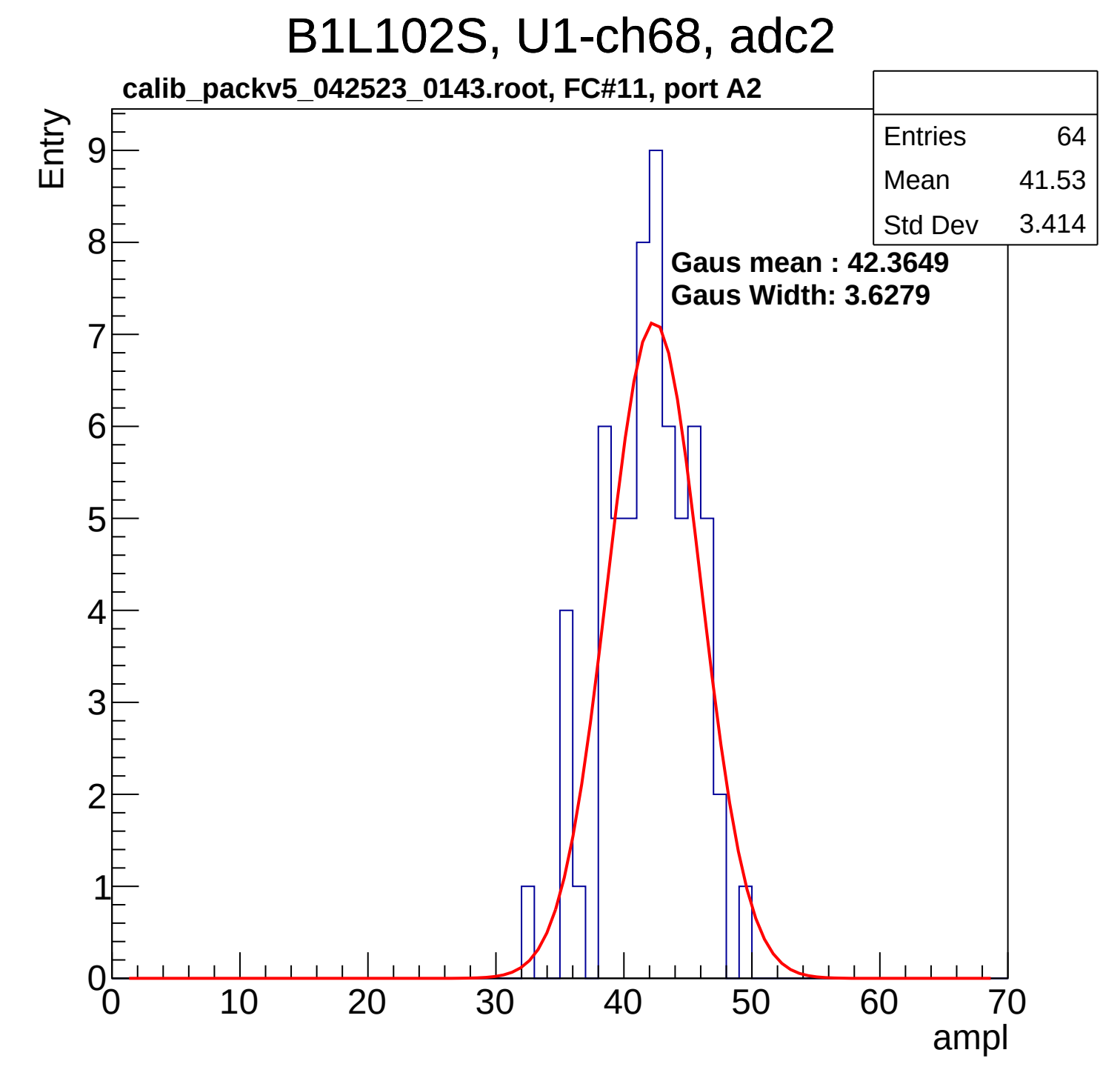

B1L102S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 58.21
Std Dev 9.425
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

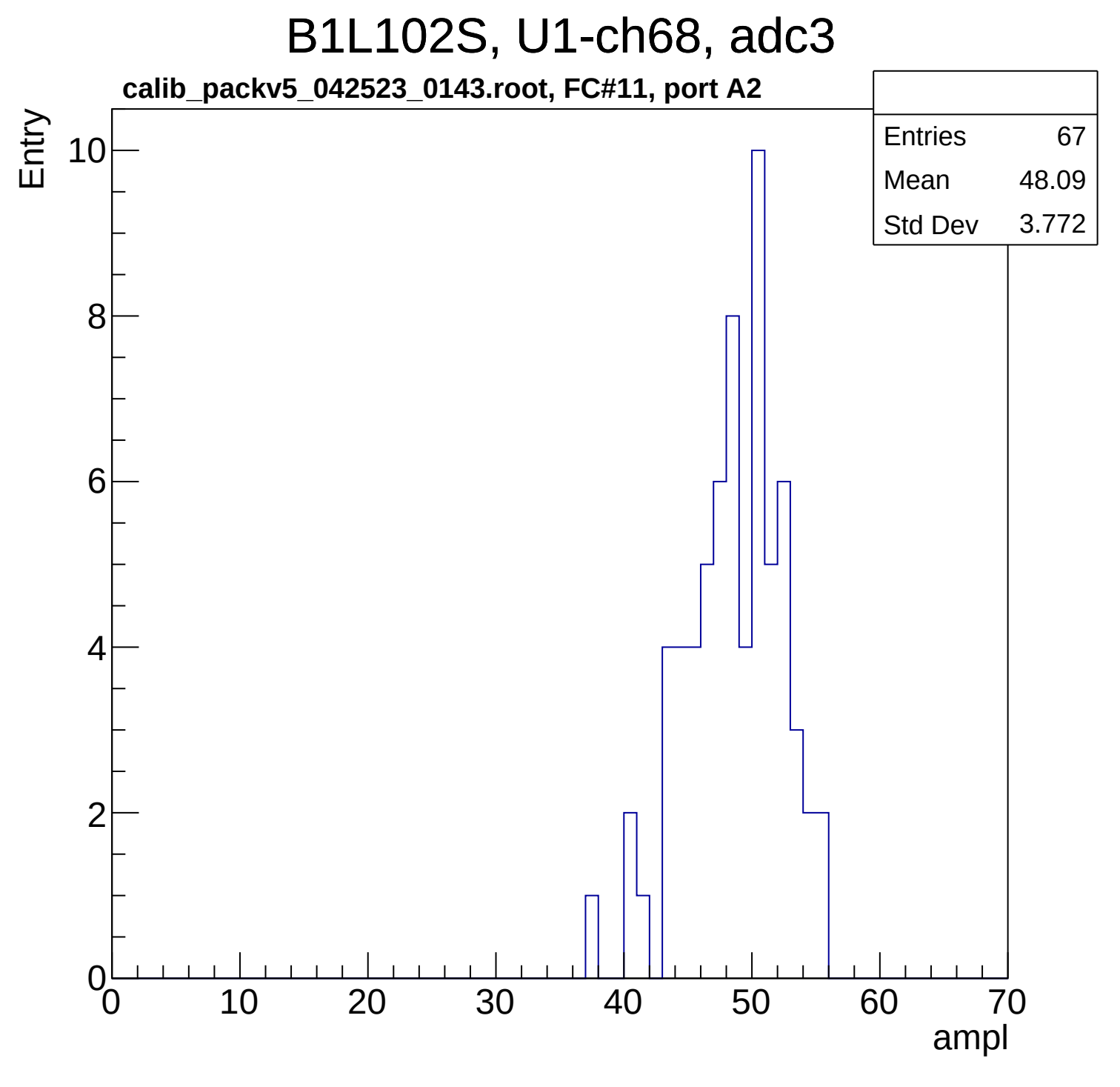

B1L102S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 62.1
Std Dev 0.7
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

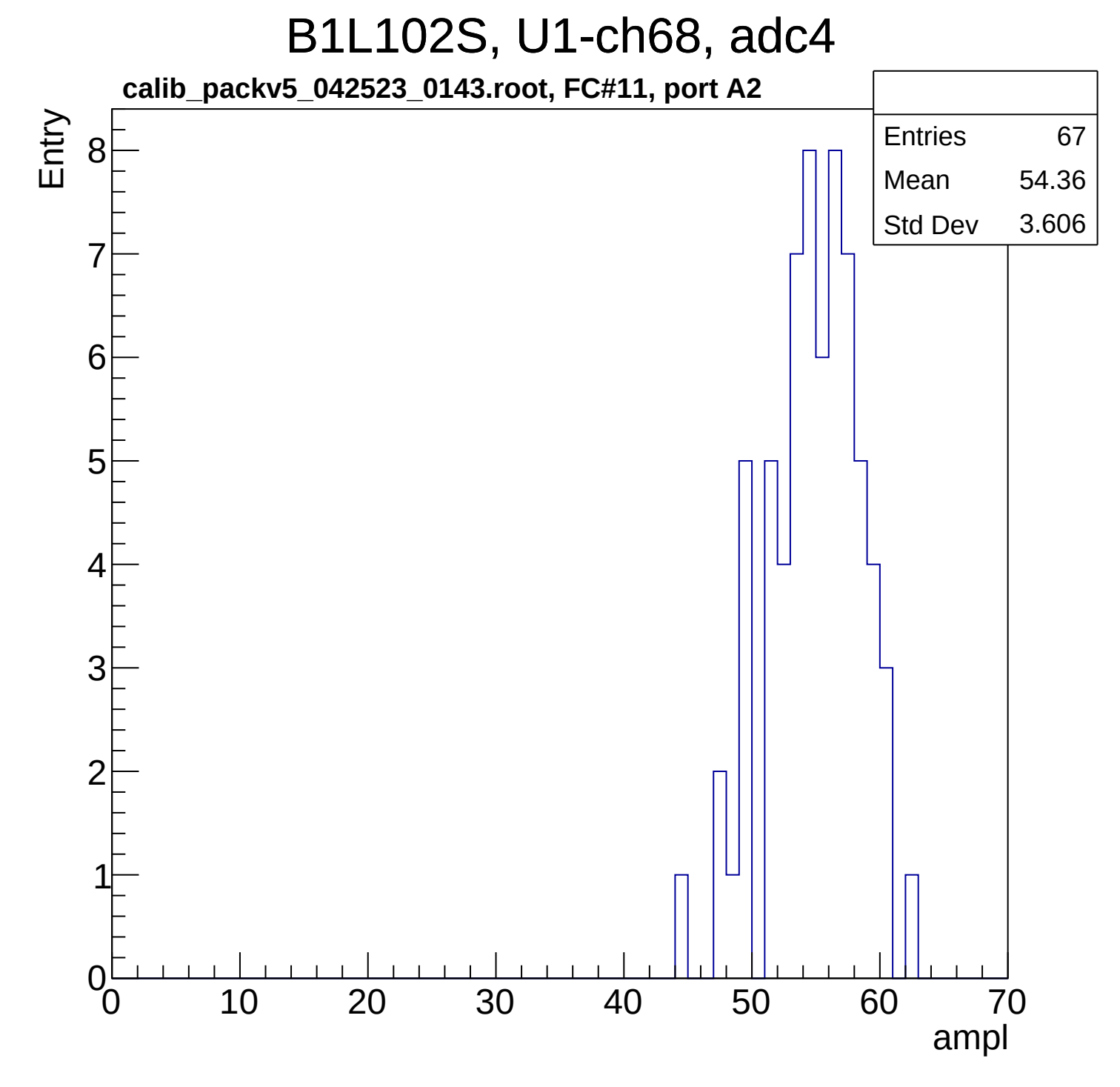

B1L102S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

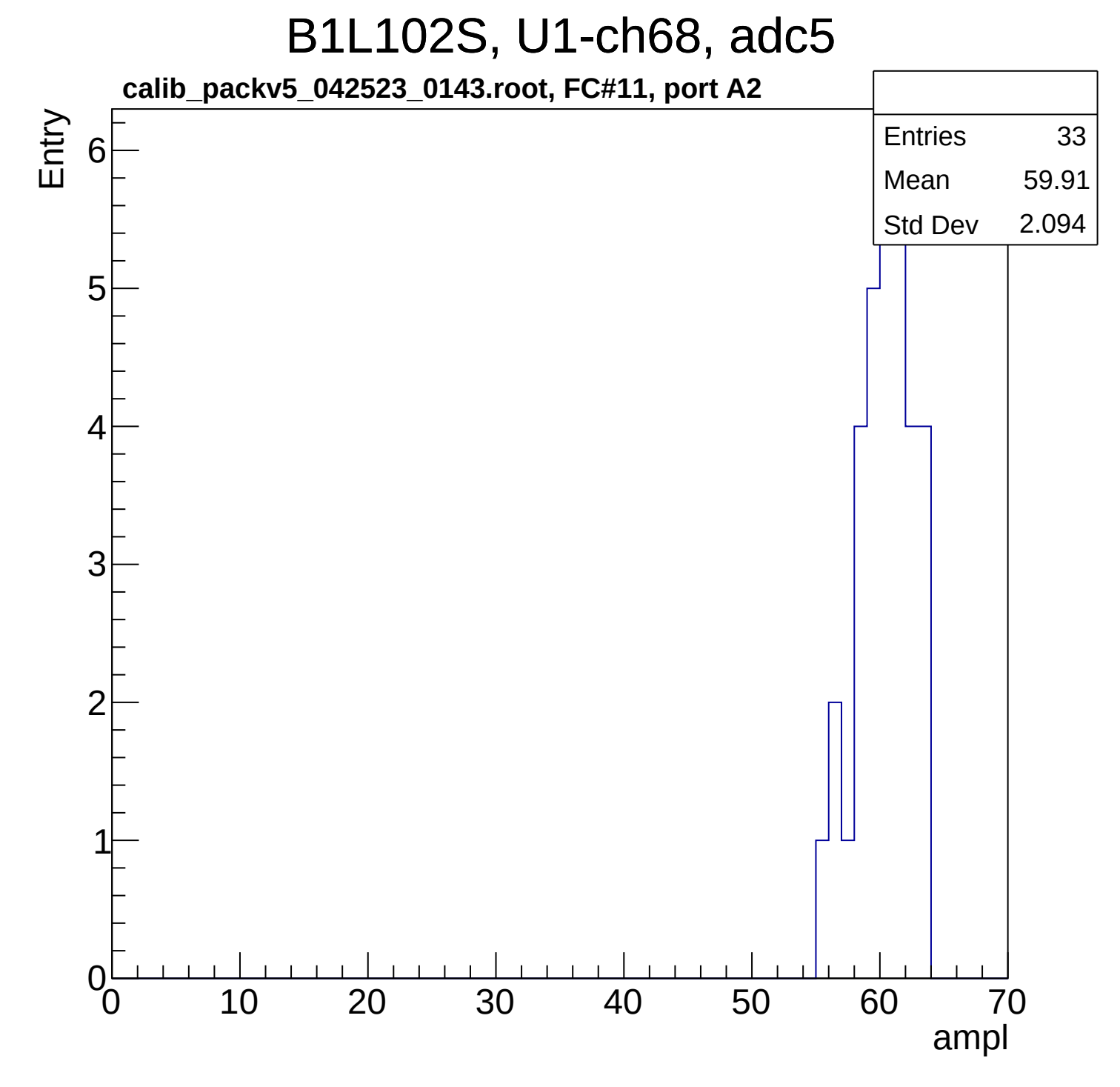

B1L102S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 27.28
Std Dev 5.036
Gaus mean : 28.7840
Gaus Width: 4.3861
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

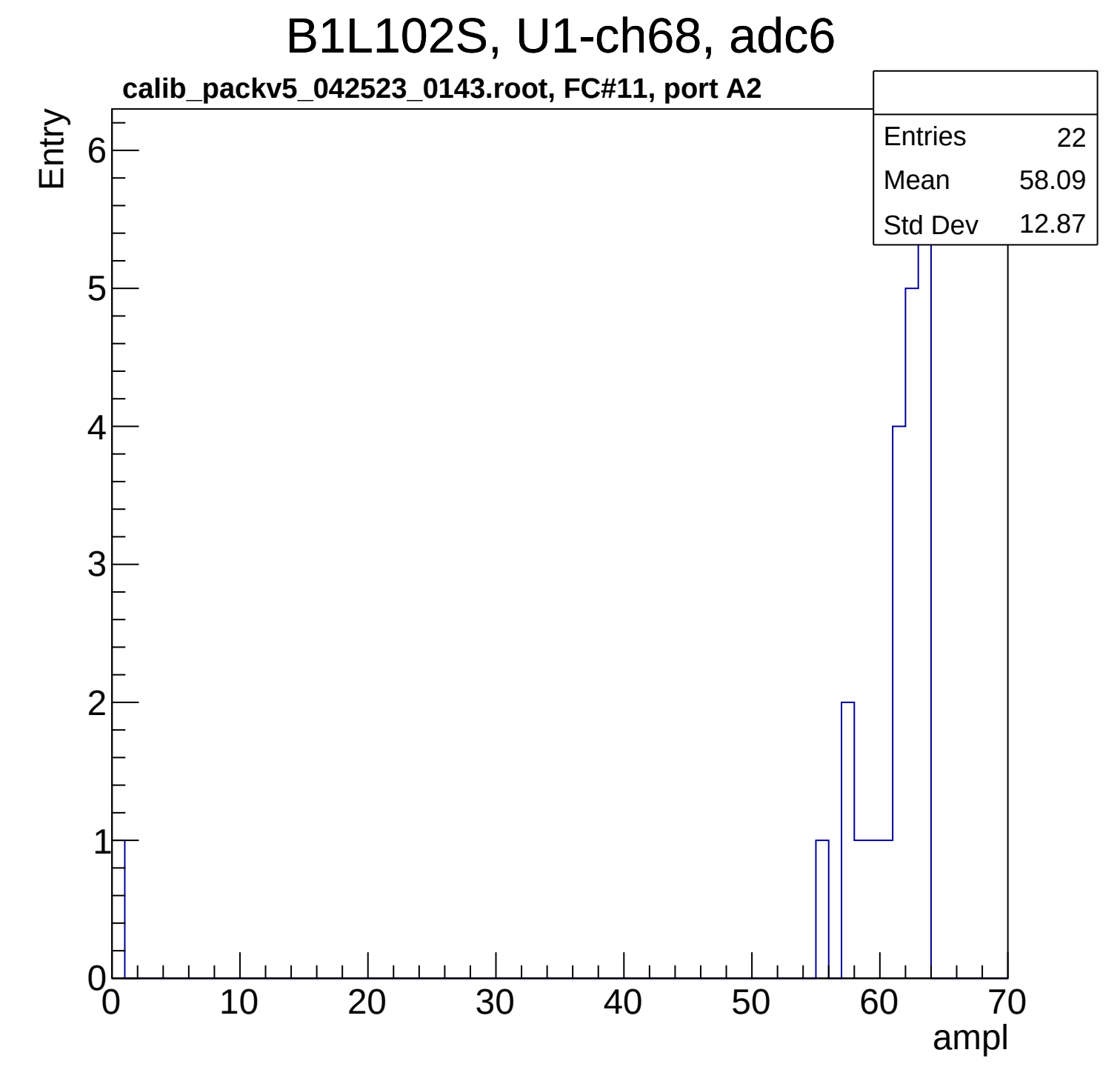

B1L102S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.97
Std Dev 3.715
Gaus mean : 35.4677
Gaus Width: 3.5227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

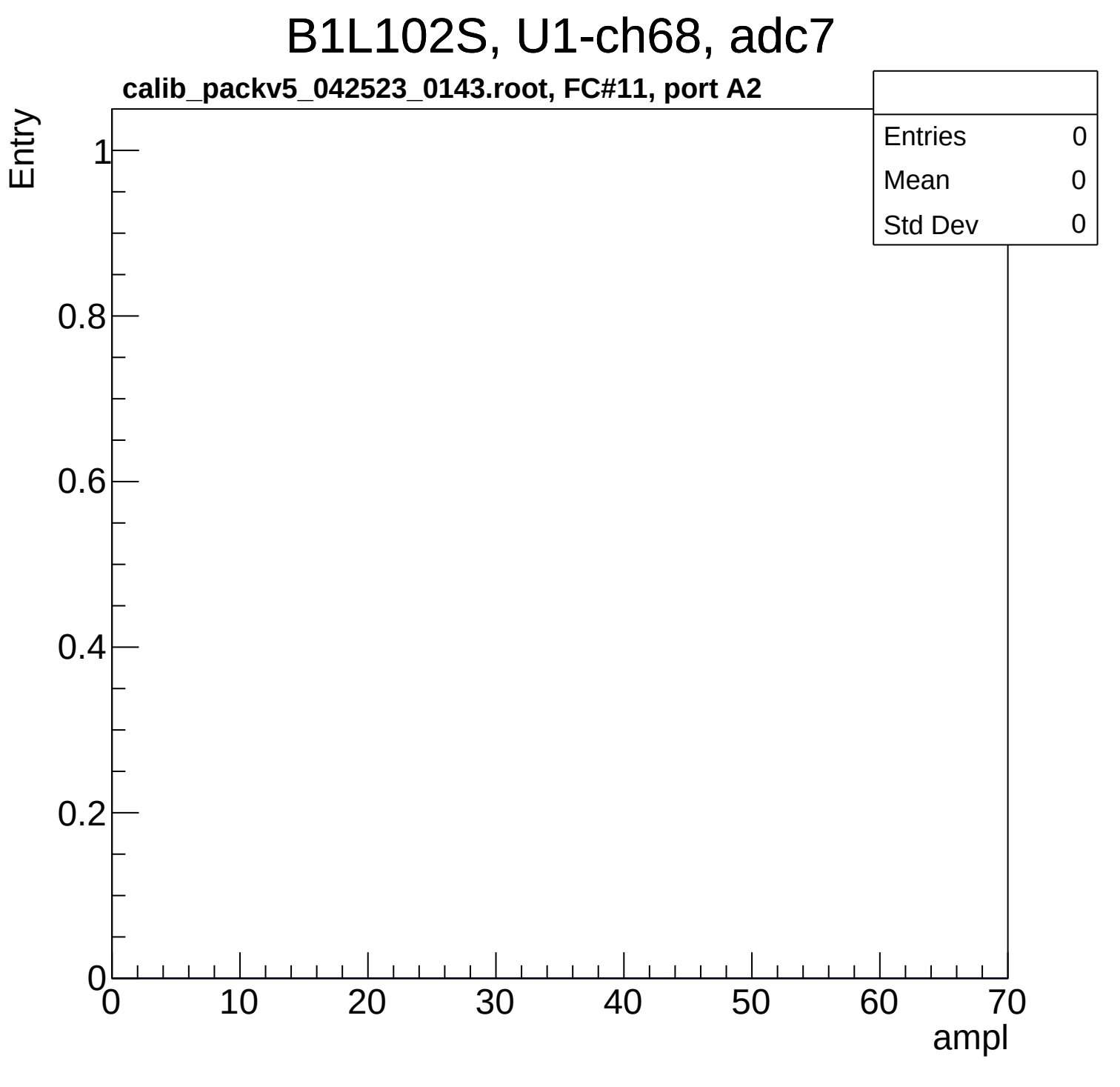

B1L102S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 41.53
Std Dev 3.414
Gaus mean : 42.3649
Gaus Width: 3.6279
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

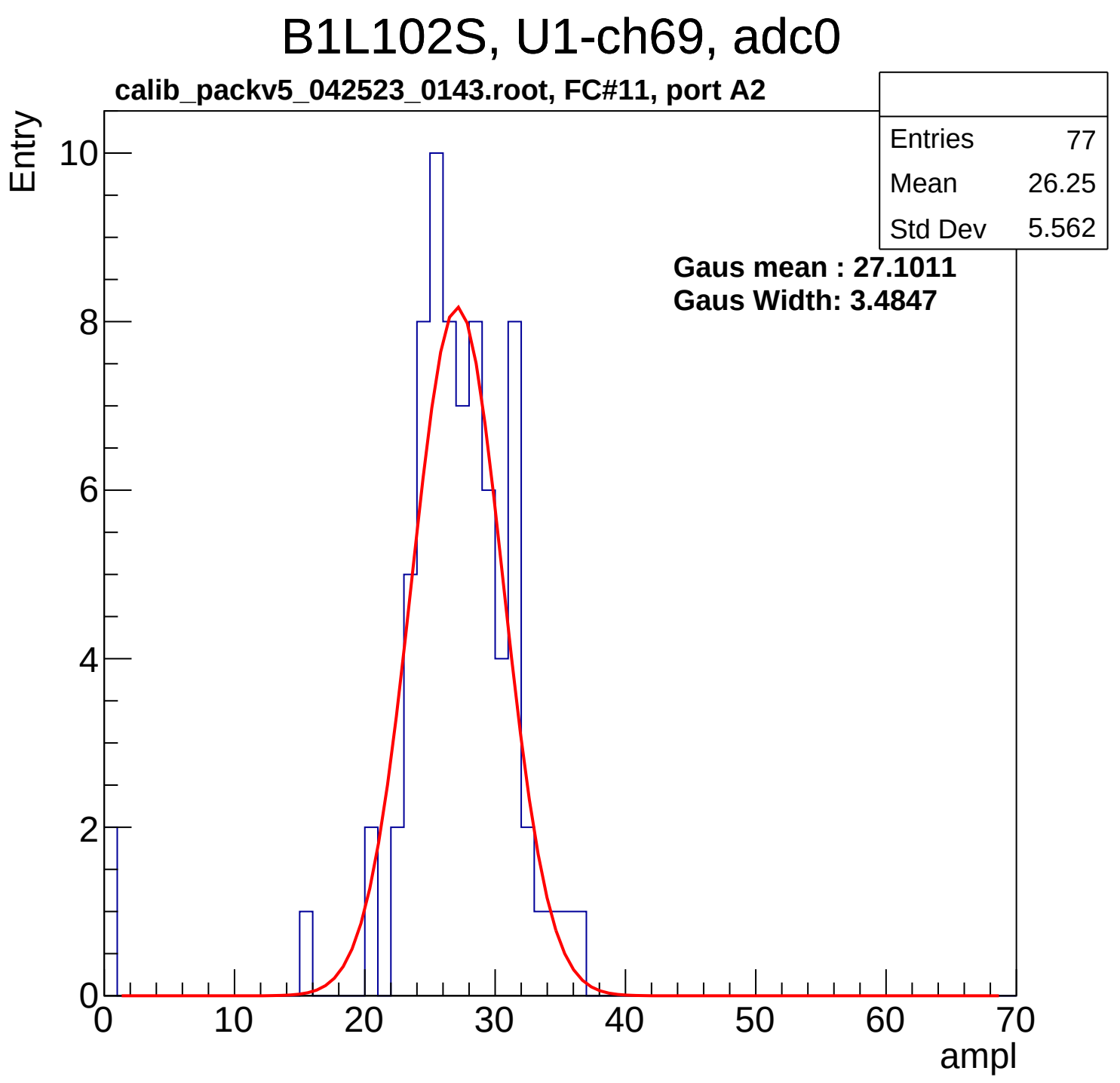

B1L102S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 48.09
Std Dev 3.772
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

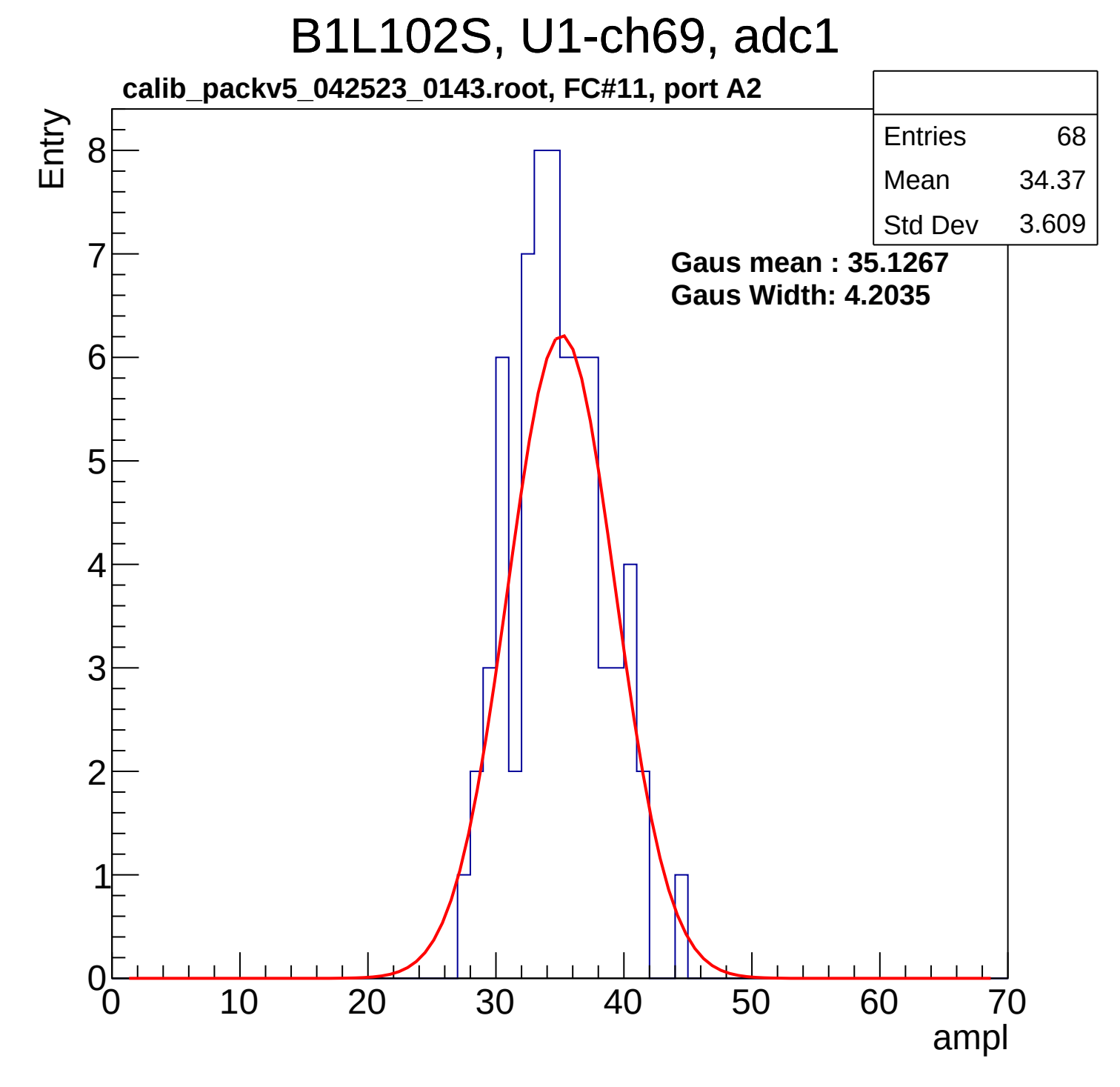

B1L102S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 54.36
Std Dev 3.606
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

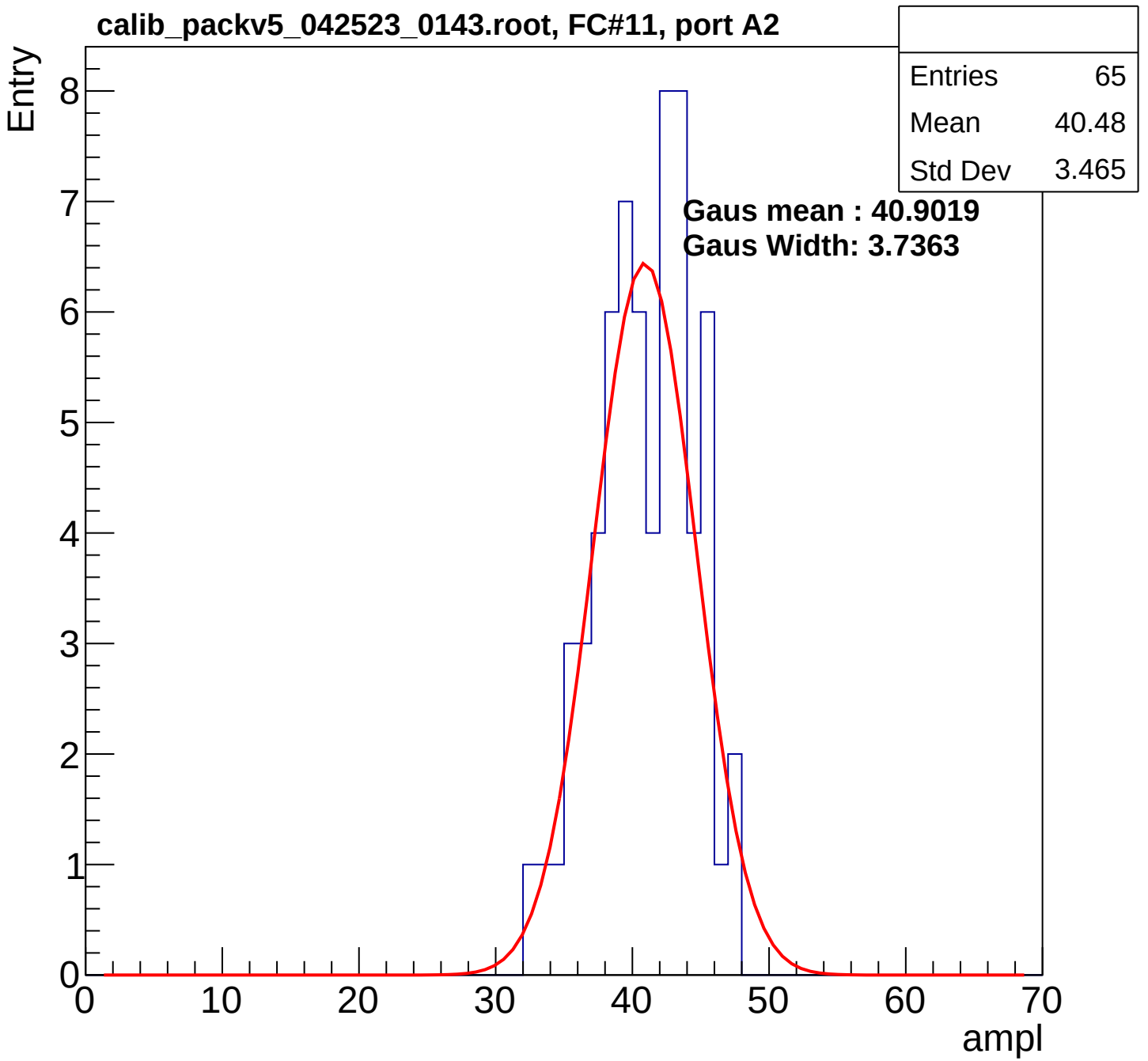

B1L102S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 33
Mean 59.91
Std Dev 2.094
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

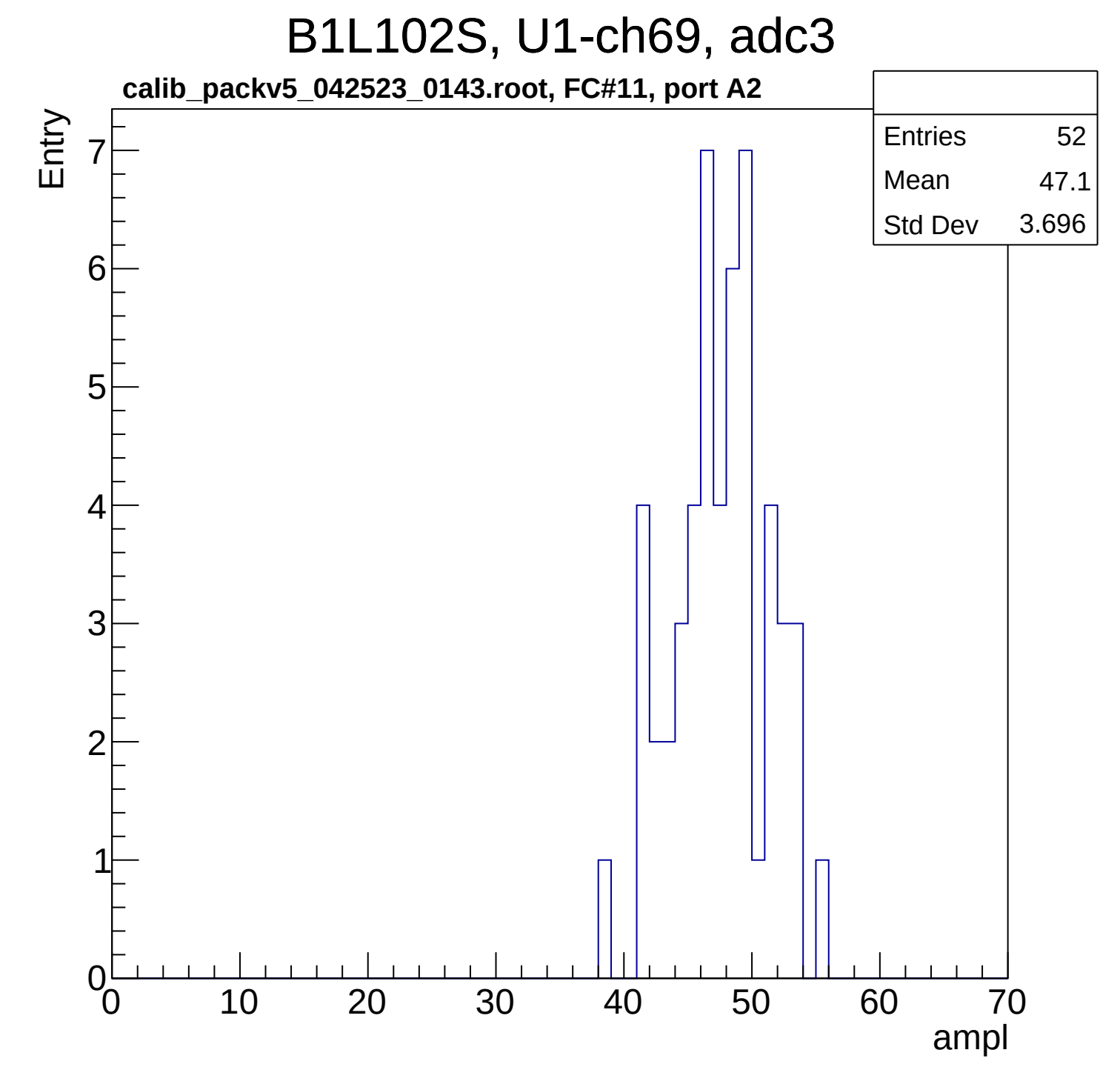

B1L102S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 22
Mean 58.09
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

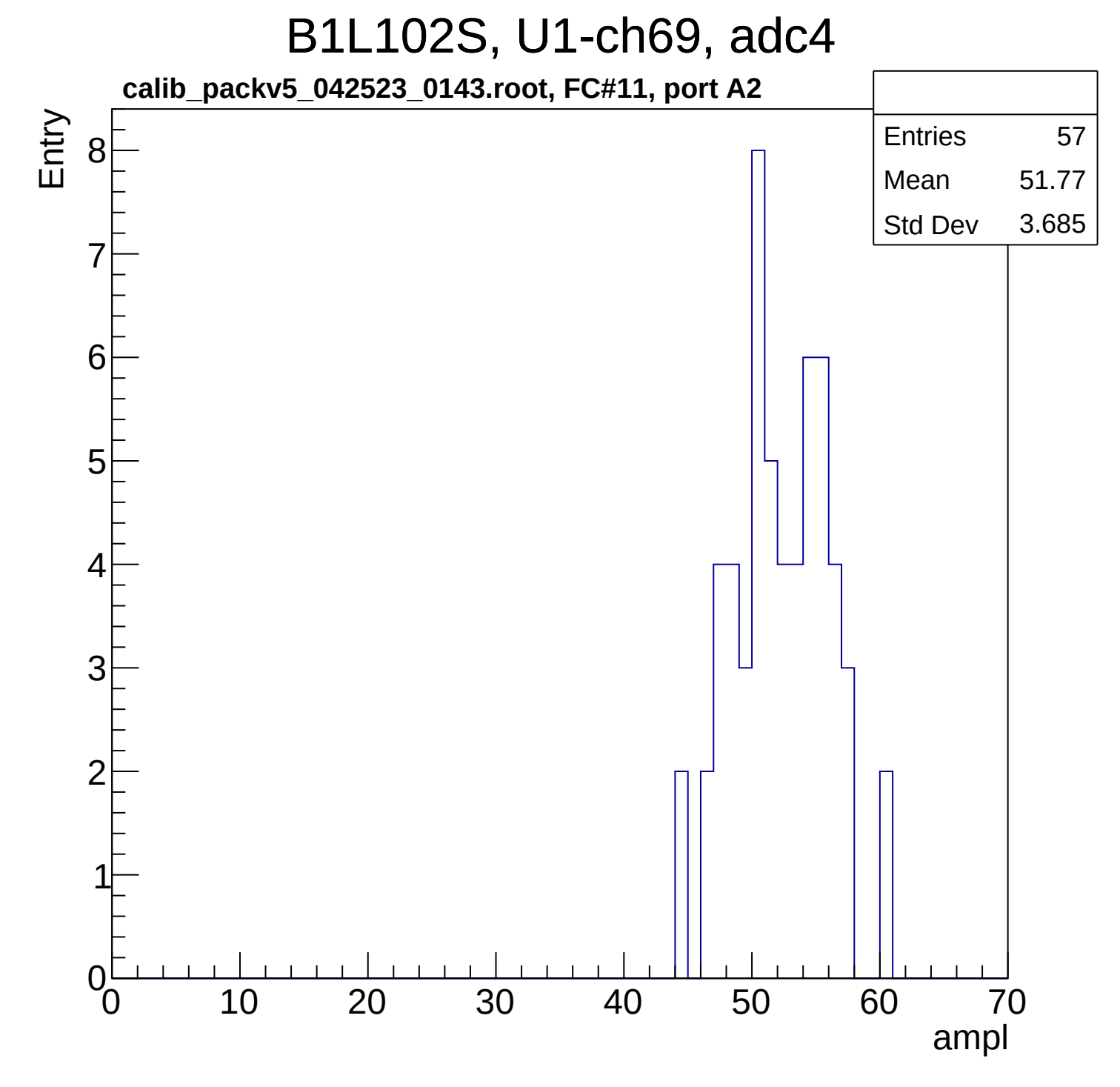

B1L102S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

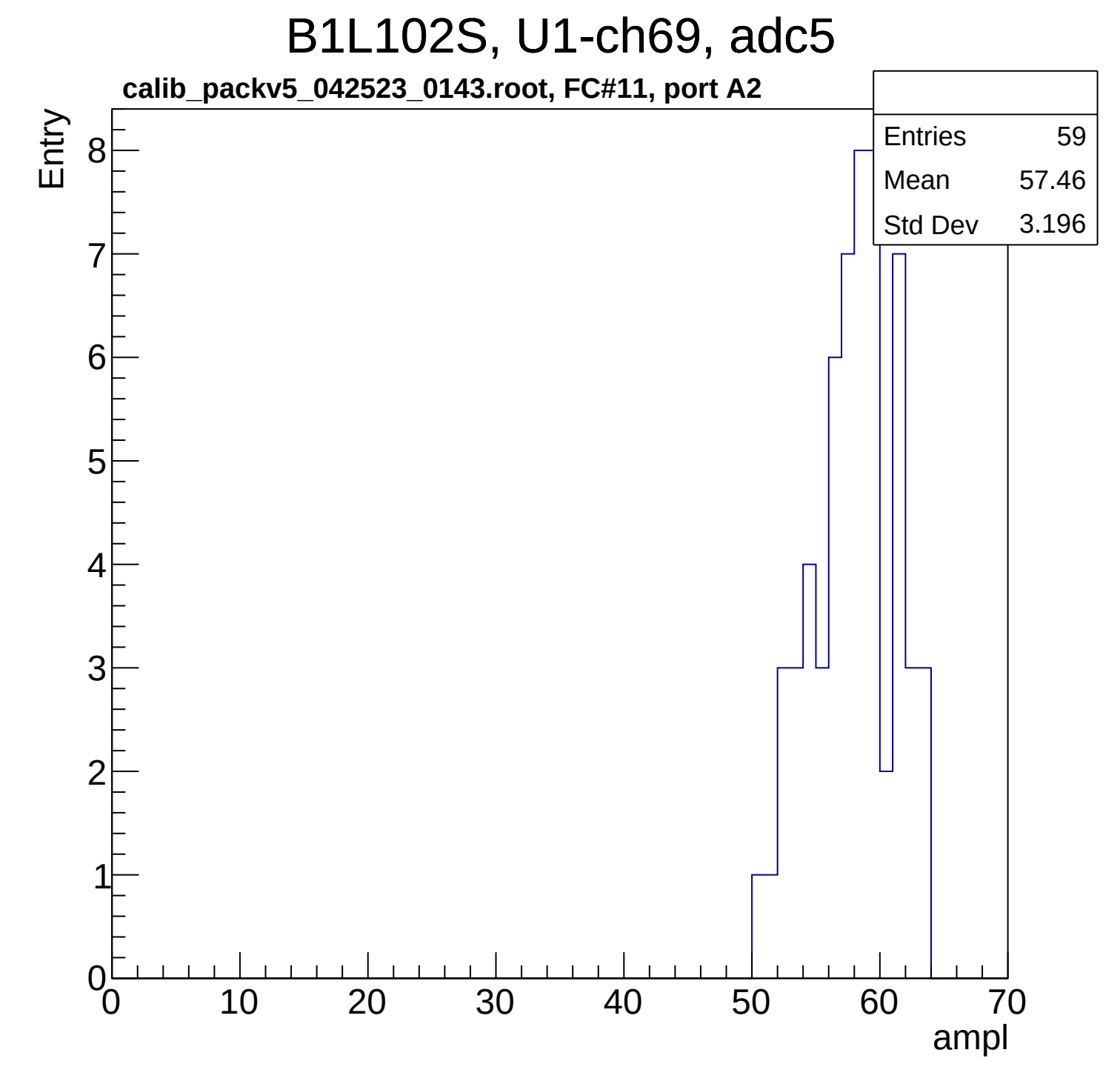

B1L102S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 26.25
Std Dev 5.562
Gaus mean : 27.1011
Gaus Width: 3.4847
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

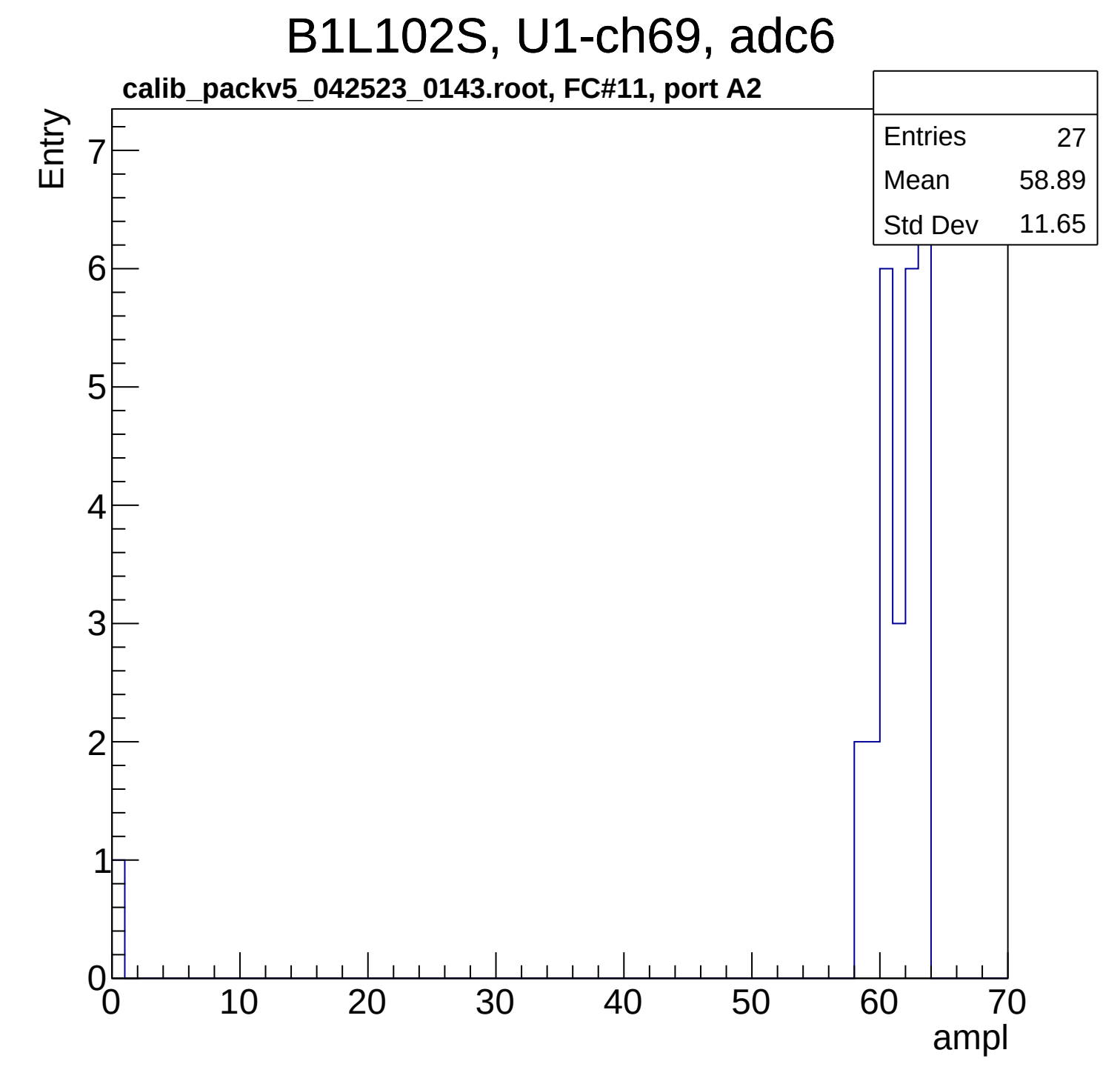

B1L102S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 34.37
Std Dev 3.609
Gaus mean : 35.1267
Gaus Width: 4.2035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

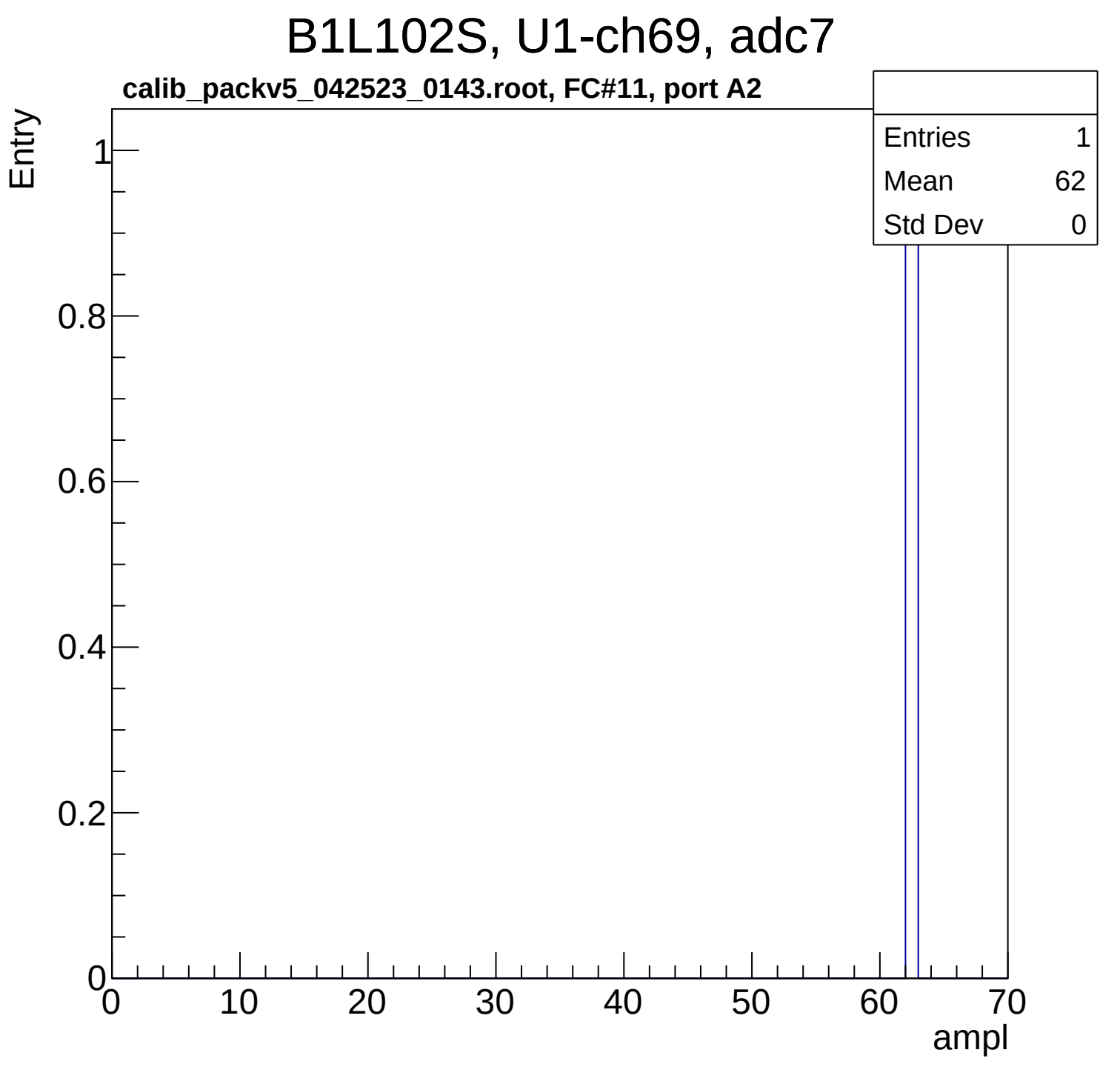

B1L102S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 40.48
Std Dev 3.465
Gaus mean : 40.9019
Gaus Width: 3.7363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

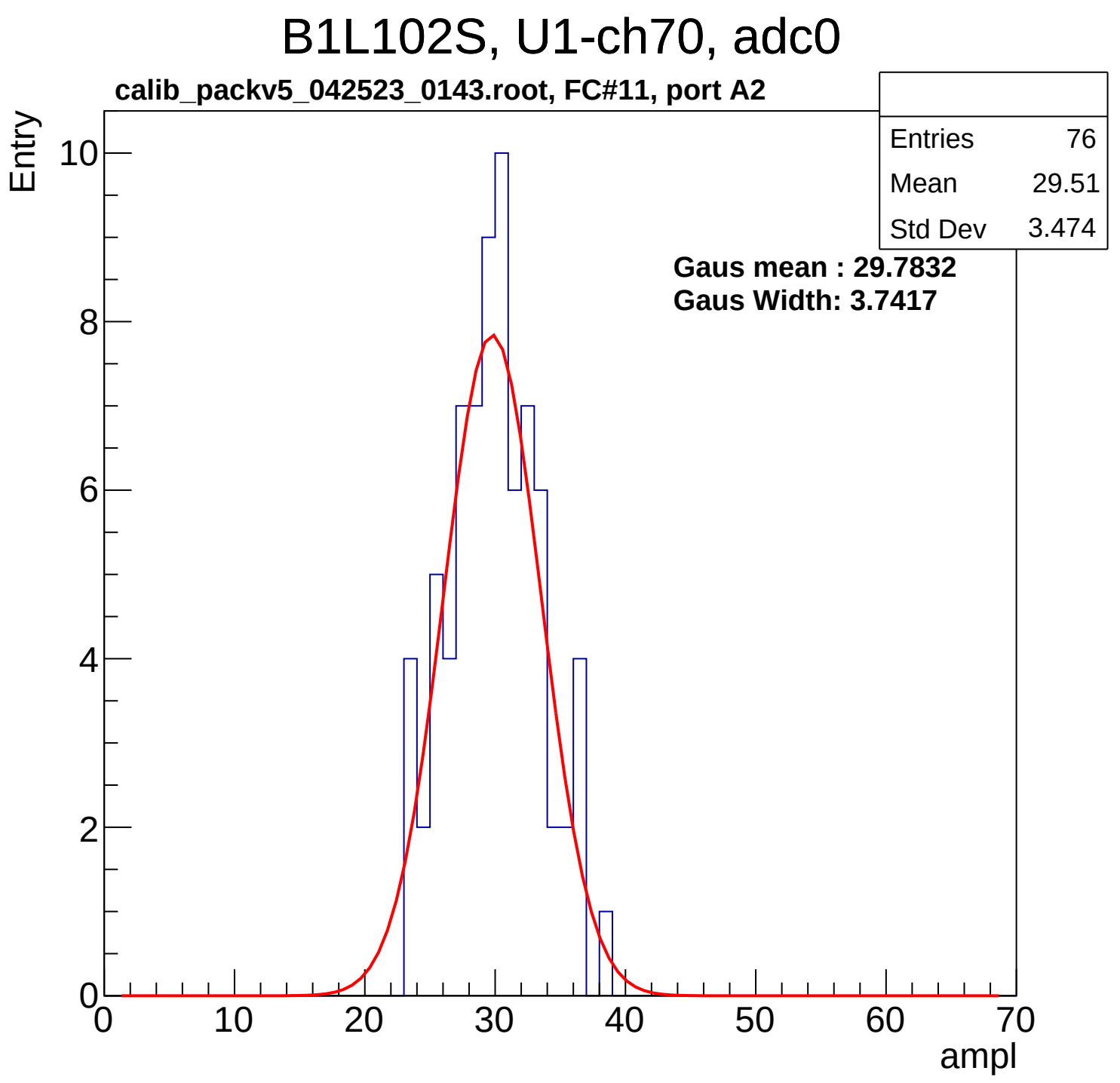

B1L102S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 47.1
Std Dev 3.696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

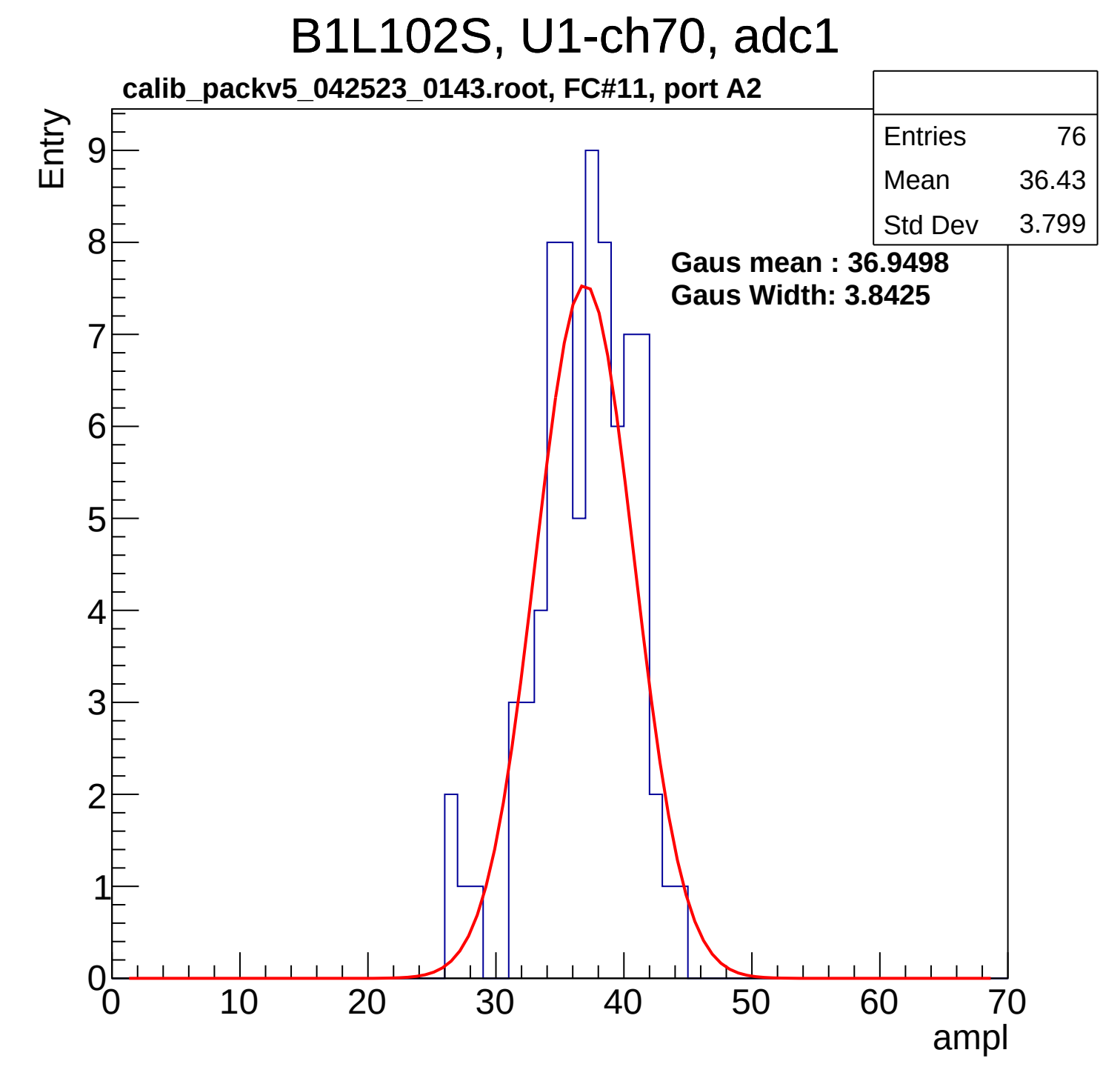

B1L102S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 51.77
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

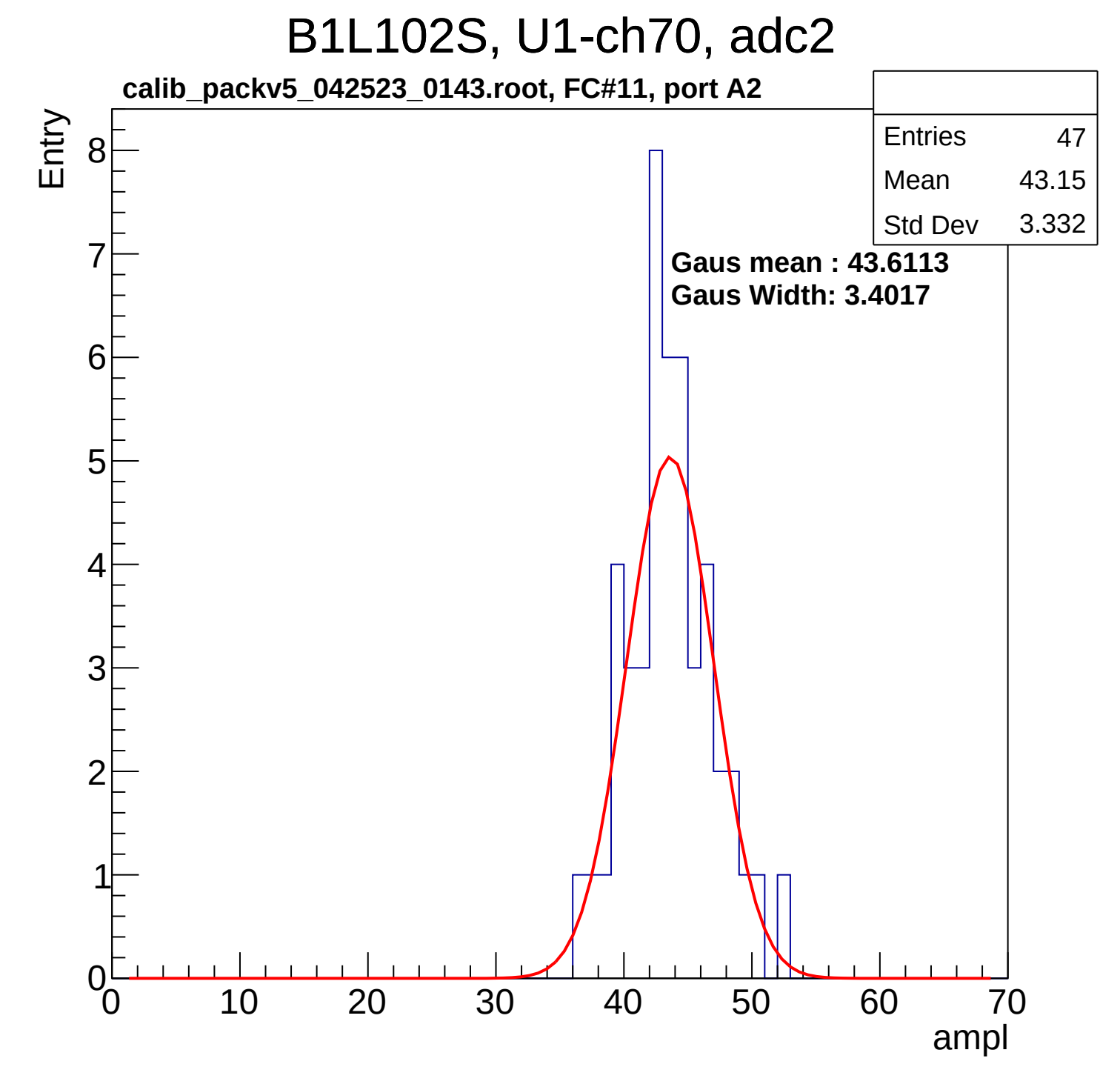

B1L102S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 57.46
Std Dev 3.196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

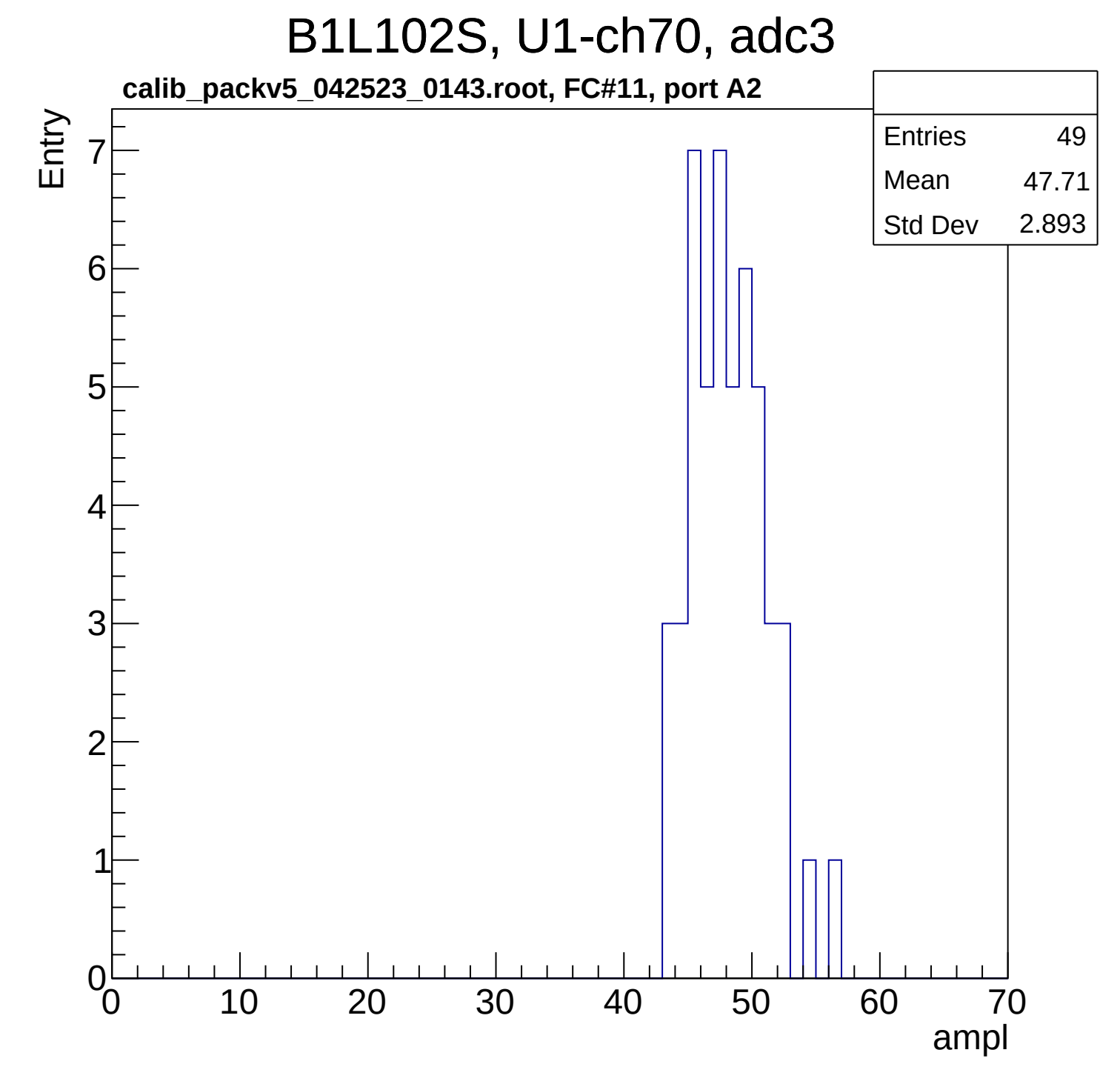

B1L102S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 27
Mean 58.89
Std Dev 11.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

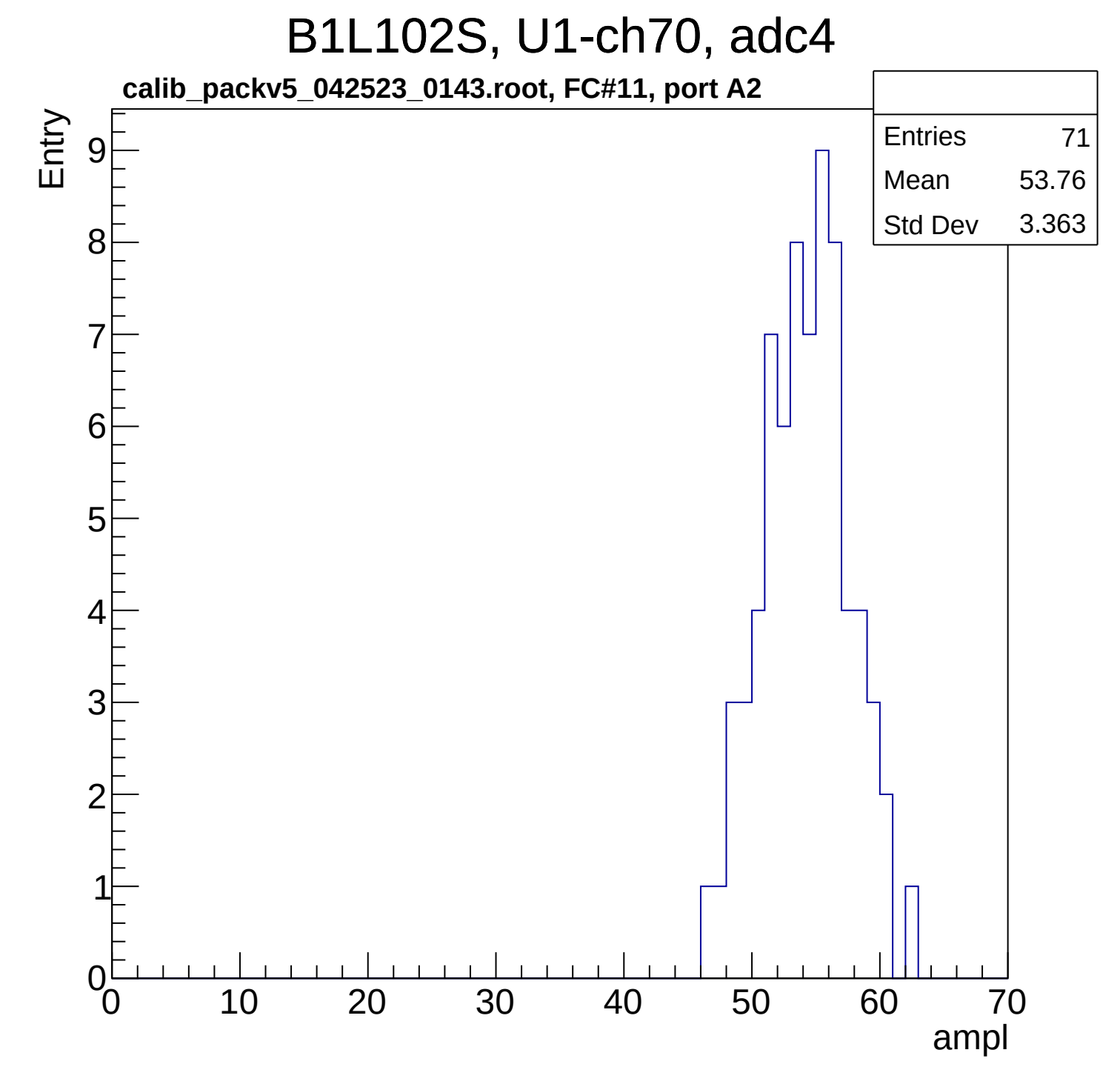

B1L102S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

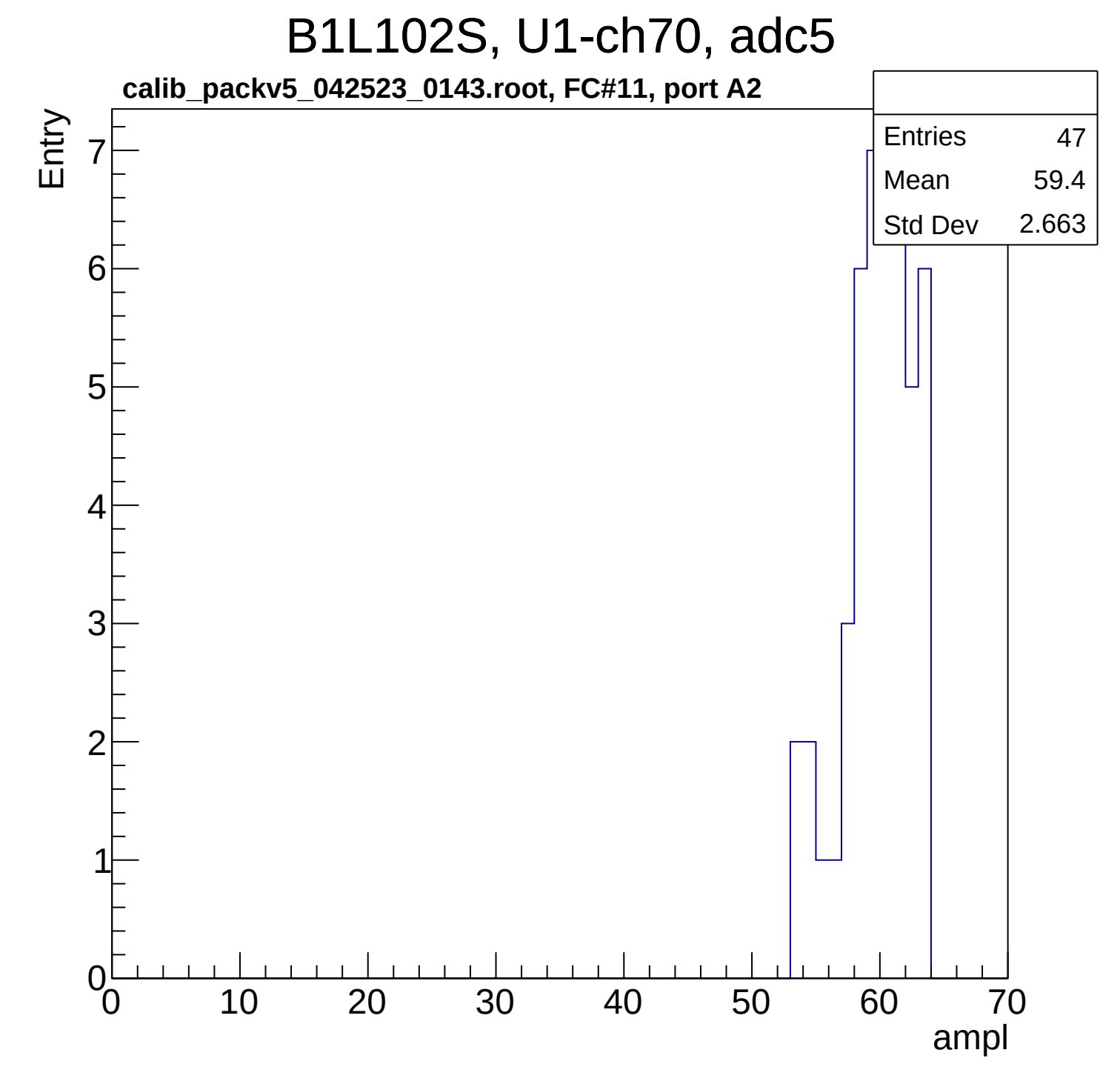

B1L102S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 29.51
Std Dev 3.474
Gaus mean : 29.7832
Gaus Width: 3.7417
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

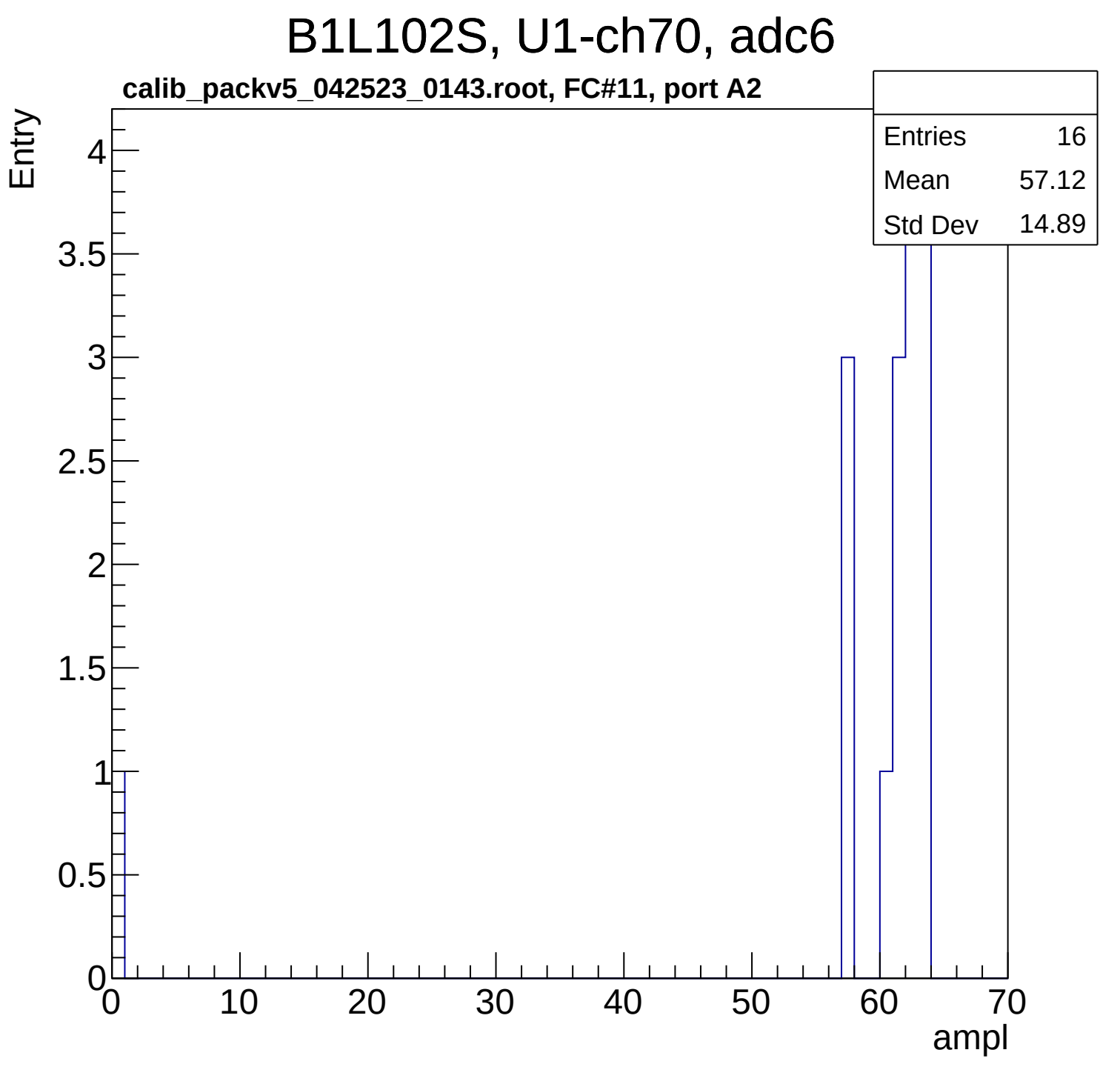

B1L102S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 36.43
Std Dev 3.799
Gaus mean : 36.9498
Gaus Width: 3.8425
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

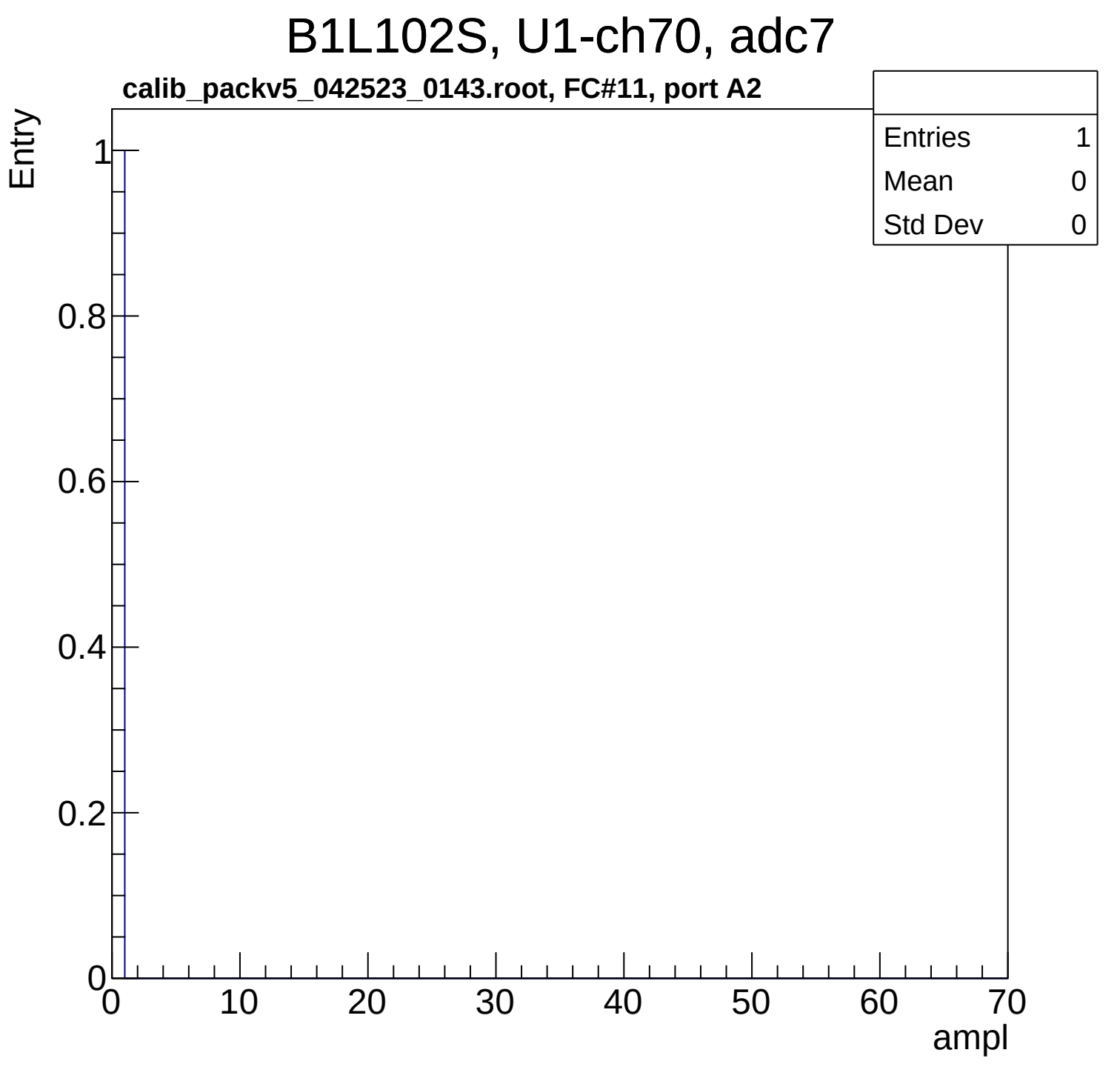

B1L102S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 43.15
Std Dev 3.332
Gaus mean : 43.6113
Gaus Width: 3.4017
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 47.71
Std Dev 2.893
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 53.76
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 59.4
Std Dev 2.663
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 57.12
Std Dev 14.89
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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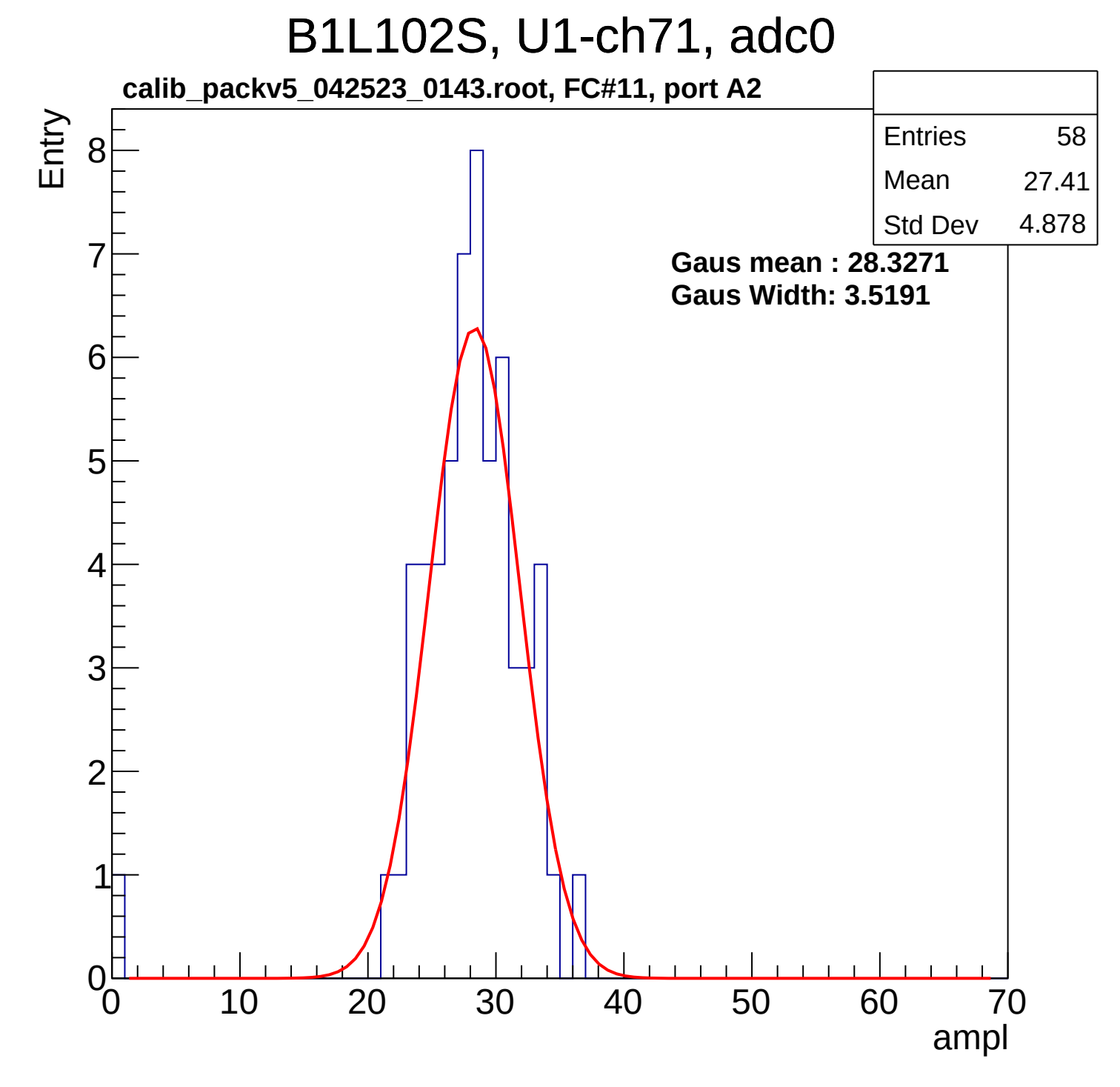
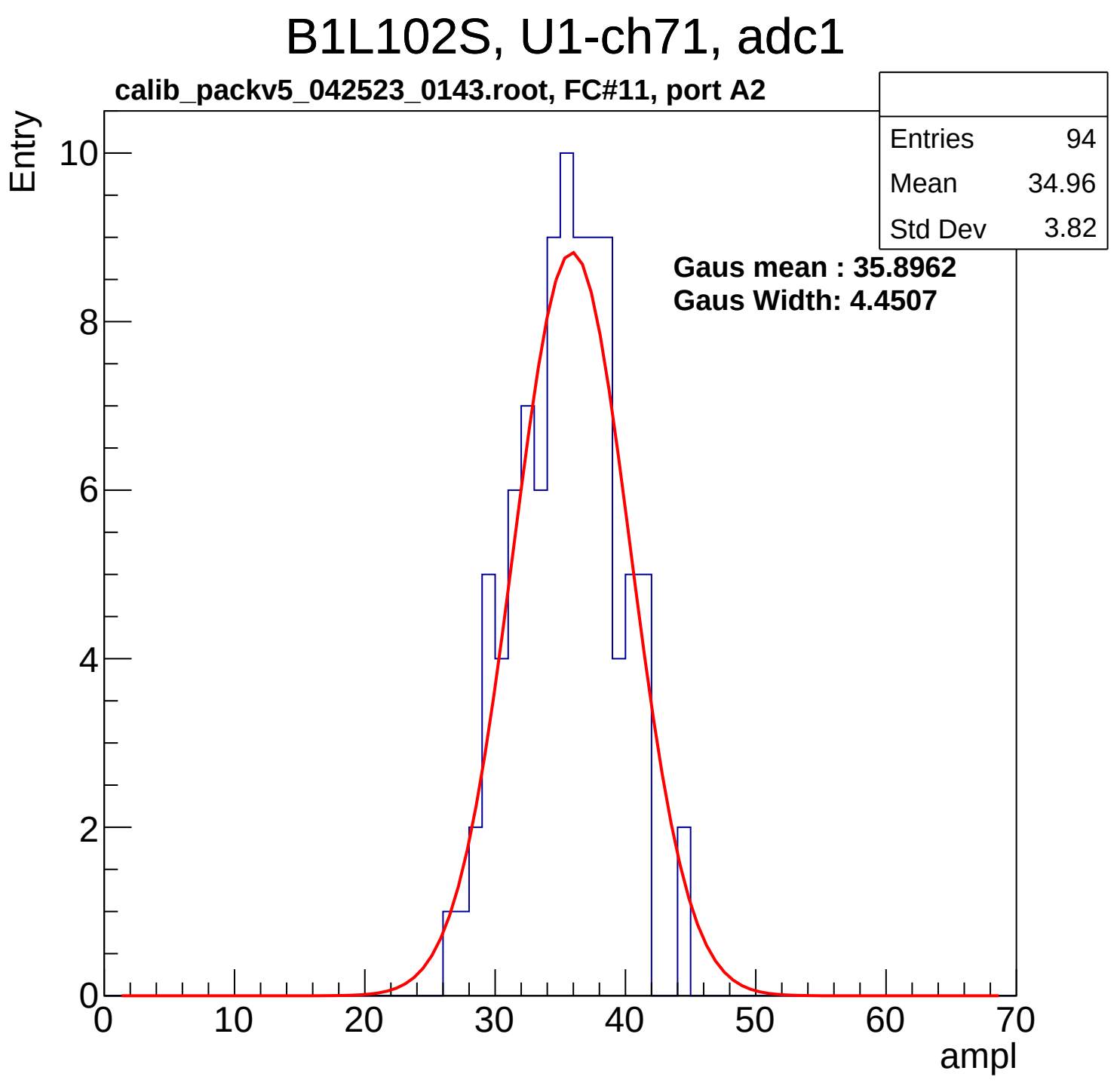
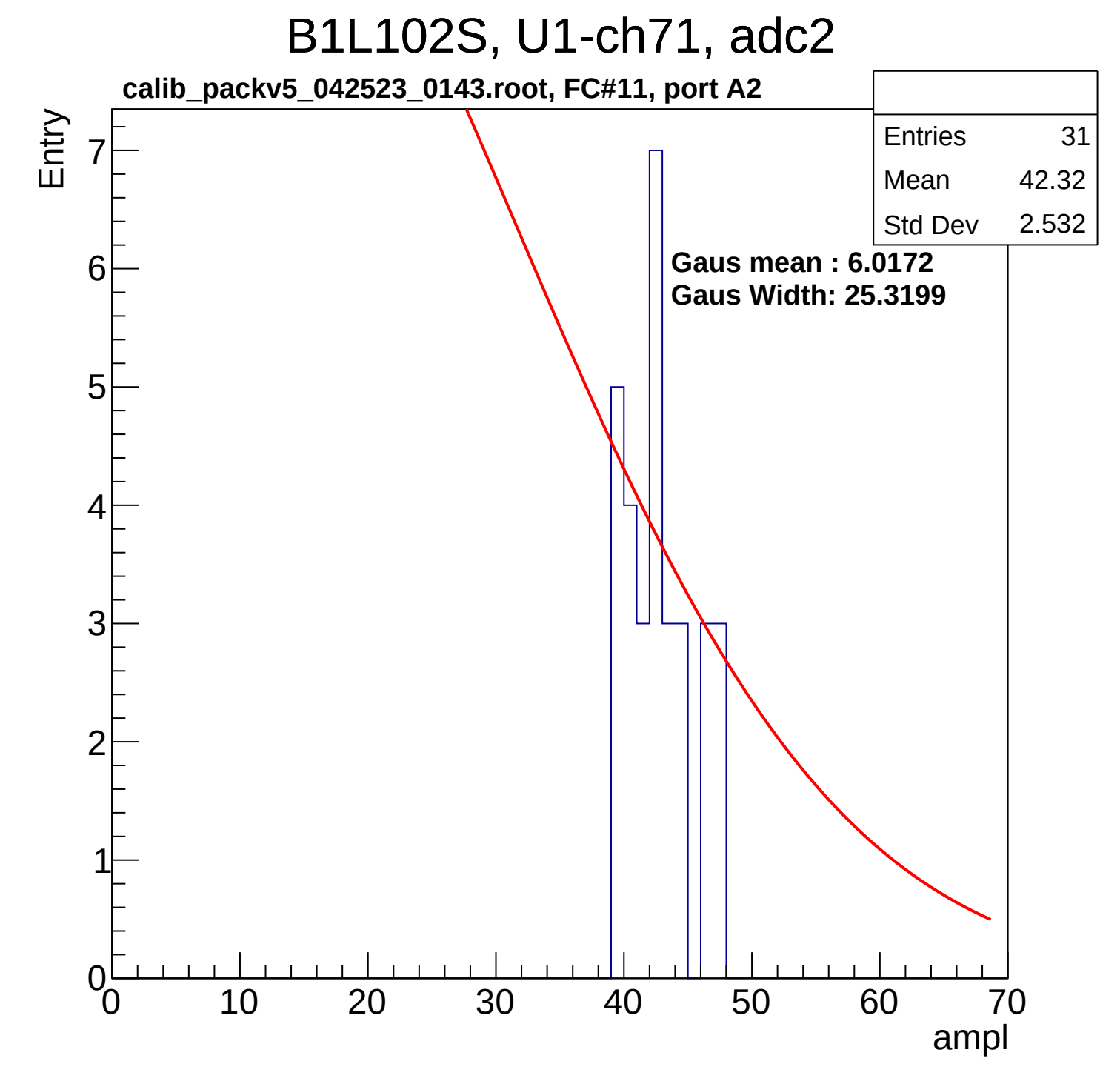
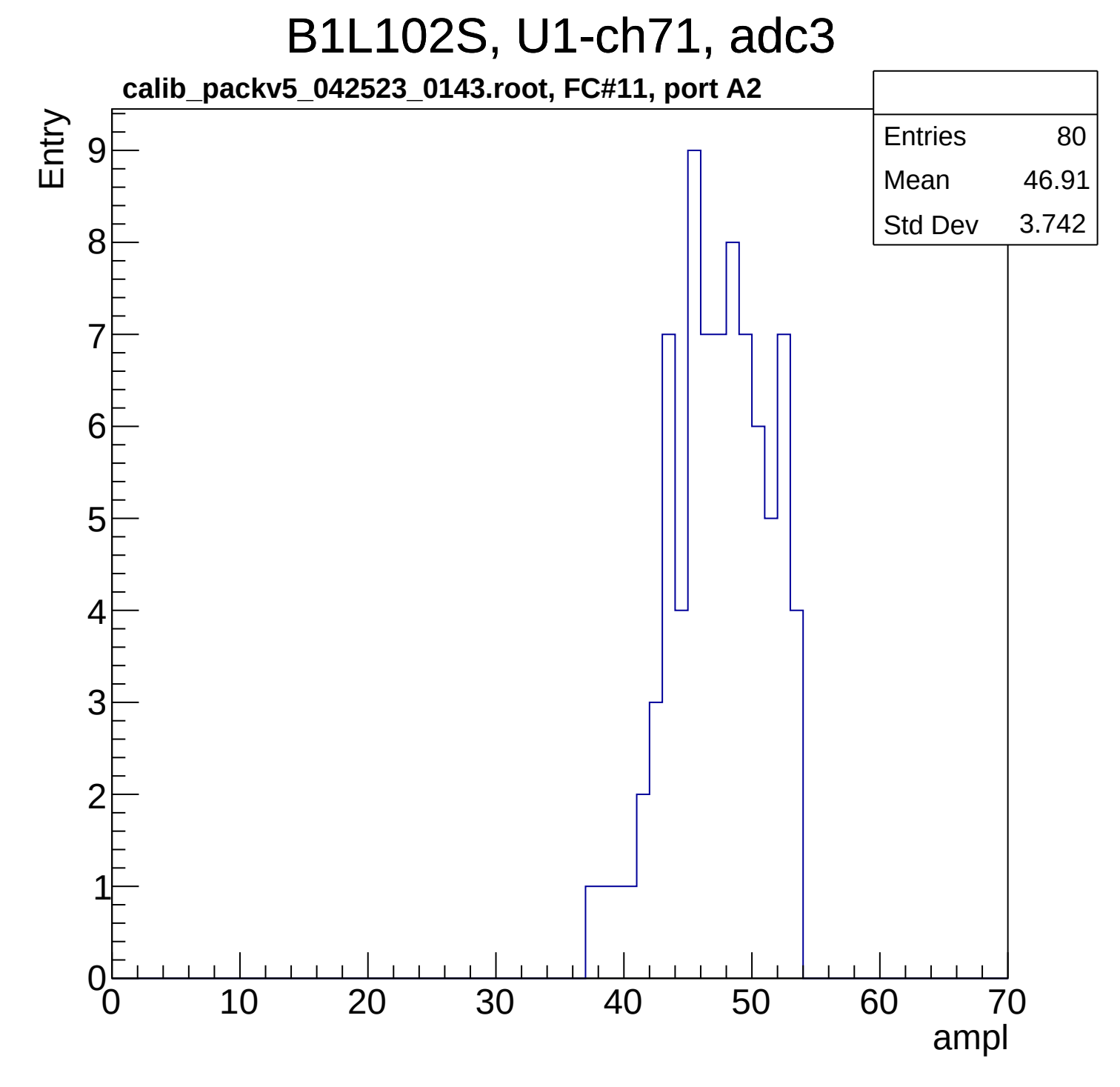
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 27.41
Std Dev 4.878
Gaus mean : 28.3271
Gaus Width: 3.5191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 94
Mean 34.96
Std Dev 3.82
Gaus mean : 35.8962
Gaus Width: 4.4507
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 31
Mean 42.32
Std Dev 2.532
Gaus mean : 6.0172
Gaus Width: 25.3199
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 46.91
Std Dev 3.742
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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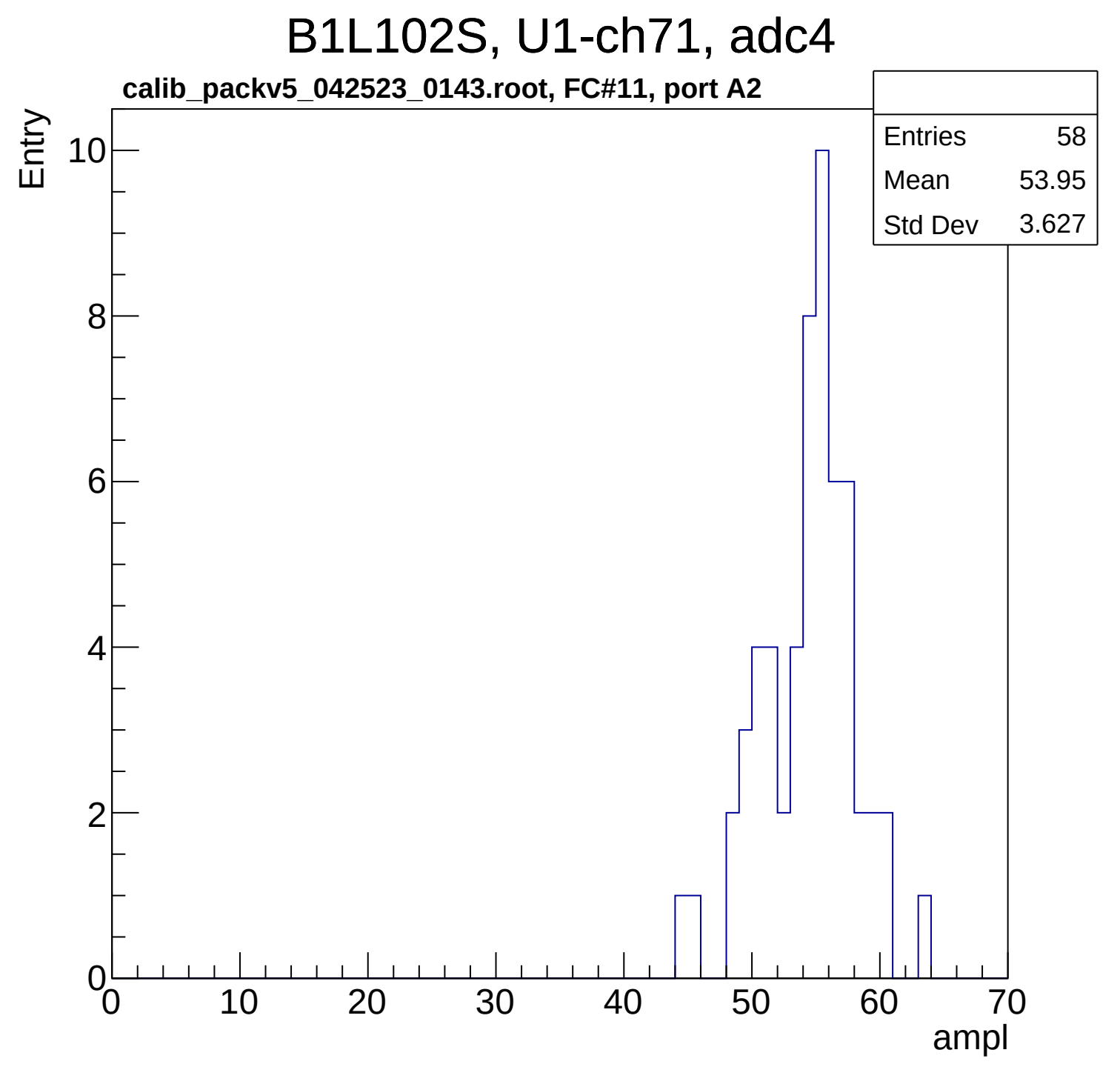
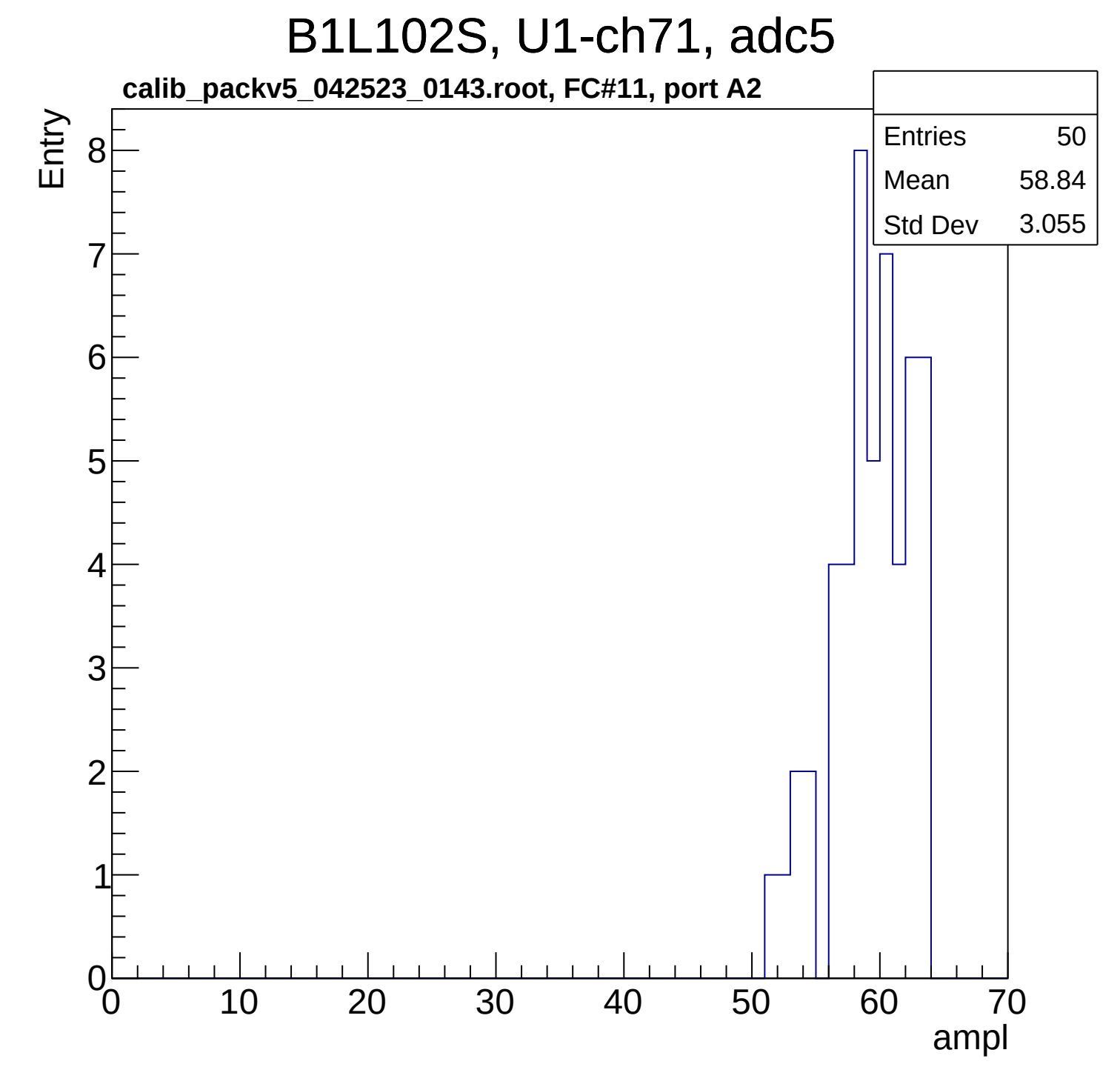
40
50
60
70


B1L102S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 53.95
Std Dev 3.627
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 50
Mean 58.84
Std Dev 3.055
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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60
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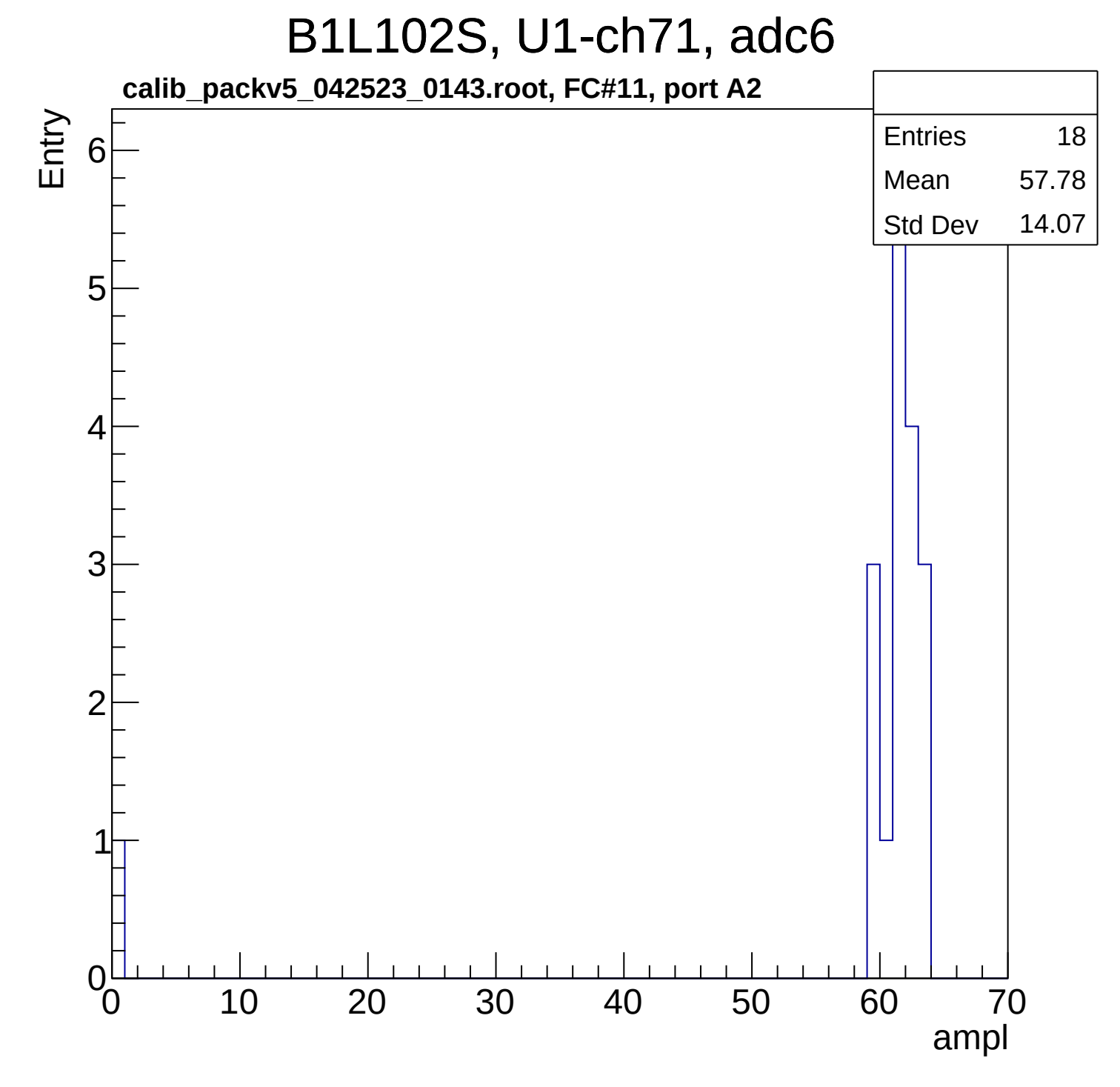
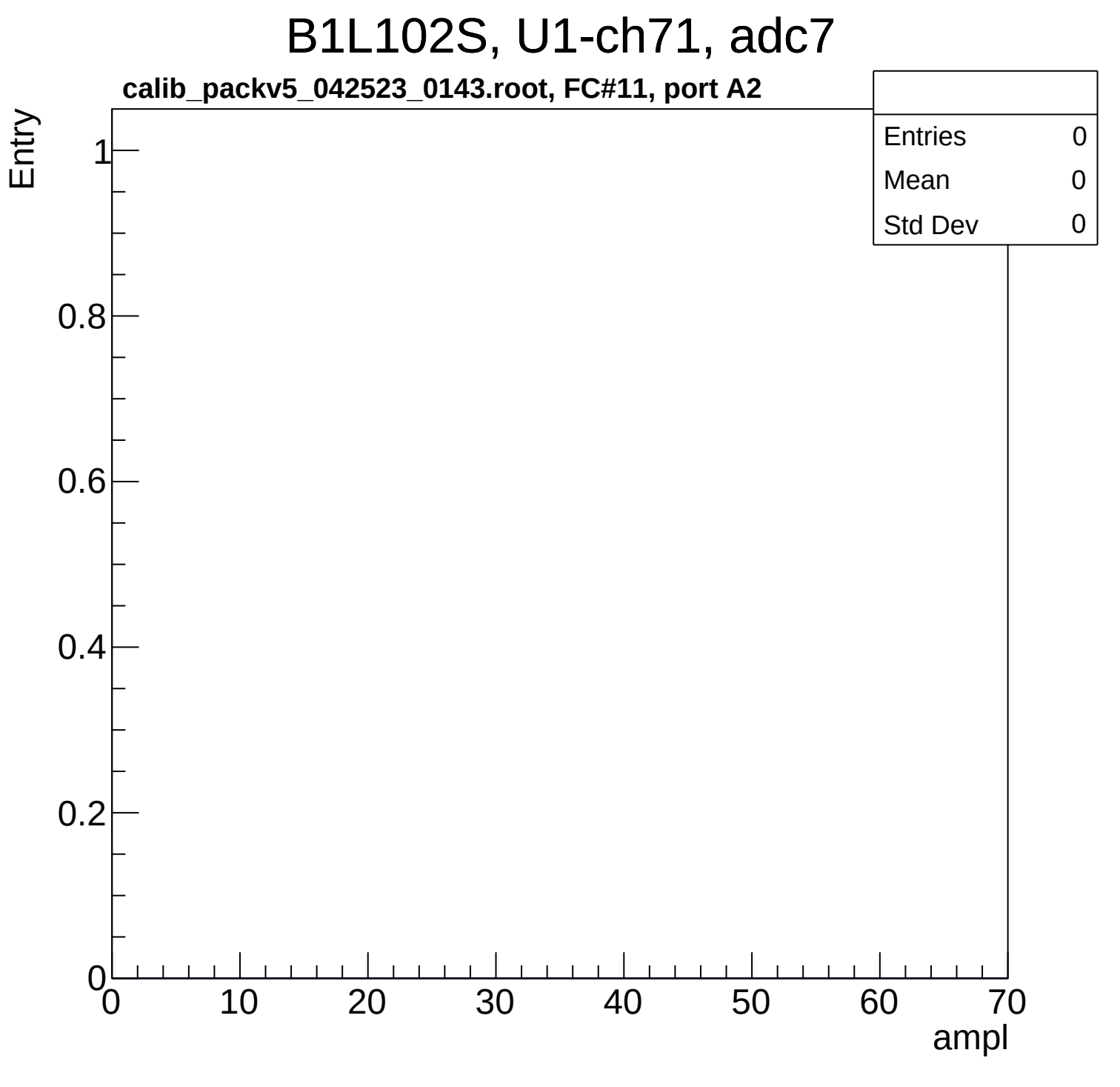
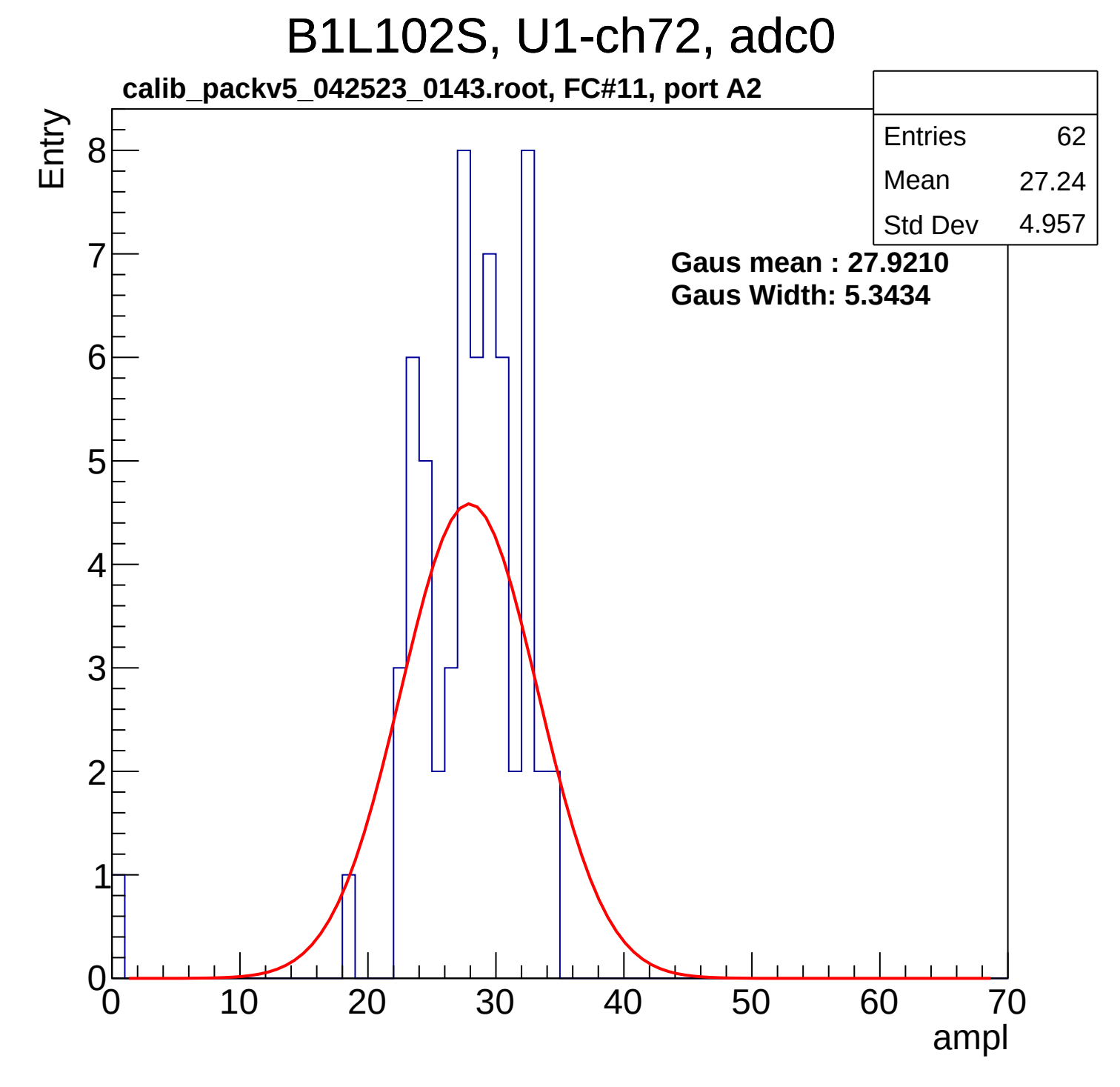
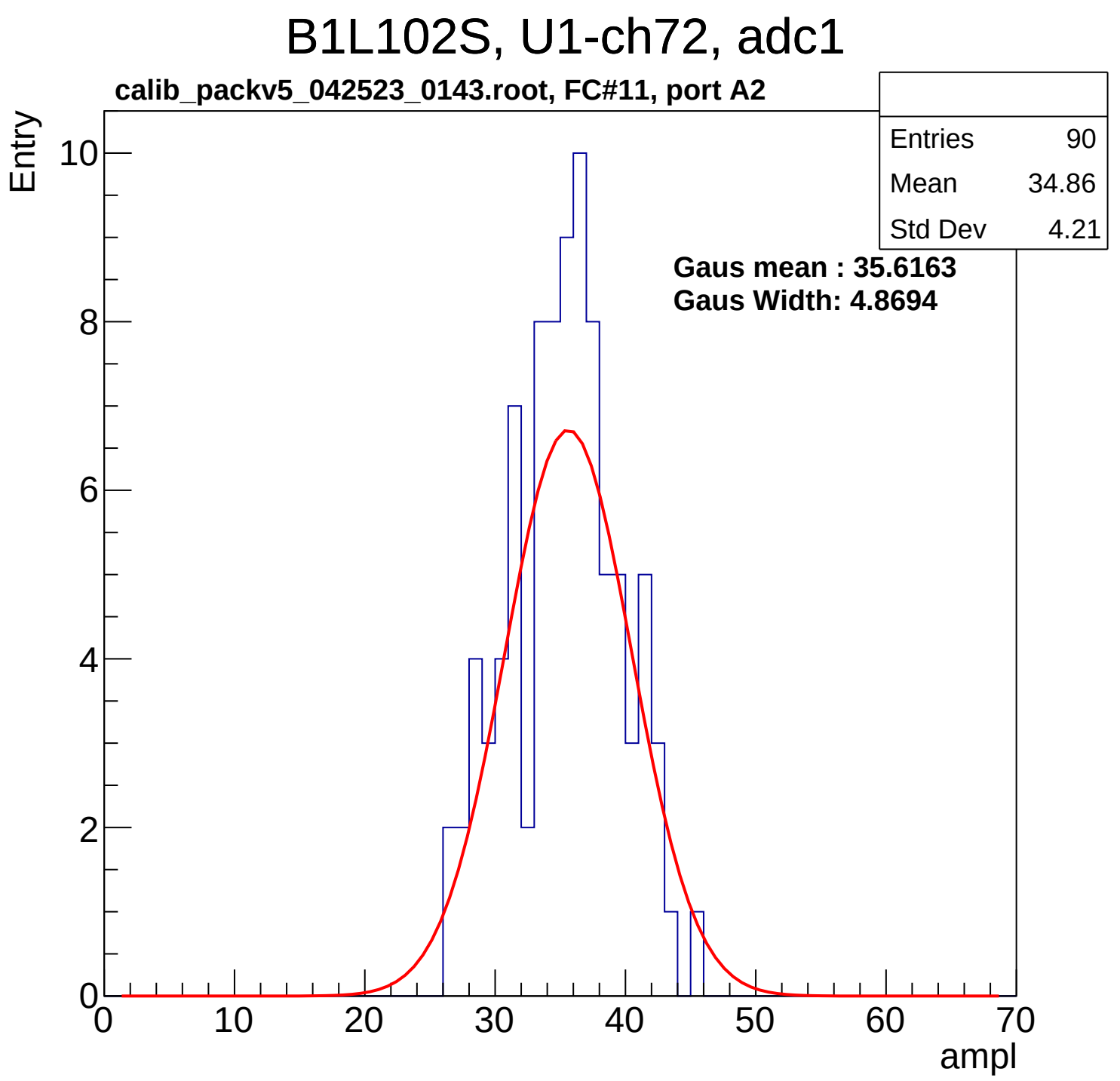
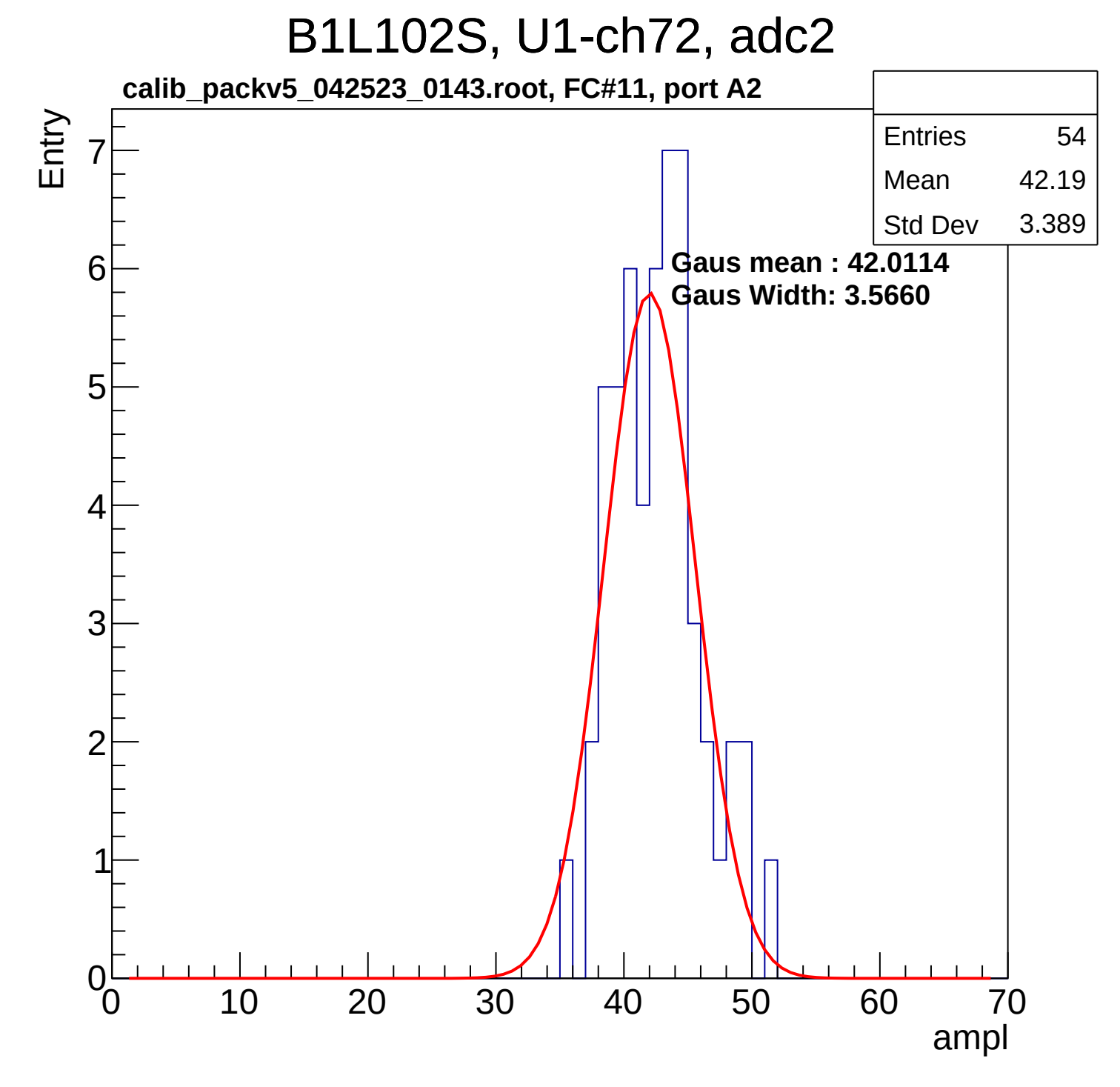
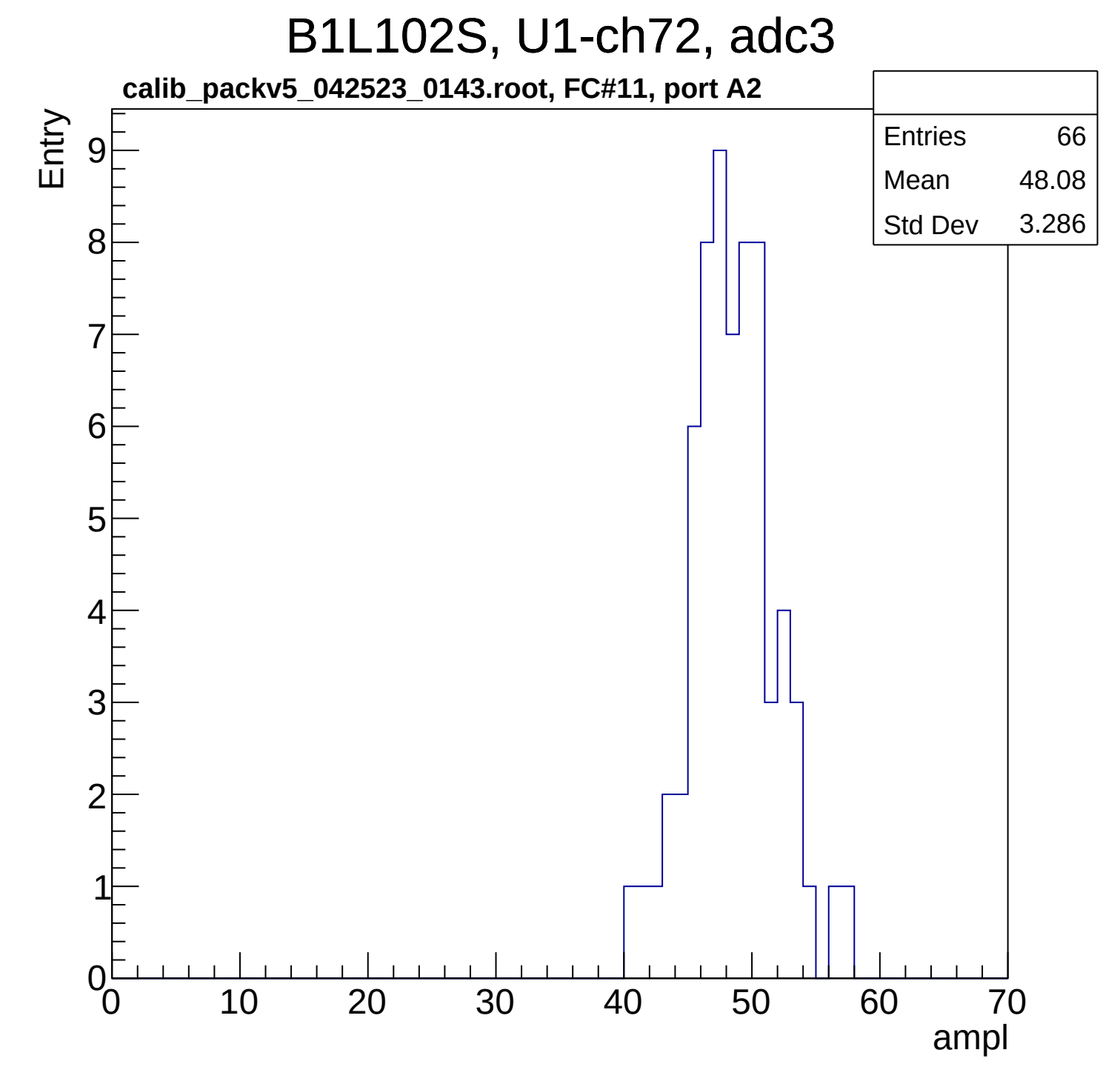

B1L102S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 57.78
Std Dev 14.07
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

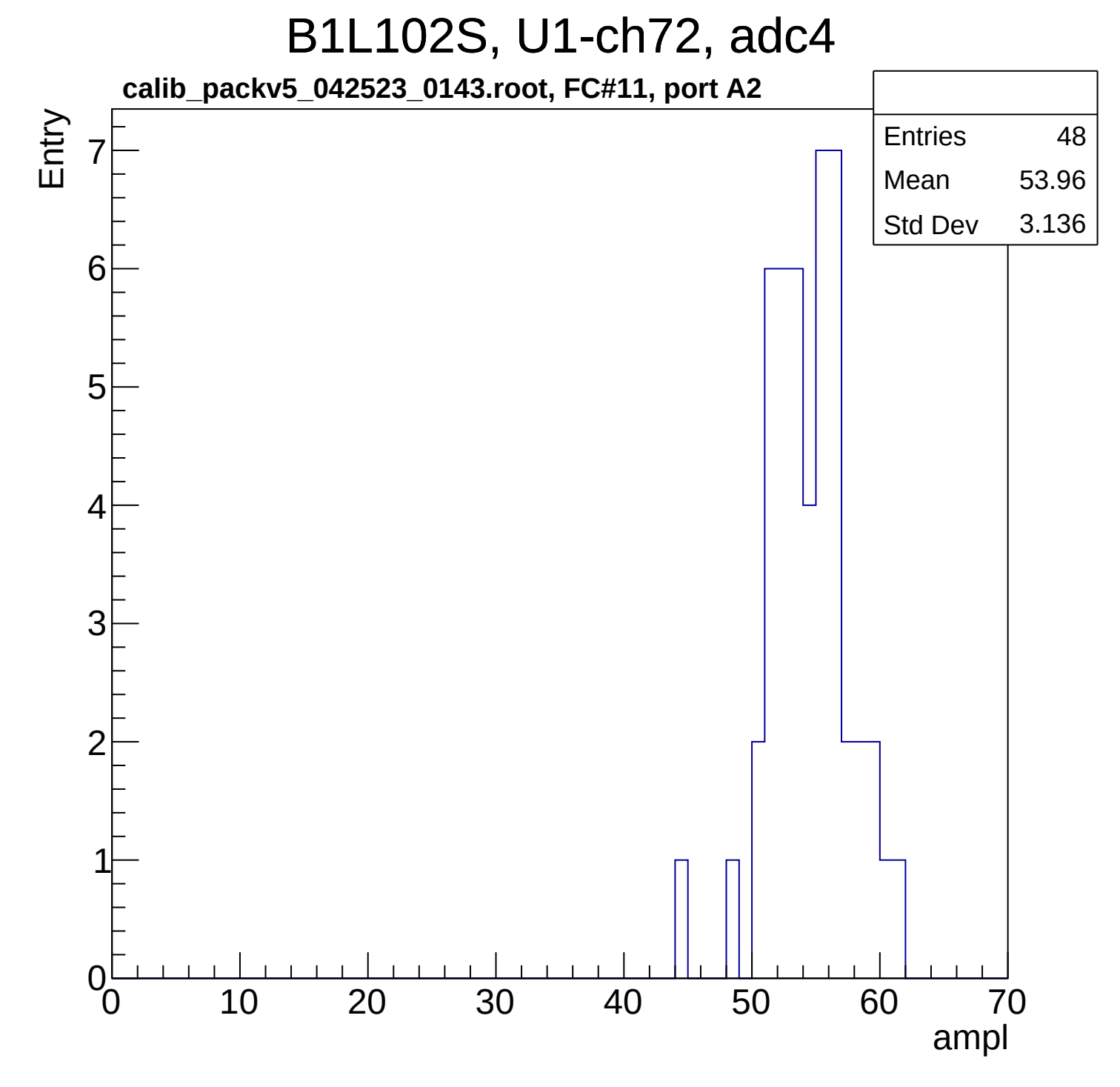

B1L102S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

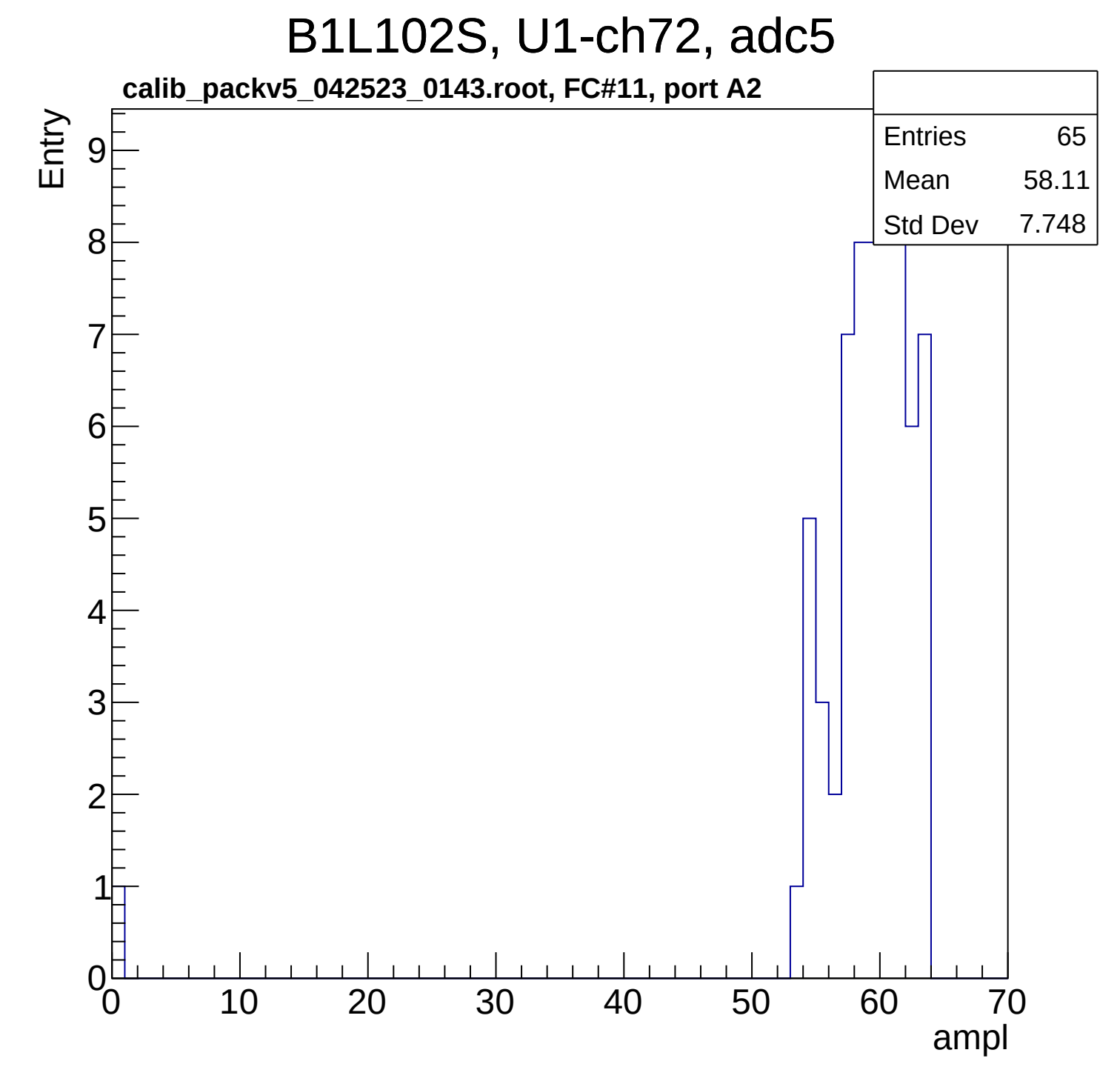

B1L102S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 27.24
Std Dev 4.957
Gaus mean : 27.9210
Gaus Width: 5.3434
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 90
Mean 34.86
Std Dev 4.21
Gaus mean : 35.6163
Gaus Width: 4.8694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 42.19
Std Dev 3.389
Gaus mean : 42.0114
Gaus Width: 3.5660
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 48.08
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 53.96
Std Dev 3.136
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 58.11
Std Dev 7.748
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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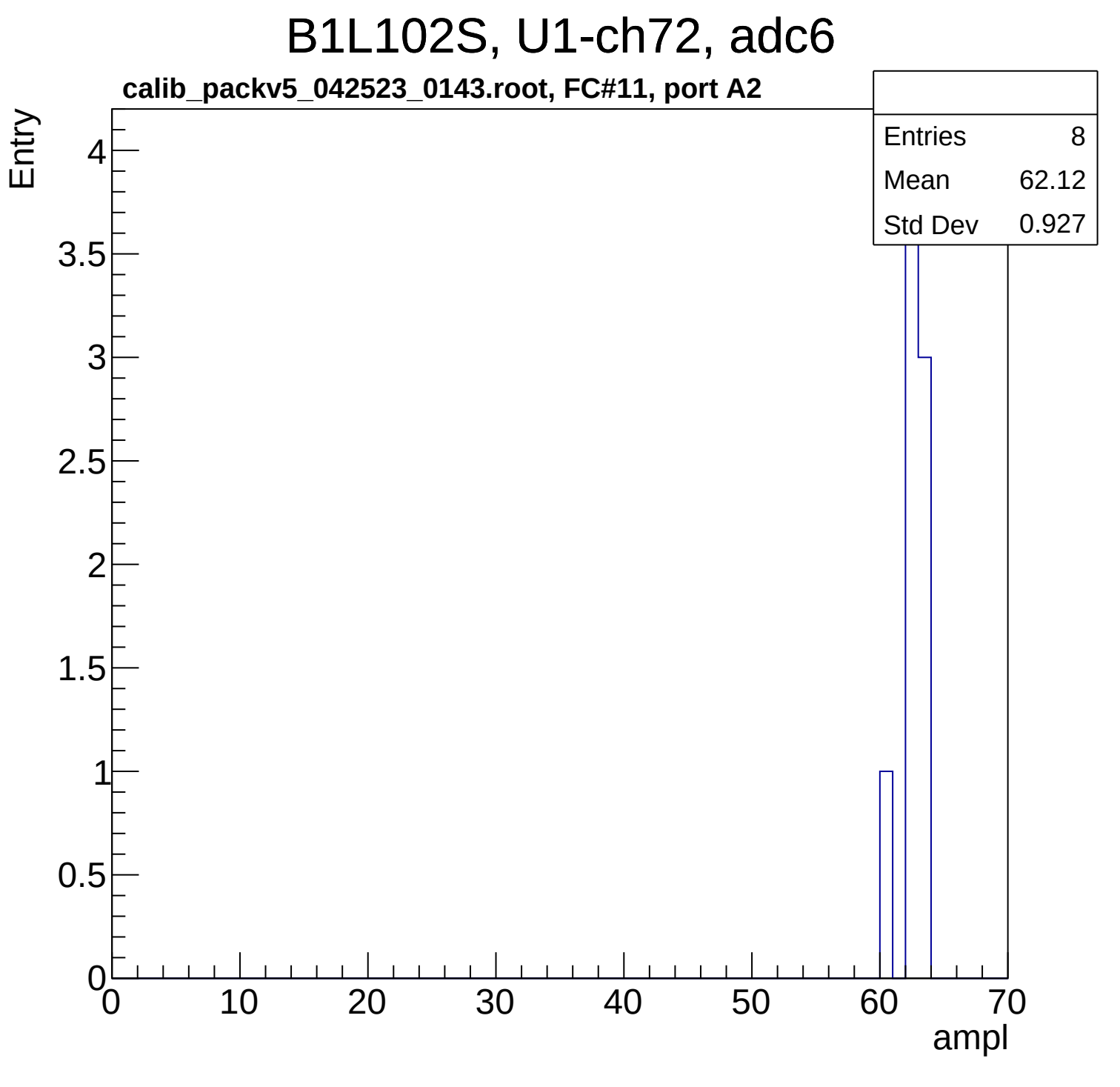
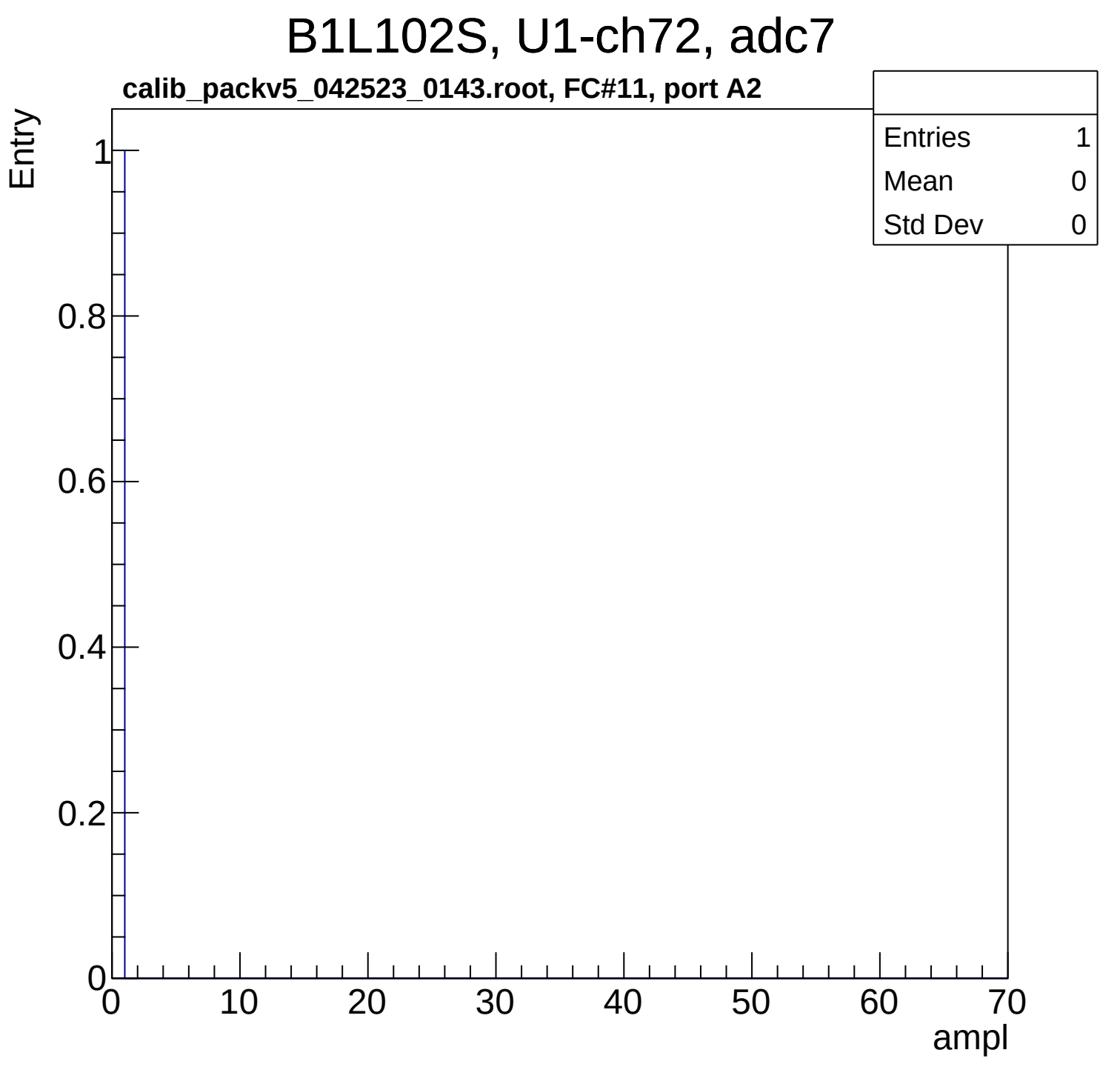
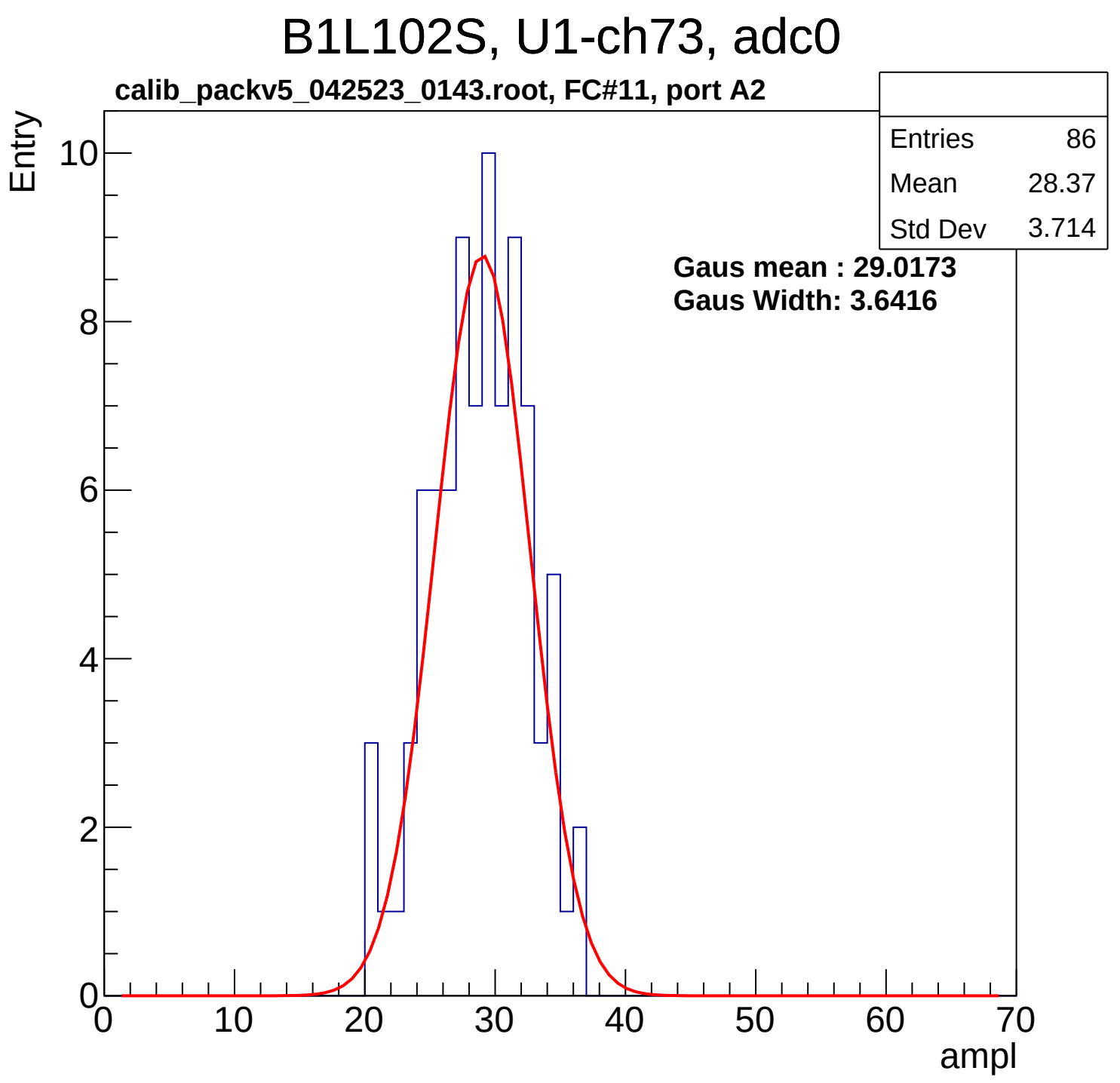
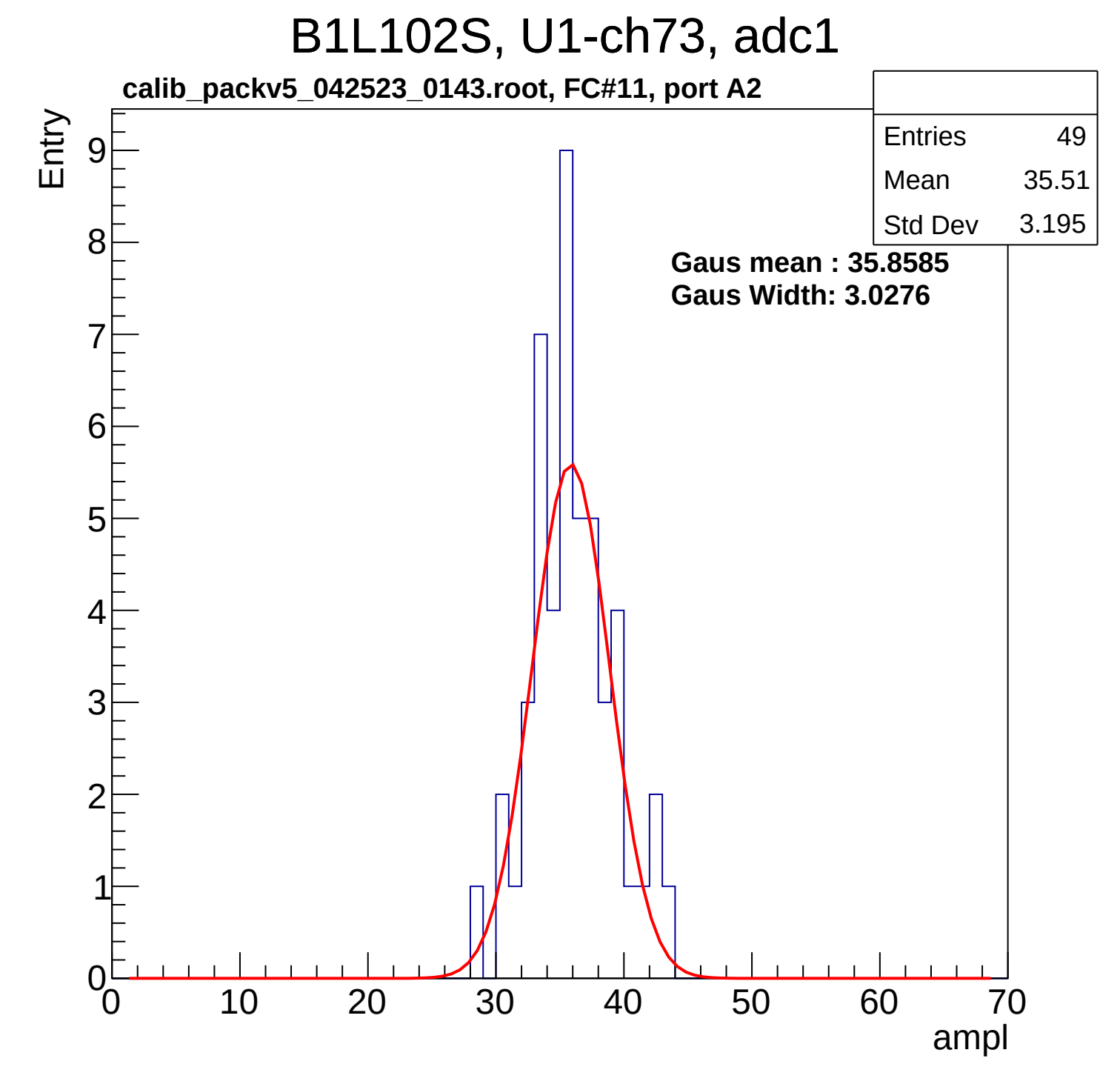
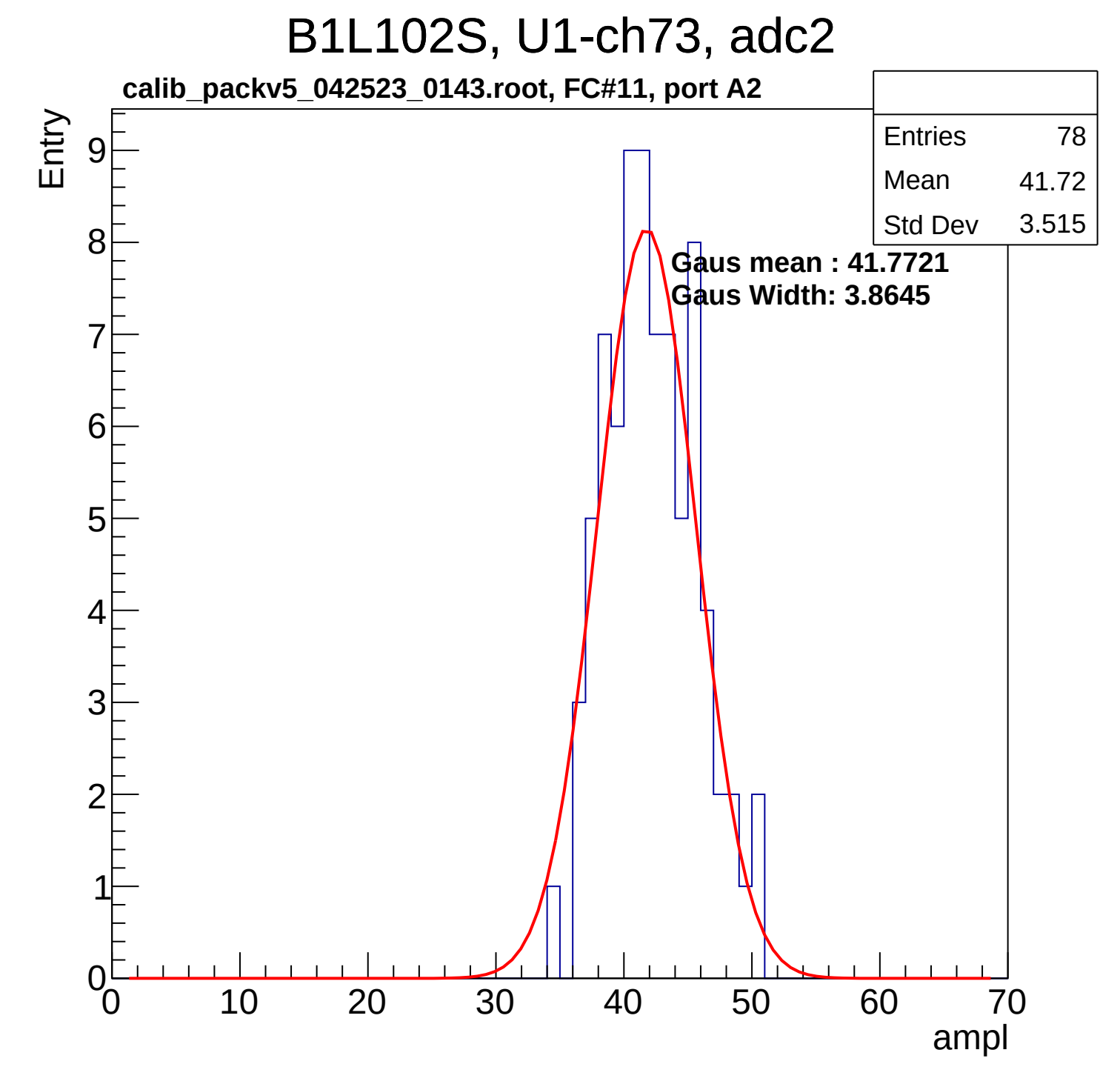
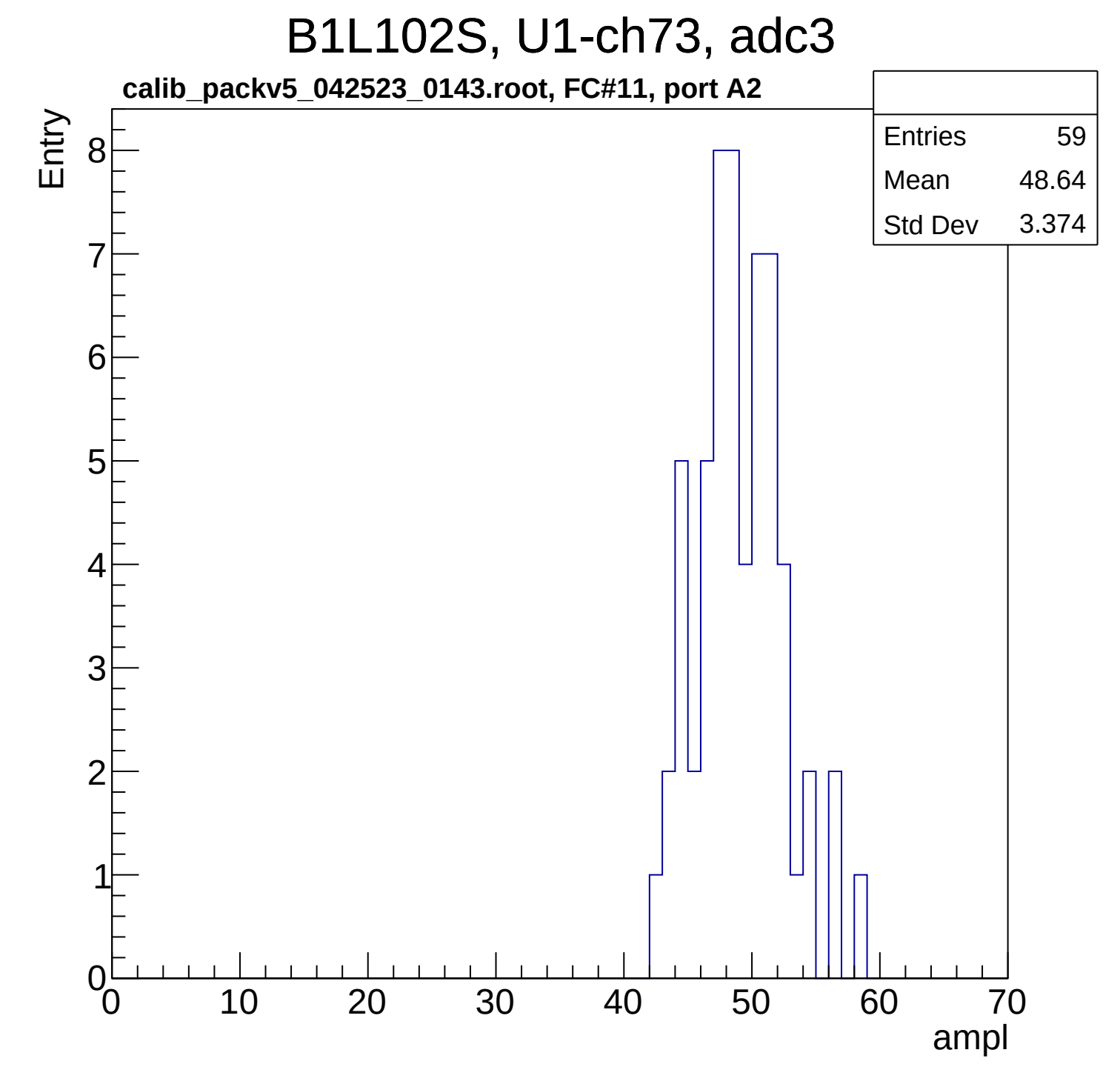

B1L102S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

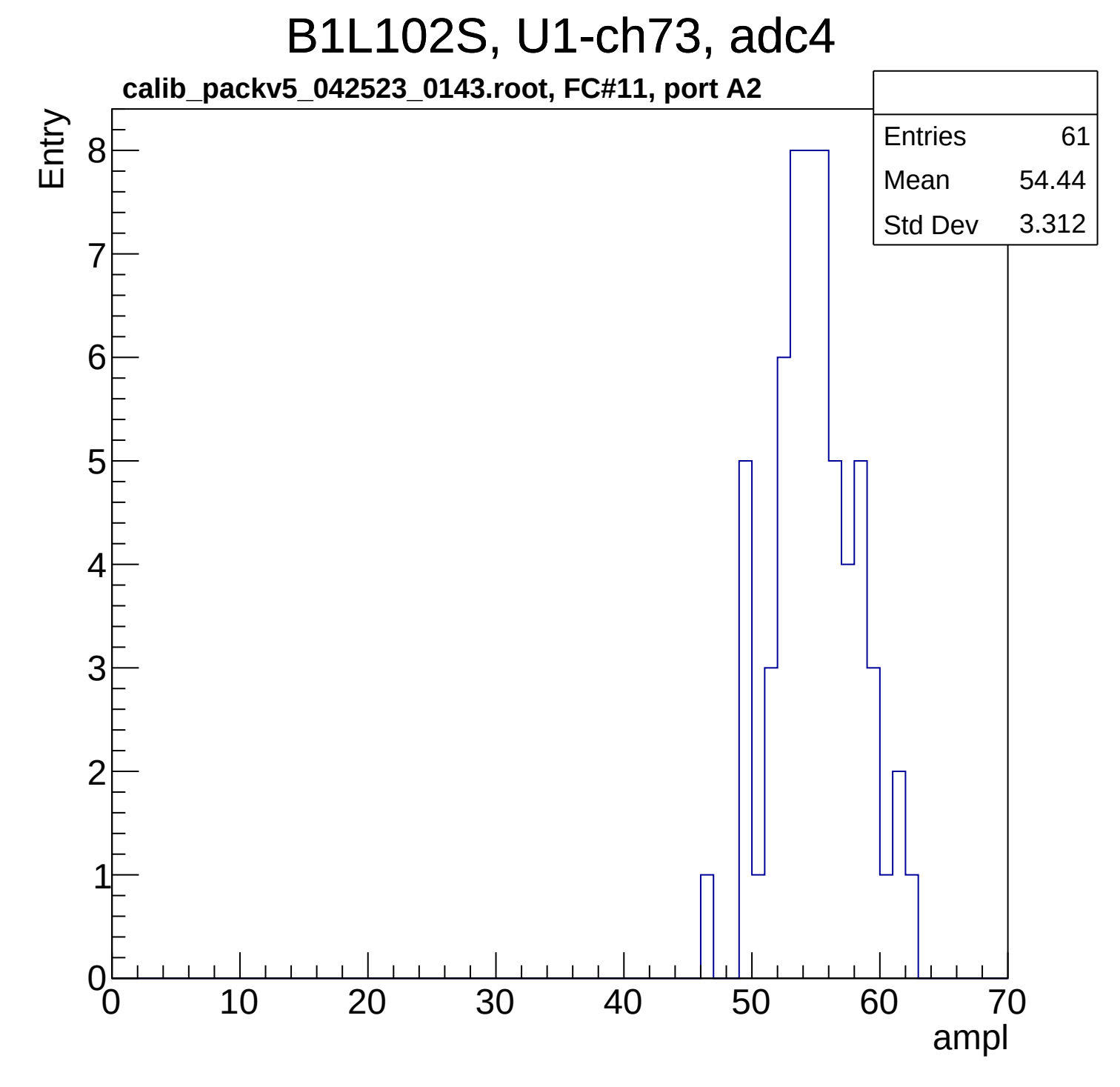

B1L102S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

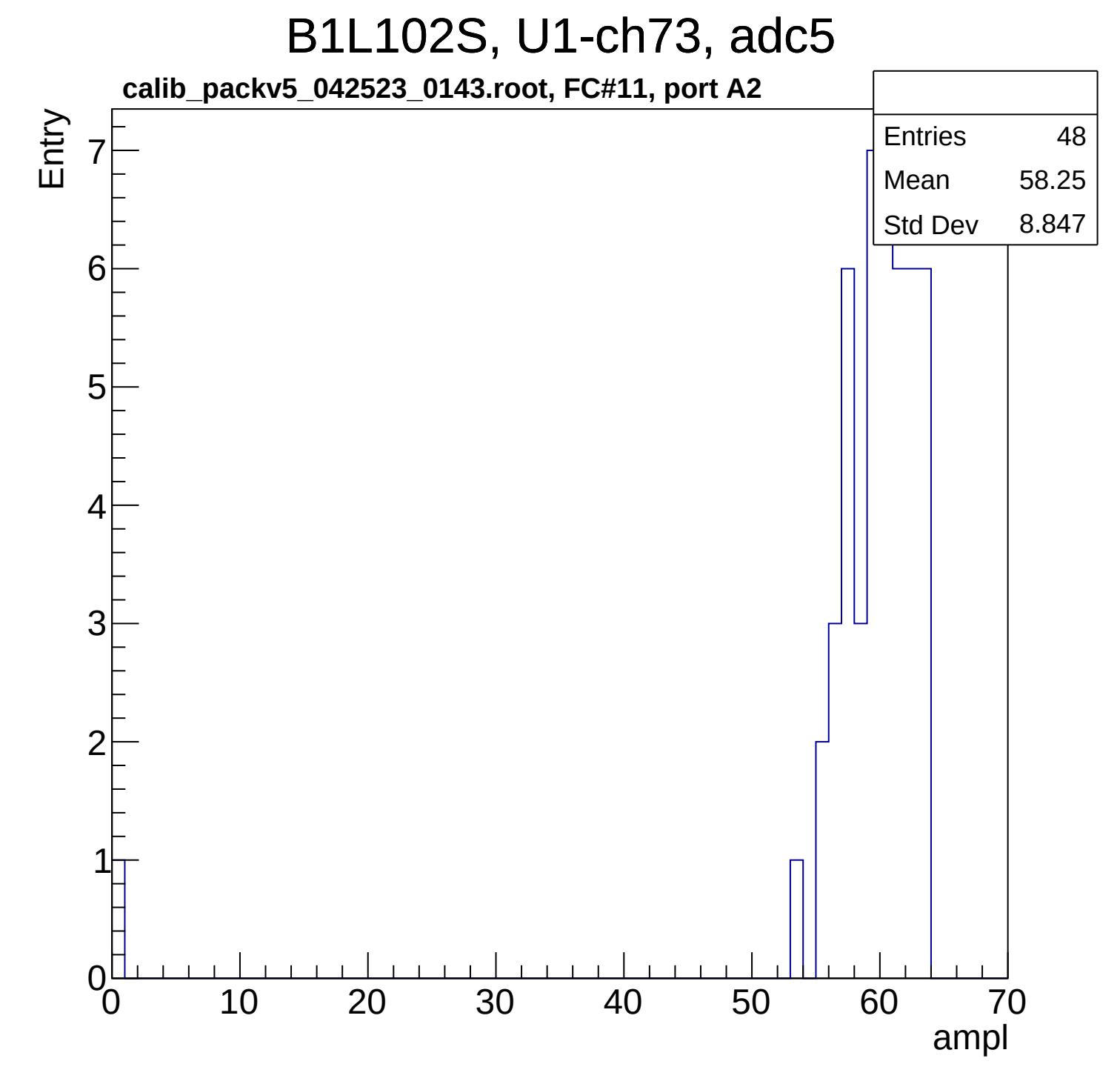

B1L102S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 28.37
Std Dev 3.714
Gaus mean : 29.0173
Gaus Width: 3.6416
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 35.51
Std Dev 3.195
Gaus mean : 35.8585
Gaus Width: 3.0276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 41.72
Std Dev 3.515
Gaus mean : 41.7721
Gaus Width: 3.8645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.64
Std Dev 3.374
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 54.44
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 58.25
Std Dev 8.847
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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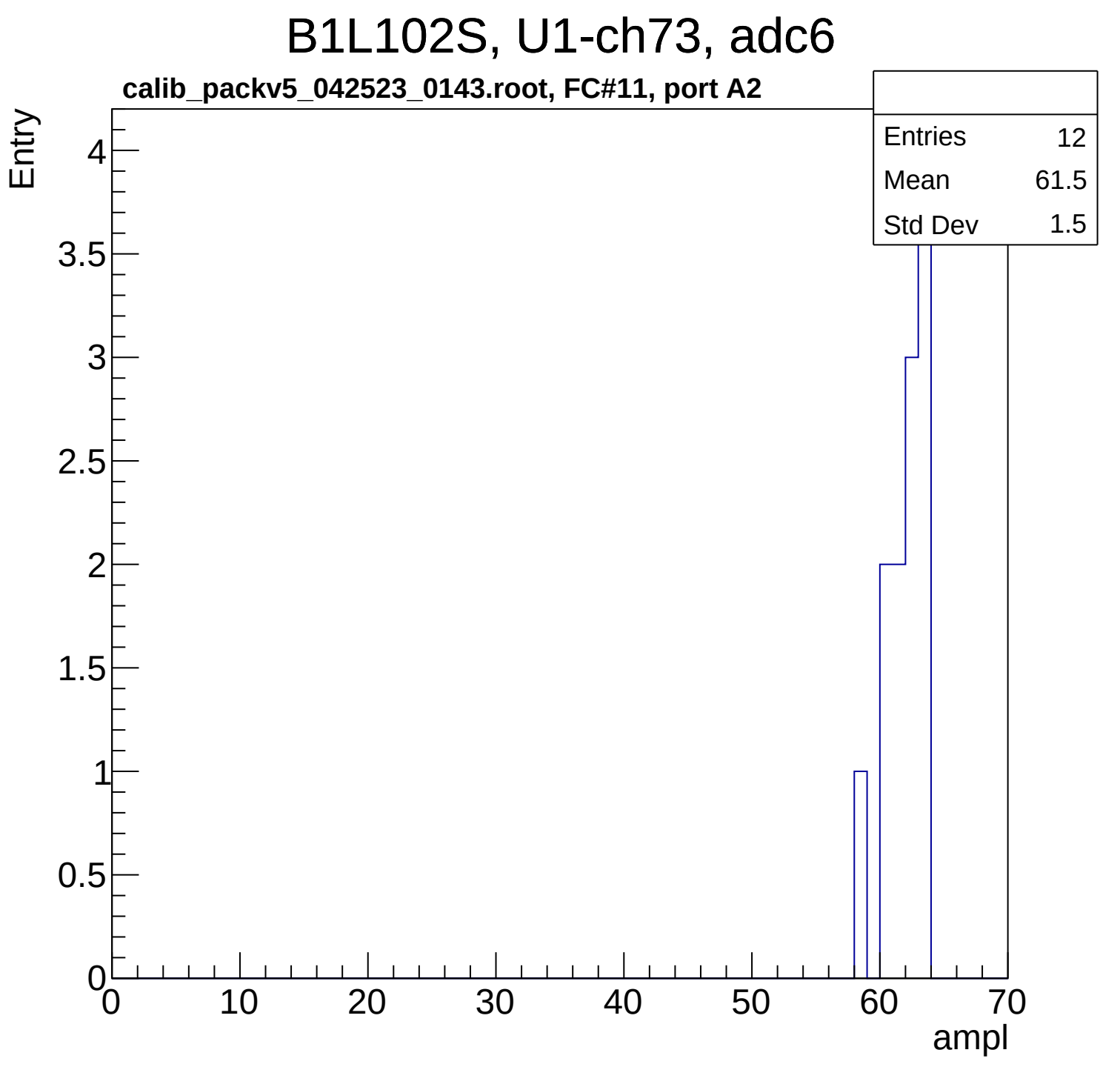
B1L102S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.5
Std Dev 1.5
Entry
ampl
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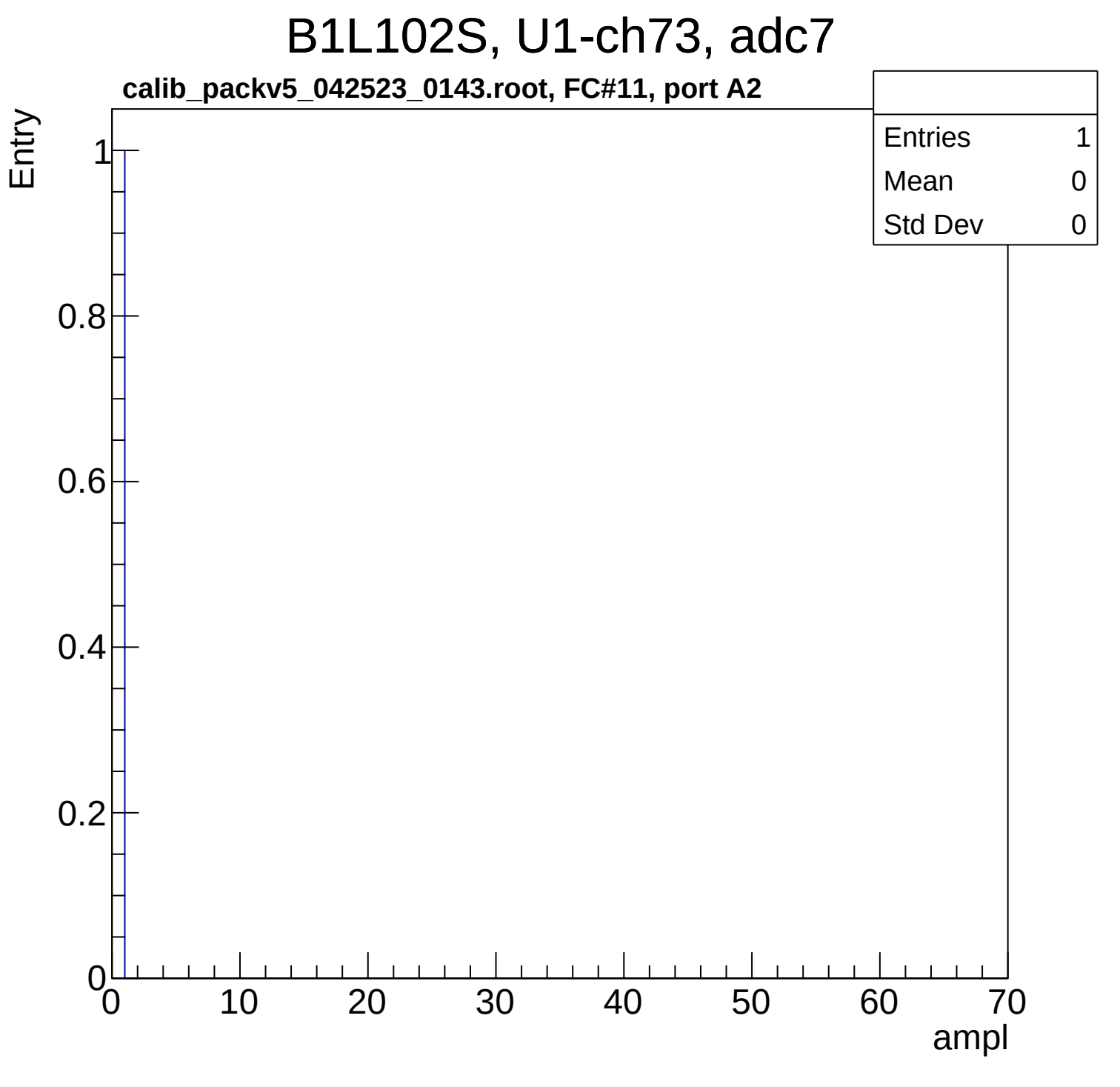
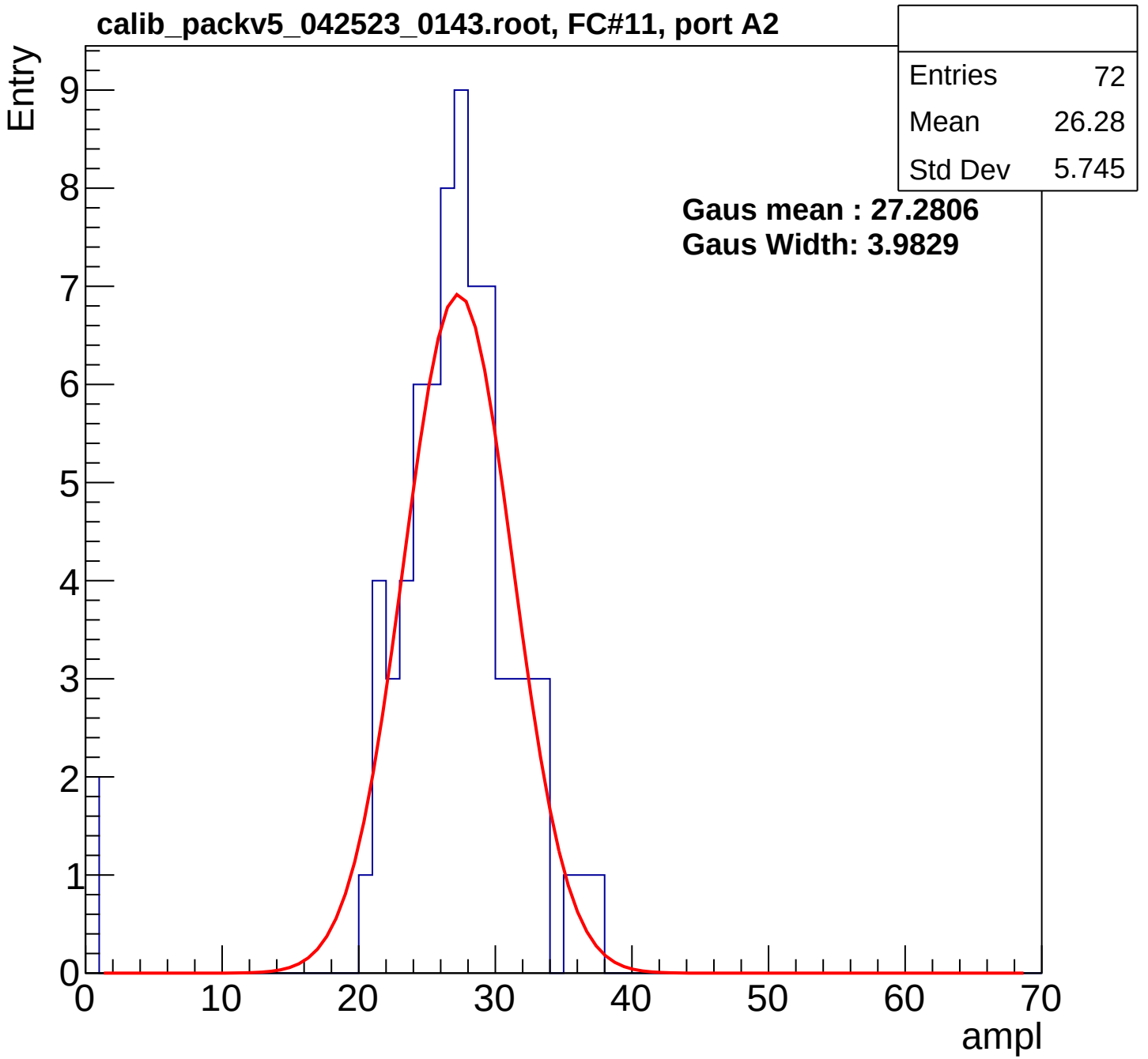
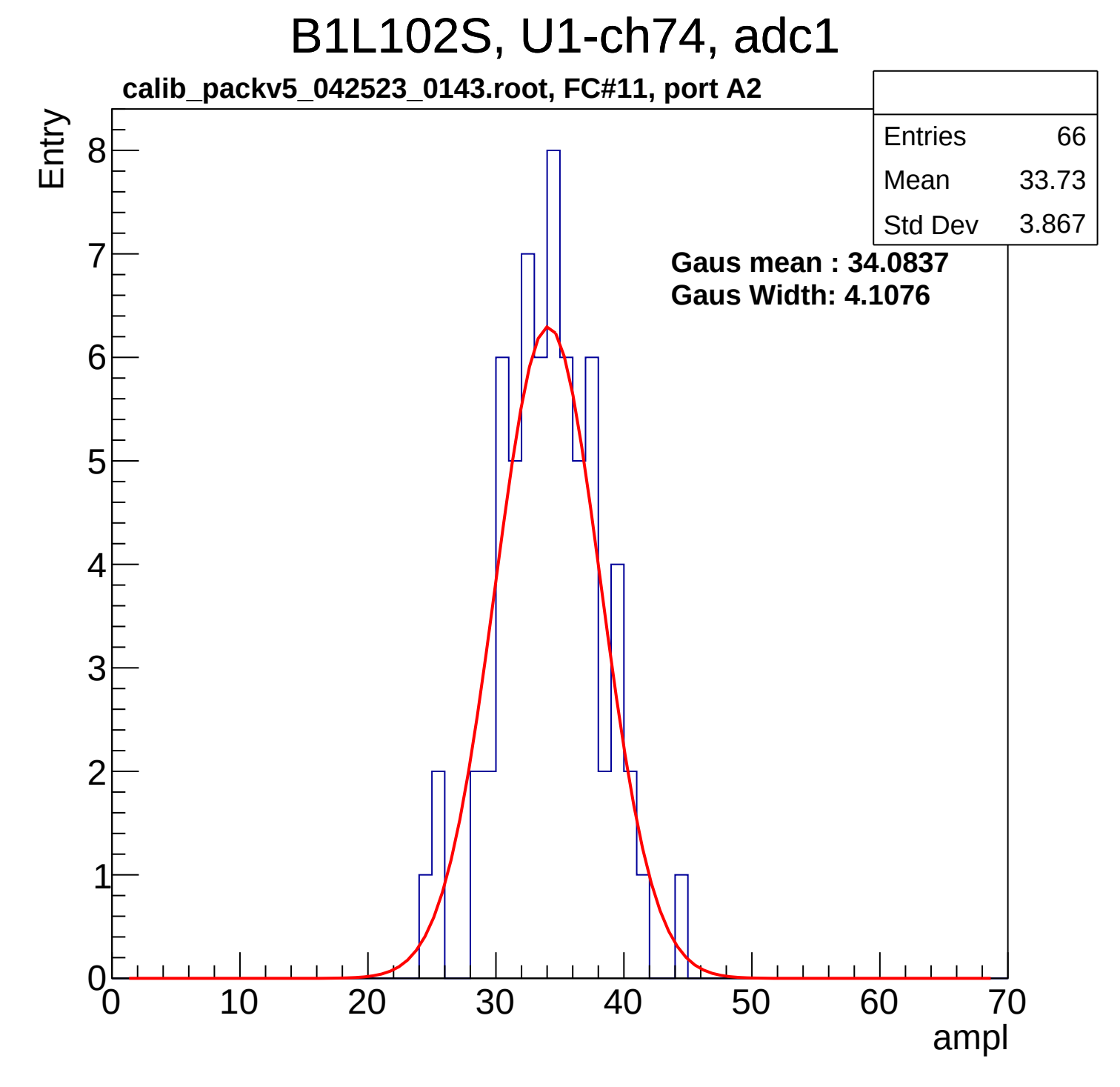
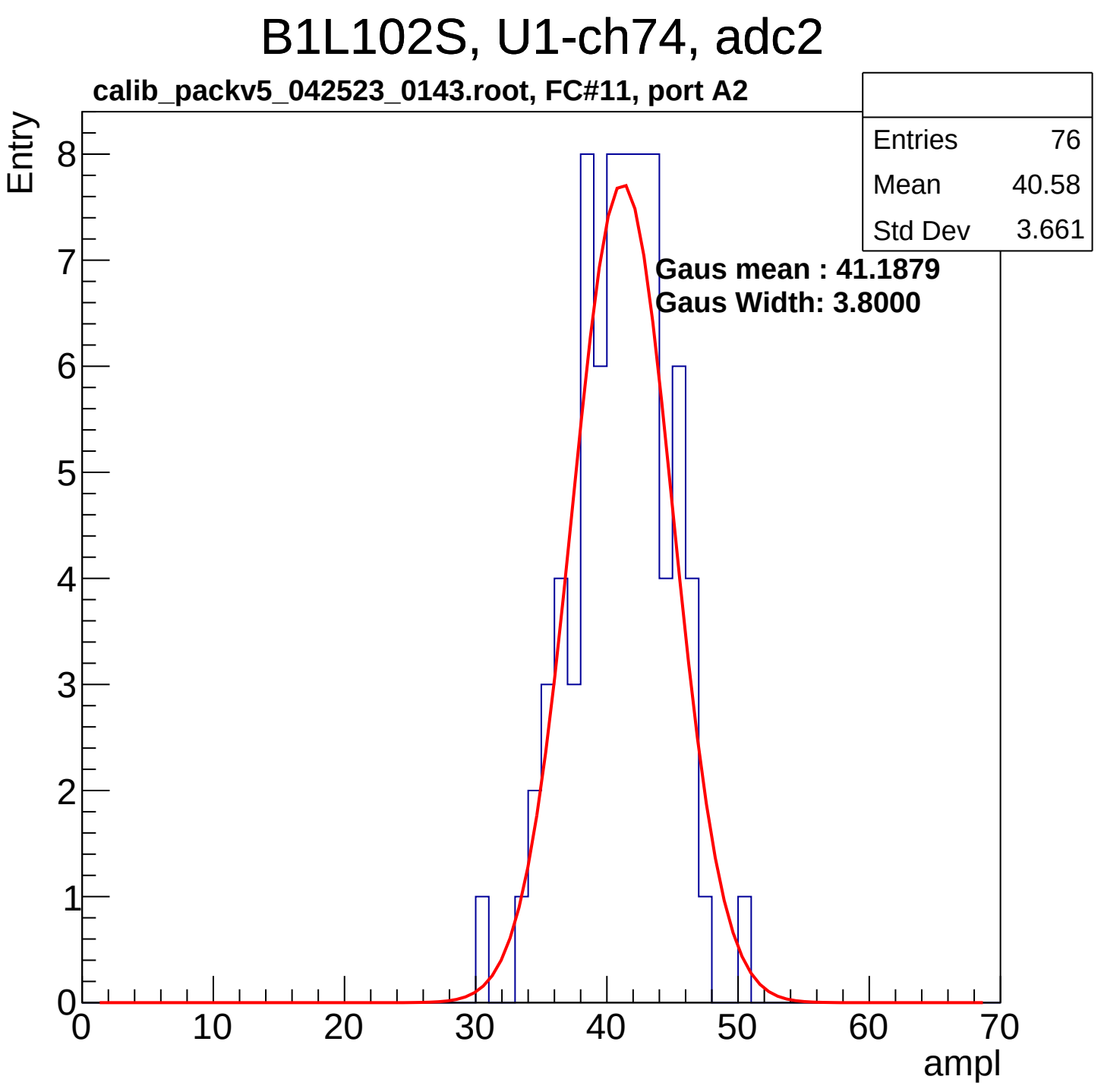
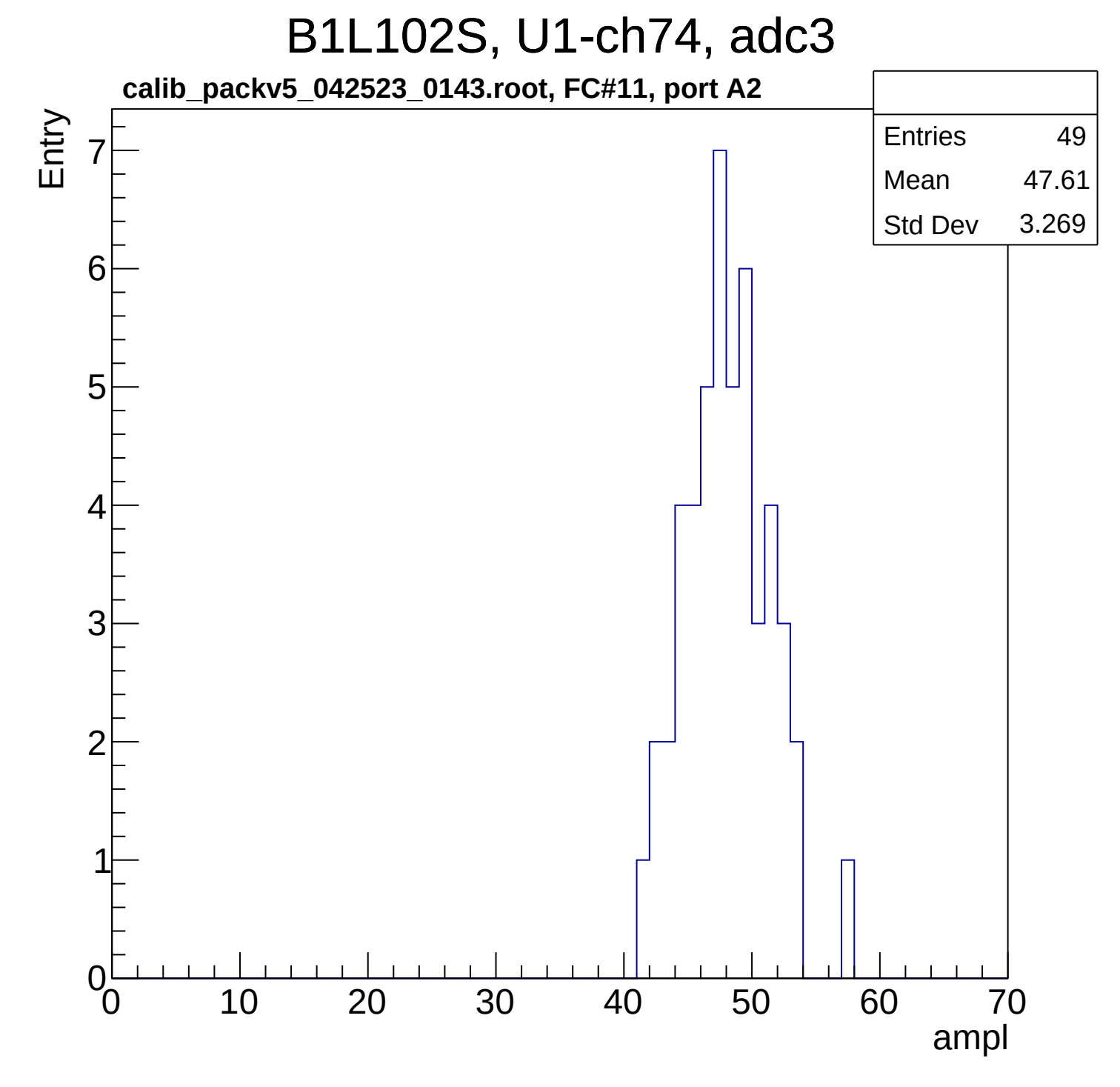
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 26.28
Std Dev 5.745
Gaus mean : 27.2806
Gaus Width: 3.9829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 33.73
Std Dev 3.867
Gaus mean : 34.0837
Gaus Width: 4.1076
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 40.58
Std Dev 3.661
Gaus mean : 41.1879
Gaus Width: 3.8000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 47.61
Std Dev 3.269
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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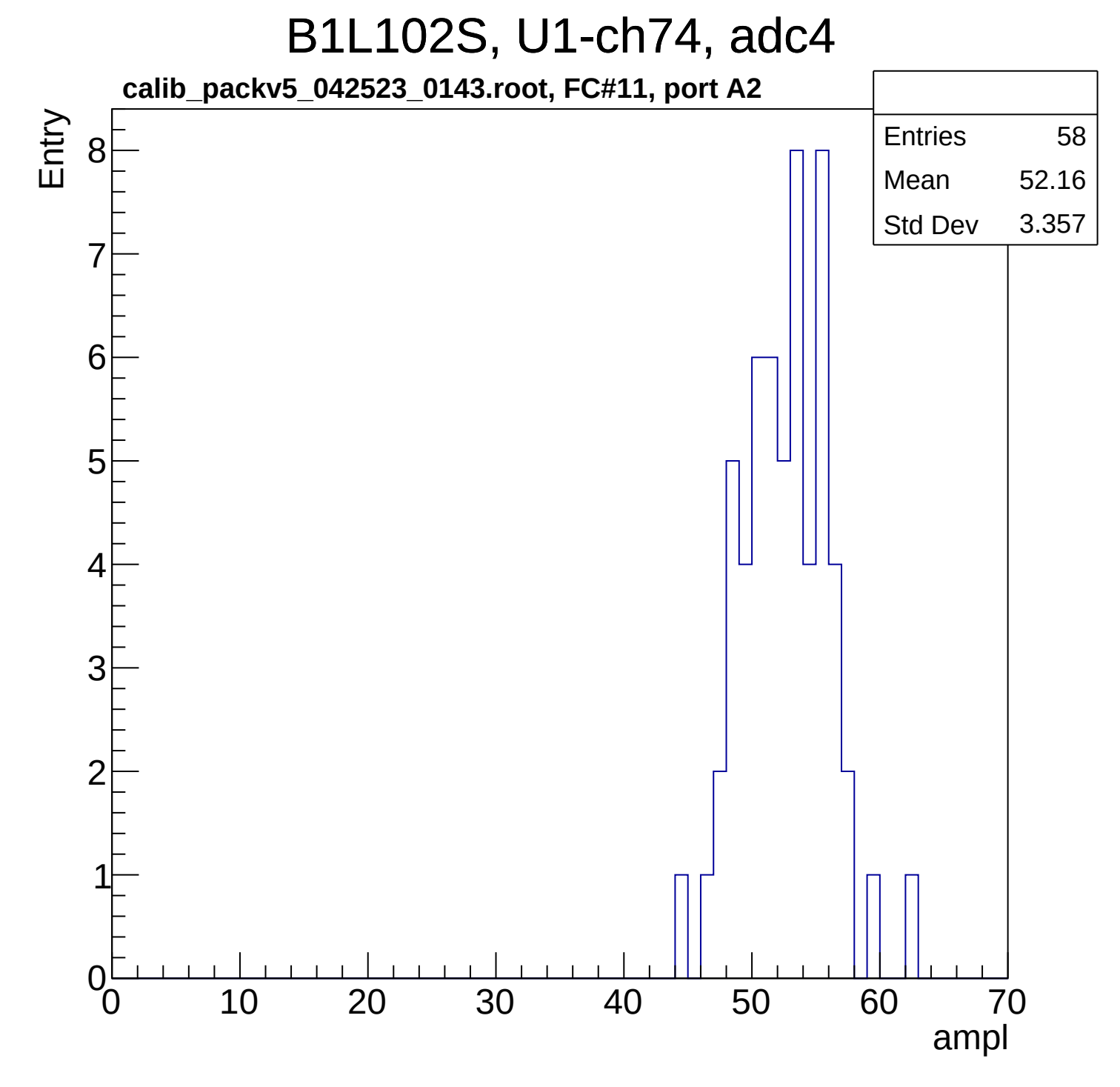
B1L102S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 52.16
Std Dev 3.357
Entry
ampl
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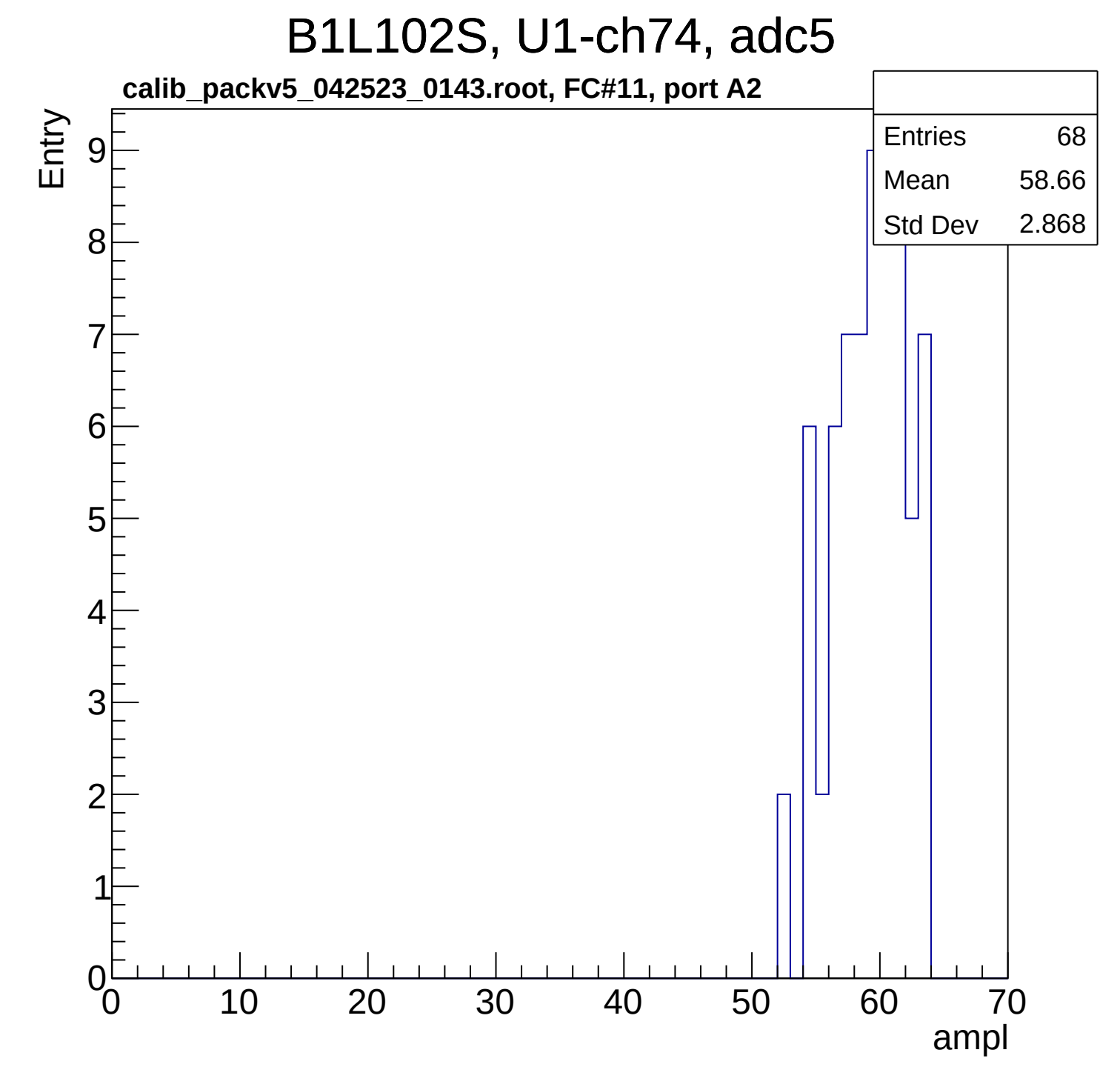
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 58.66
Std Dev 2.868
Entry
ampl
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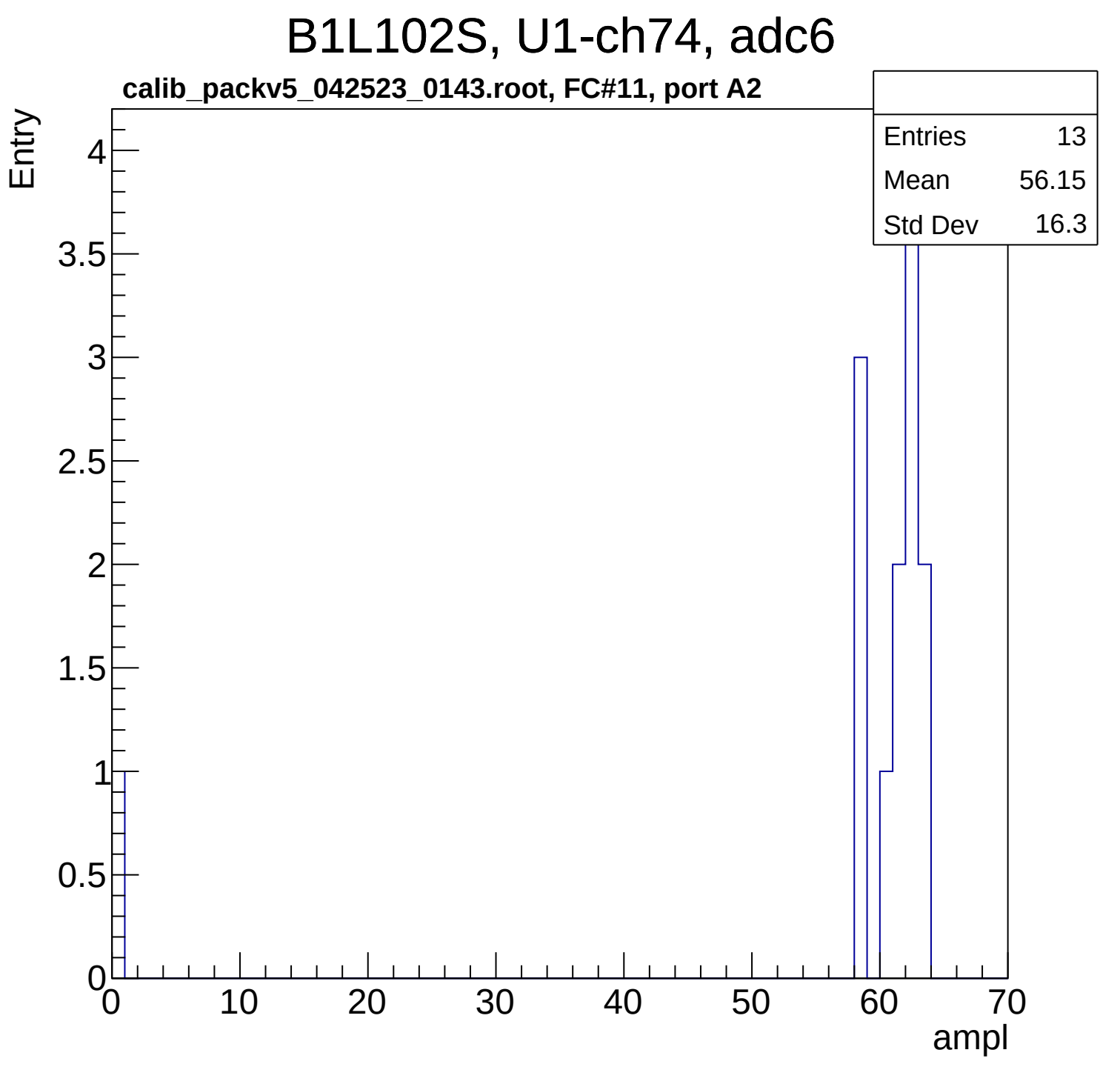
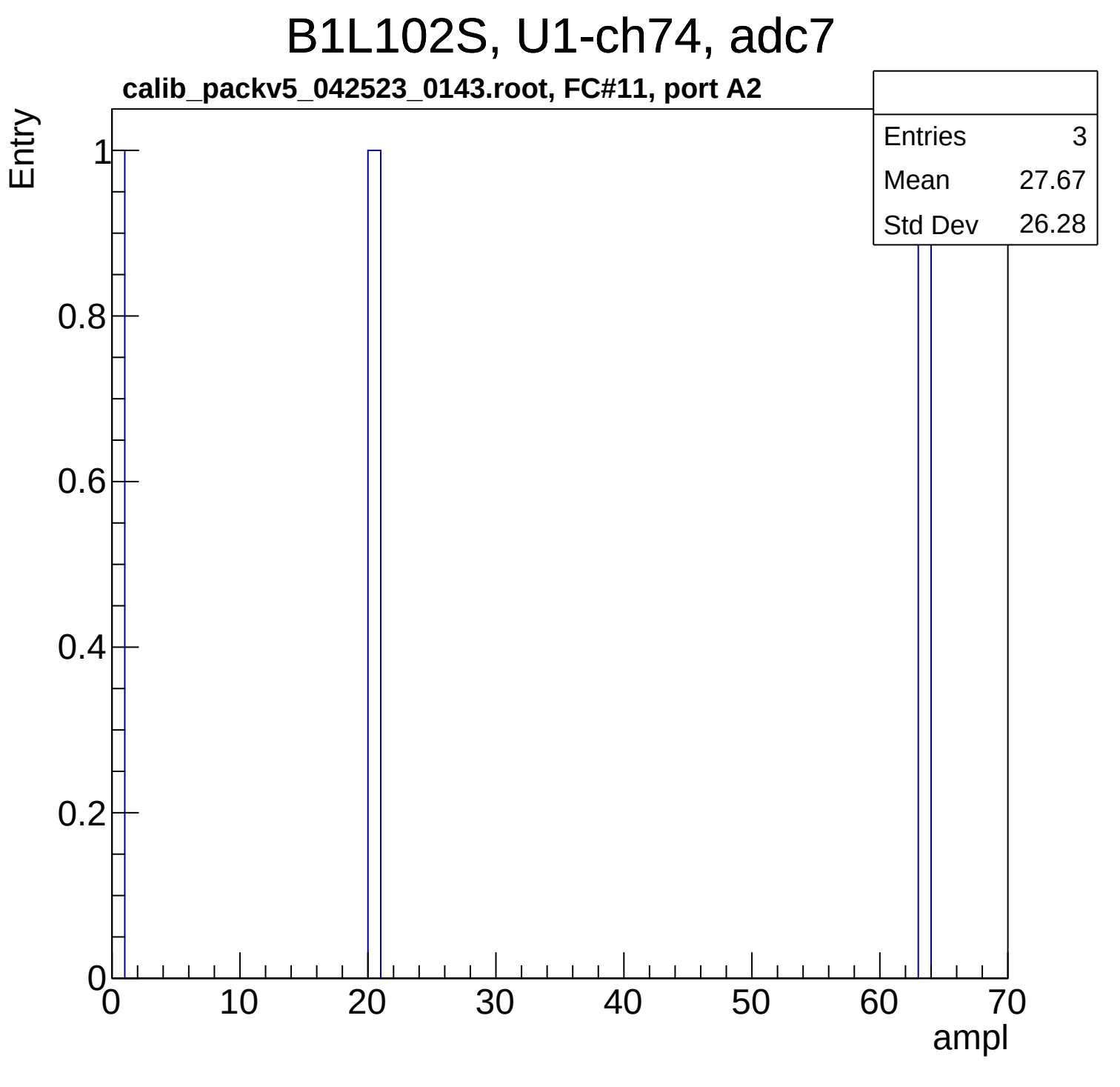
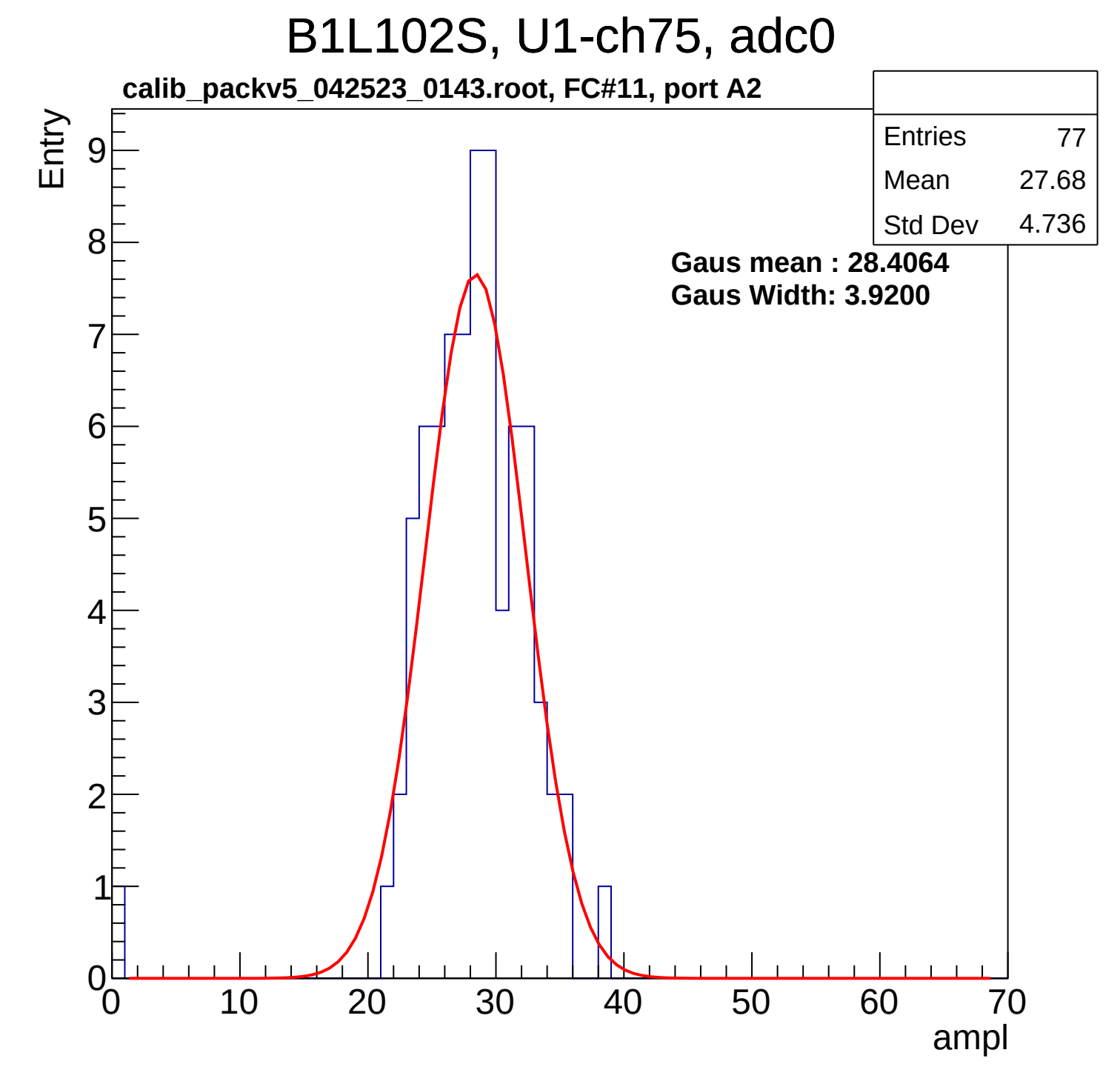
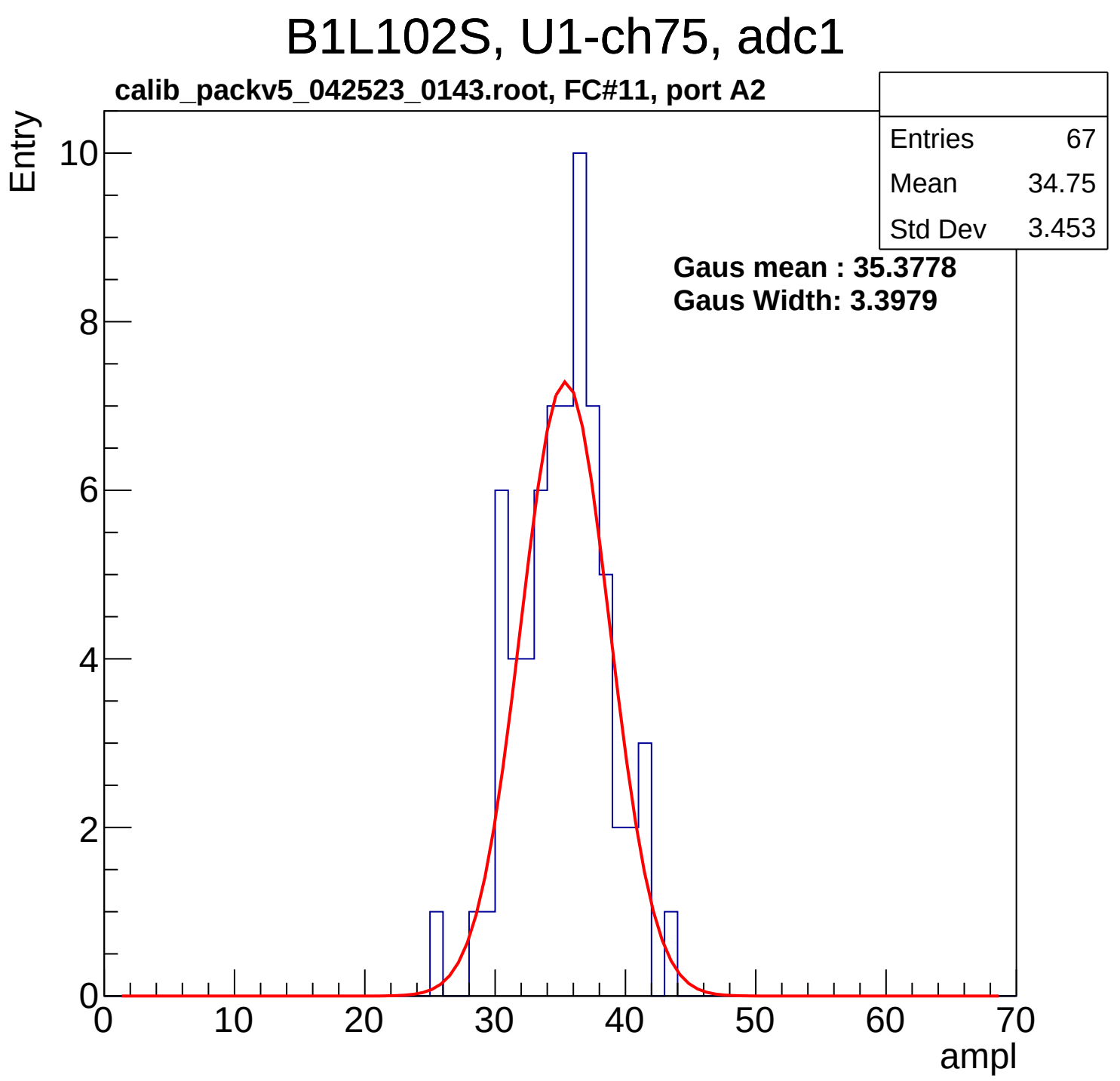
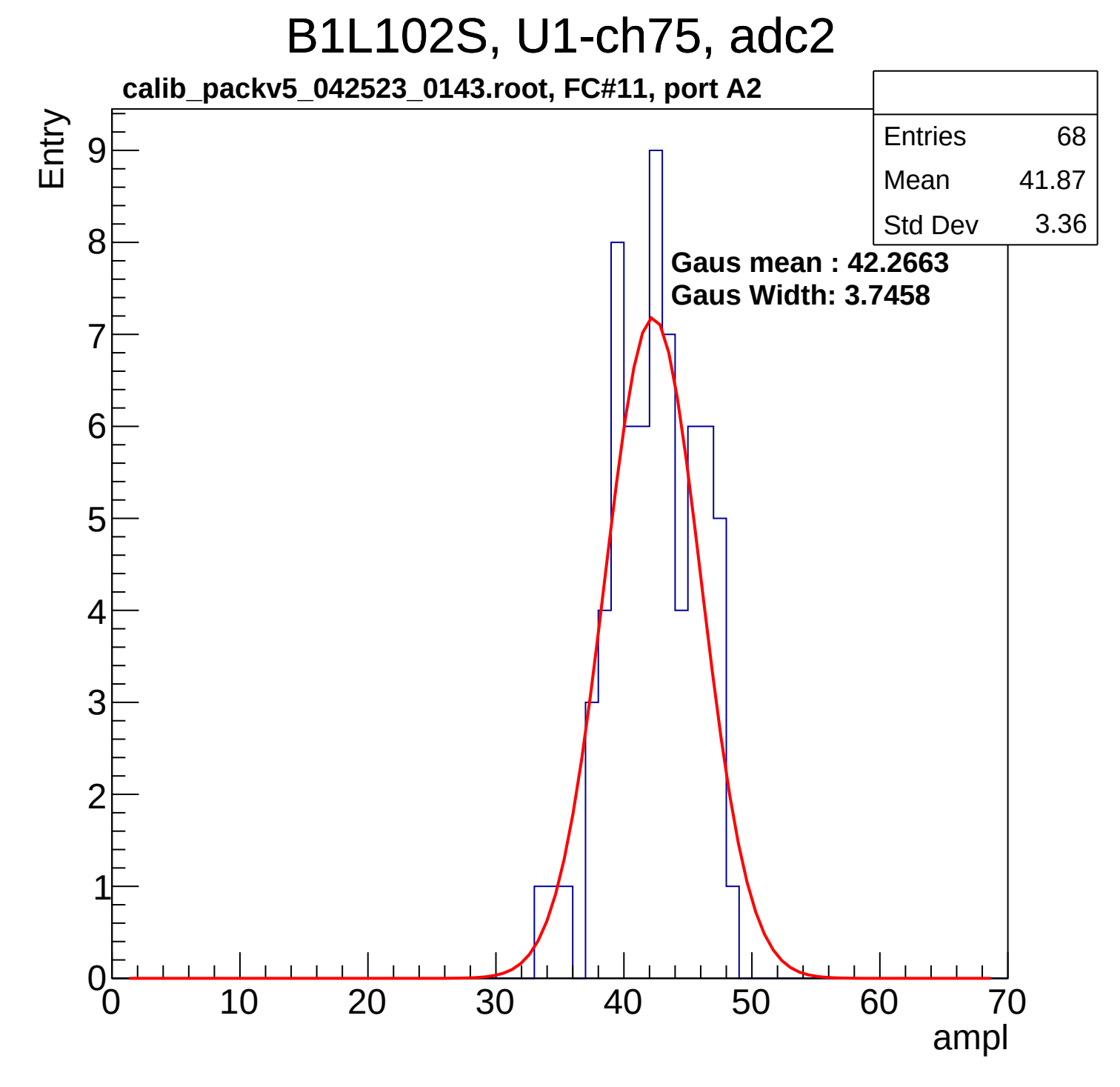
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

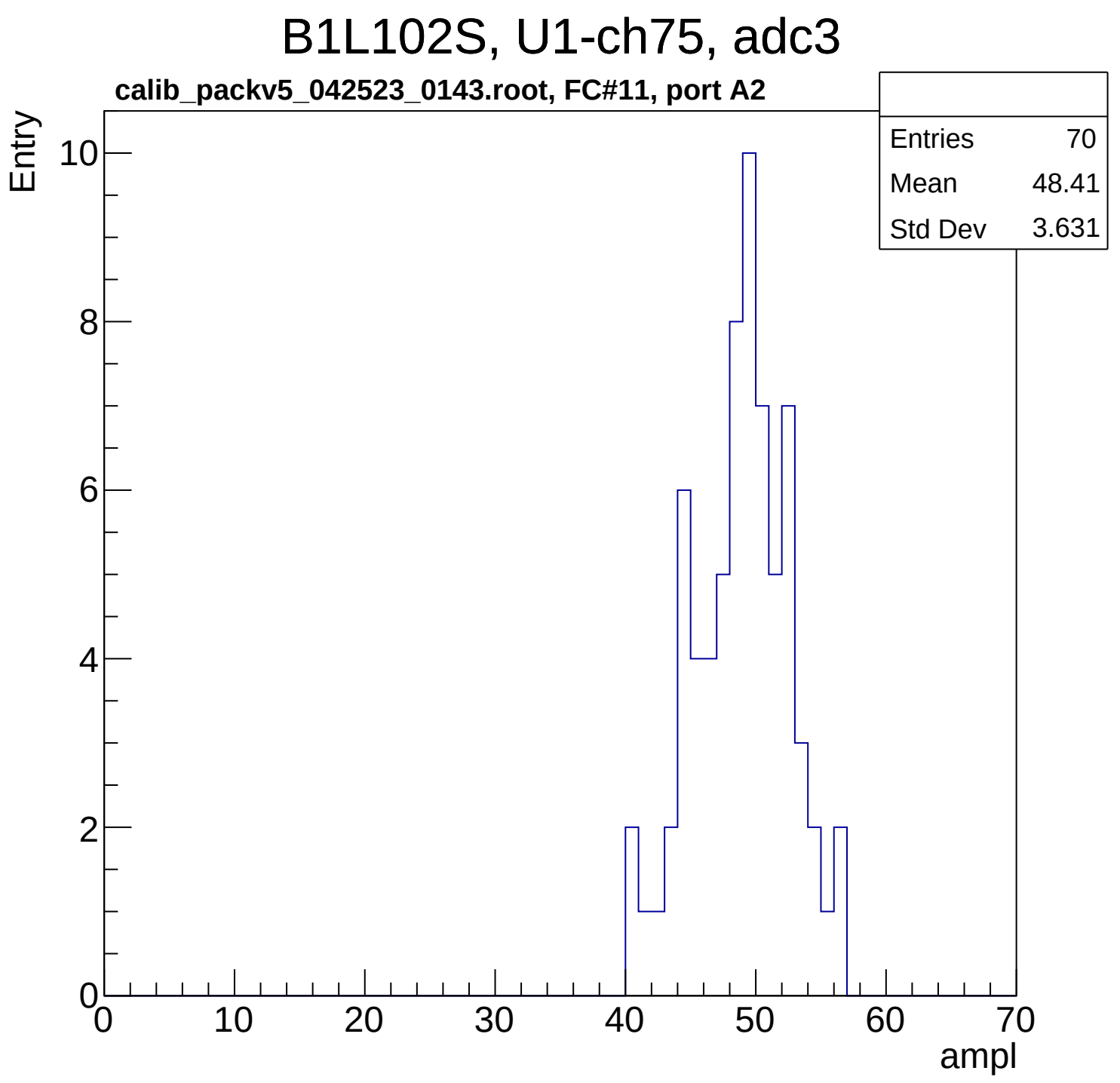

B1L102S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 56.15
Std Dev 16.3
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

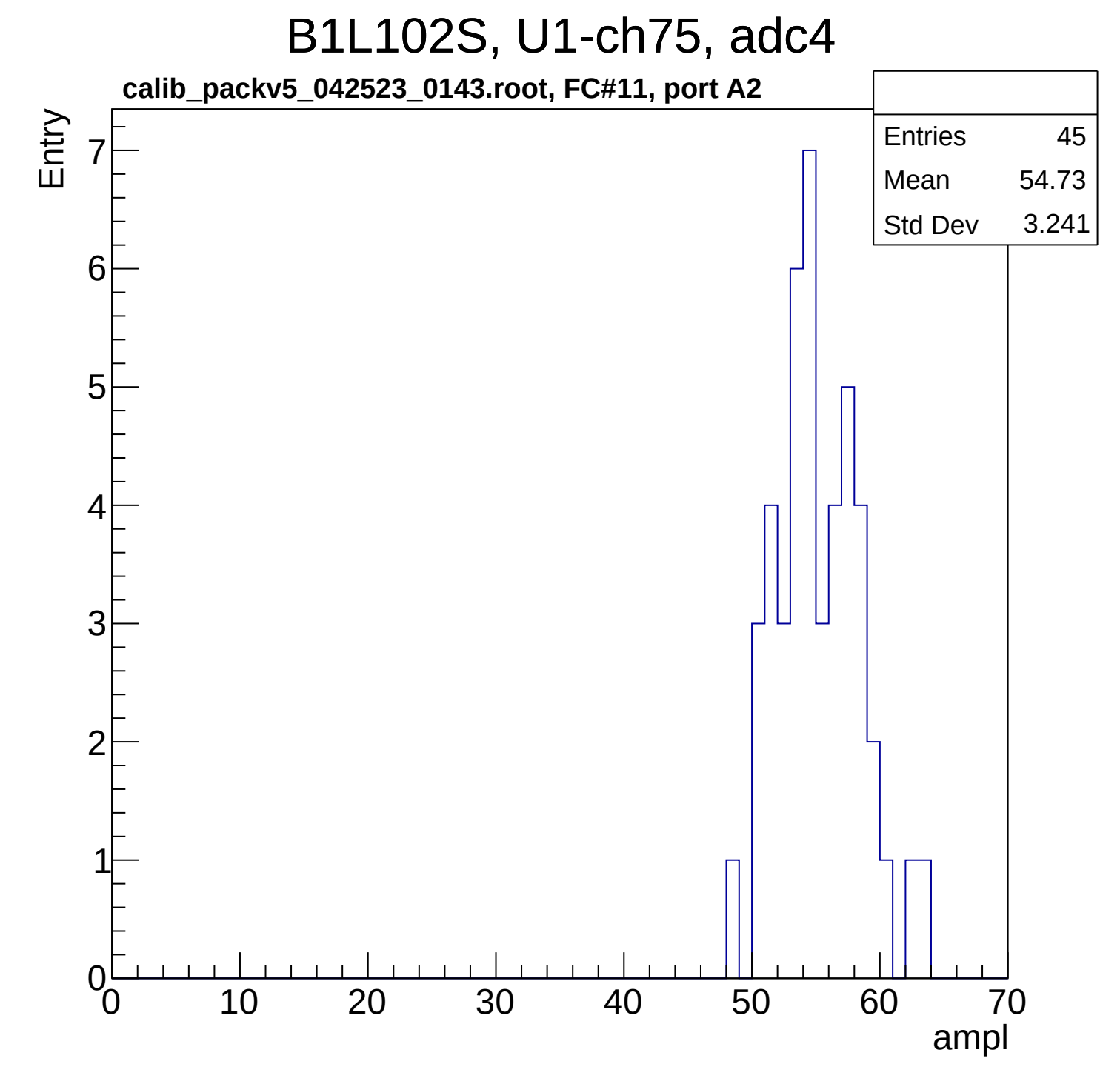

B1L102S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 27.67
Std Dev 26.28
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

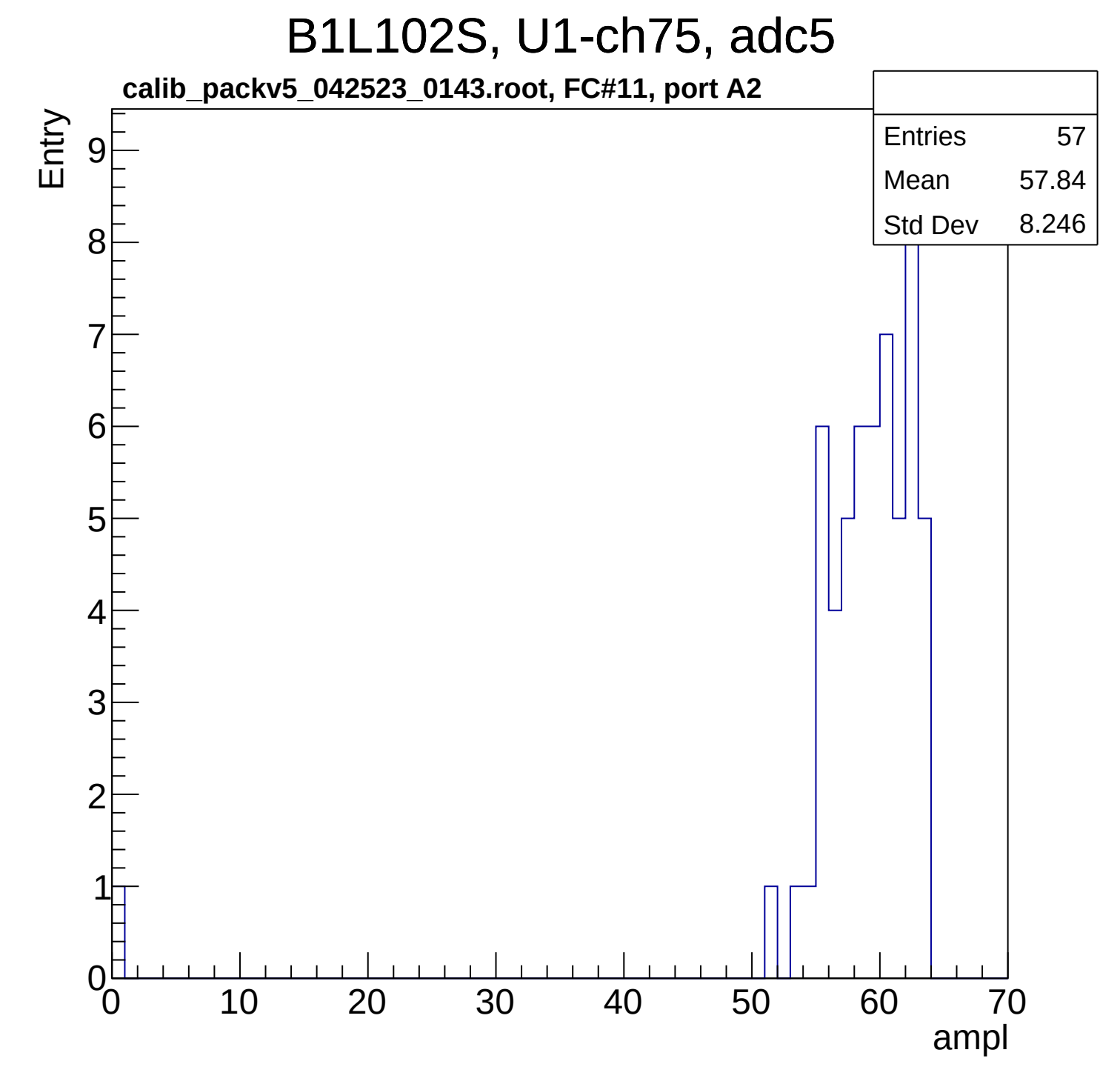

B1L102S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 27.68
Std Dev 4.736
Gaus mean : 28.4064
Gaus Width: 3.9200
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

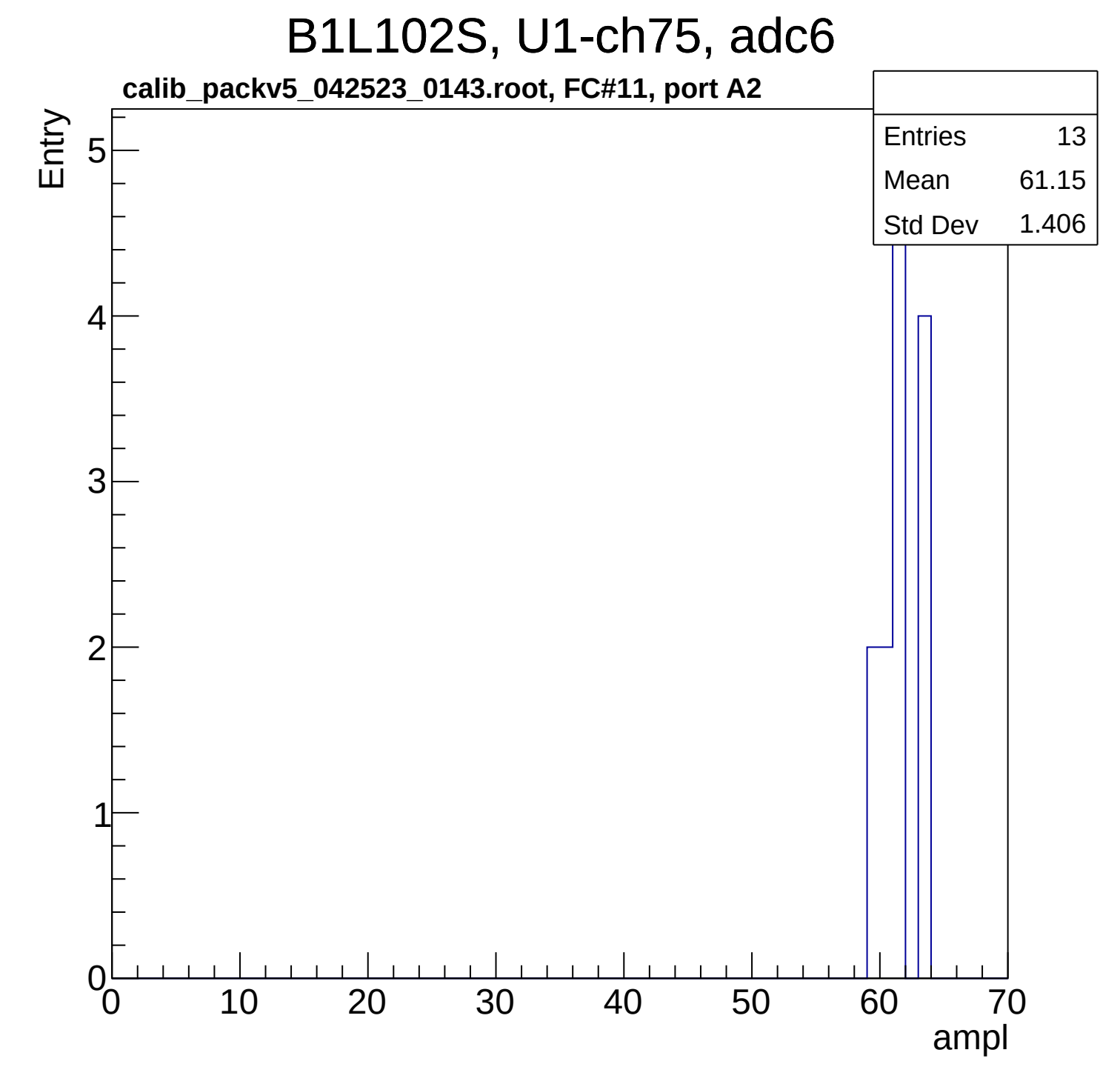

B1L102S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 34.75
Std Dev 3.453
Gaus mean : 35.3778
Gaus Width: 3.3979
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

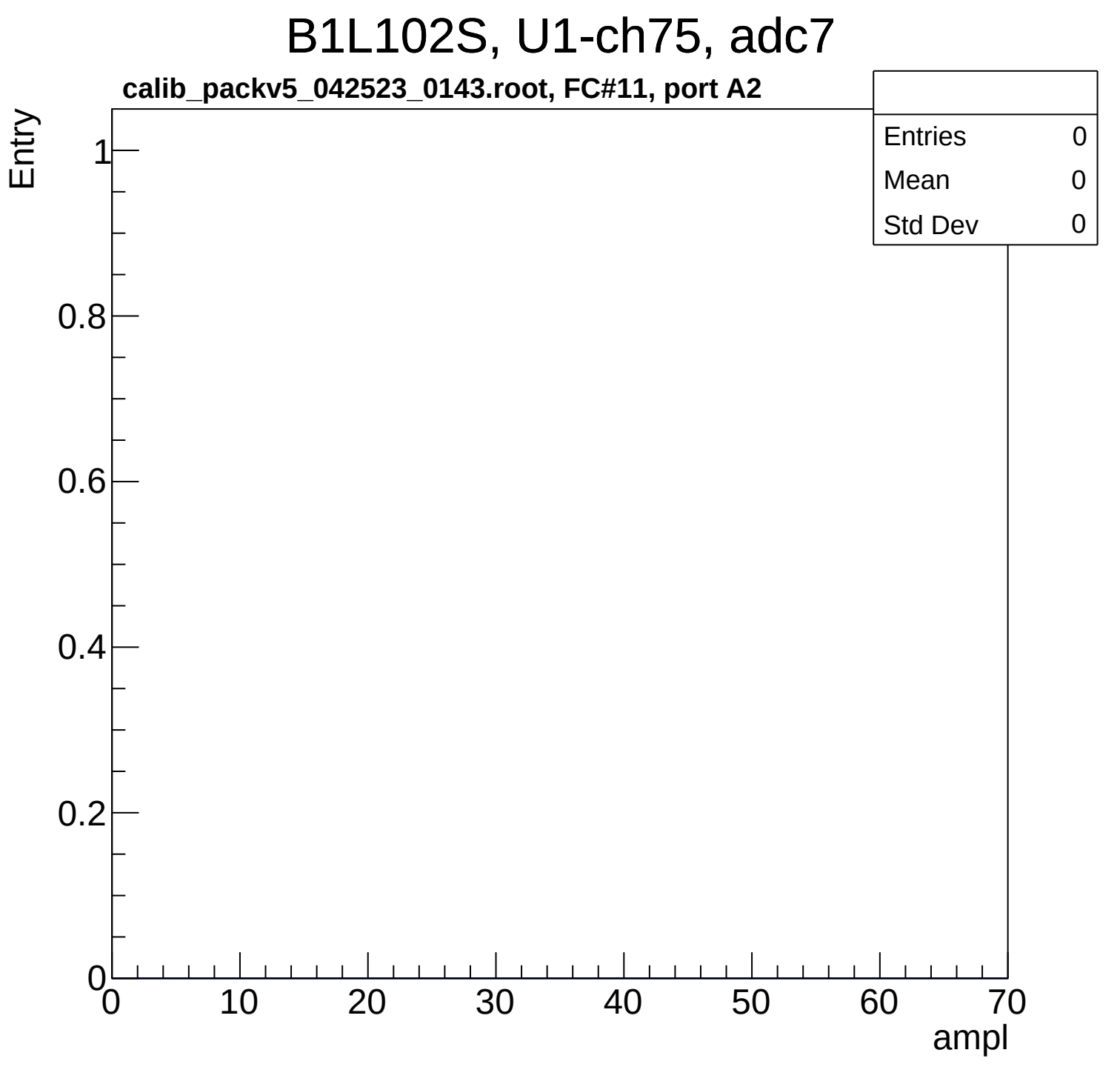

B1L102S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 41.87
Std Dev 3.36
Gaus mean : 42.2663
Gaus Width: 3.7458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

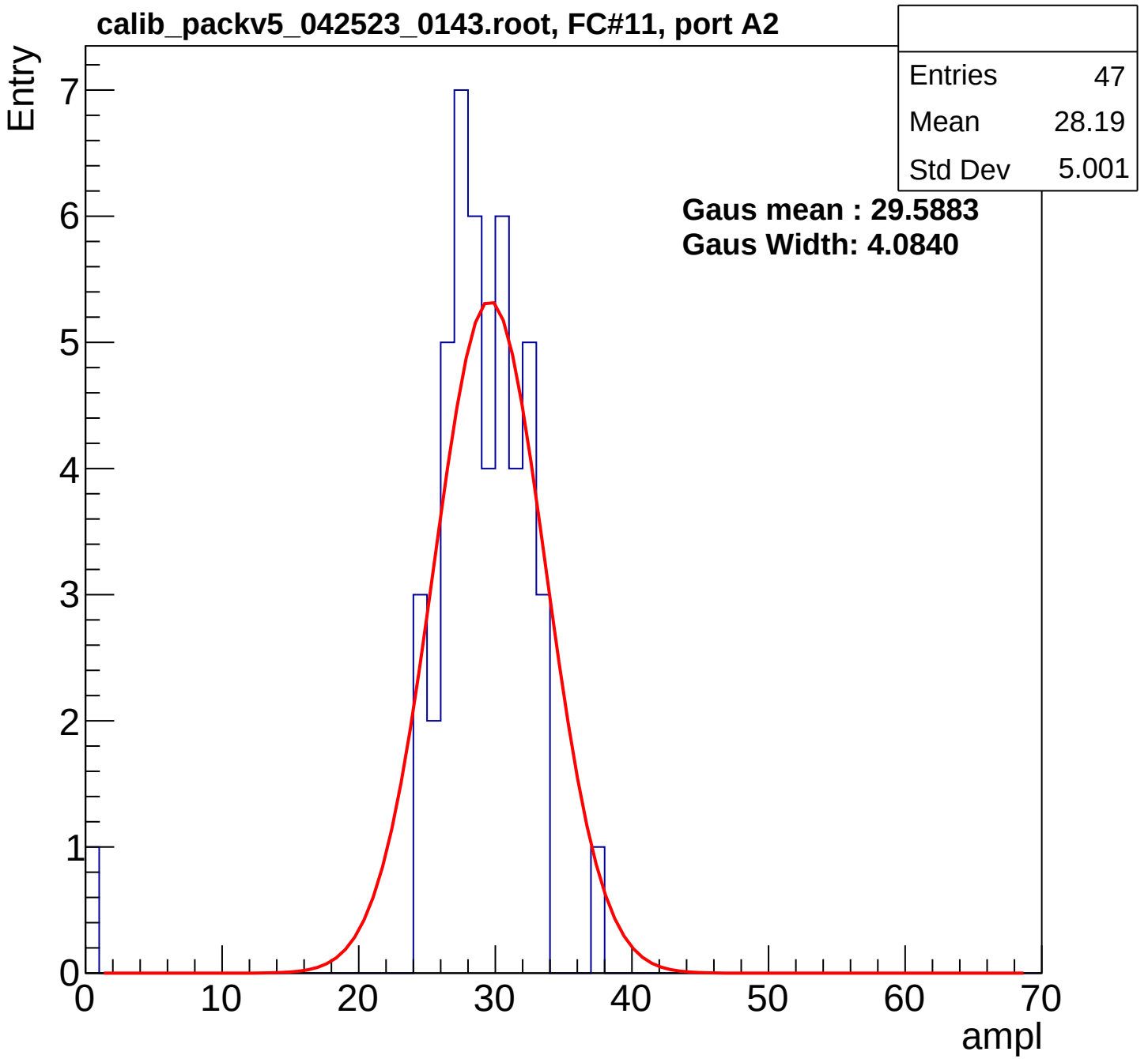

B1L102S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 48.41
Std Dev 3.631
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

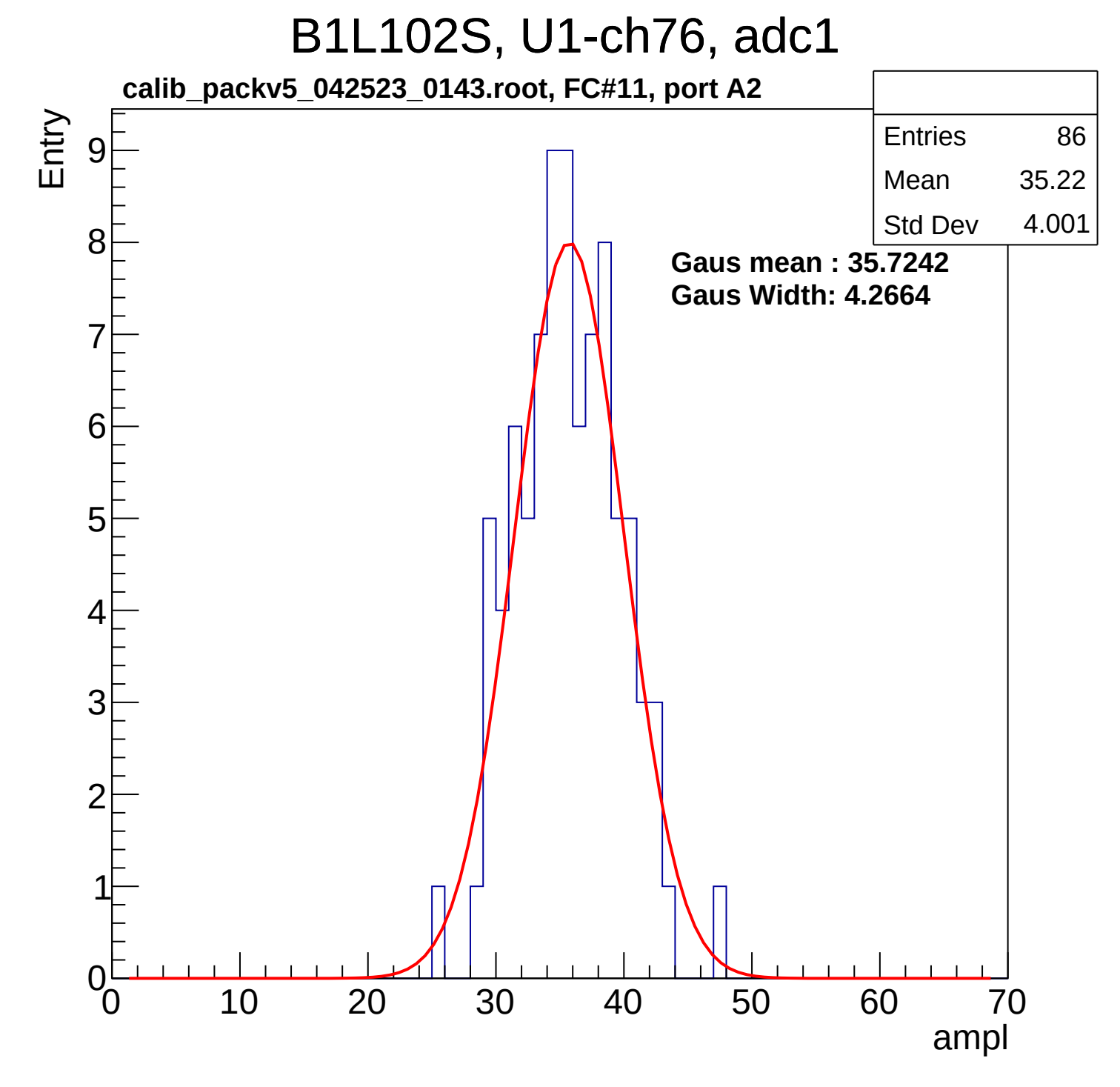

B1L102S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 45
Mean 54.73
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

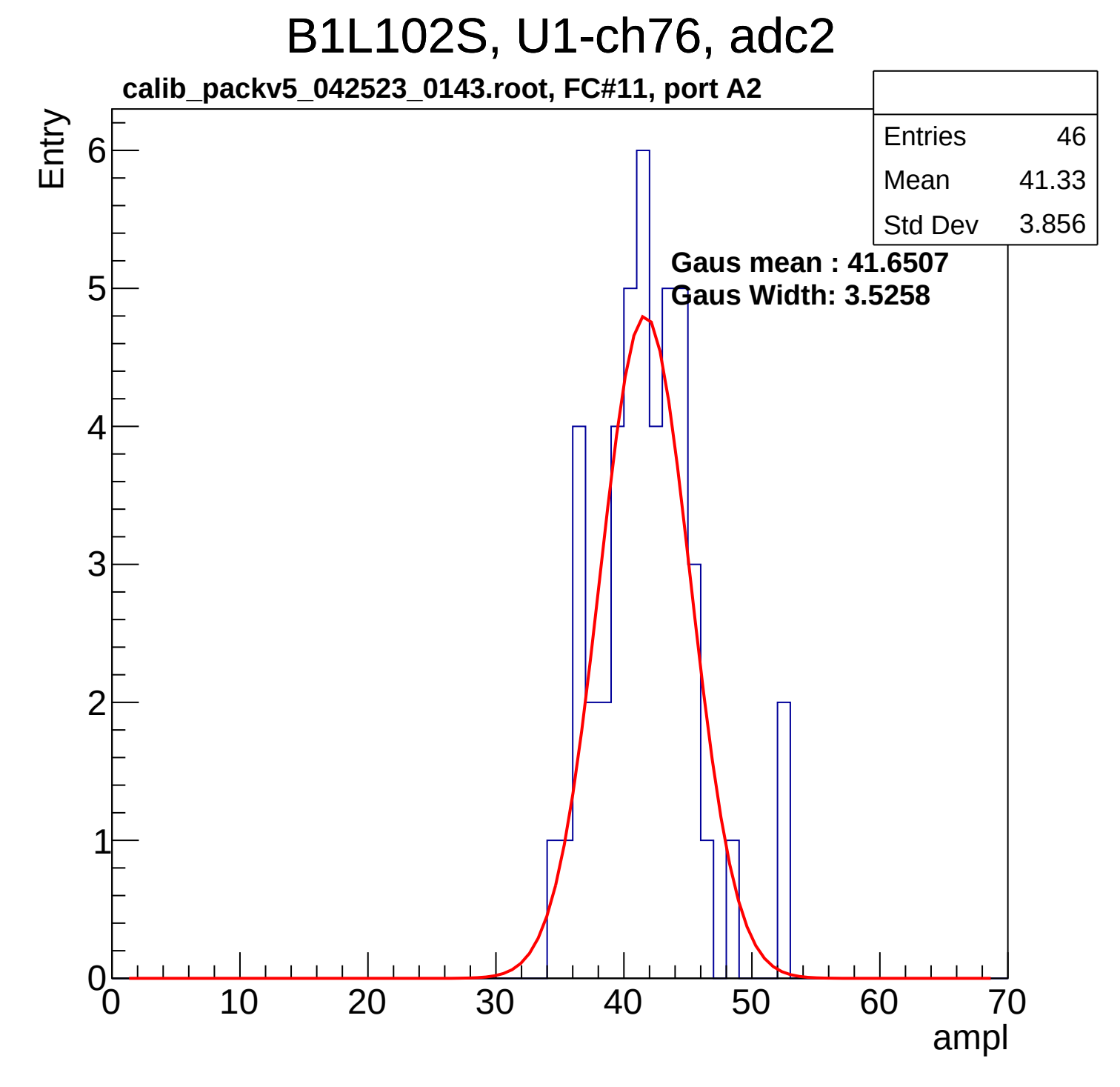

B1L102S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 57.84
Std Dev 8.246
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

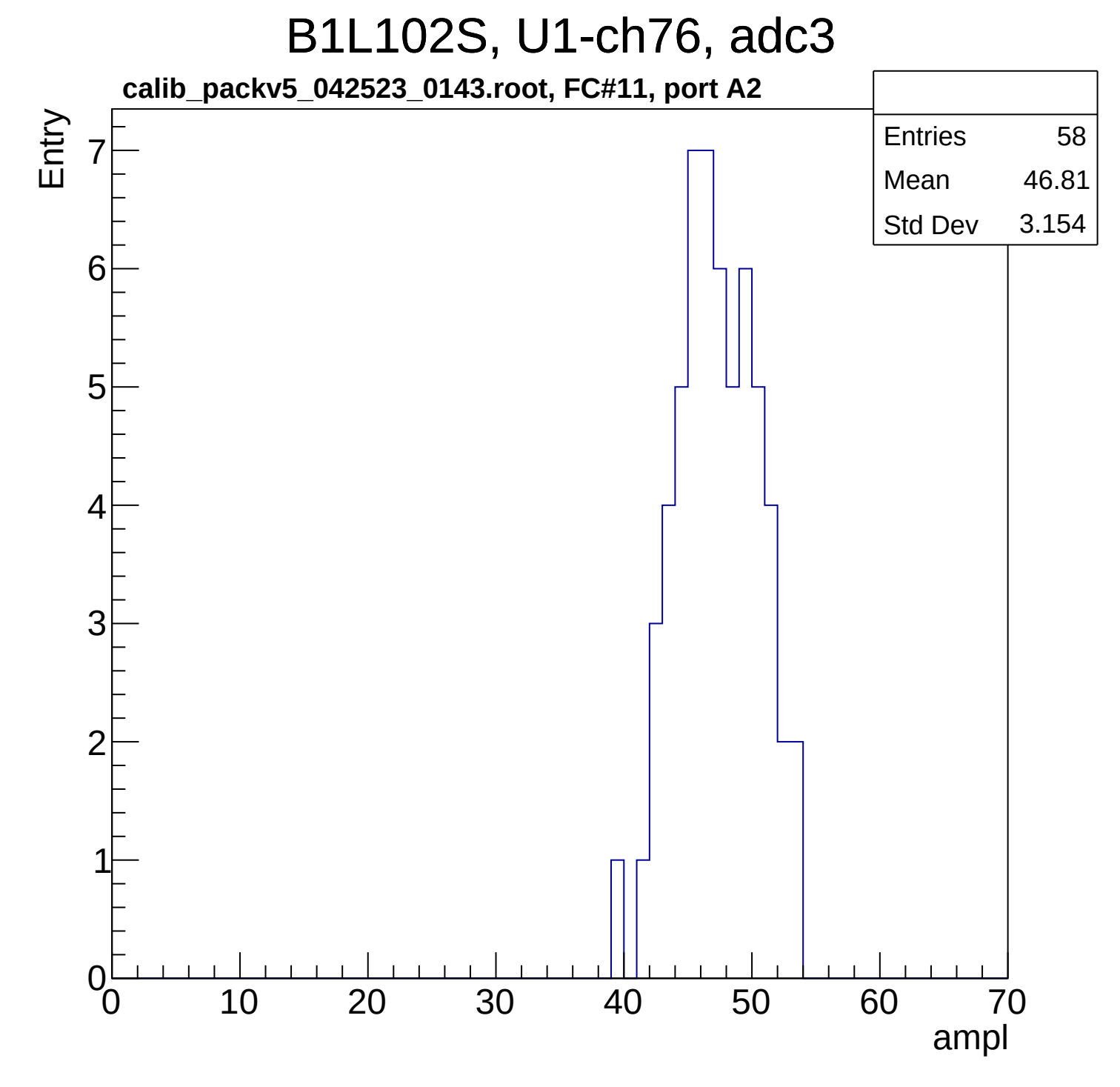

B1L102S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 61.15
Std Dev 1.406
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

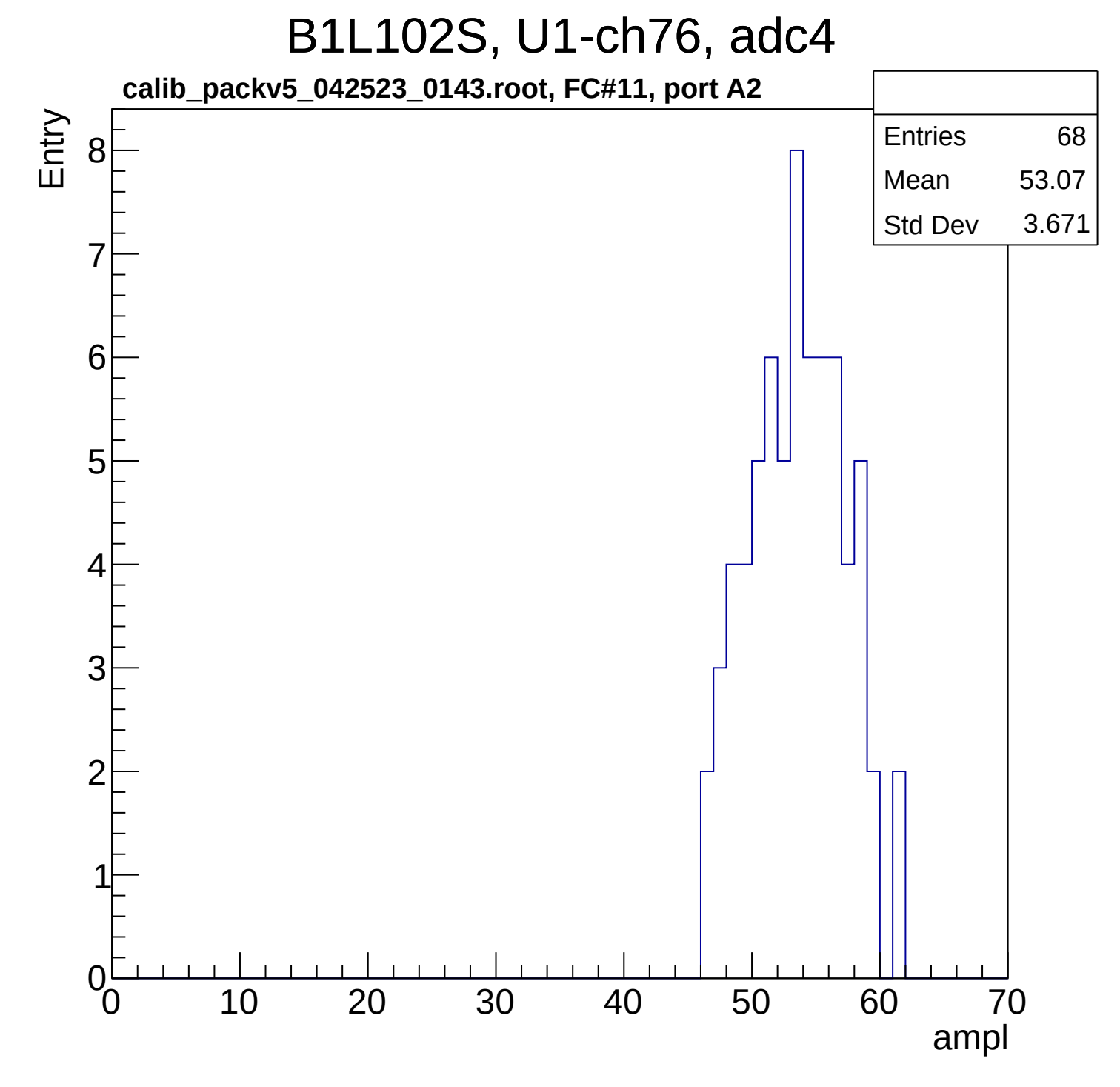

B1L102S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

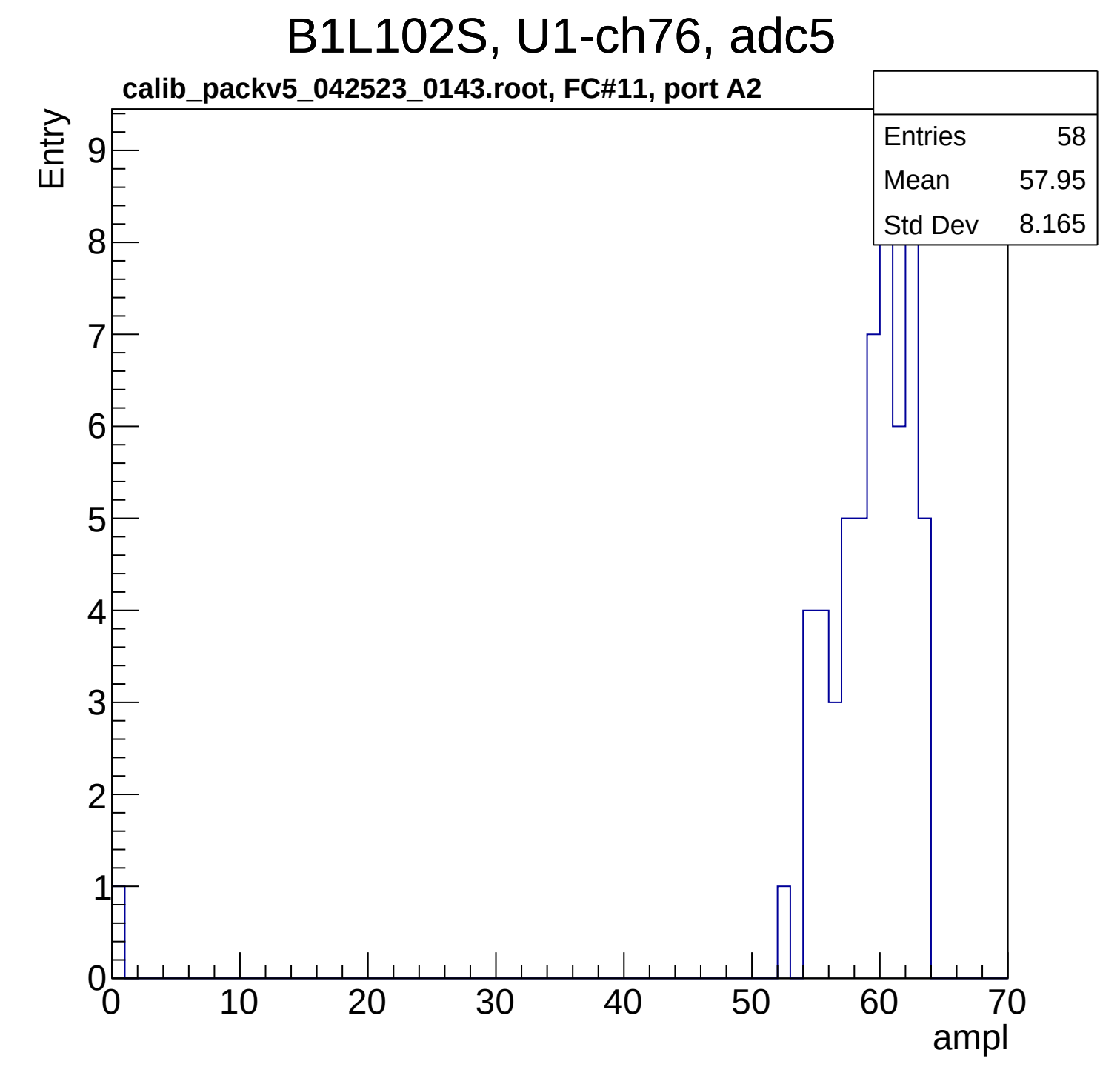

B1L102S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 28.19
Std Dev 5.001
Gaus mean : 29.5883
Gaus Width: 4.0840
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

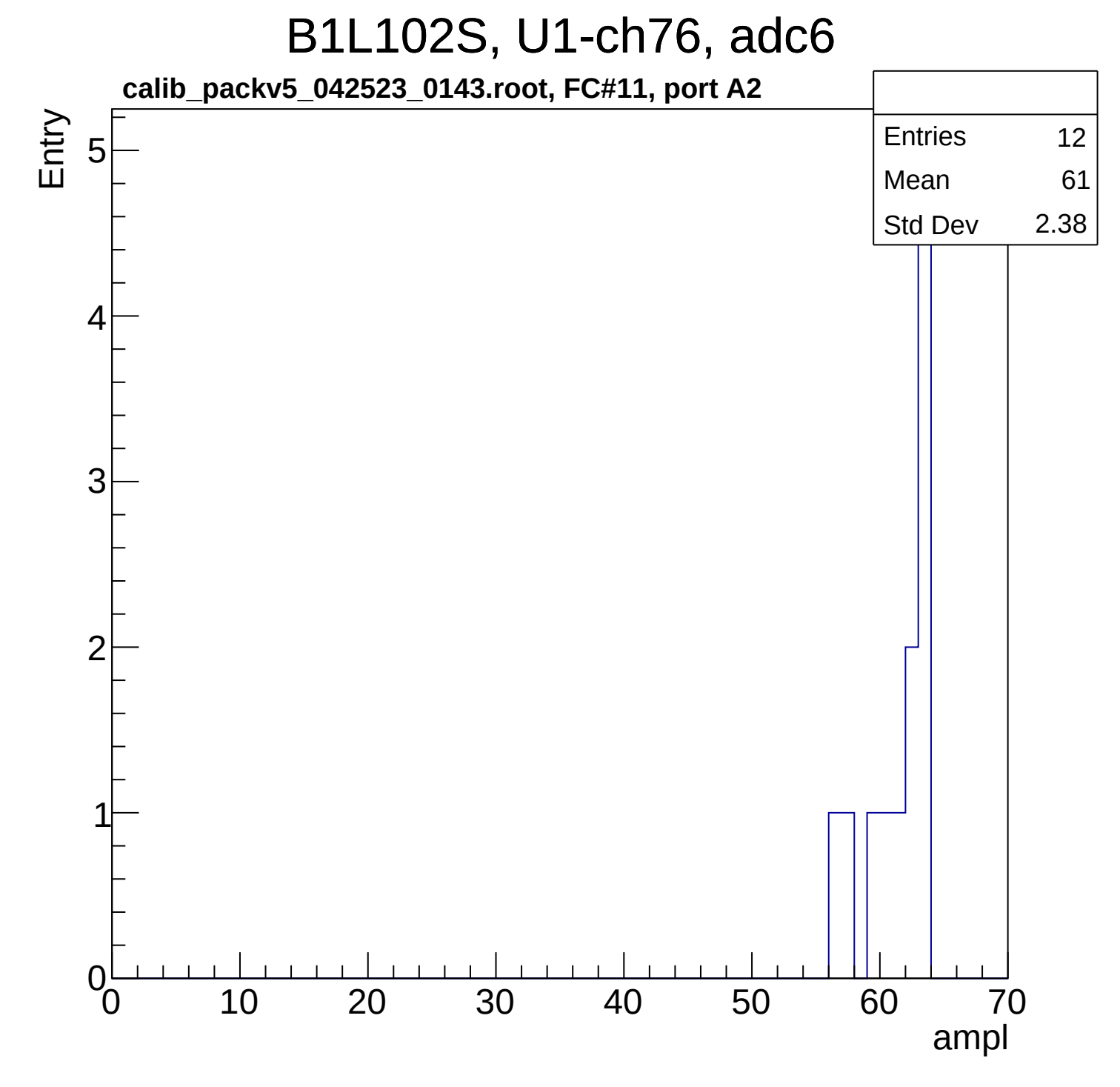

B1L102S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 35.22
Std Dev 4.001
Gaus mean : 35.7242
Gaus Width: 4.2664
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

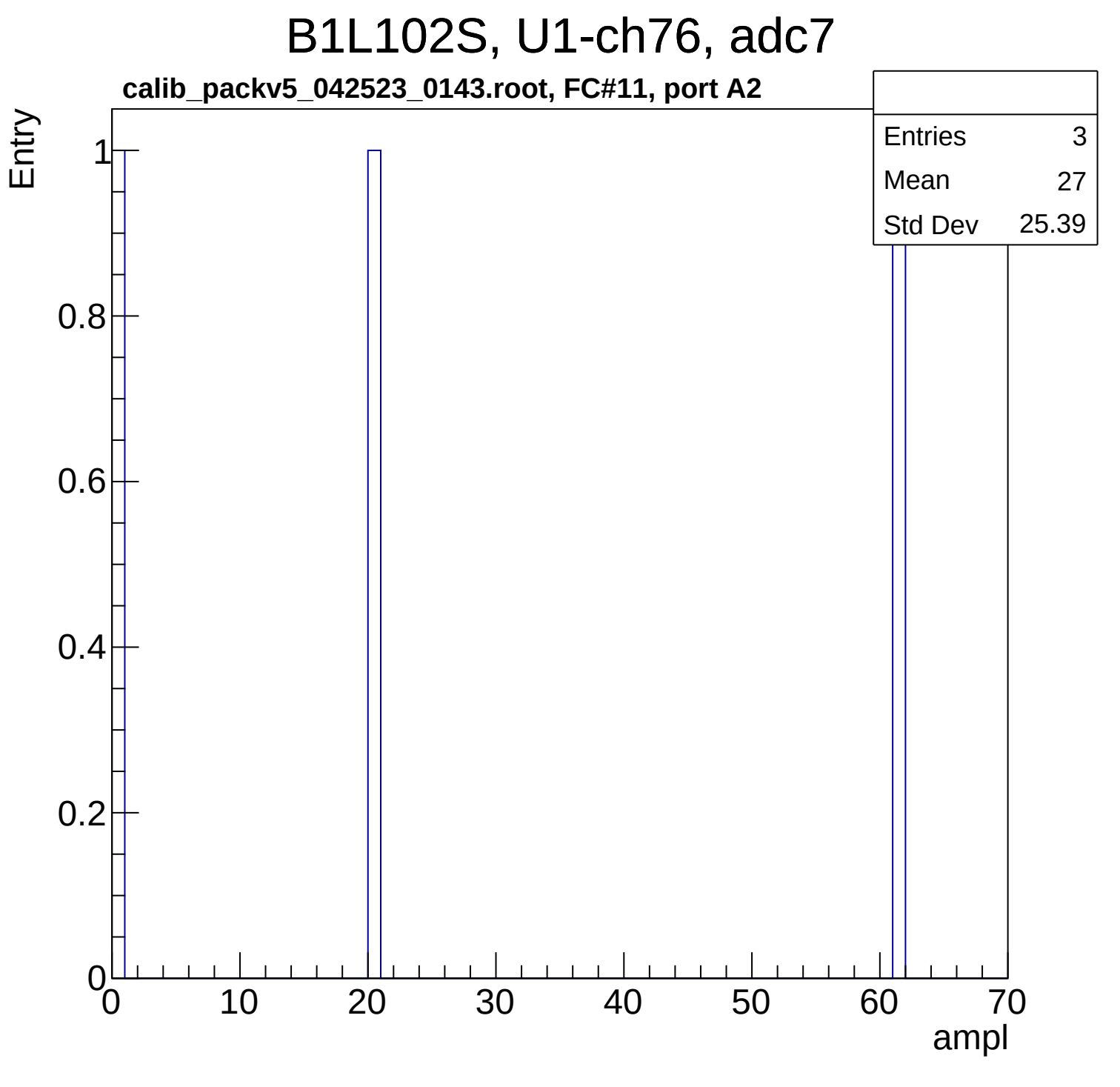

B1L102S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 41.33
Std Dev 3.856
Gaus mean : 41.6507
Gaus Width: 3.5258
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

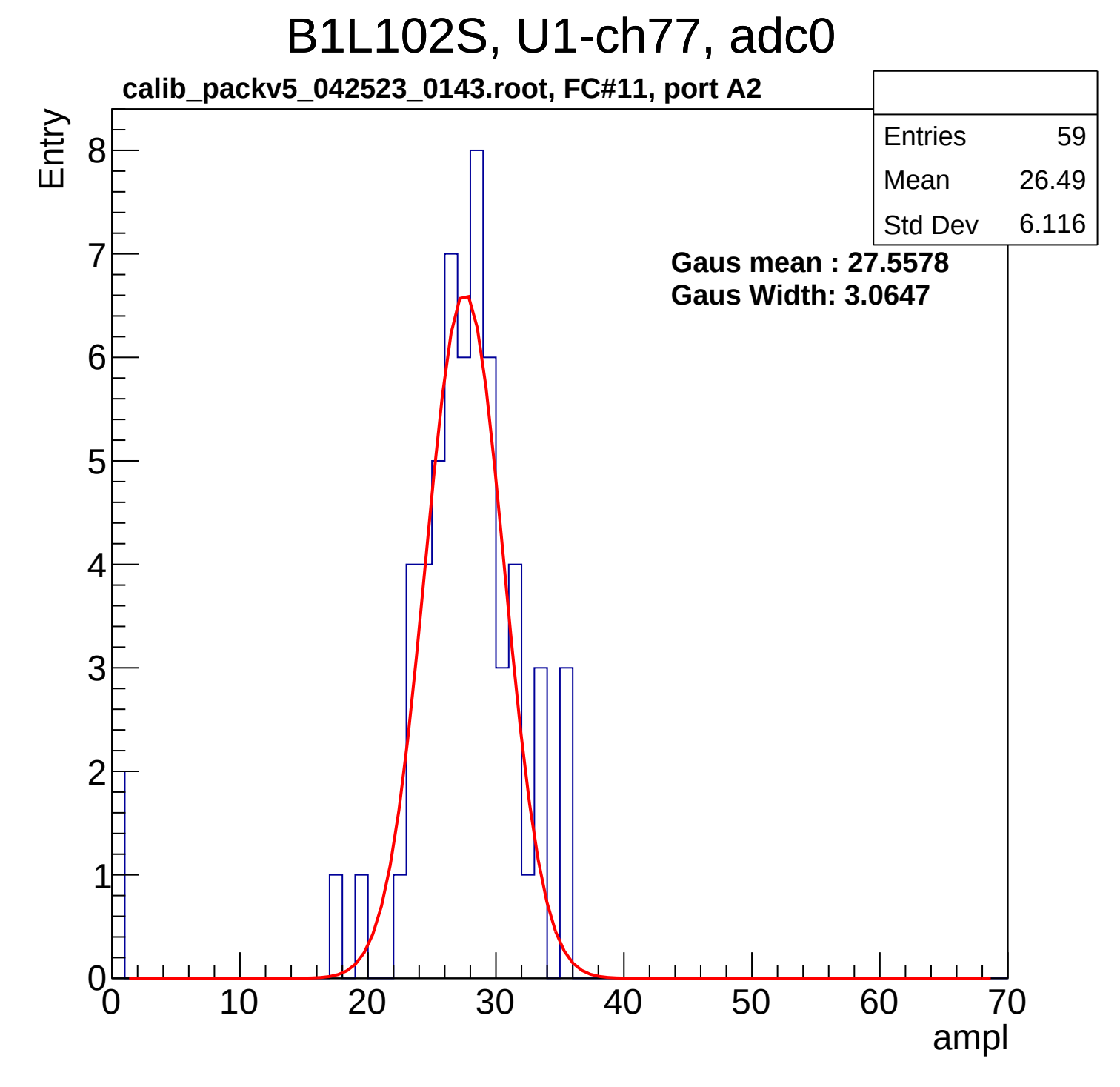

B1L102S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 46.81
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

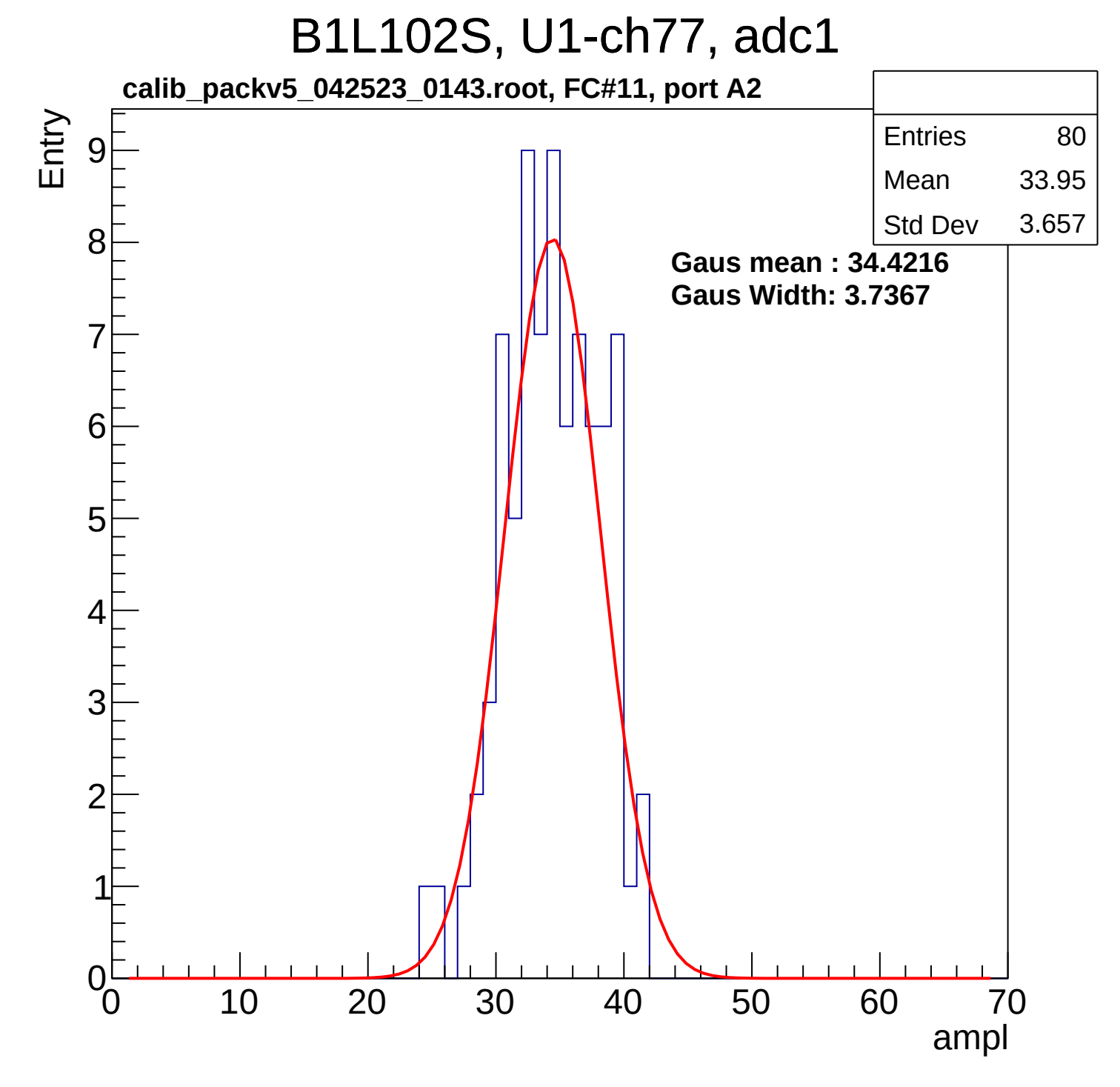

B1L102S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 53.07
Std Dev 3.671
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

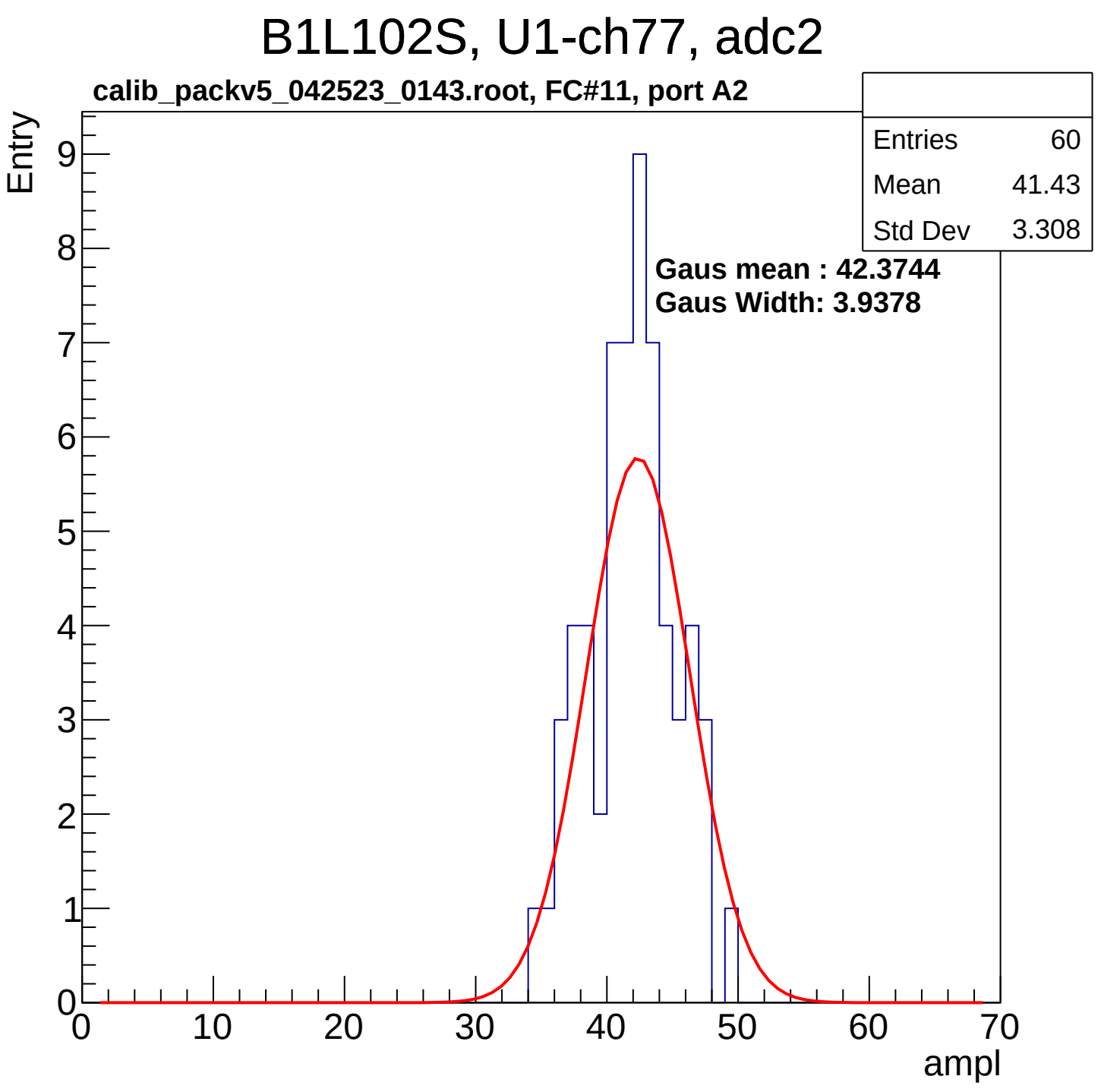

B1L102S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 57.95
Std Dev 8.165
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

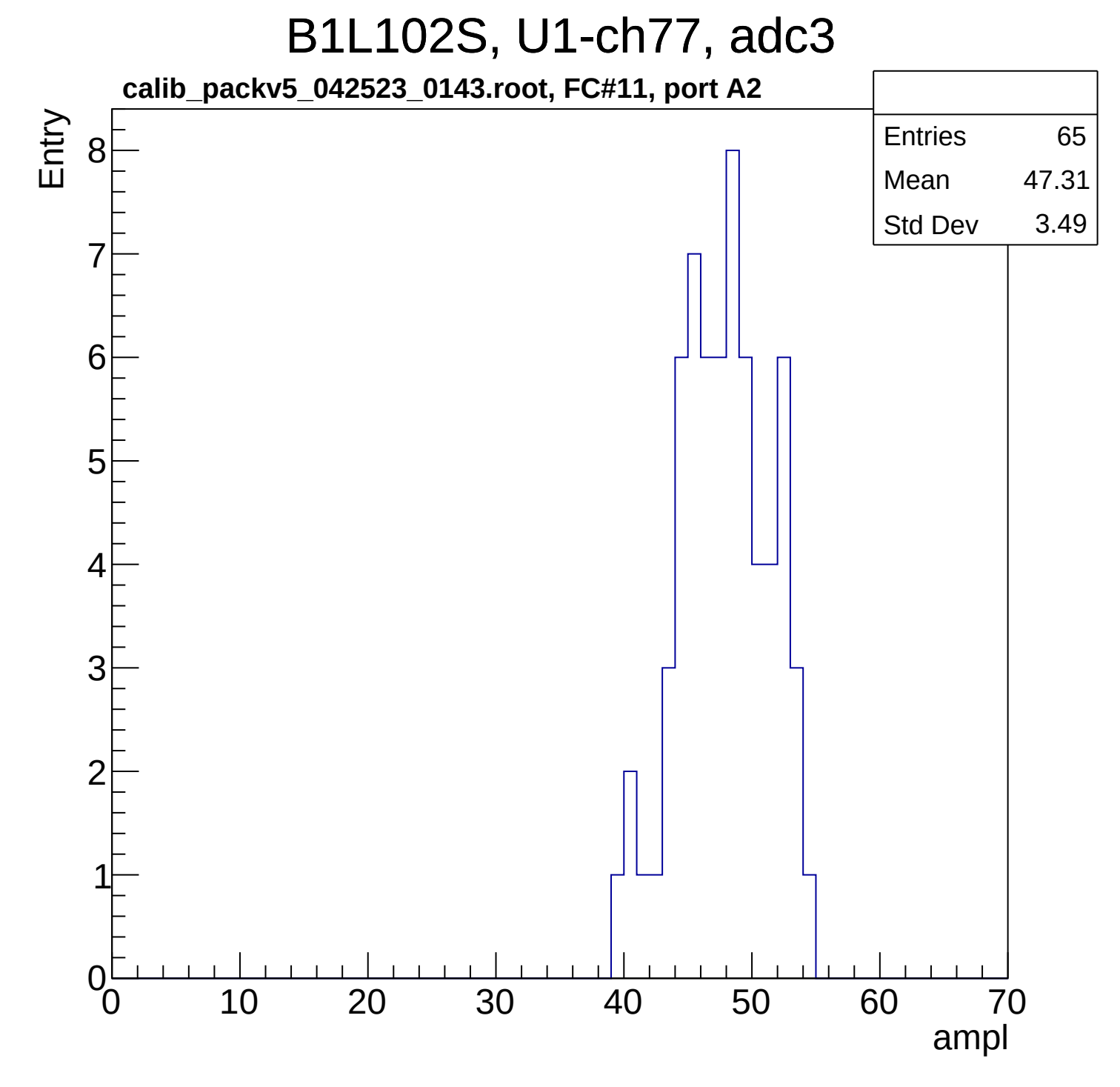

B1L102S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61
Std Dev 2.38
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

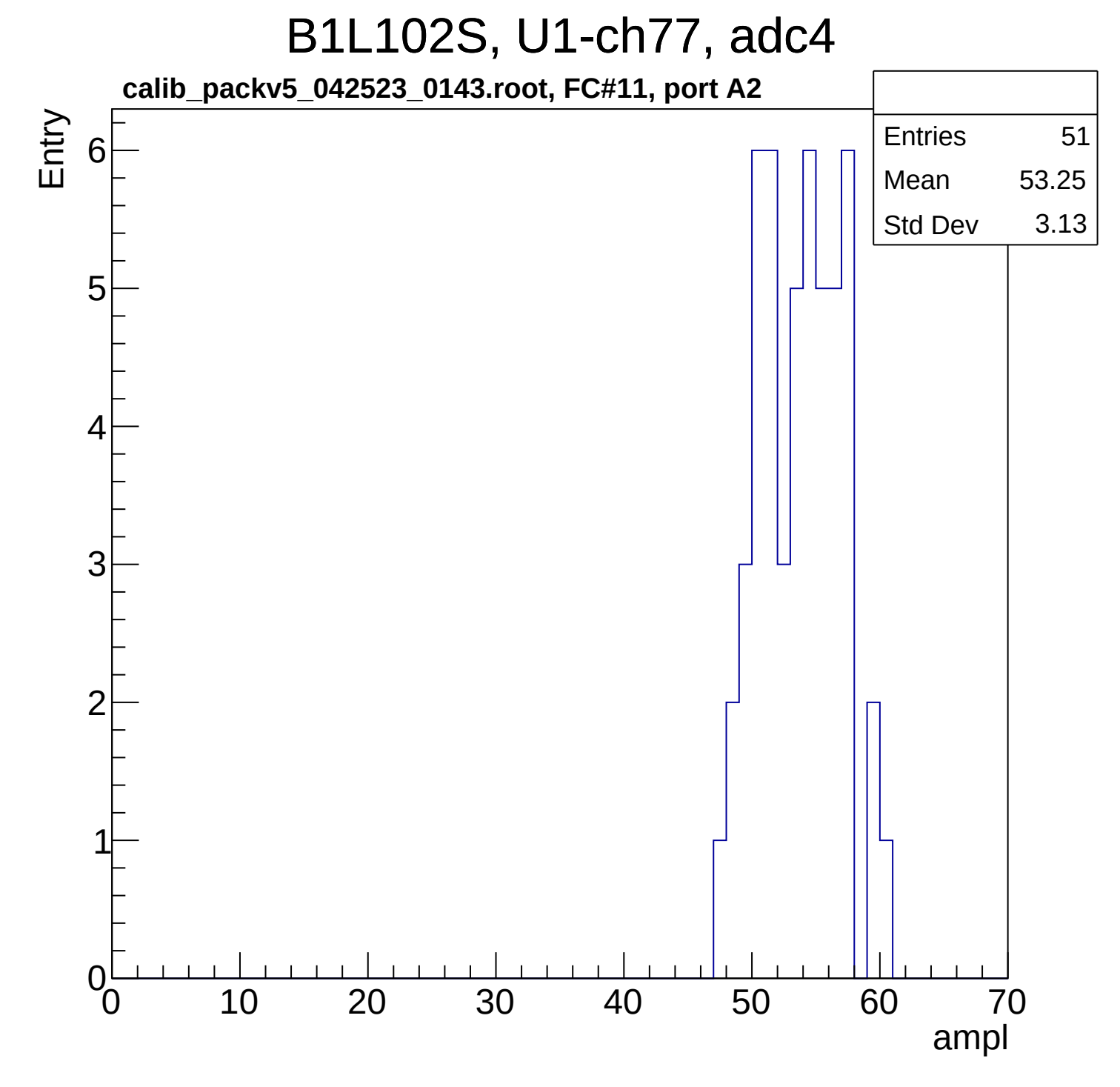

B1L102S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 27
Std Dev 25.39
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

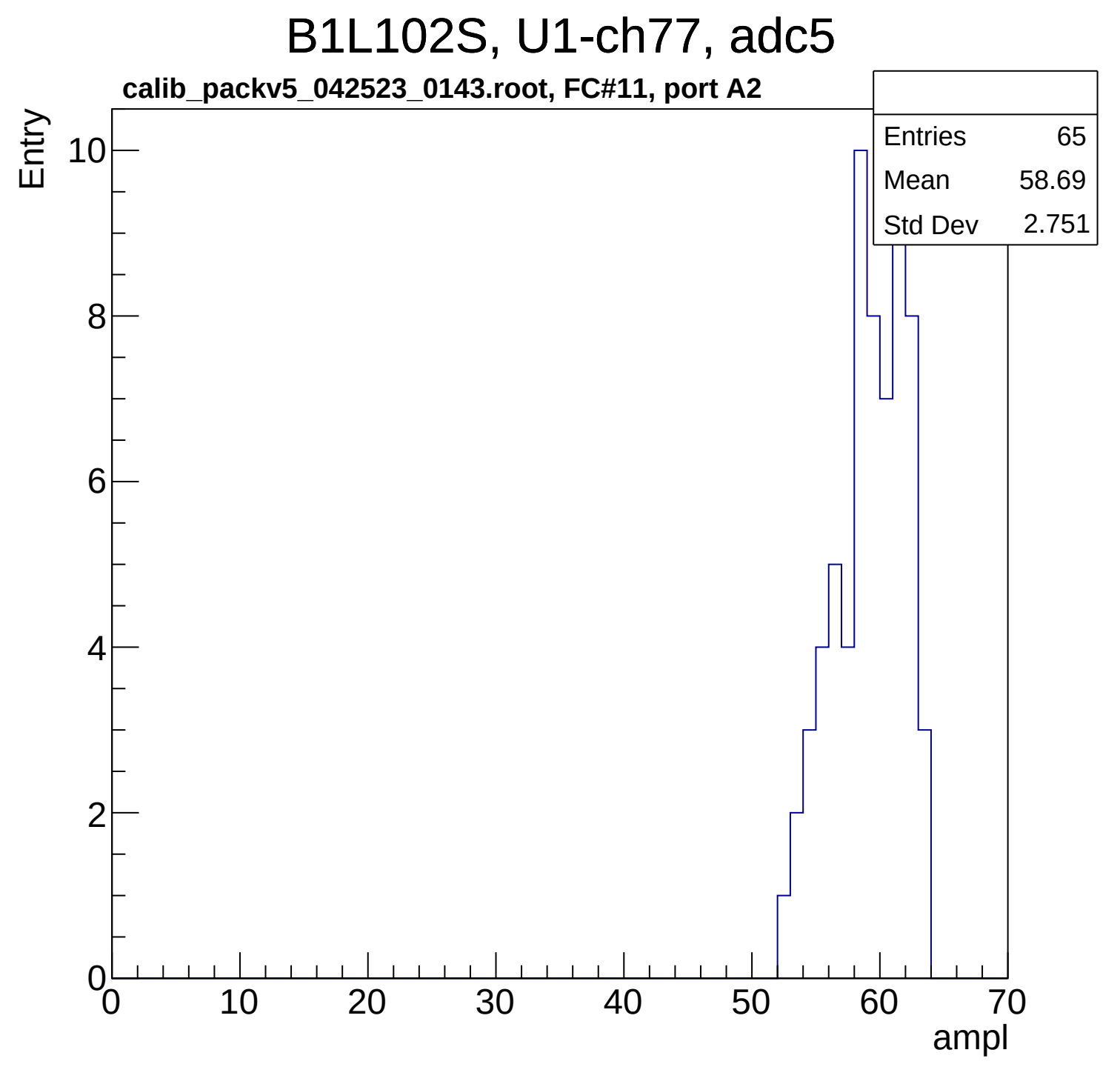

B1L102S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 26.49
Std Dev 6.116
Gaus mean : 27.5578
Gaus Width: 3.0647
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

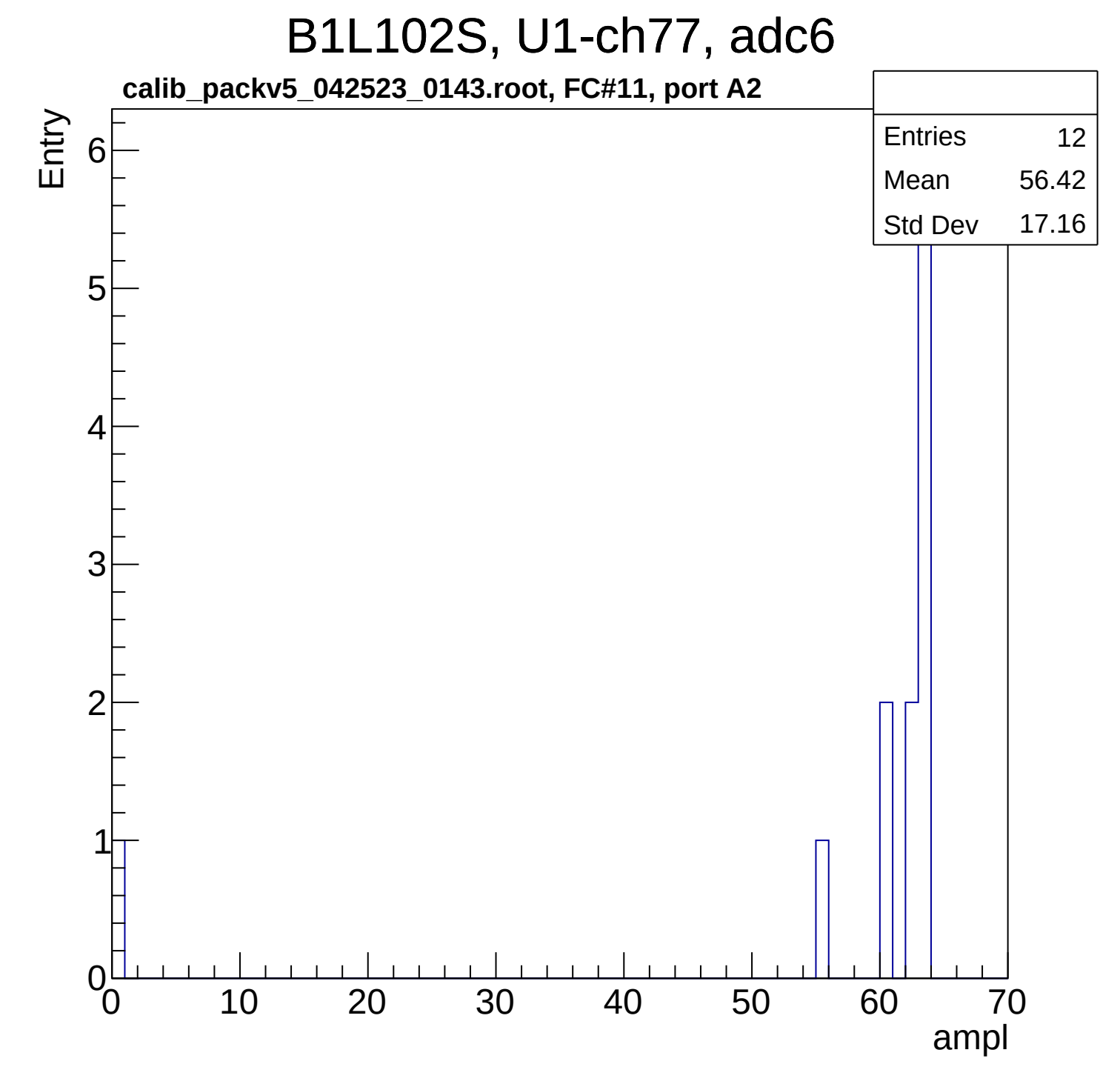

B1L102S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 33.95
Std Dev 3.657
Gaus mean : 34.4216
Gaus Width: 3.7367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

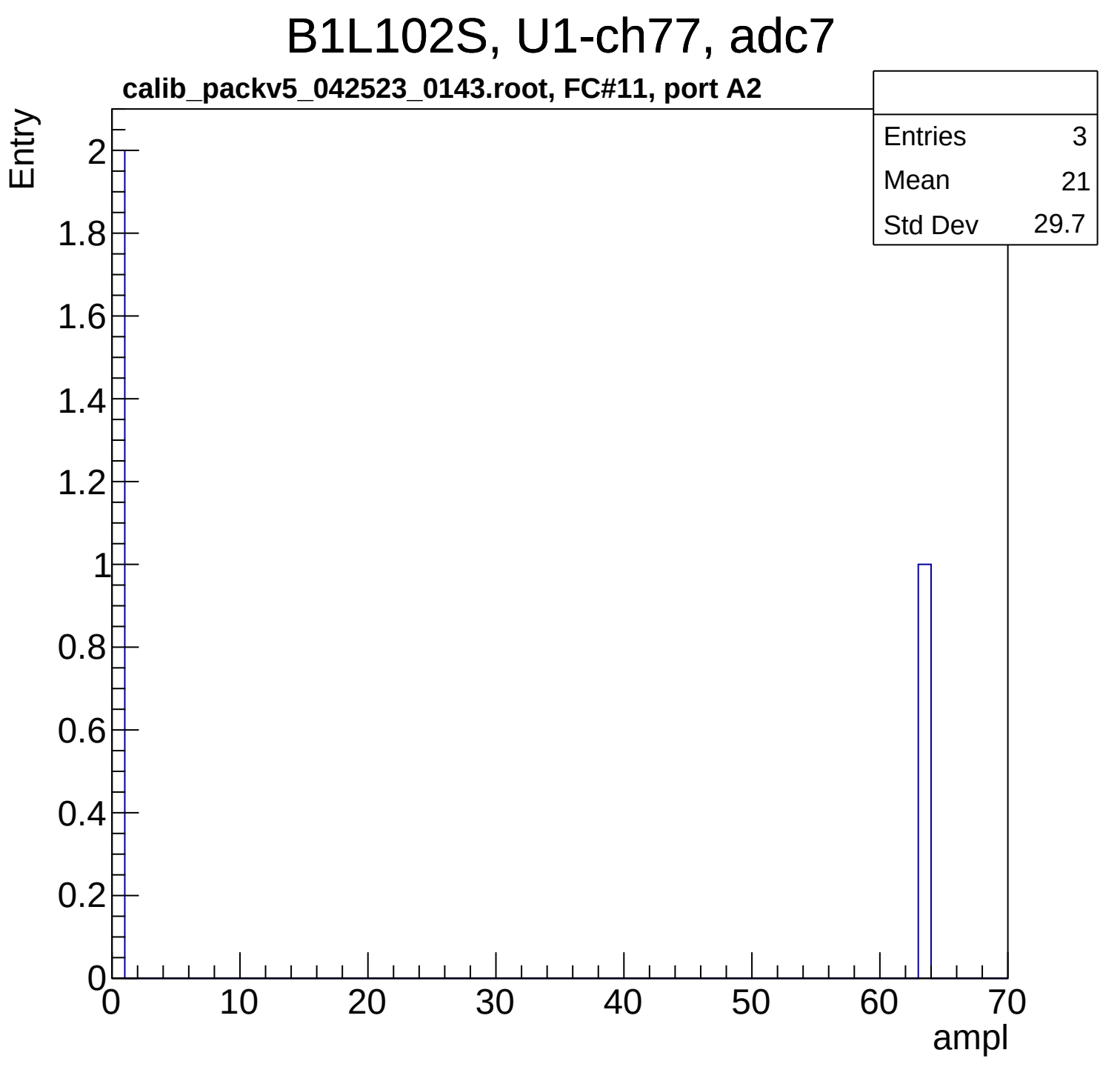

B1L102S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 41.43
Std Dev 3.308
Gaus mean : 42.3744
Gaus Width: 3.9378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

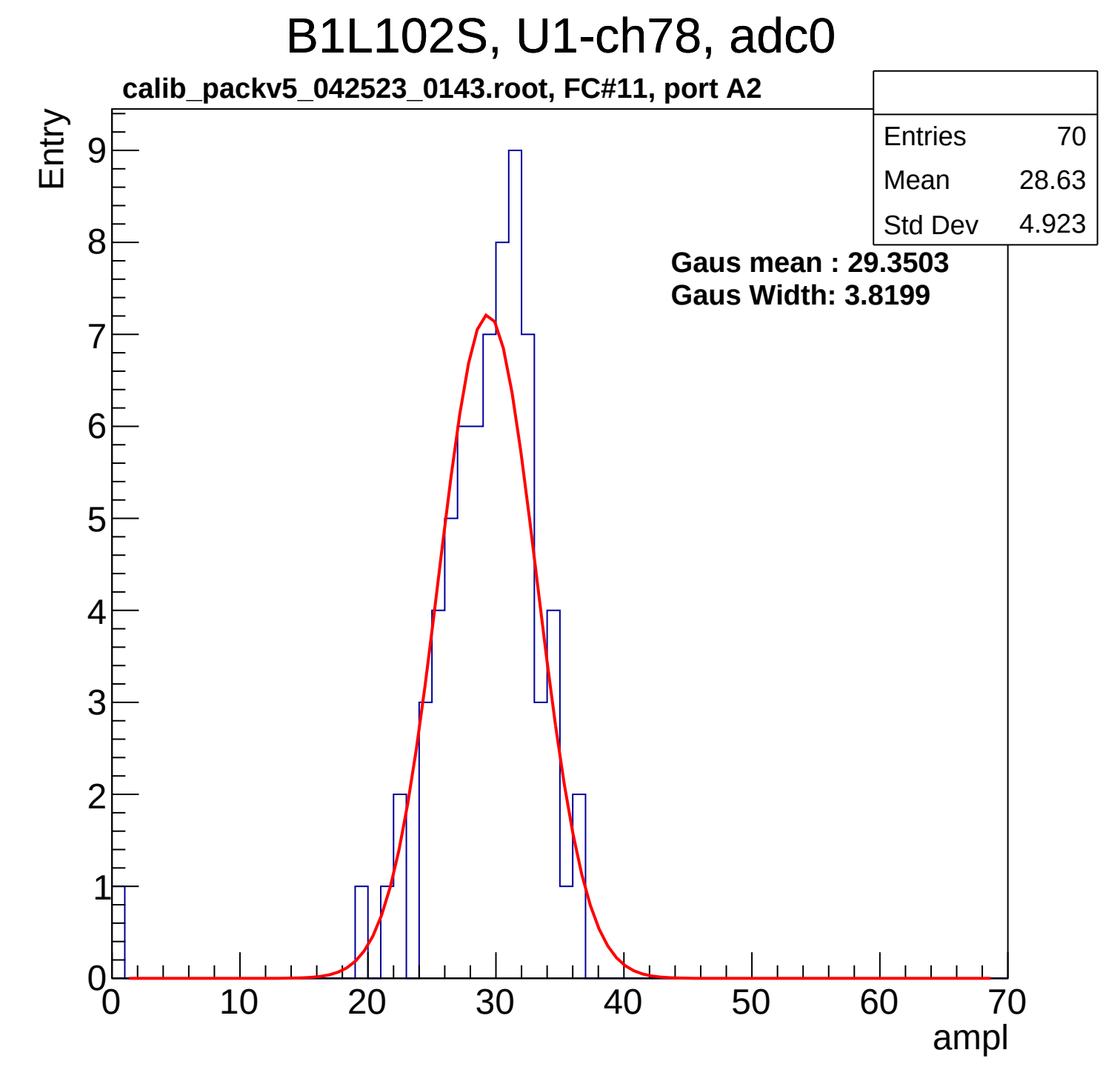

B1L102S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 47.31
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

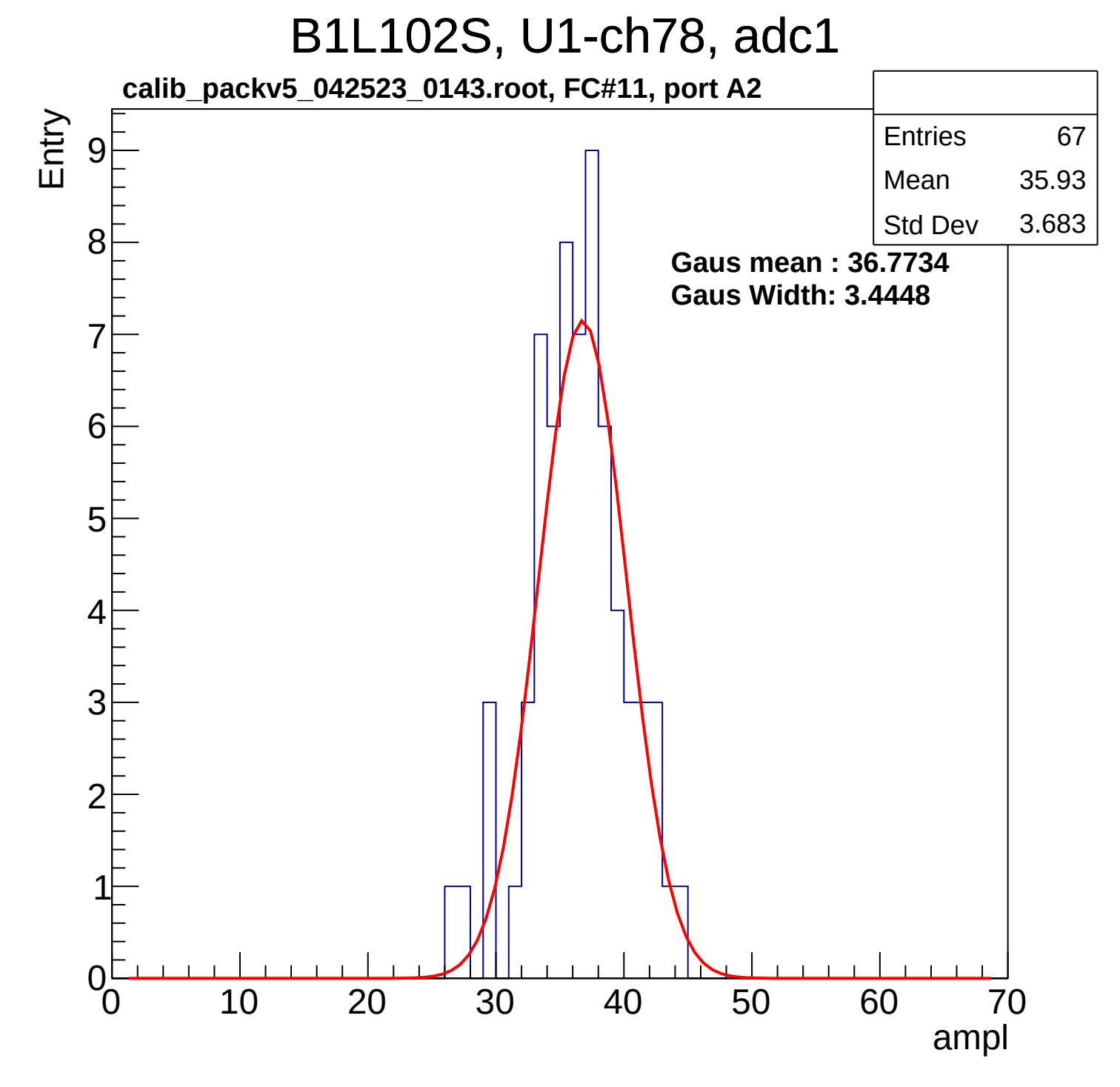

B1L102S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 53.25
Std Dev 3.13
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

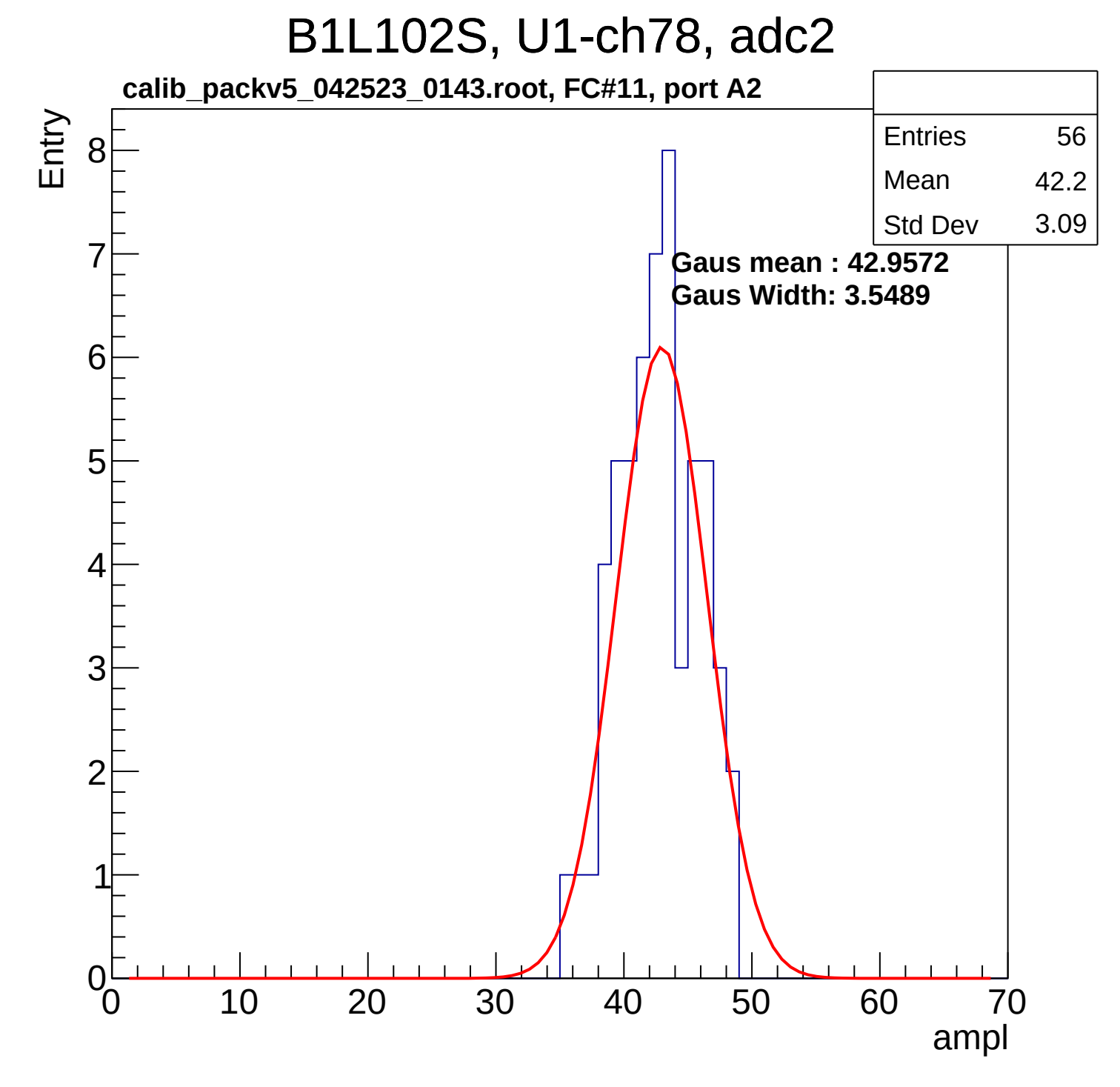

B1L102S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 58.69
Std Dev 2.751
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

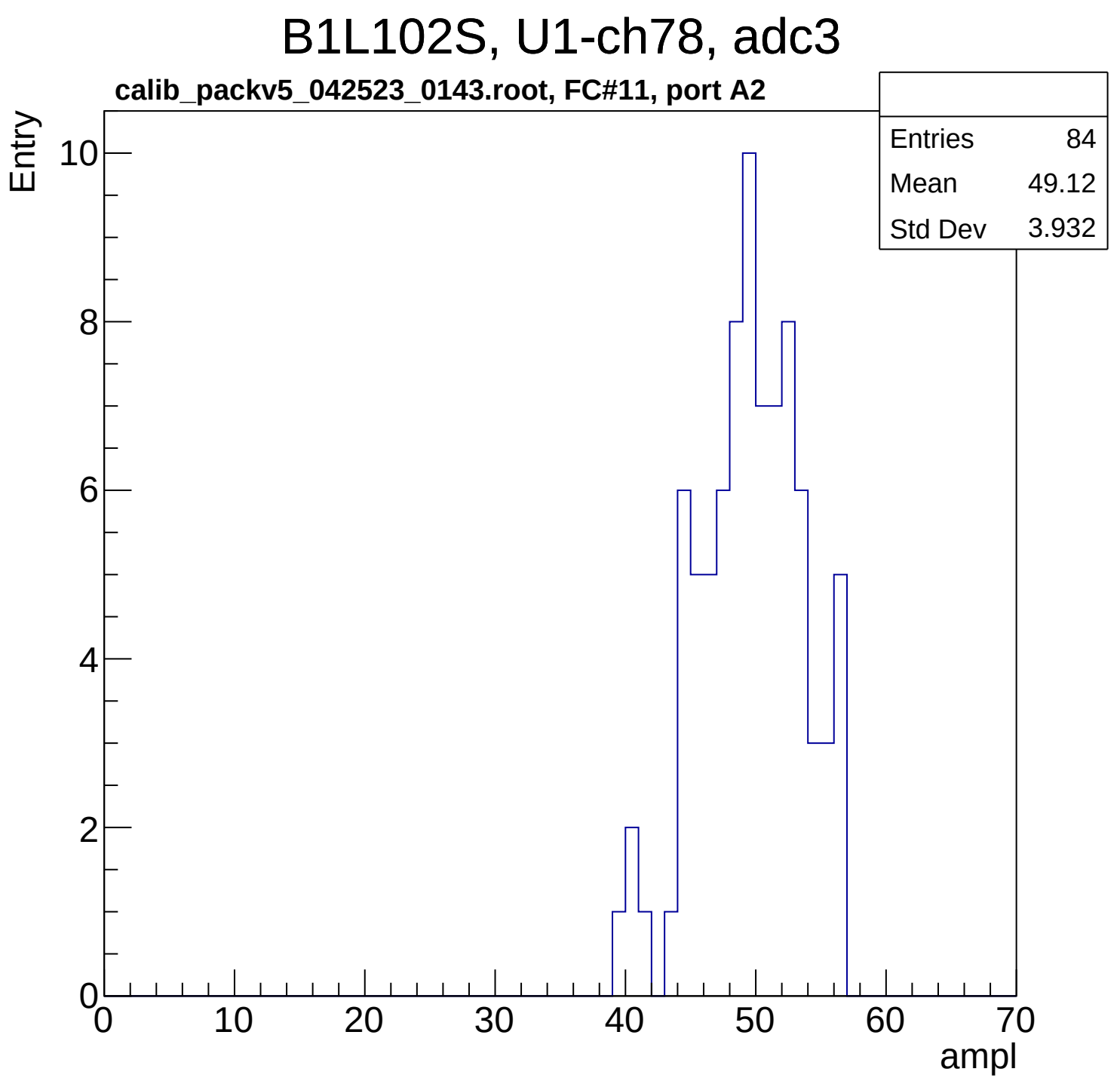

B1L102S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 56.42
Std Dev 17.16
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

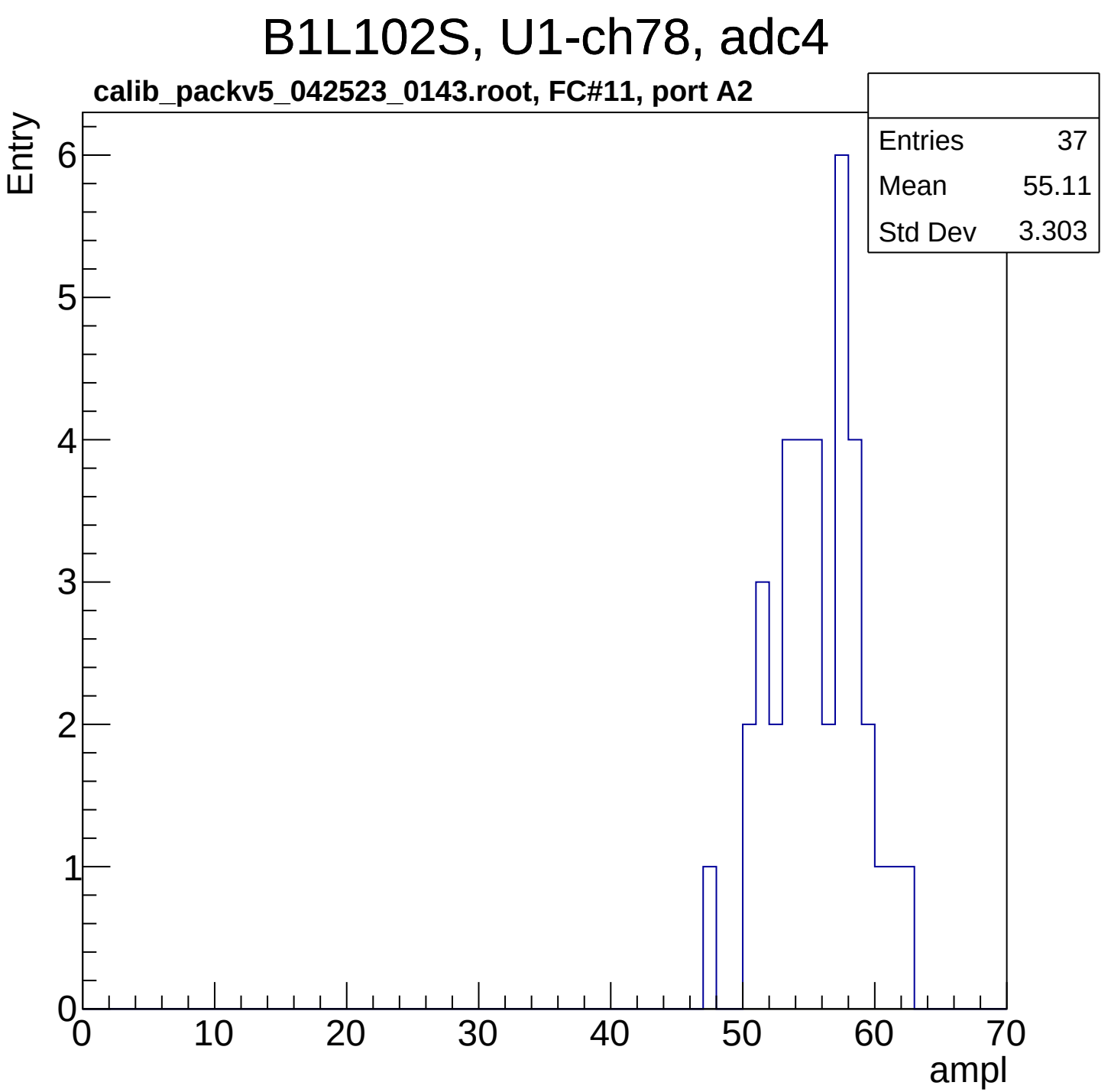

B1L102S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

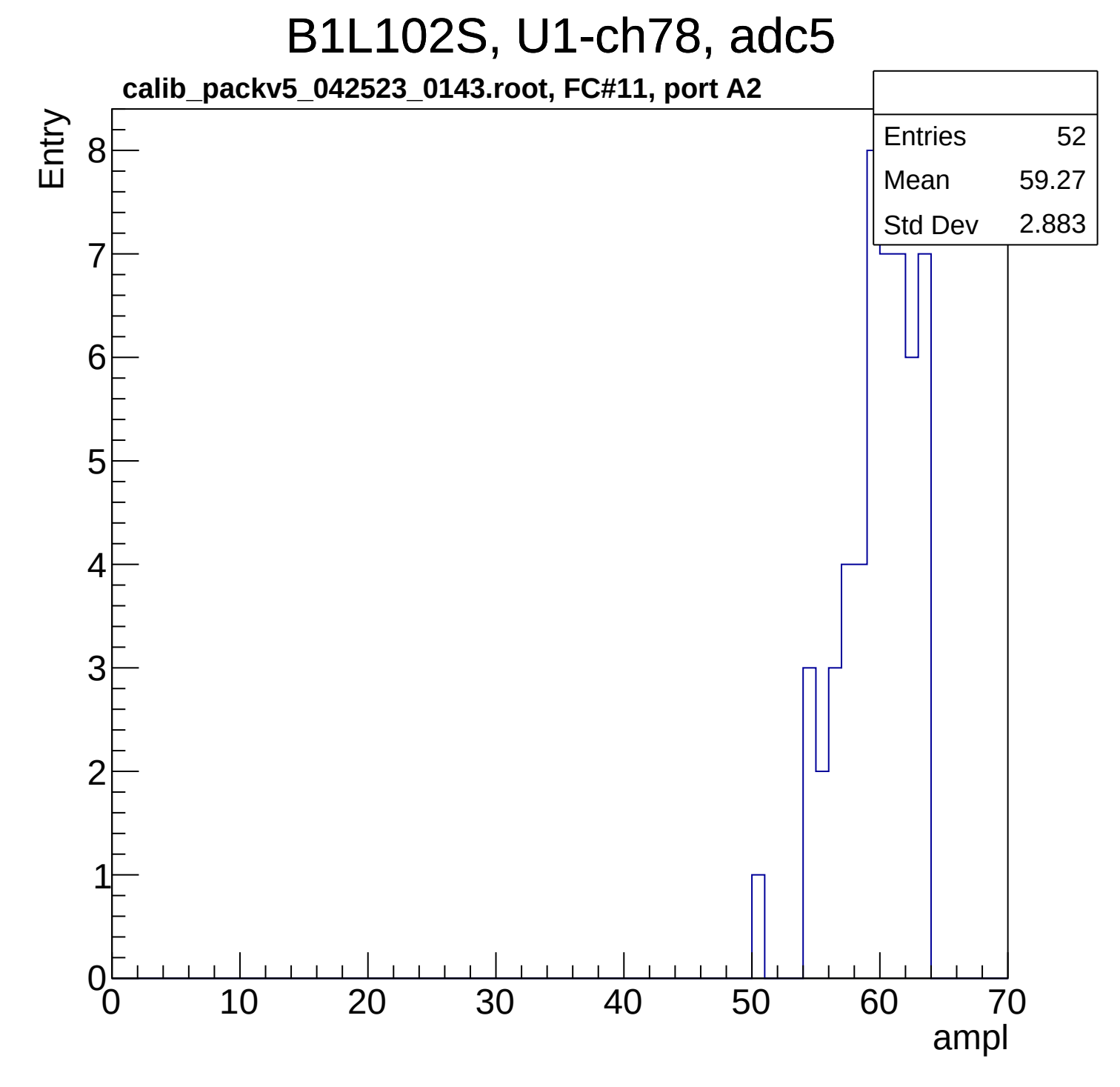

B1L102S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.63
Std Dev 4.923
Gaus mean : 29.3503
Gaus Width: 3.8199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.93
Std Dev 3.683
Gaus mean : 36.7734
Gaus Width: 3.4448
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 42.2
Std Dev 3.09
Gaus mean : 42.9572
Gaus Width: 3.5489
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 49.12
Std Dev 3.932
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 55.11
Std Dev 3.303
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 59.27
Std Dev 2.883
Entry
ampl
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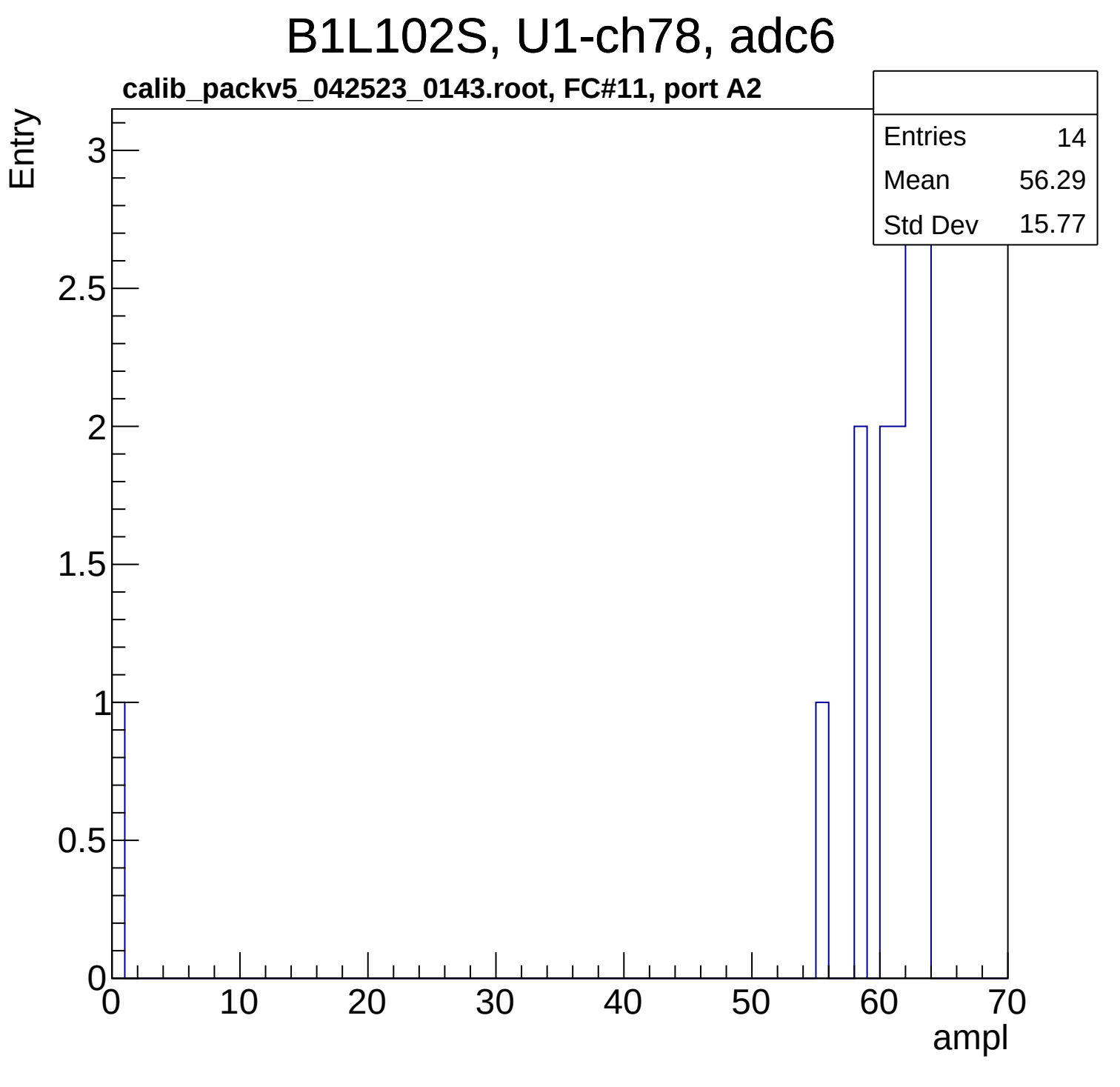
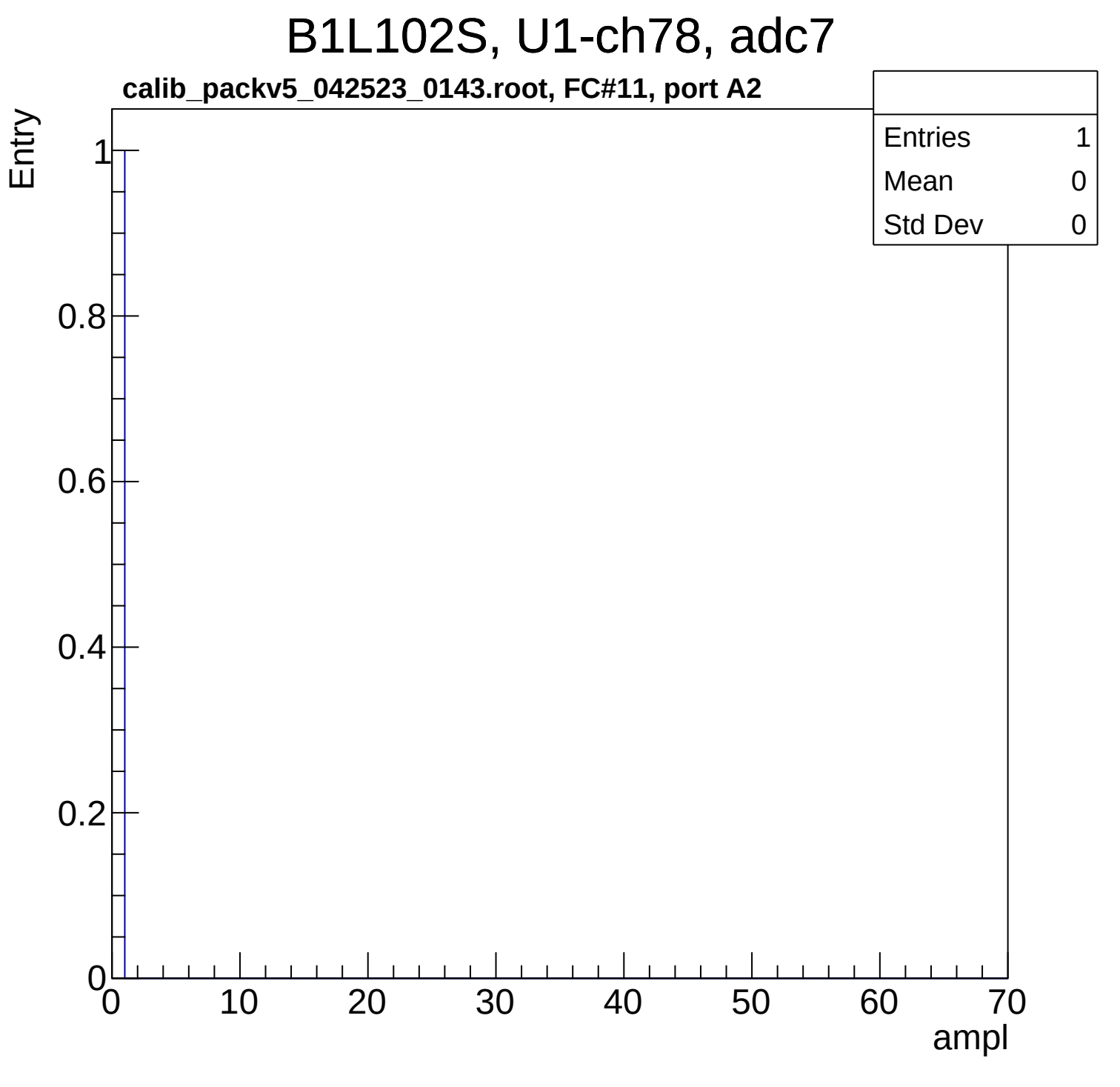
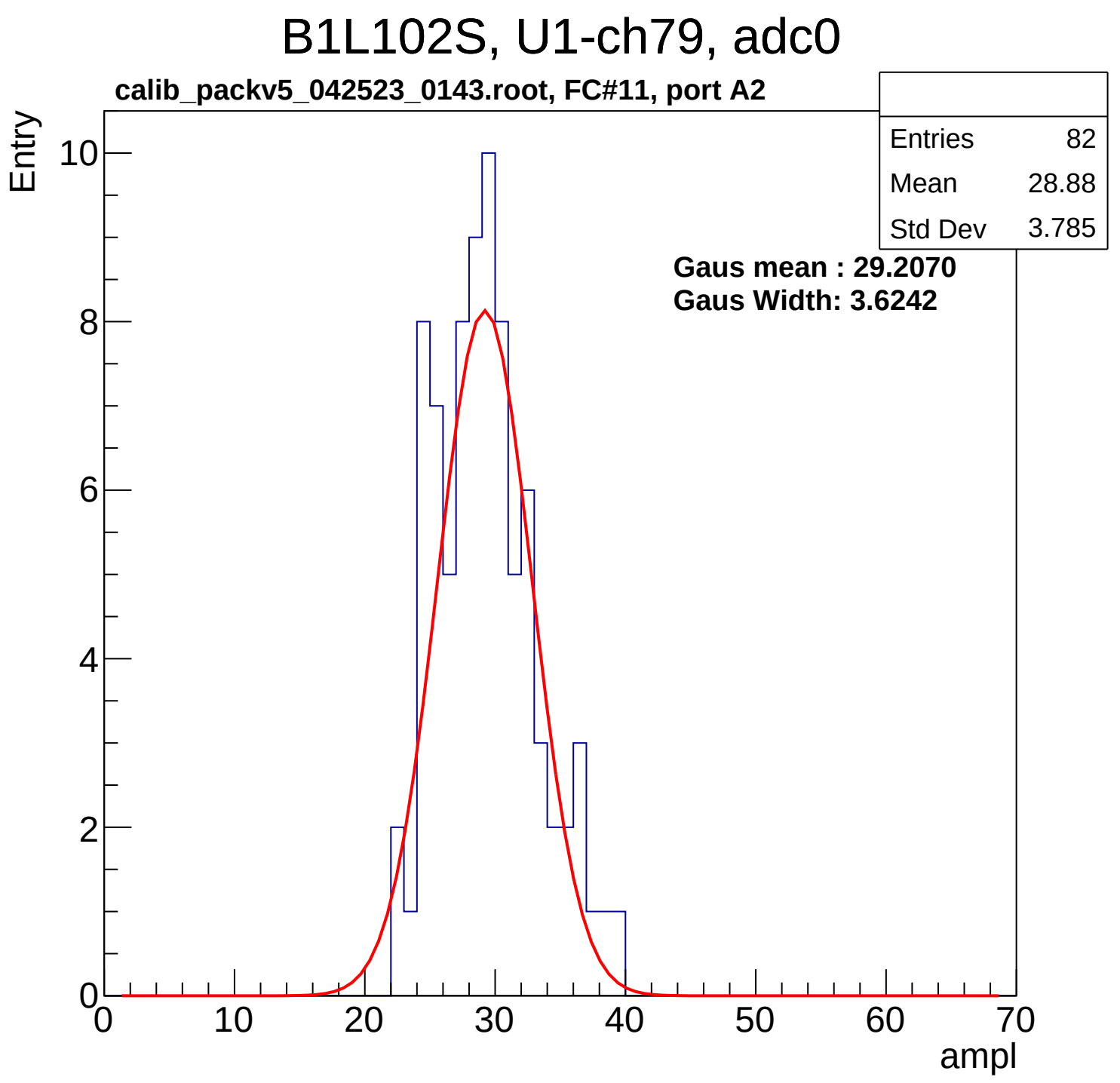
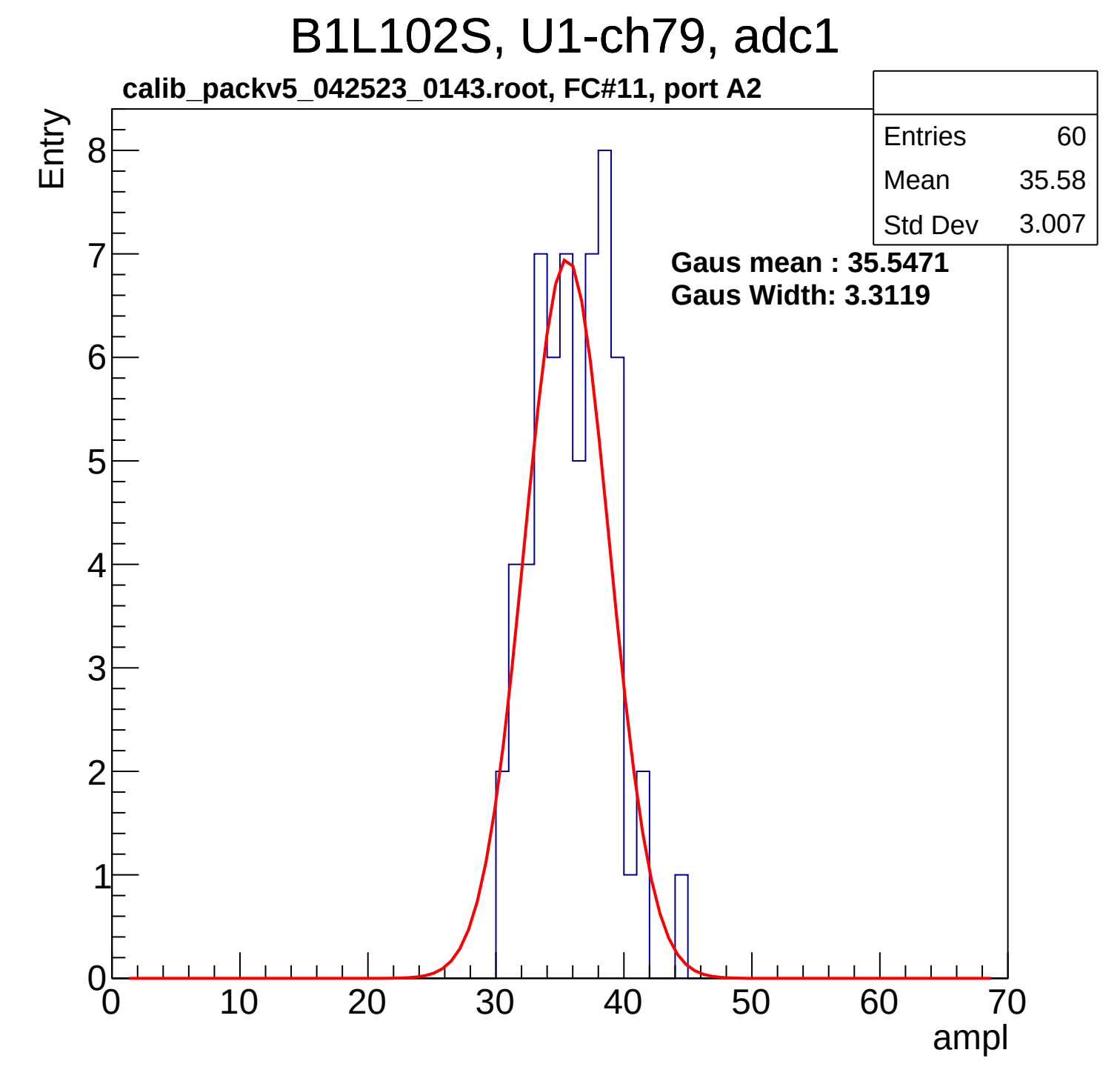
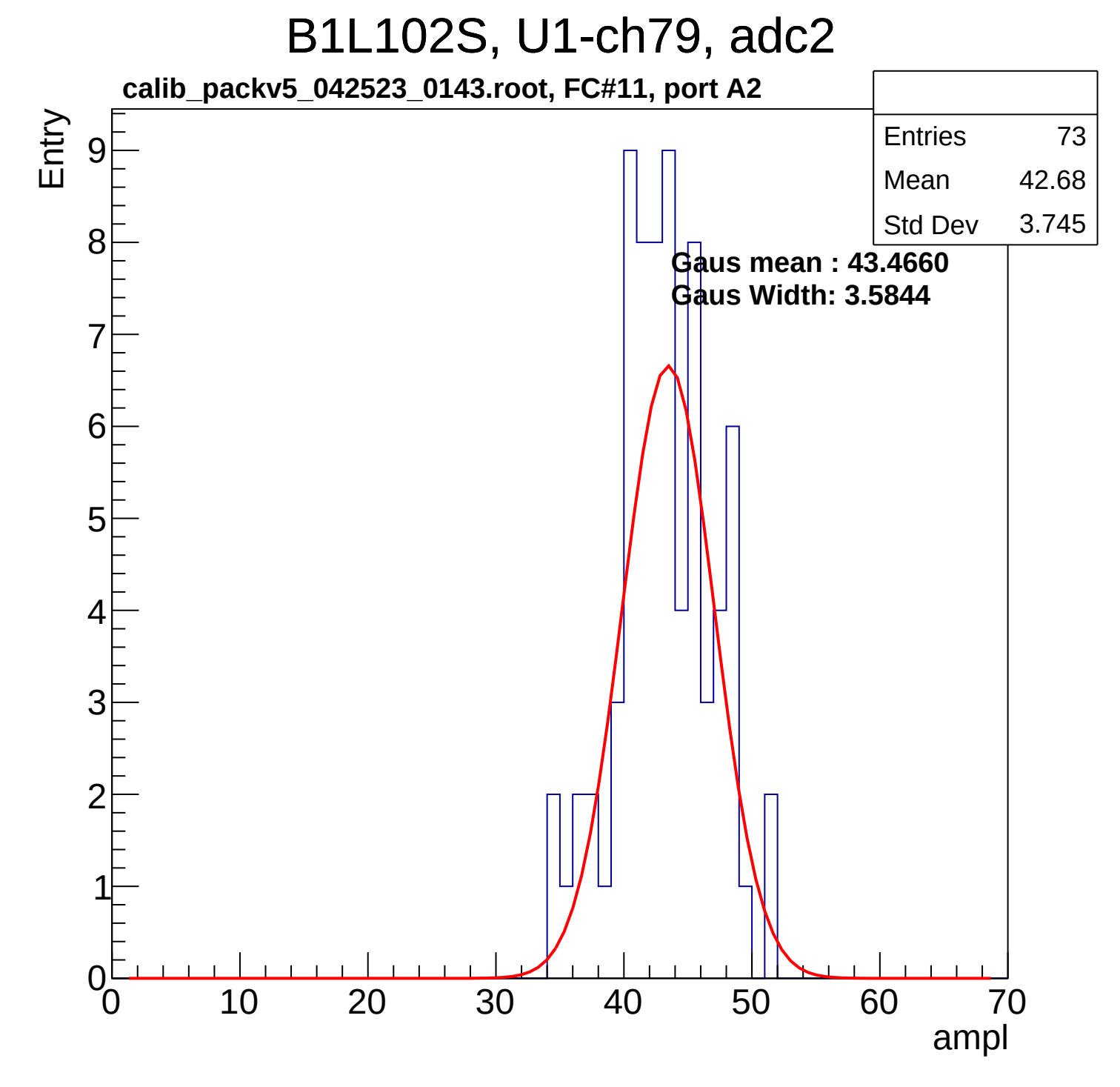
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

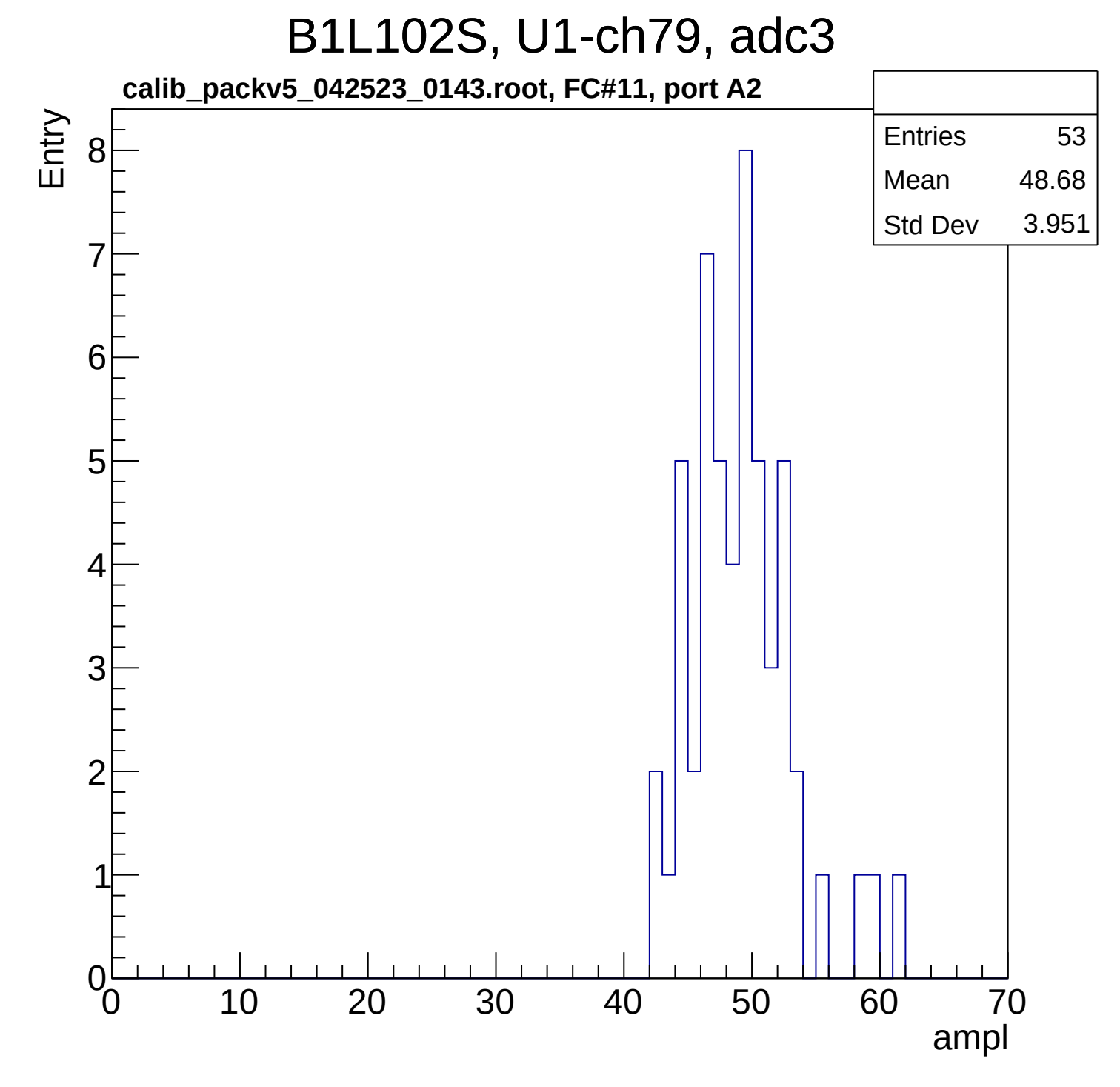

B1L102S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 56.29
Std Dev 15.77
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

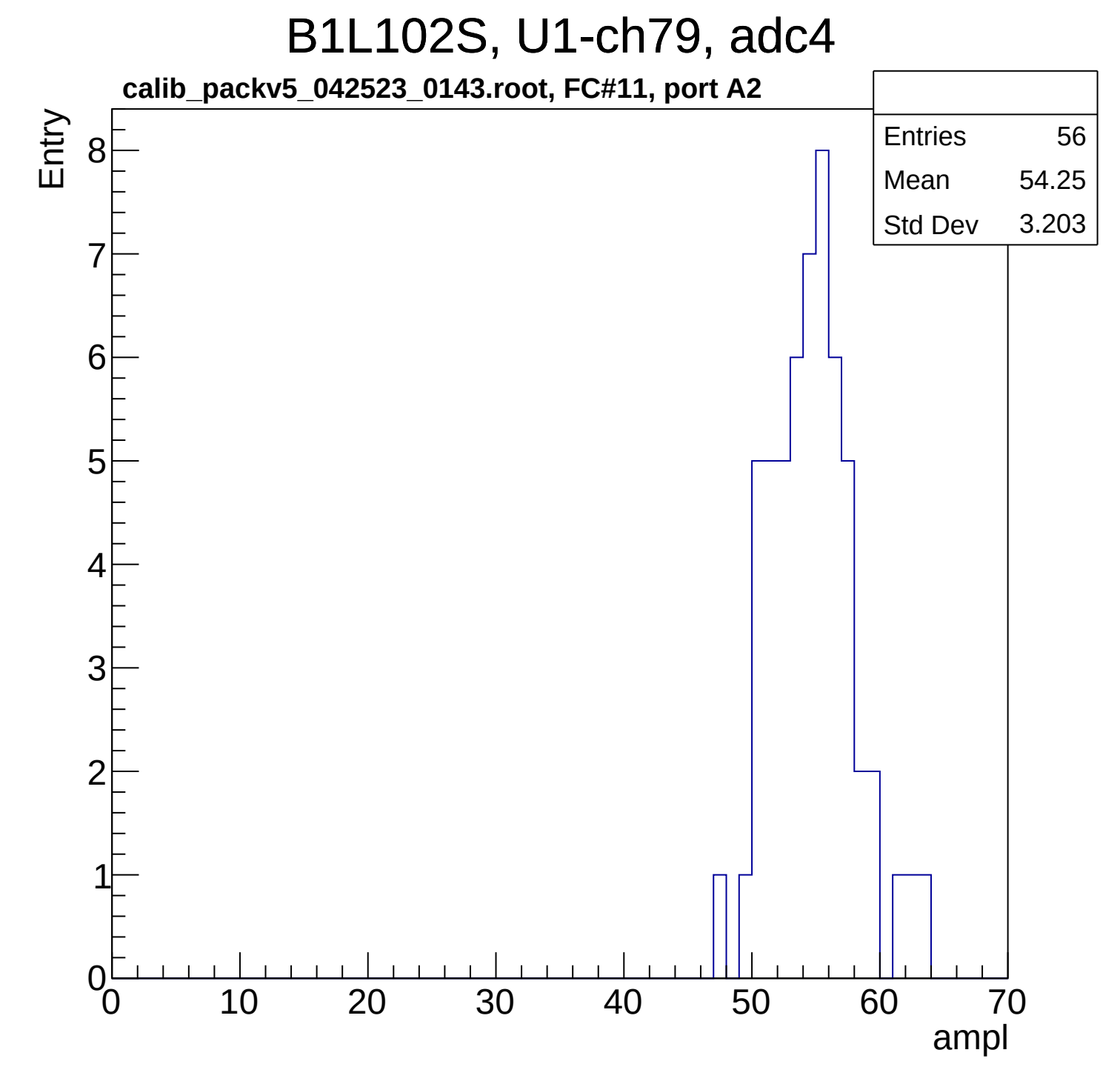

B1L102S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

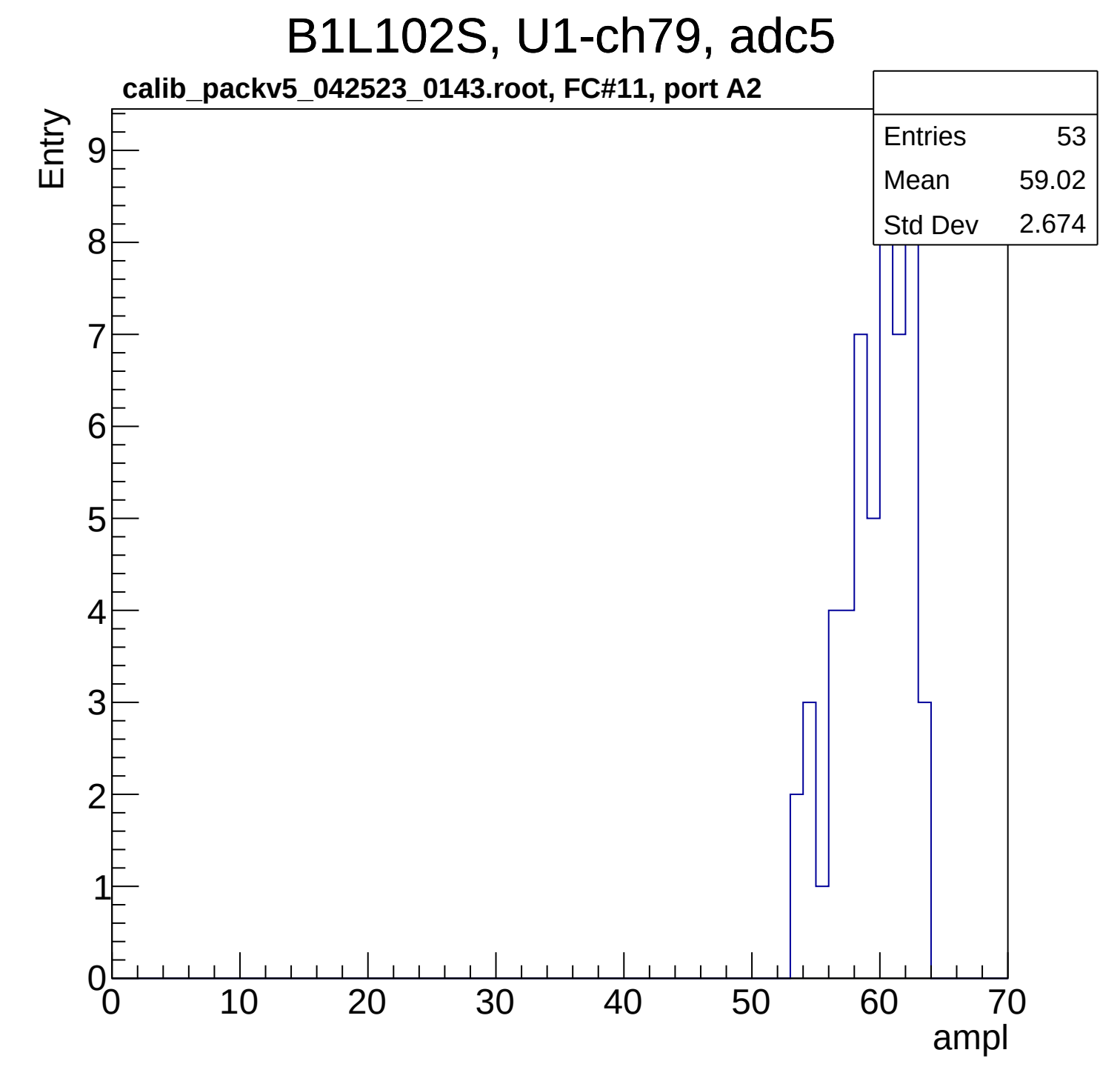

B1L102S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 28.88
Std Dev 3.785
Gaus mean : 29.2070
Gaus Width: 3.6242
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

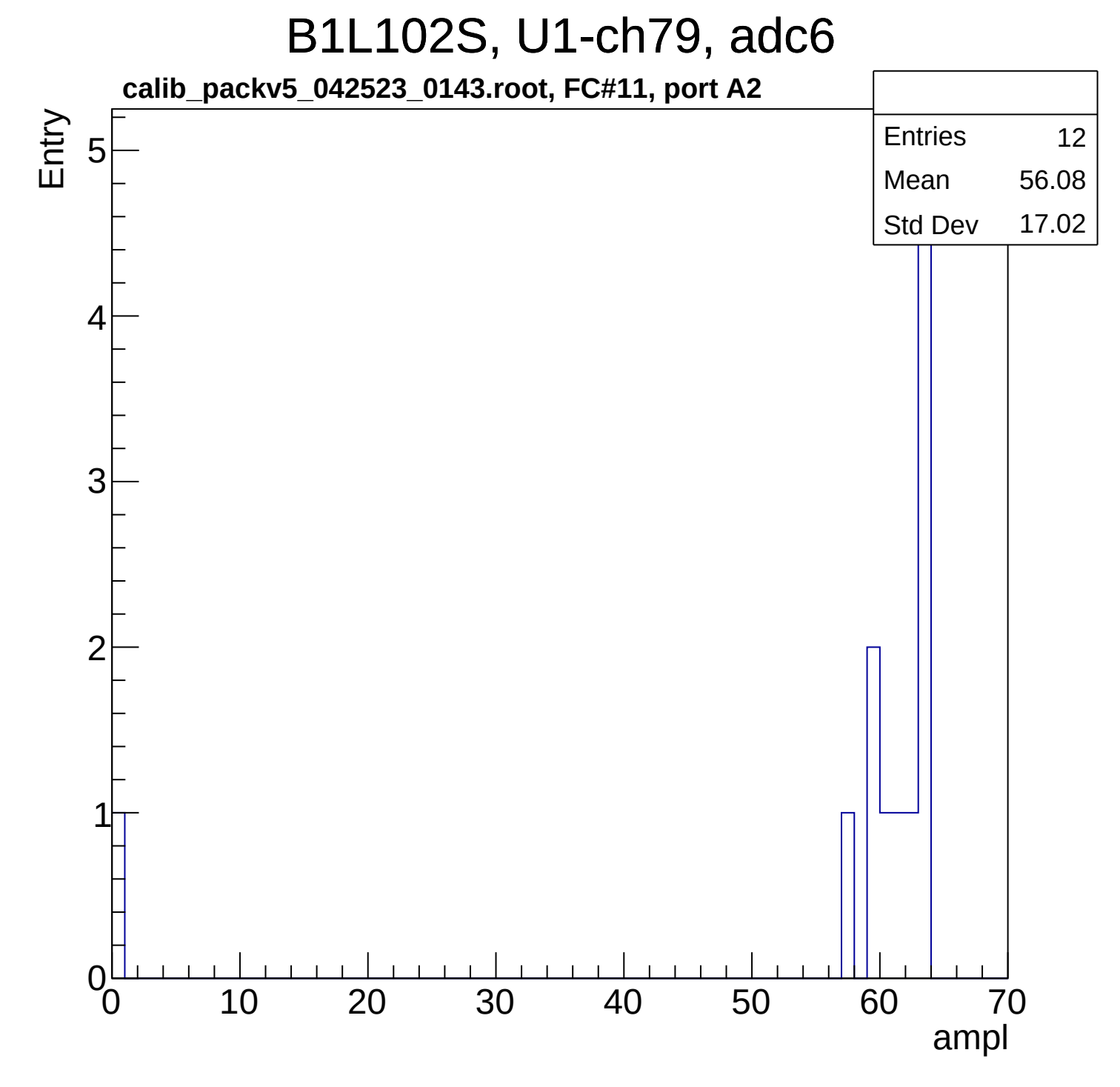

B1L102S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 35.58
Std Dev 3.007
Gaus mean : 35.5471
Gaus Width: 3.3119
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 42.68
Std Dev 3.745
Gaus mean : 43.4660
Gaus Width: 3.5844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 48.68
Std Dev 3.951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 54.25
Std Dev 3.203
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 59.02
Std Dev 2.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 56.08
Std Dev 17.02
Entry
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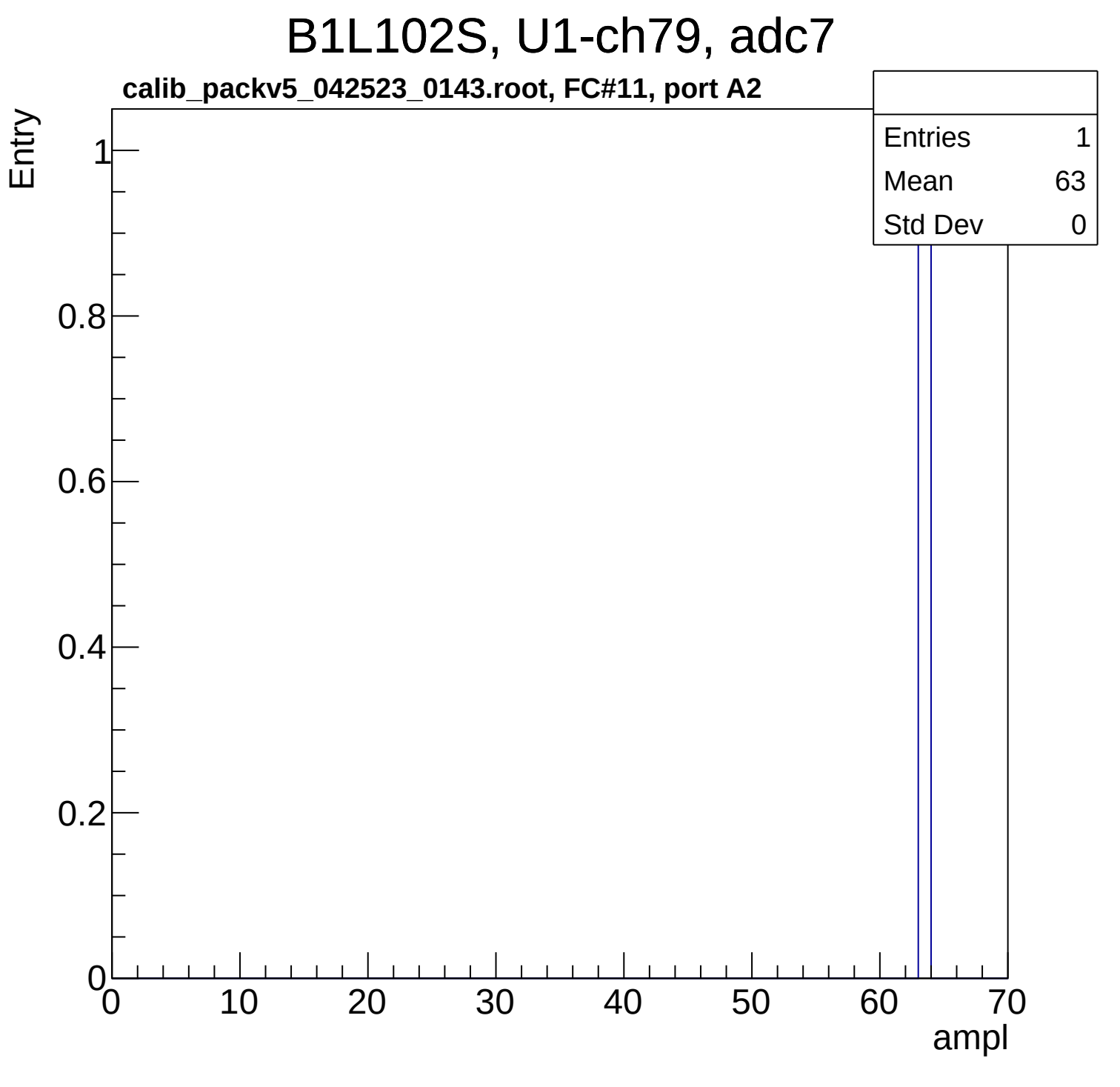
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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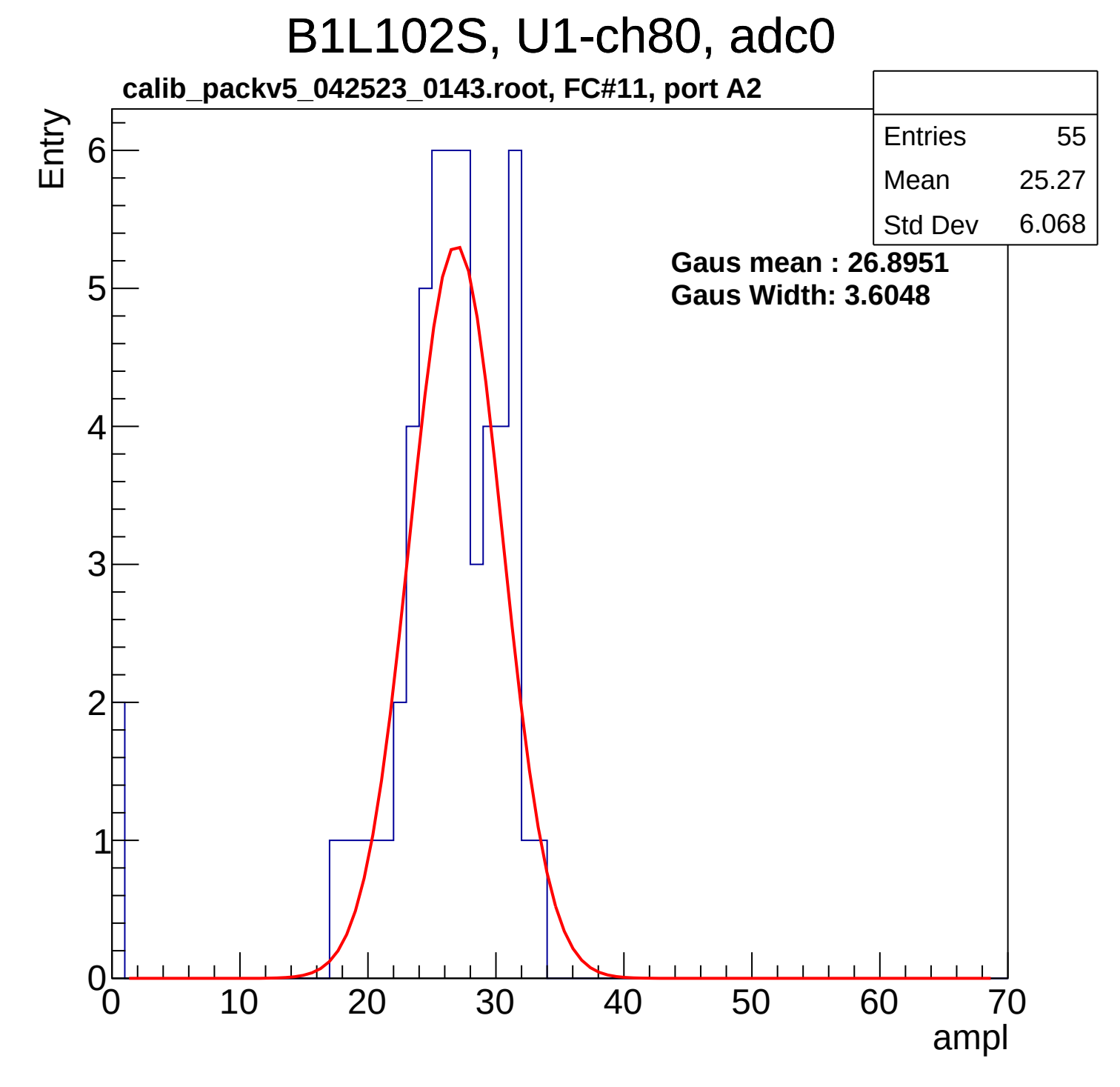
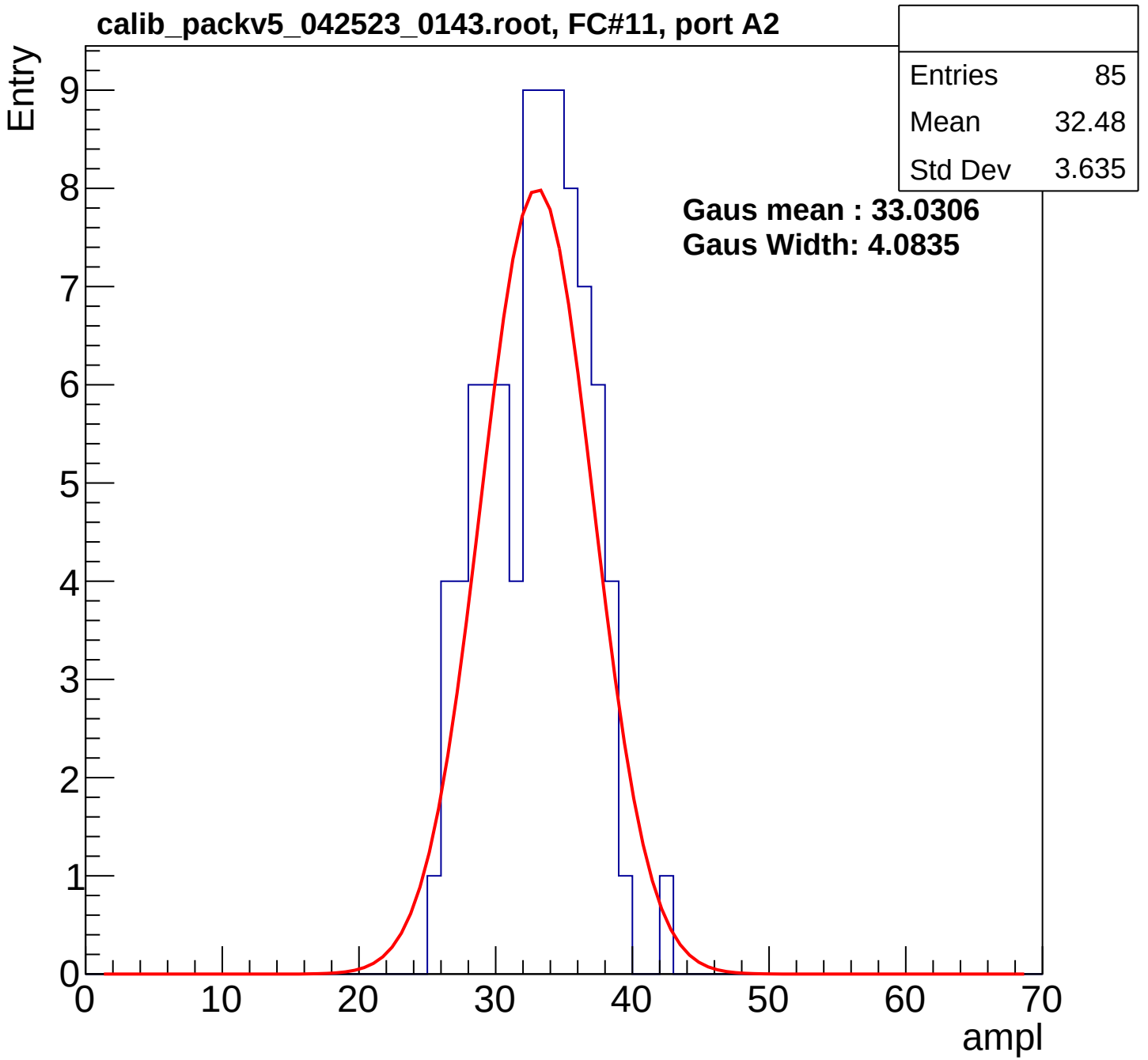
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 25.27
Std Dev 6.068
Gaus mean : 26.8951
Gaus Width: 3.6048
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 85
Mean 32.48
Std Dev 3.635
Gaus mean : 33.0306
Gaus Width: 4.0835
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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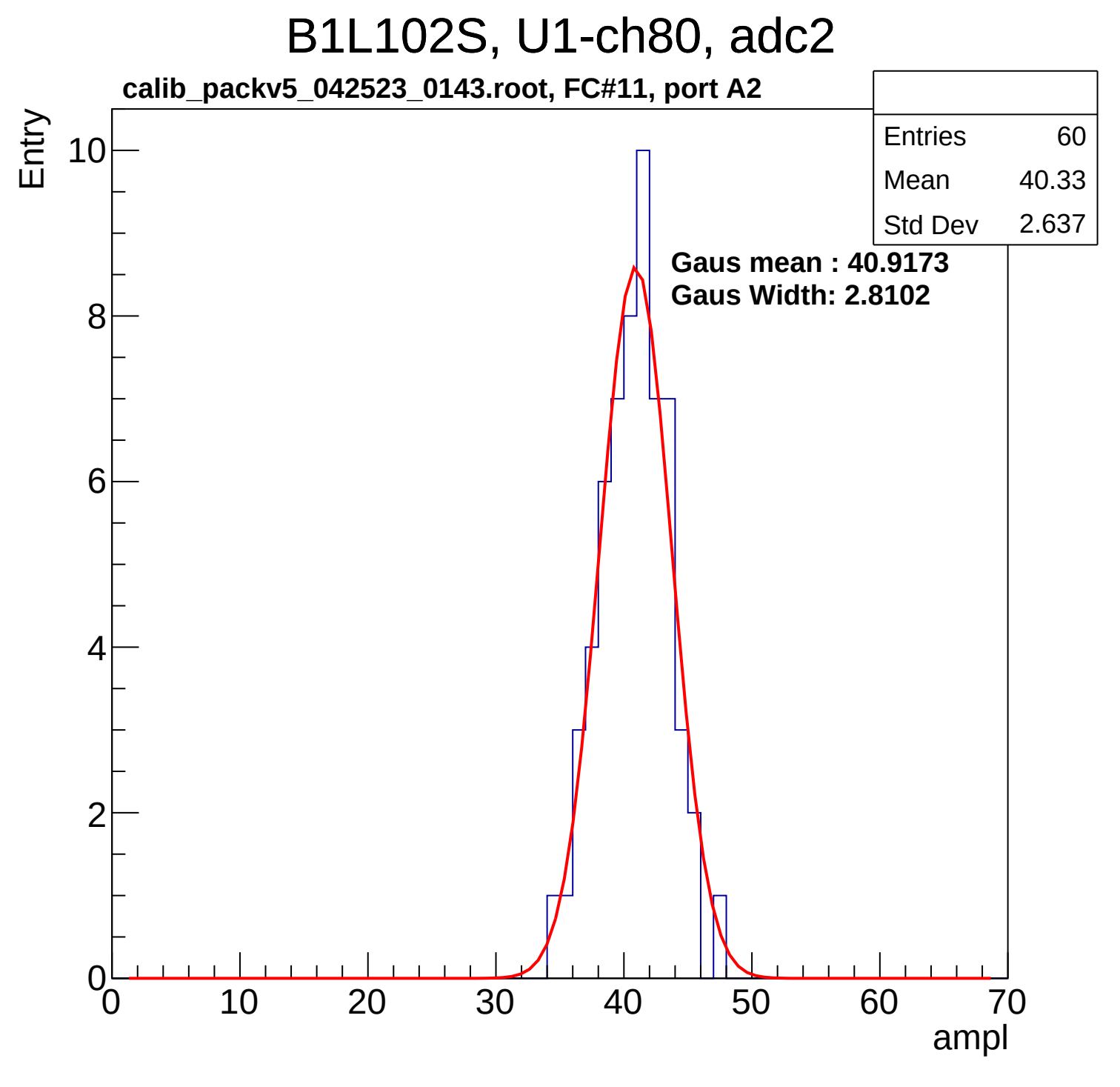
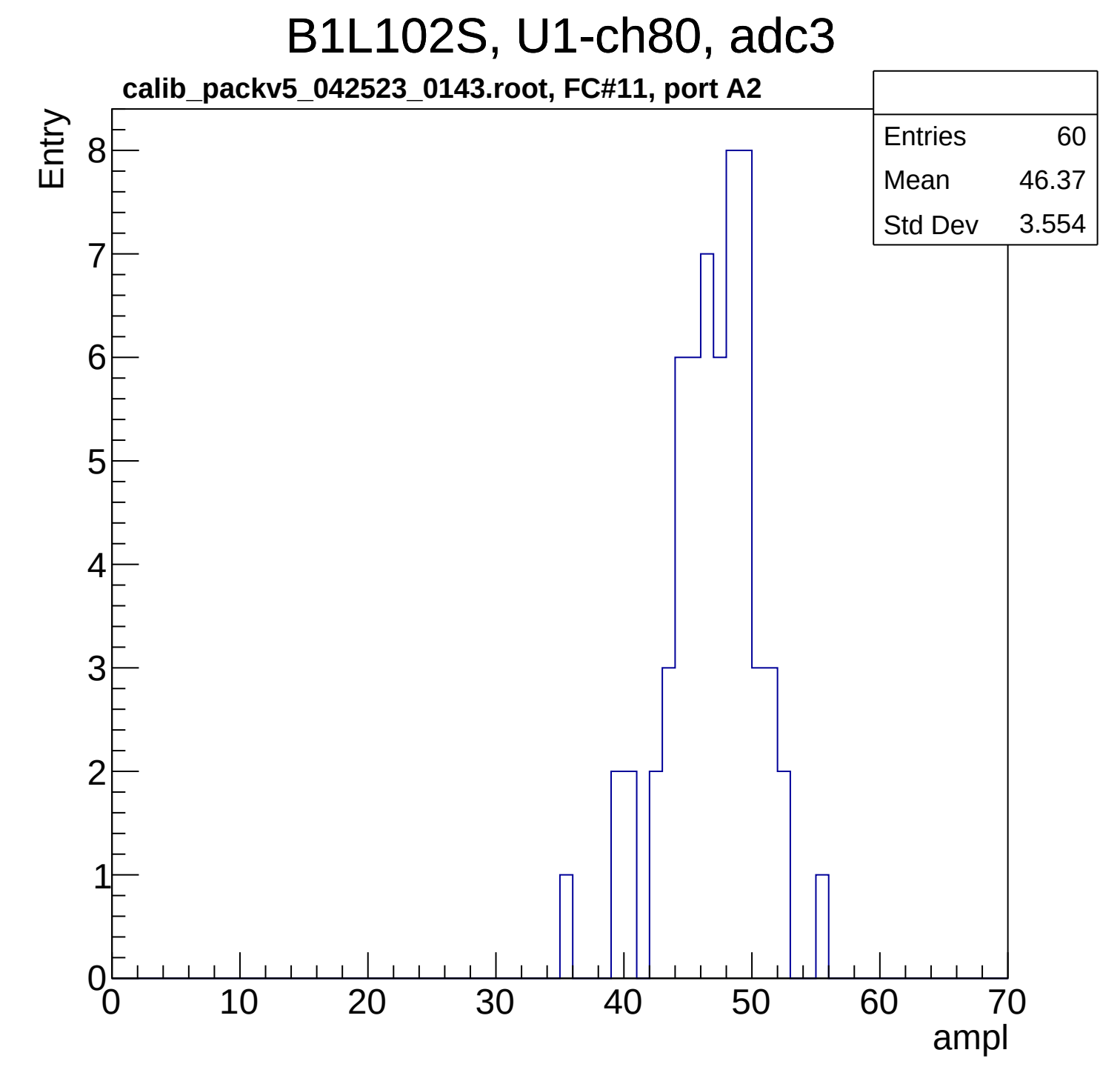

B1L102S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 40.33
Std Dev 2.637
Gaus mean : 40.9173
Gaus Width: 2.8102
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 46.37
Std Dev 3.554
Entry
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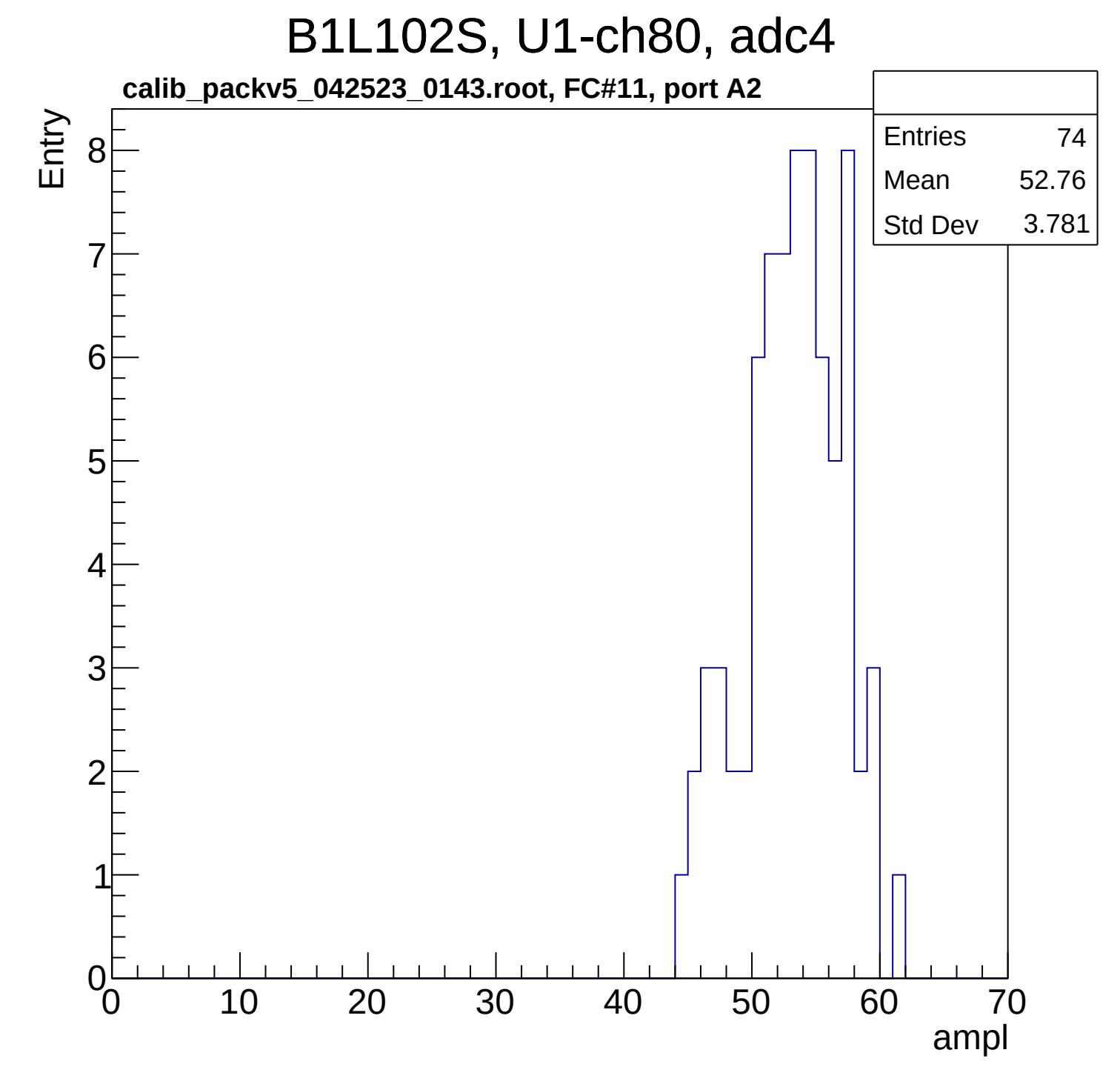
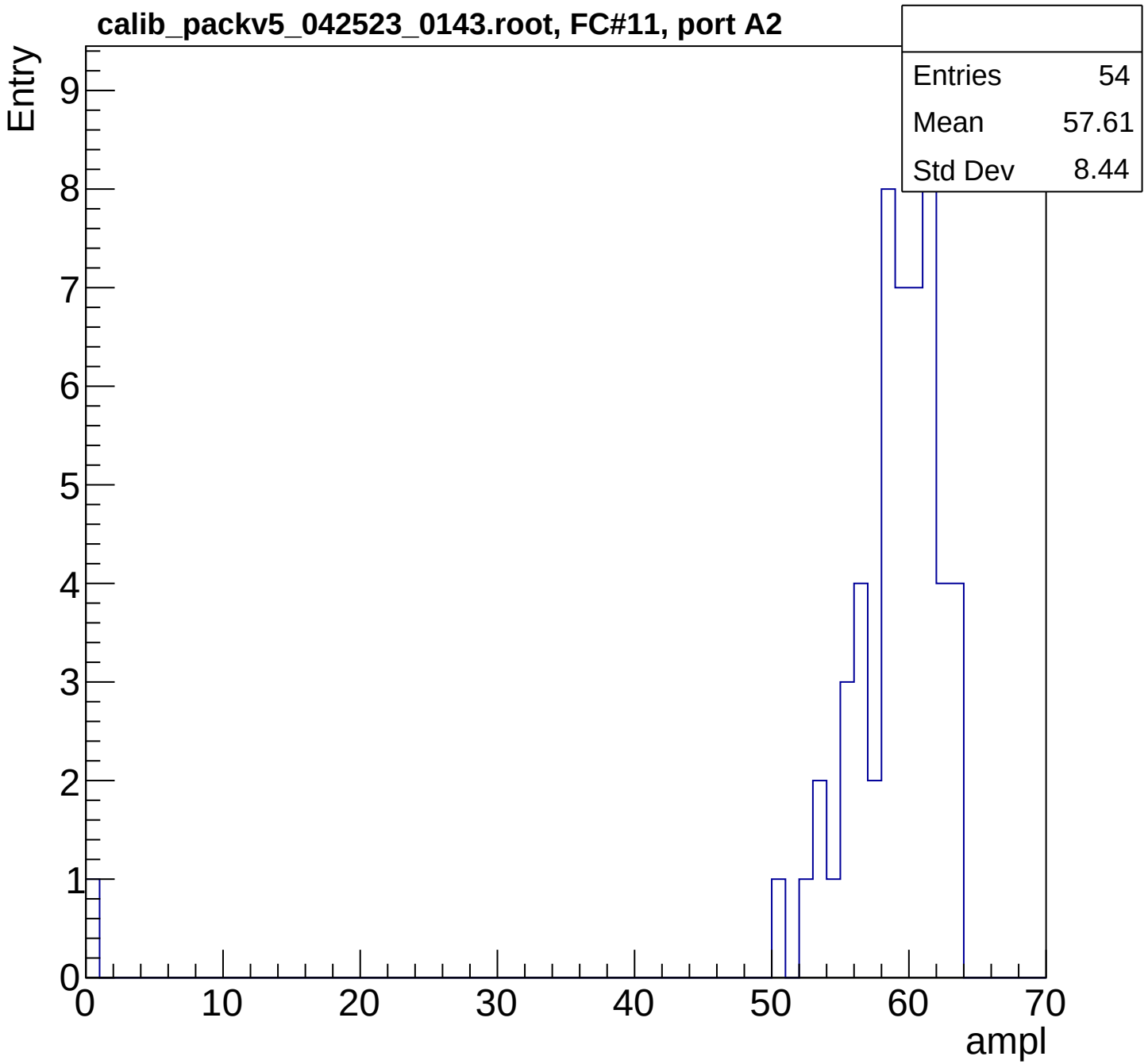
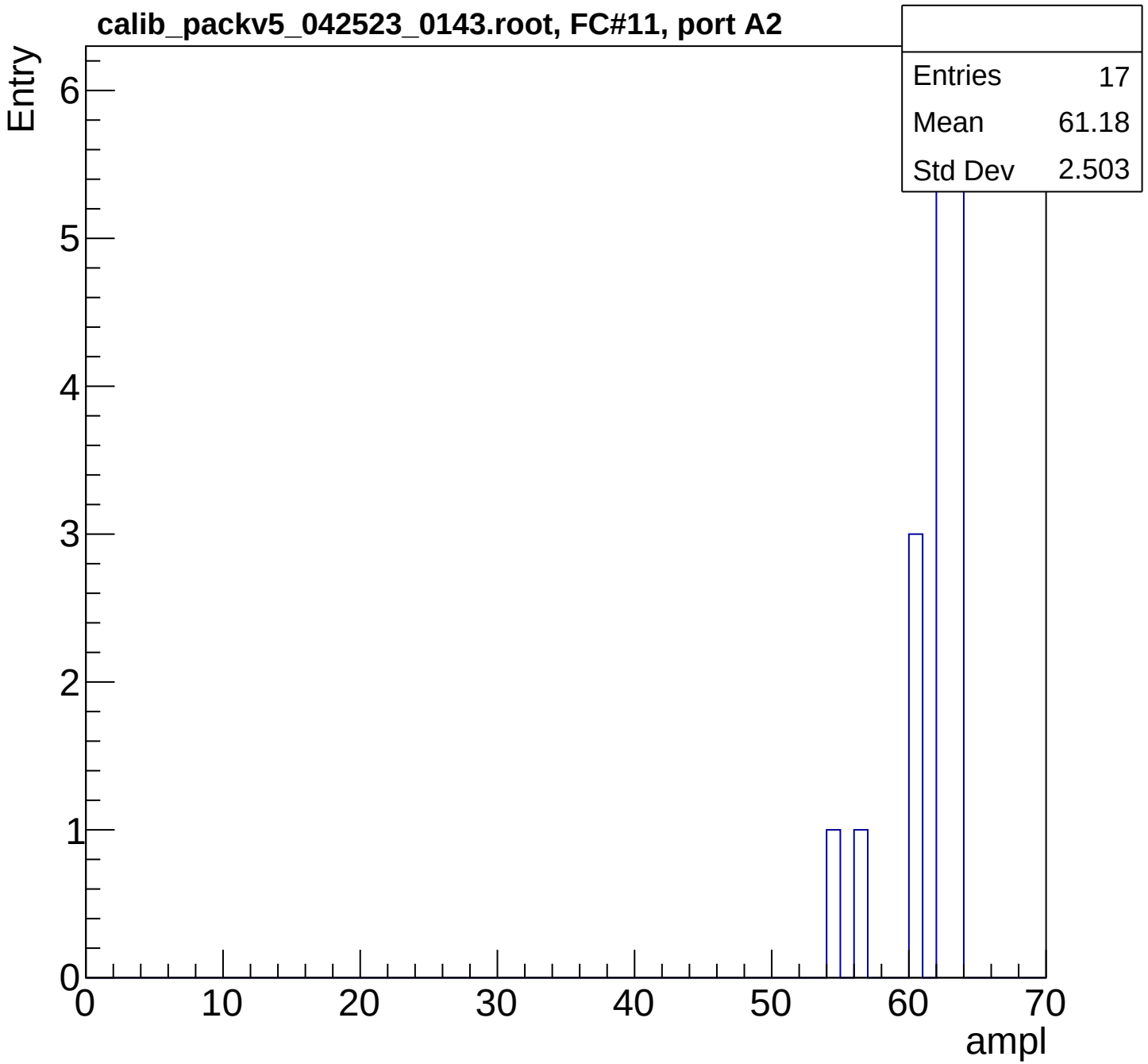
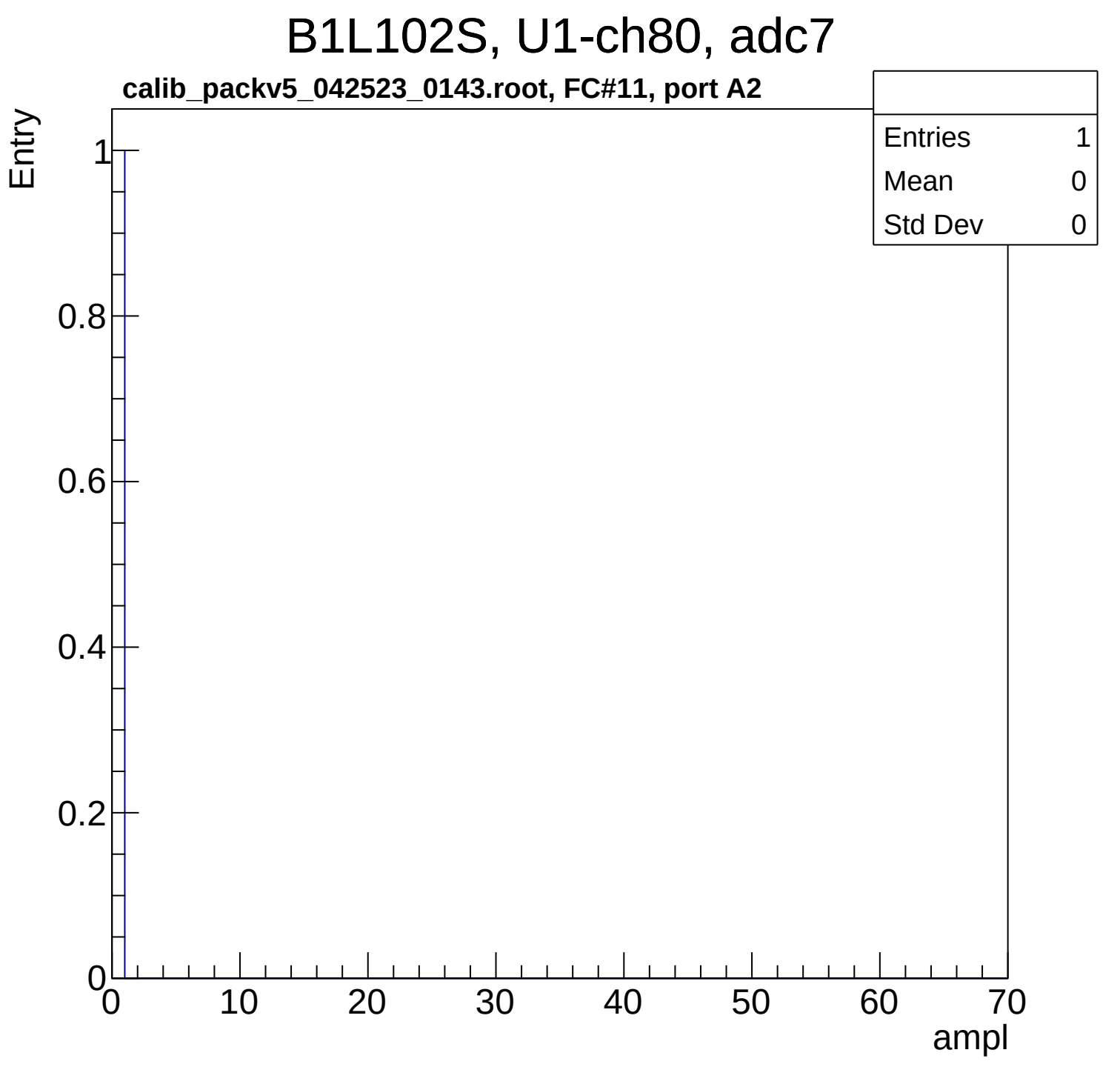
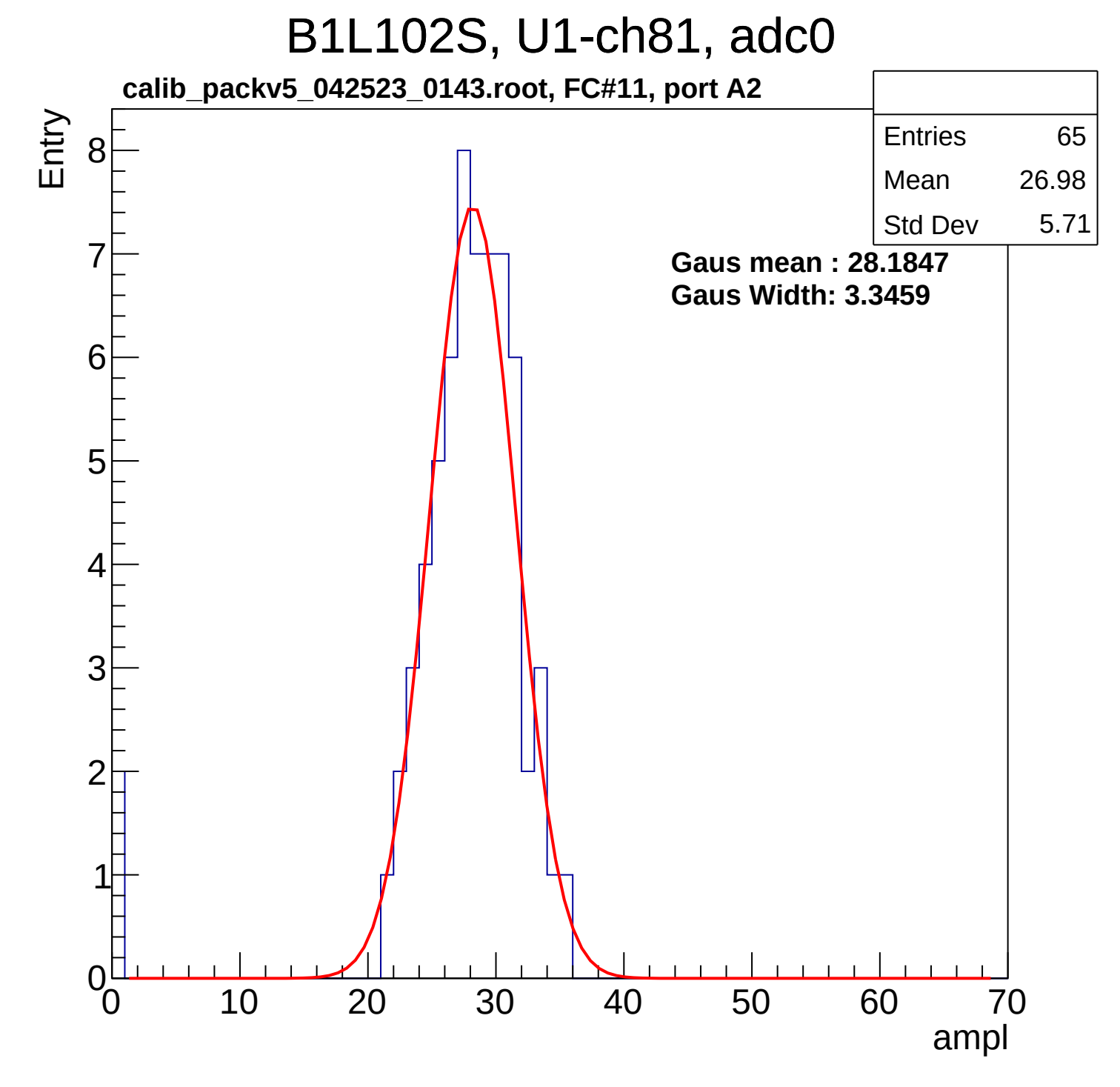
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

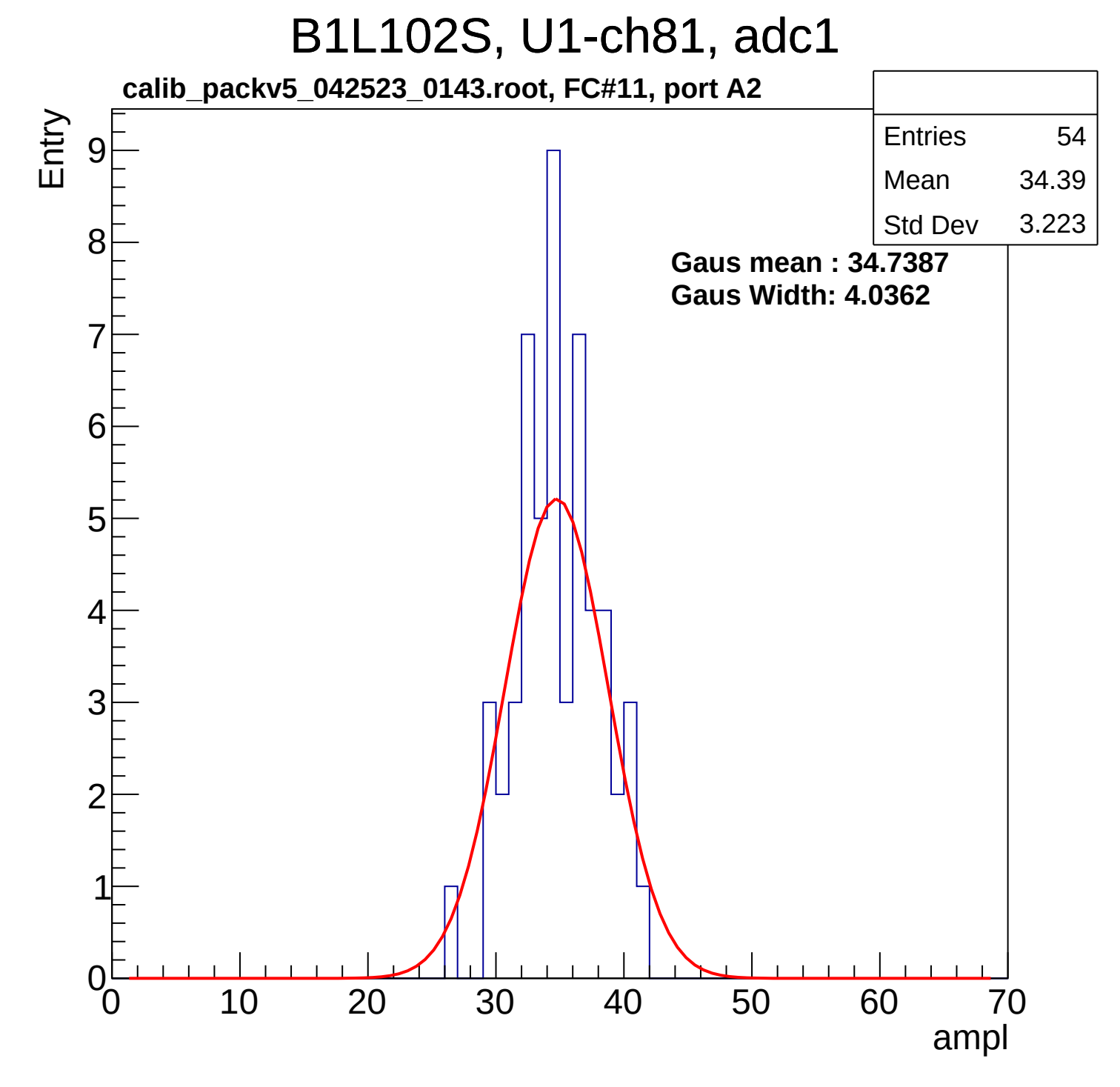

B1L102S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 52.76
Std Dev 3.781
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

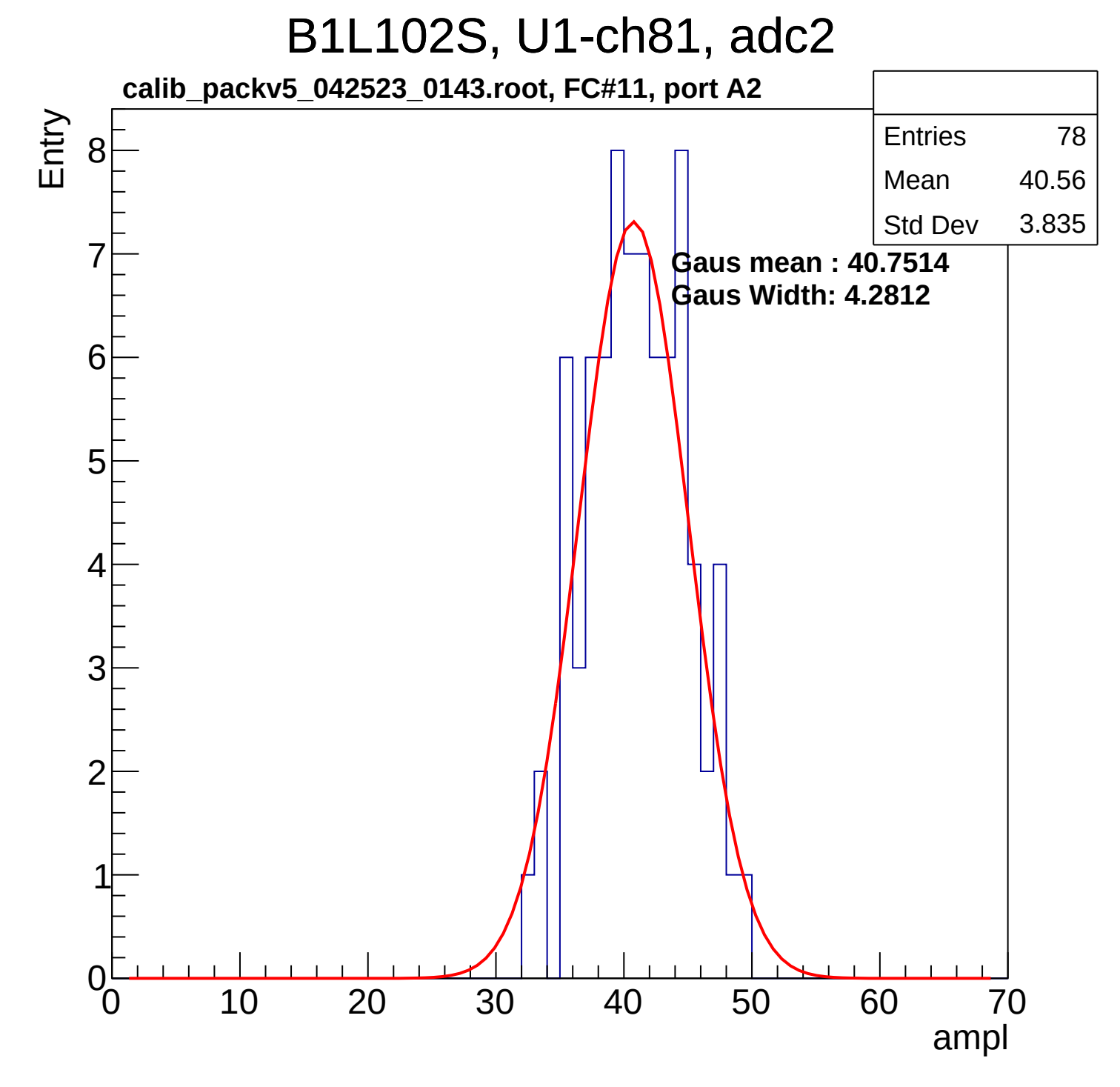

B1L102S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 57.61
Std Dev 8.44
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

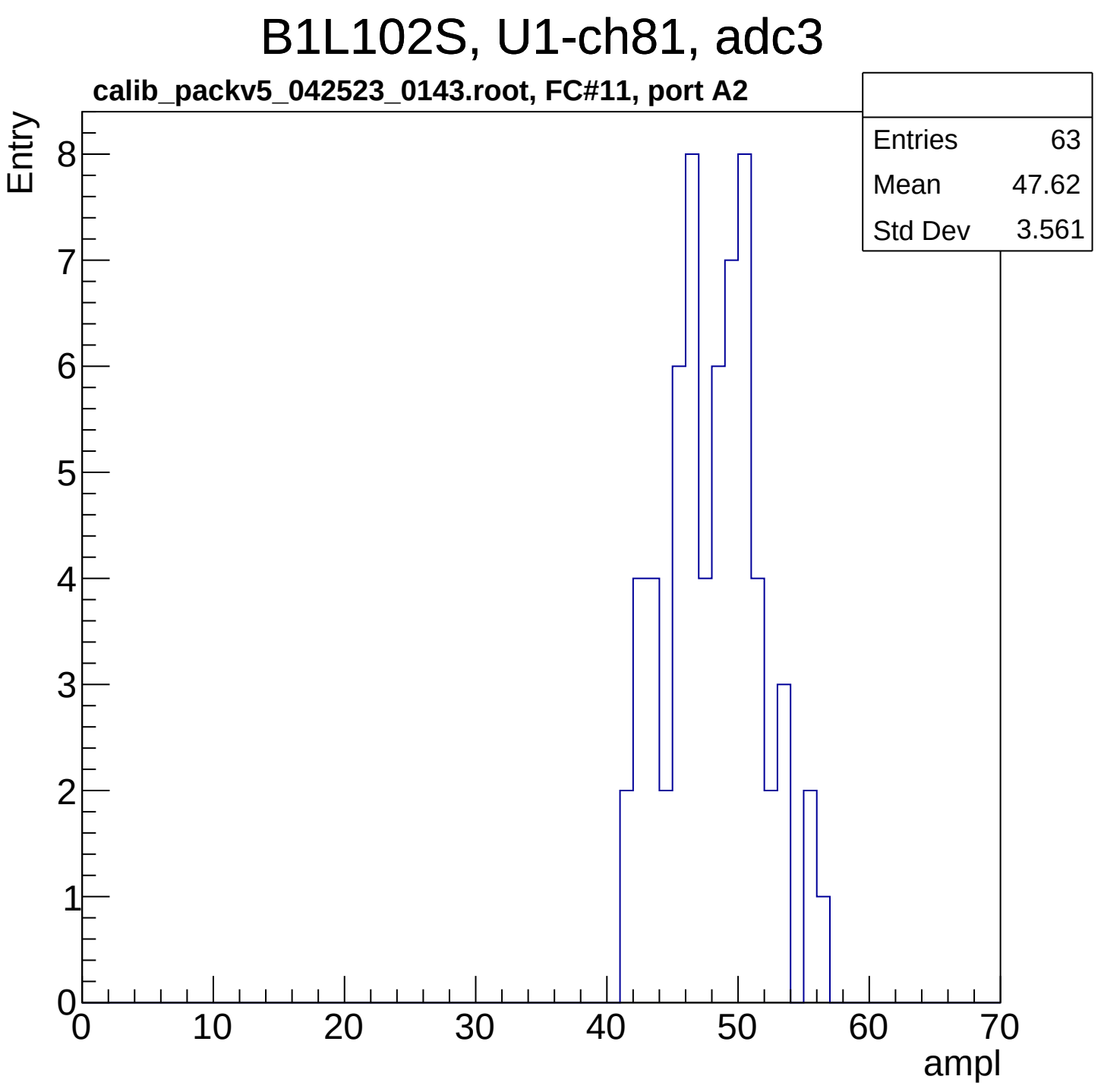

B1L102S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 17
Mean 61.18
Std Dev 2.503
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

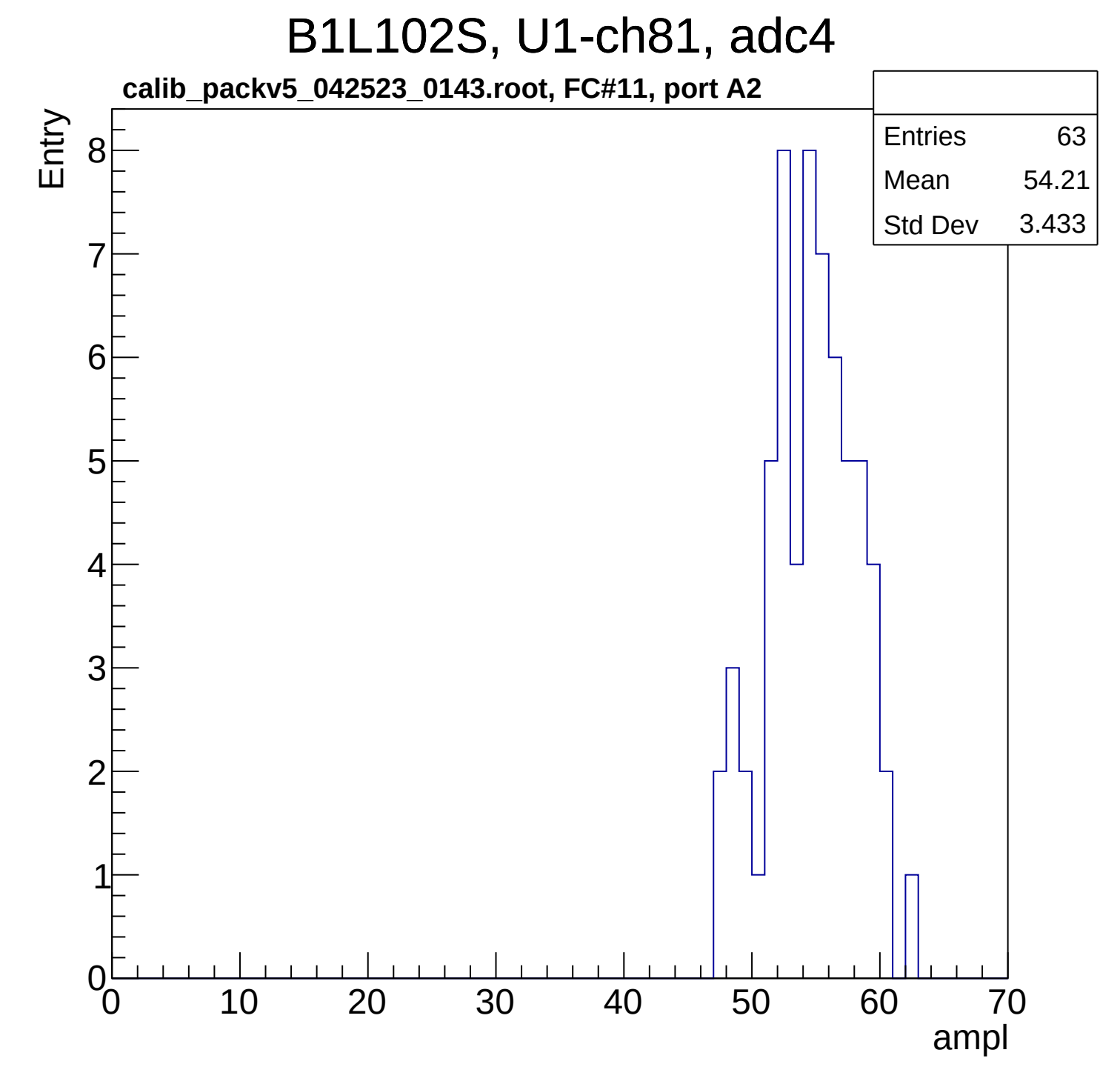

B1L102S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 26.98
Std Dev 5.71
Gaus mean : 28.1847
Gaus Width: 3.3459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 34.39
Std Dev 3.223
Gaus mean : 34.7387
Gaus Width: 4.0362
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 40.56
Std Dev 3.835
Gaus mean : 40.7514
Gaus Width: 4.2812
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 47.62
Std Dev 3.561
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 54.21
Std Dev 3.433
Entry
ampl
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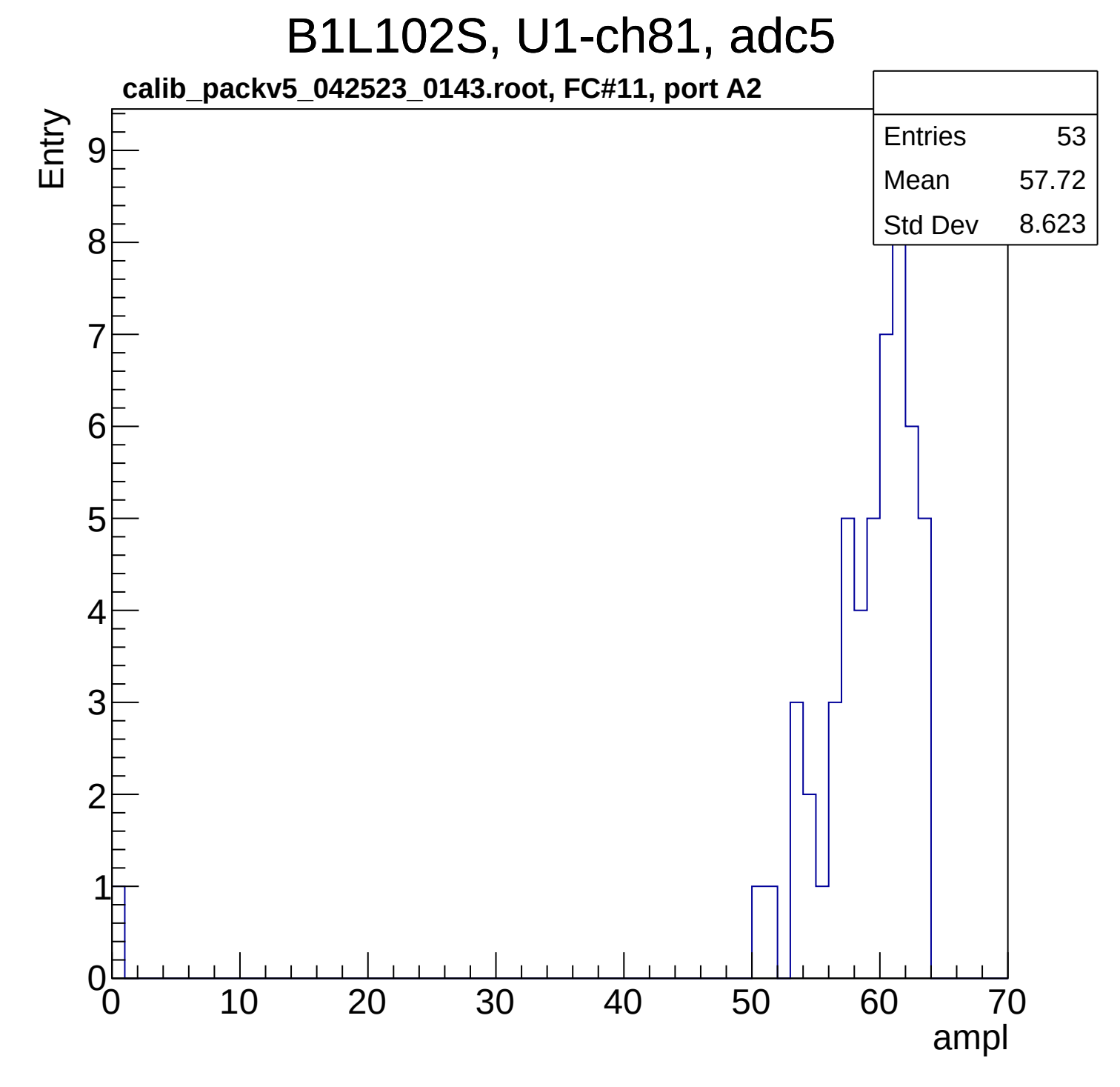
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 57.72
Std Dev 8.623
Entry
ampl
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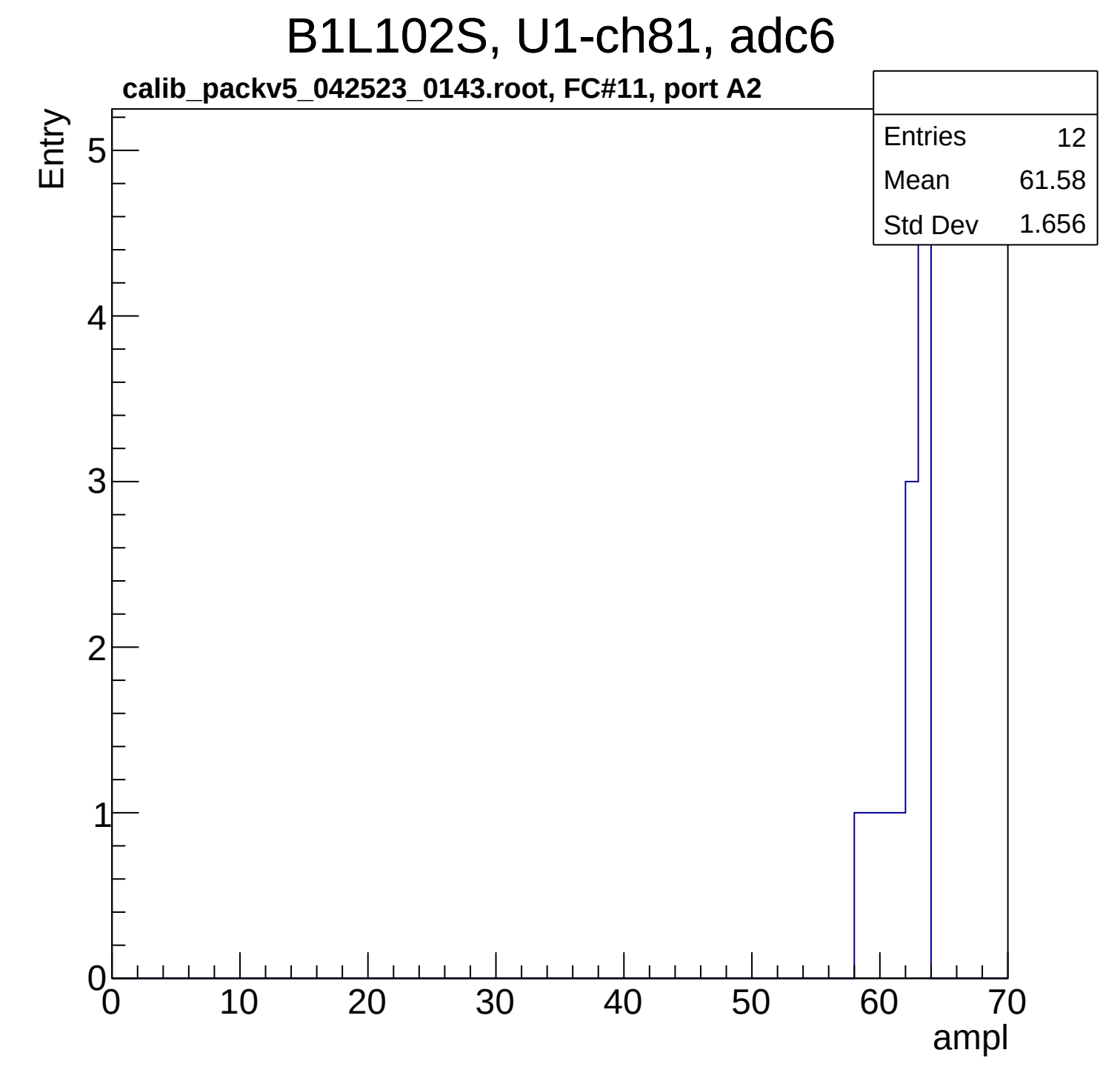
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.58
Std Dev 1.656
Entry
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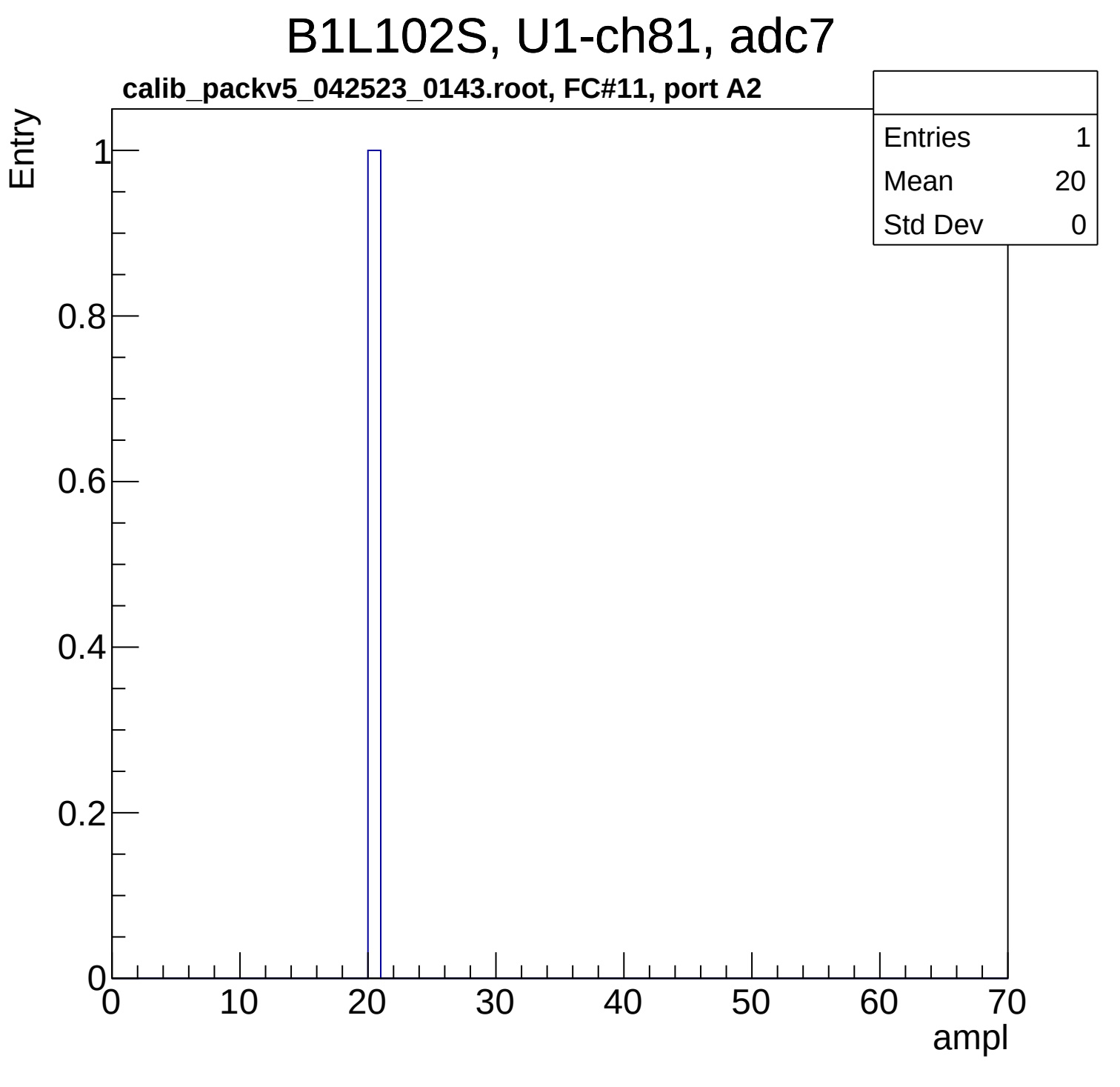
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
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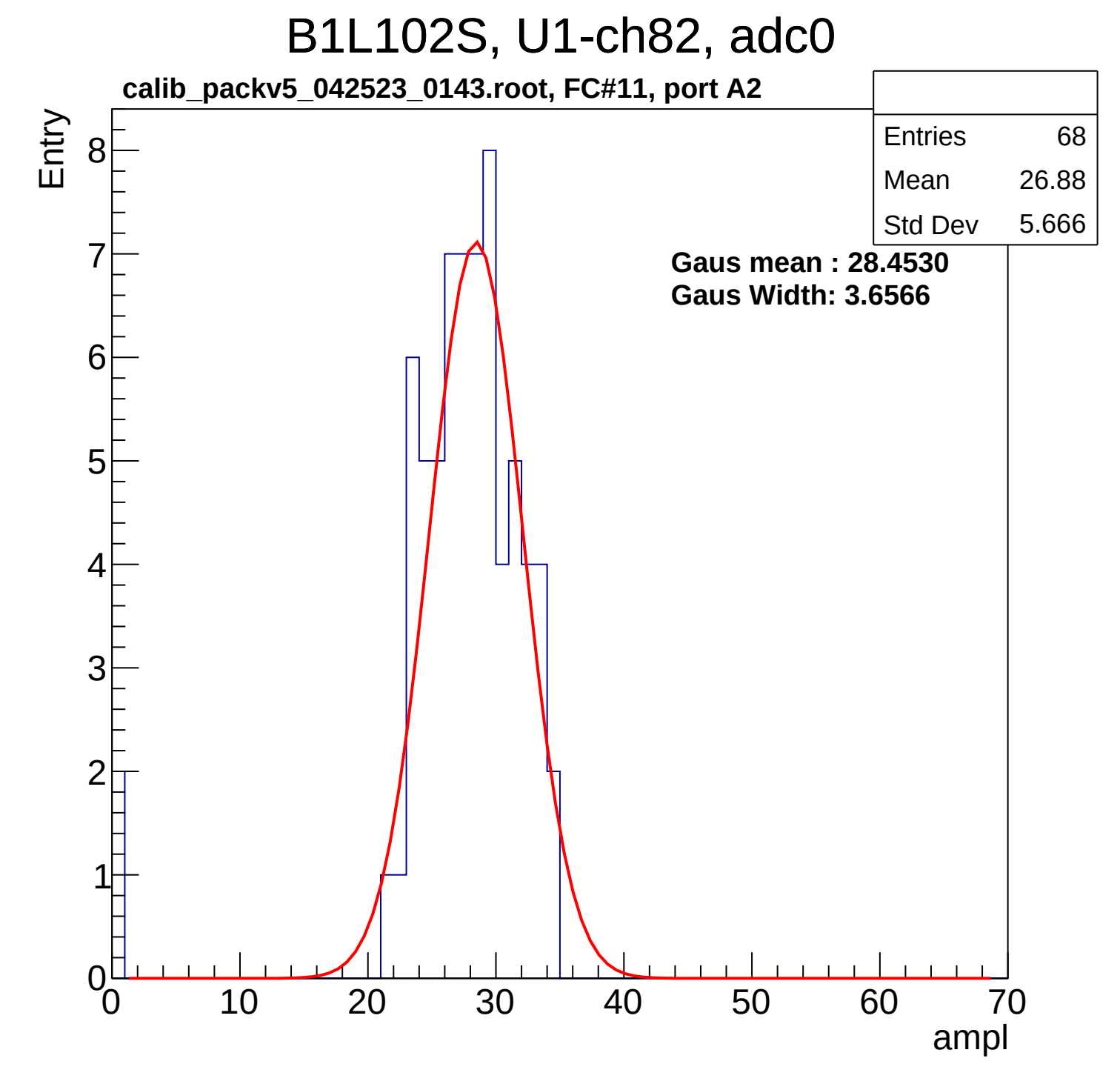
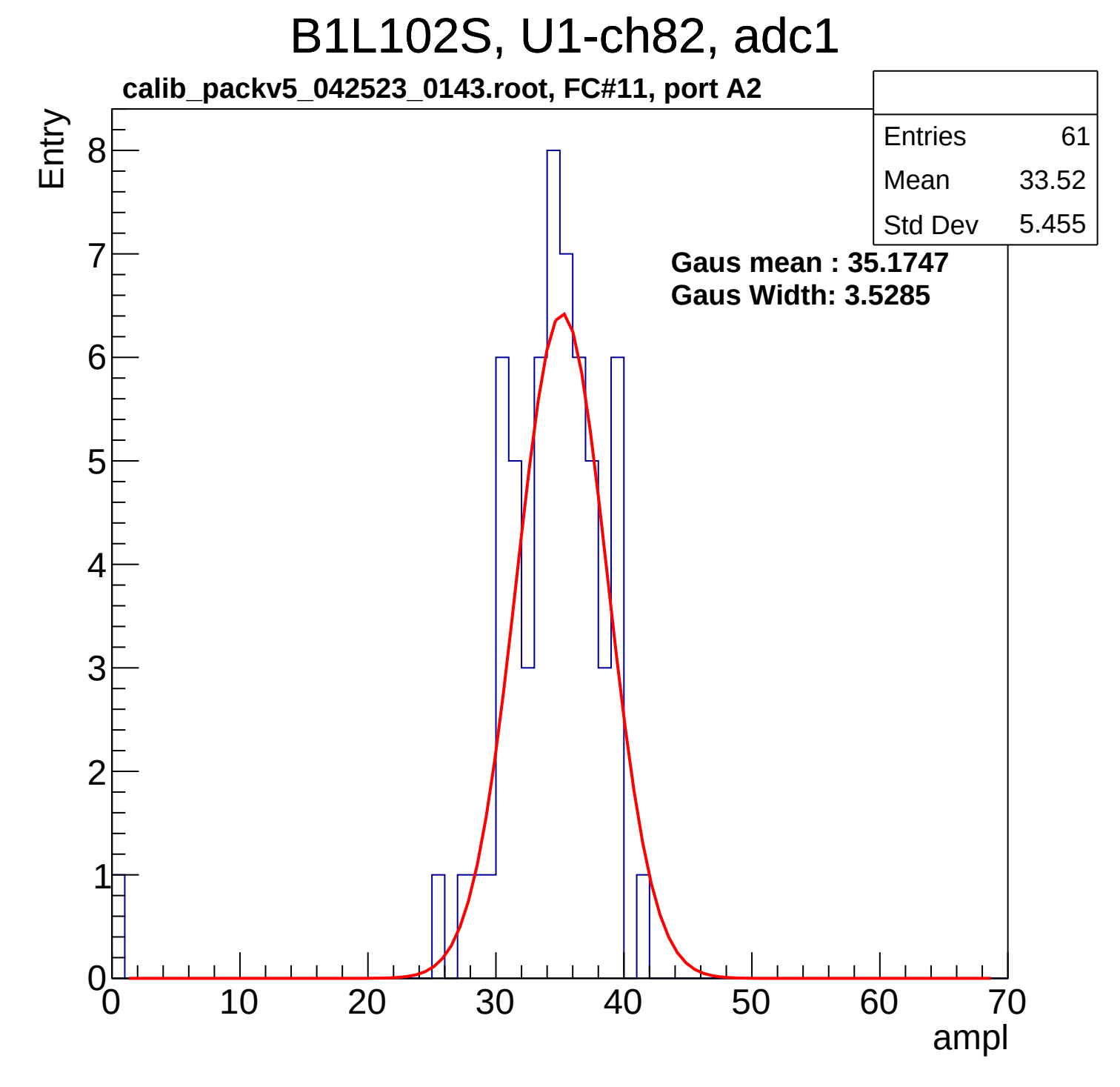
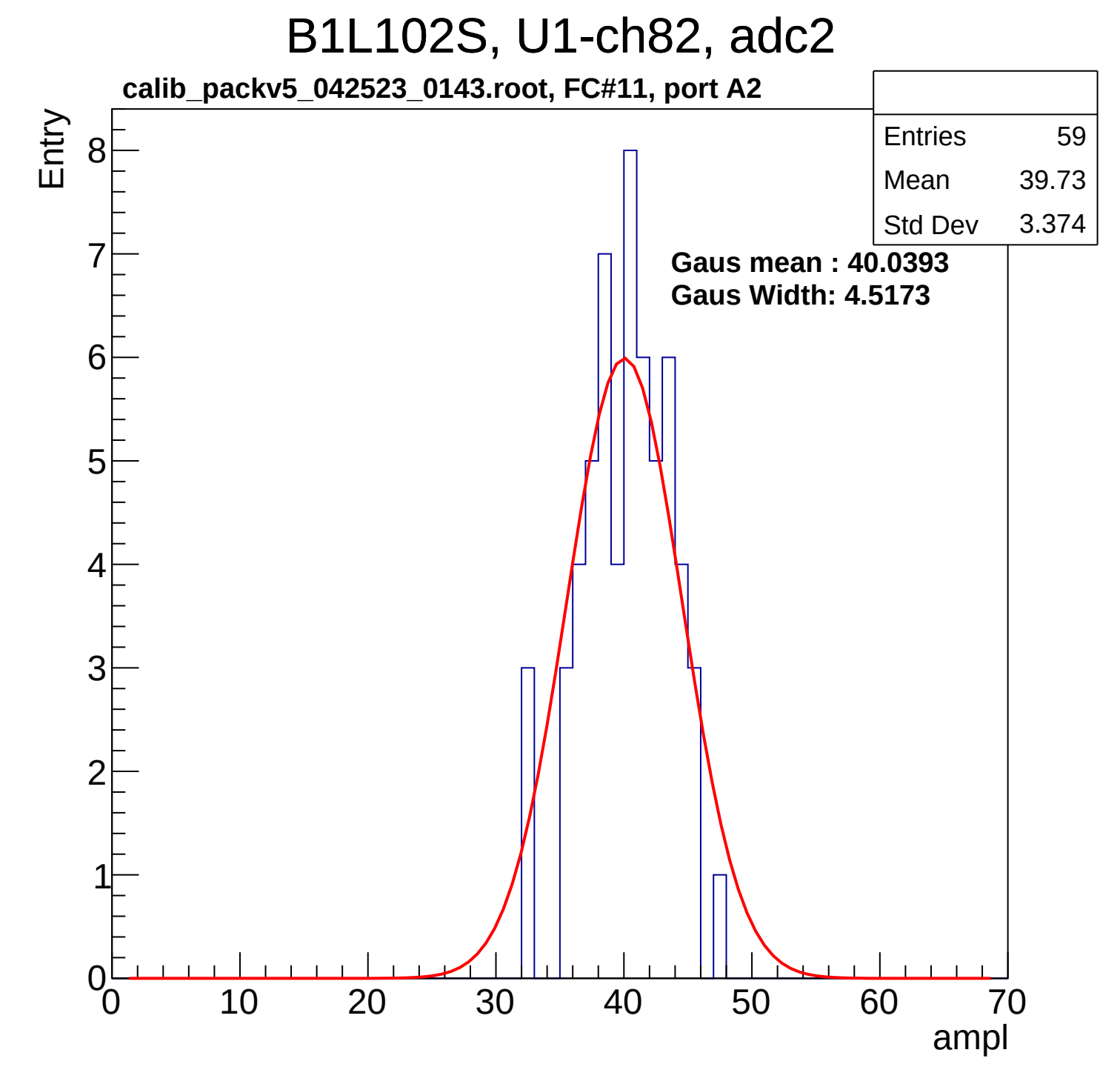
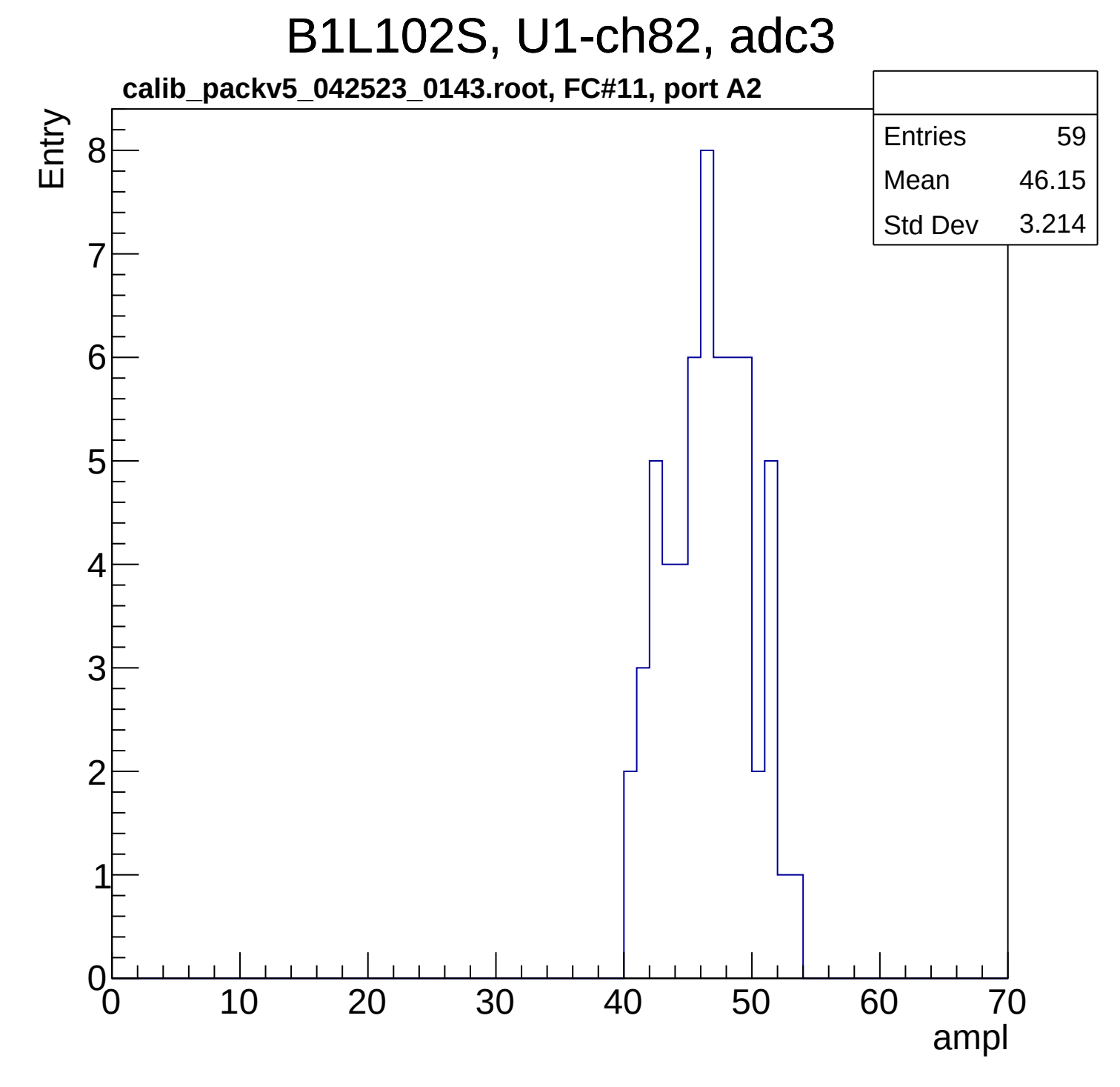
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 26.88
Std Dev 5.666
Gaus mean : 28.4530
Gaus Width: 3.6566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 33.52
Std Dev 5.455
Gaus mean : 35.1747
Gaus Width: 3.5285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 39.73
Std Dev 3.374
Gaus mean : 40.0393
Gaus Width: 4.5173
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 46.15
Std Dev 3.214
Entry
ampl
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1
2
3
4
5
6
7
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10
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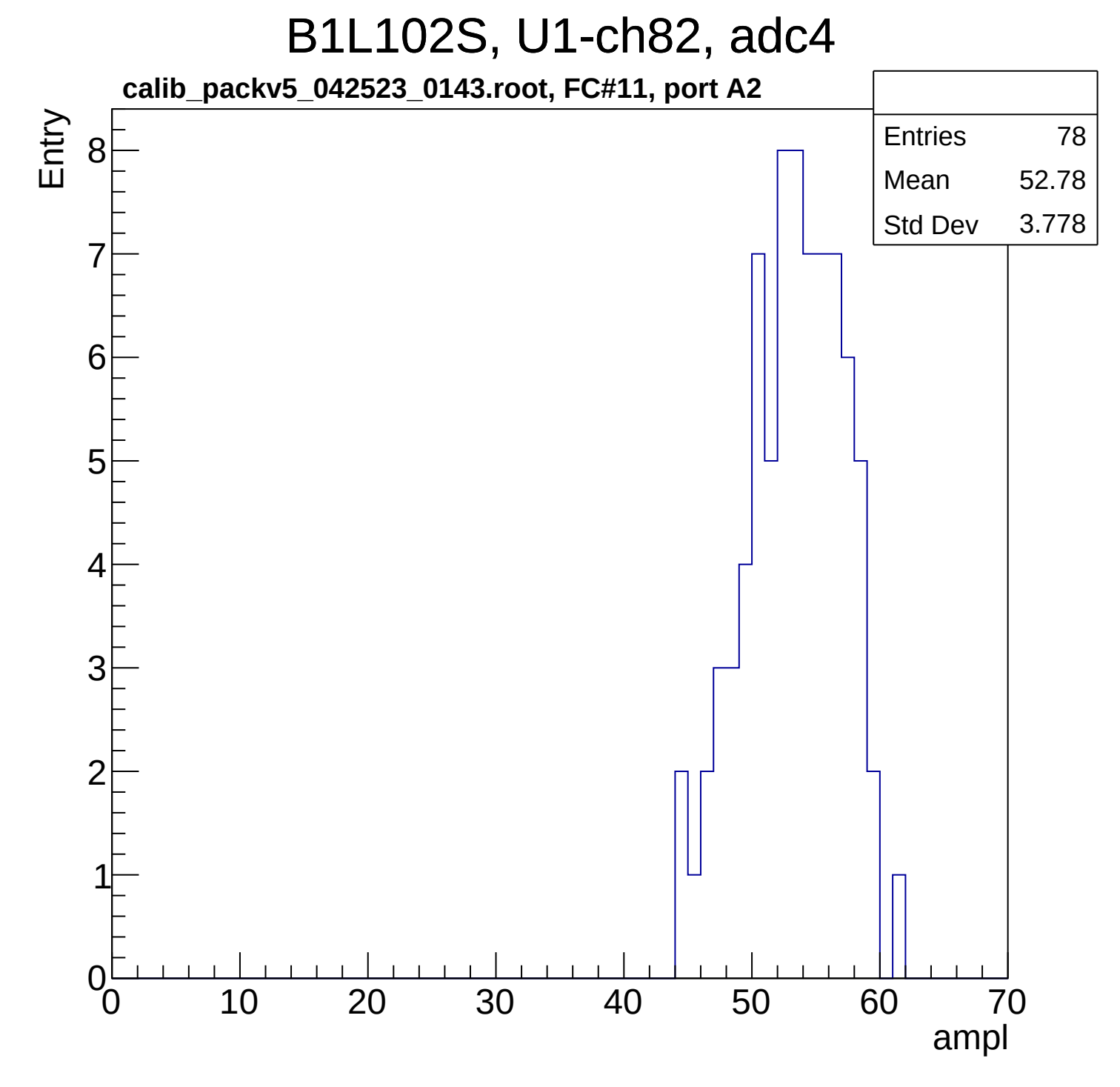
B1L102S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 52.78
Std Dev 3.778
Entry
ampl
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4
5
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10
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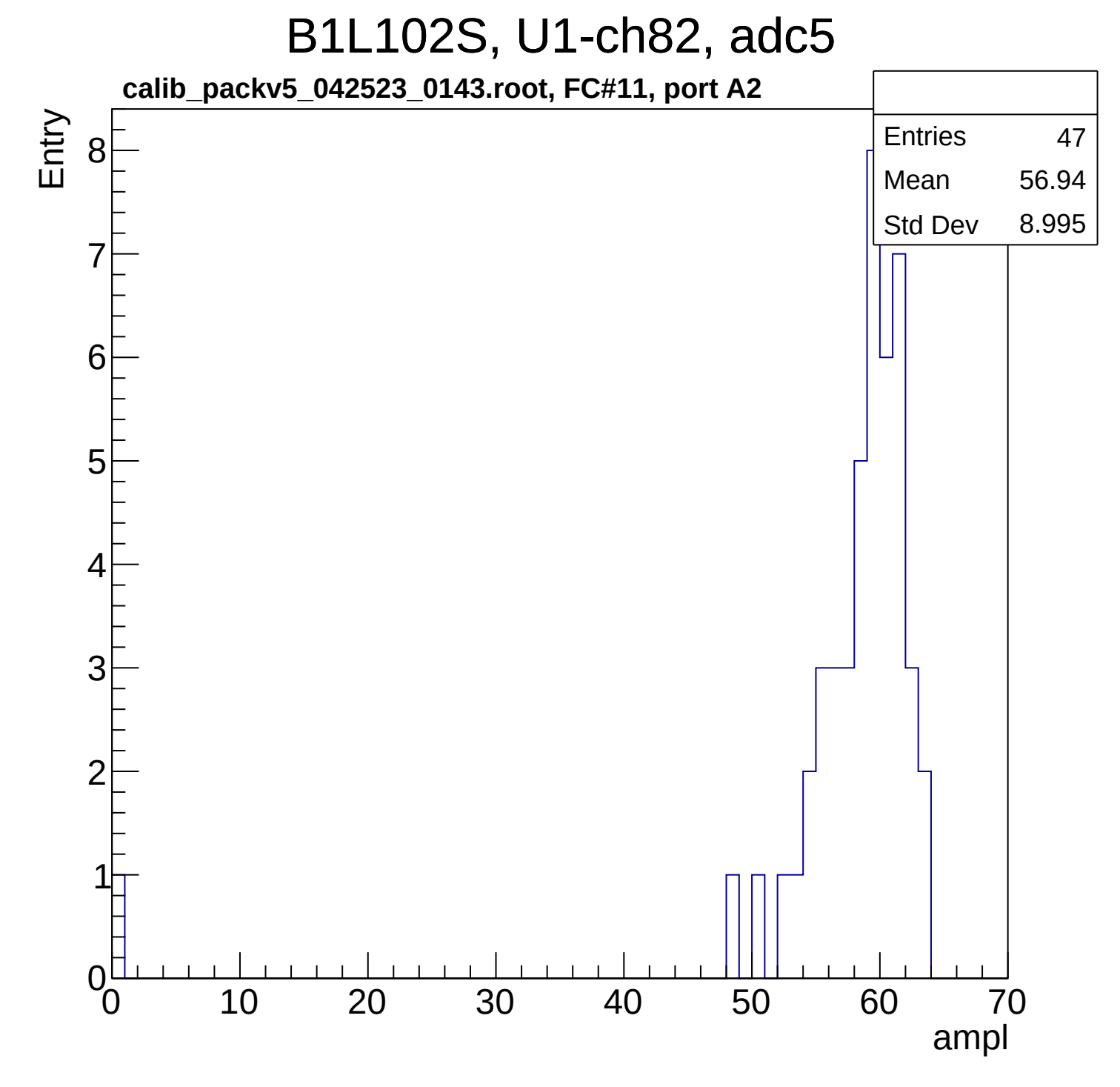
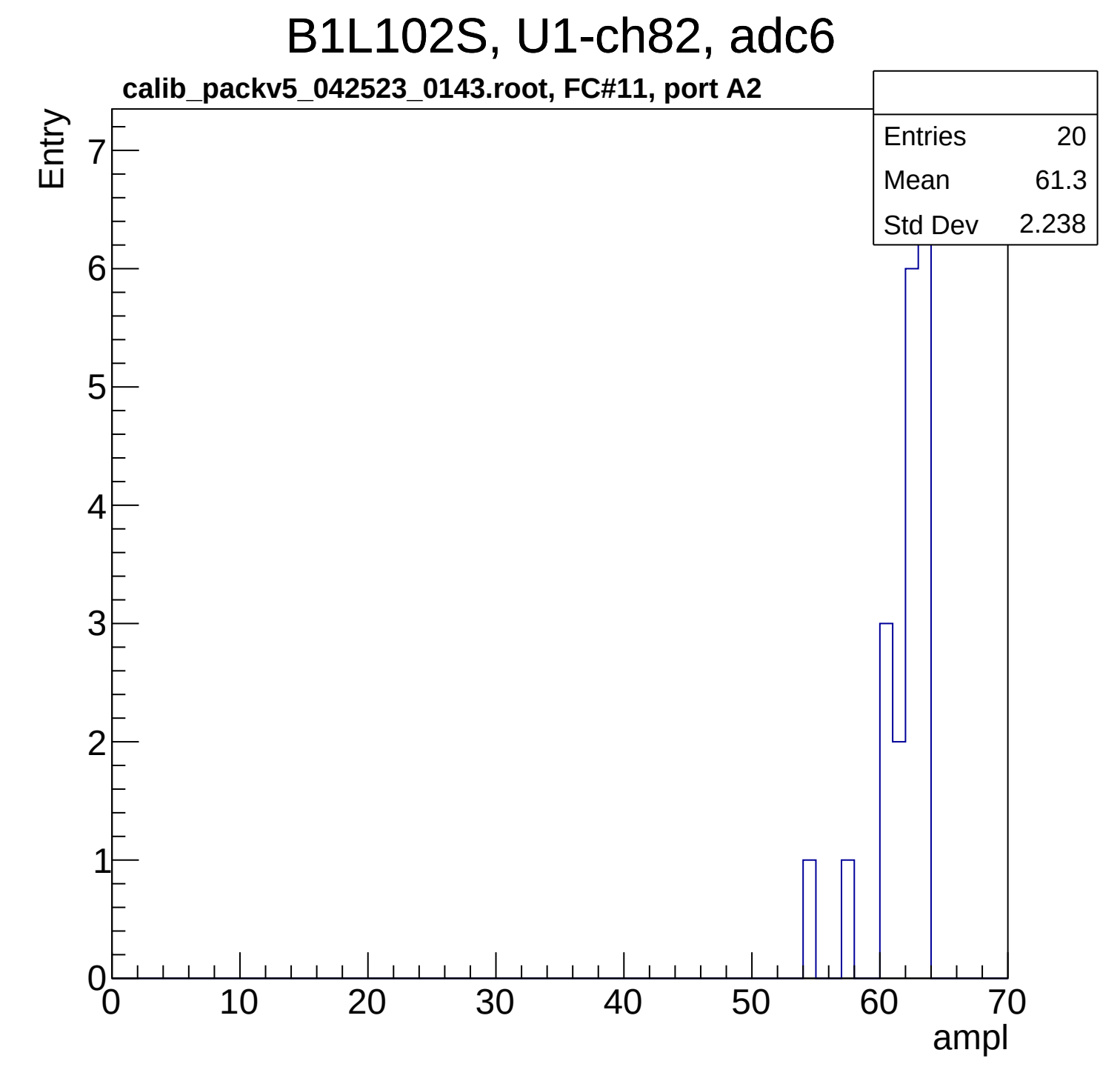
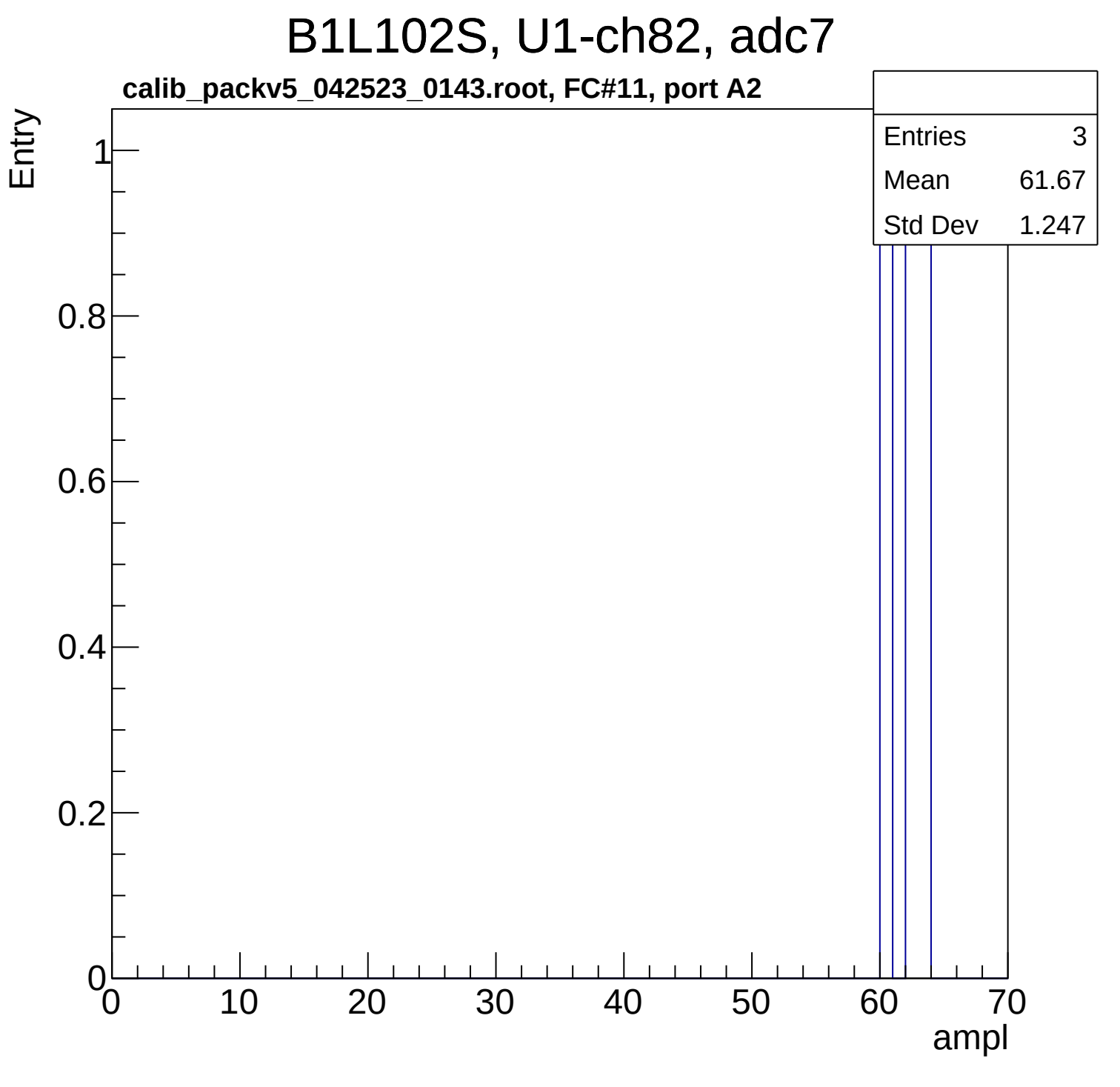

B1L102S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 56.94
Std Dev 8.995
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 20
Mean 61.3
Std Dev 2.238
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
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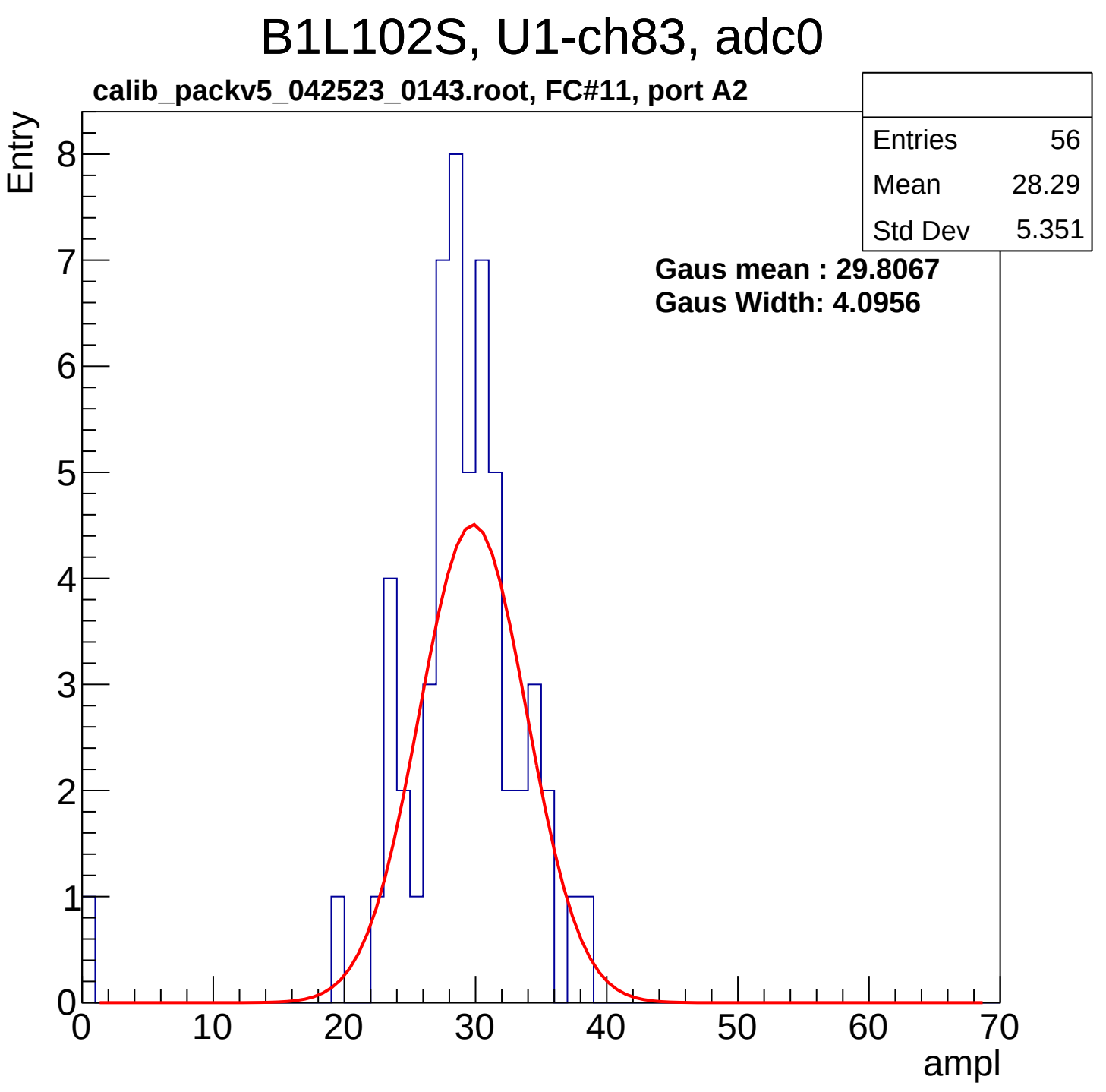
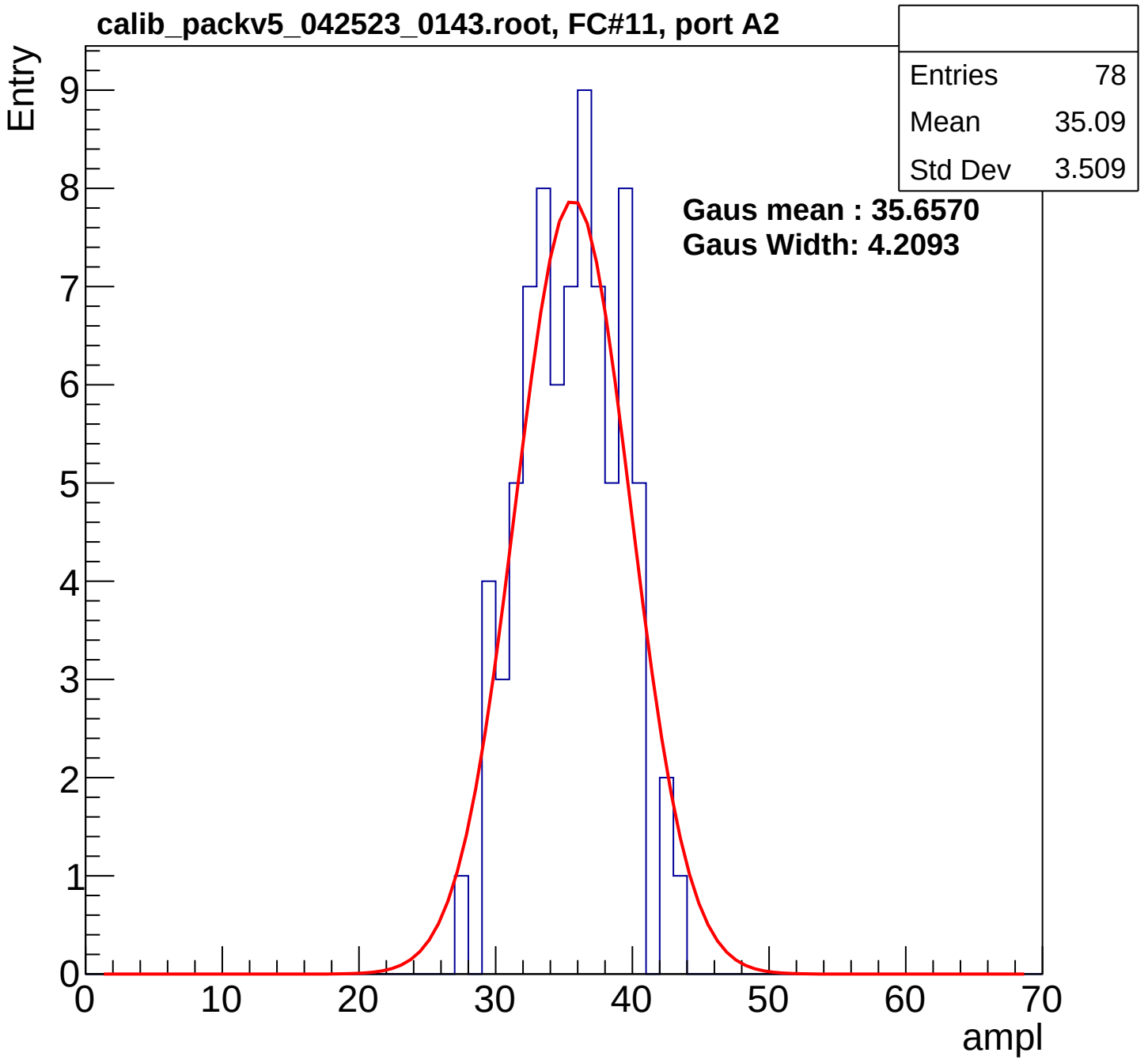
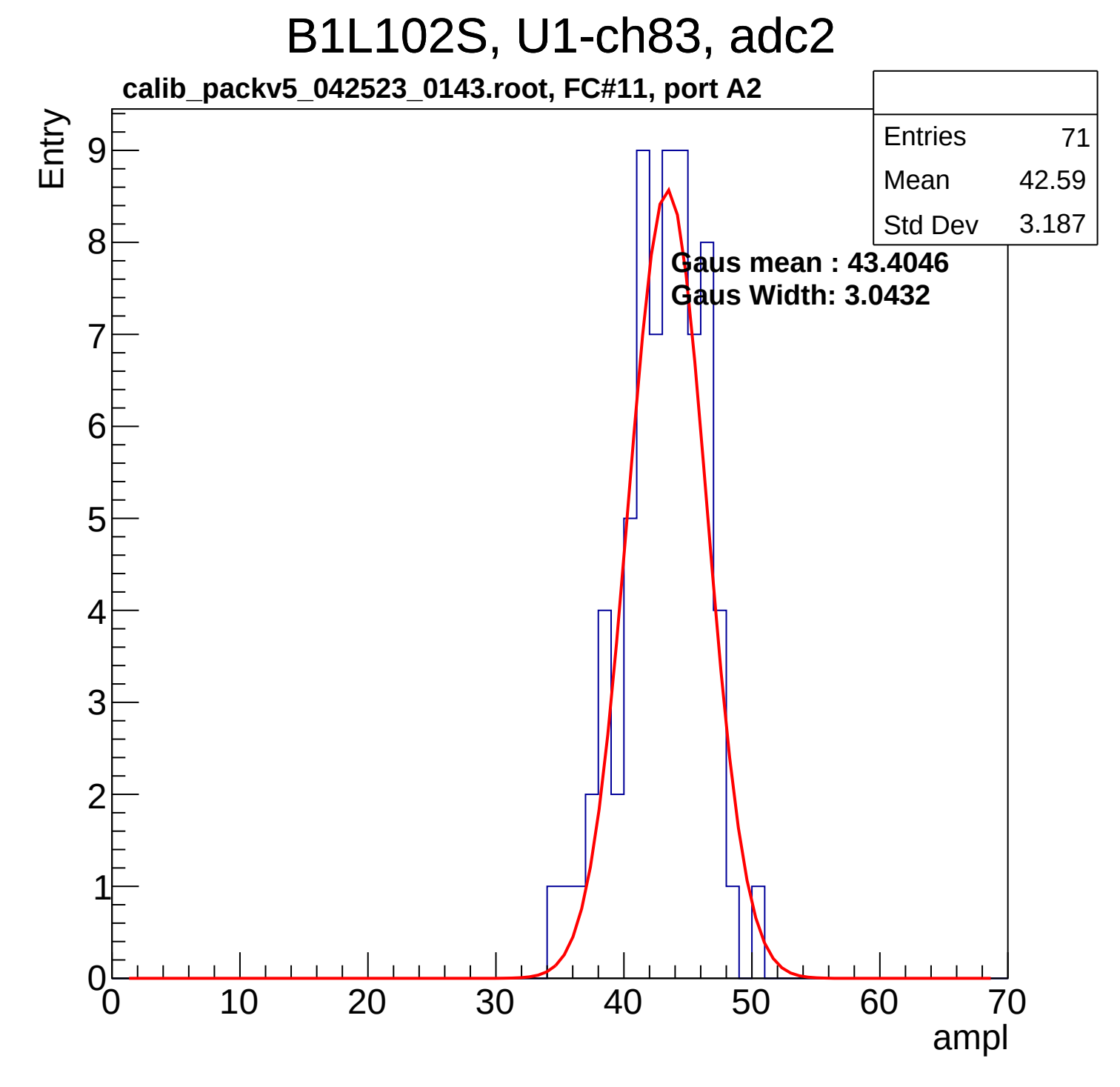
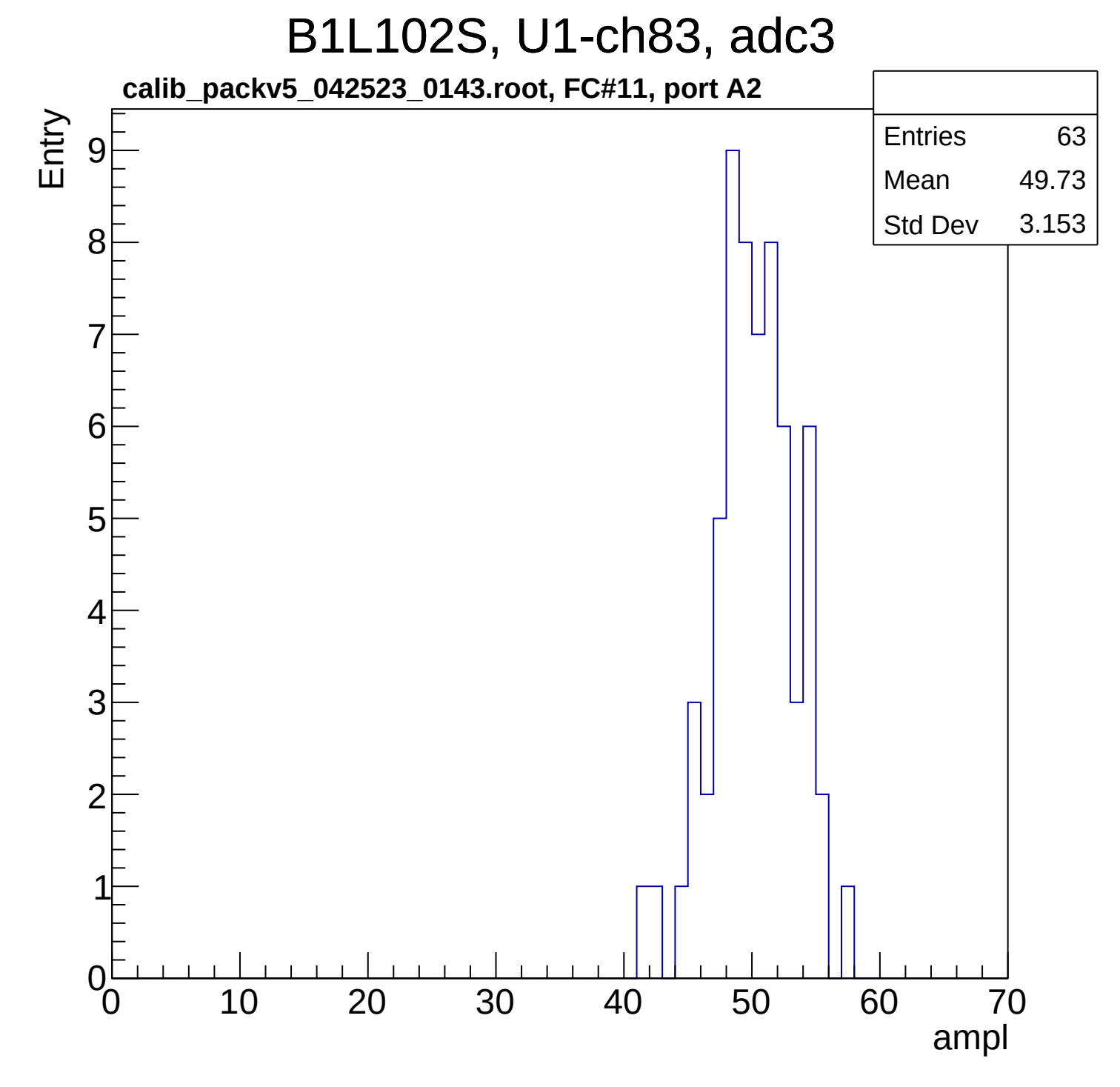
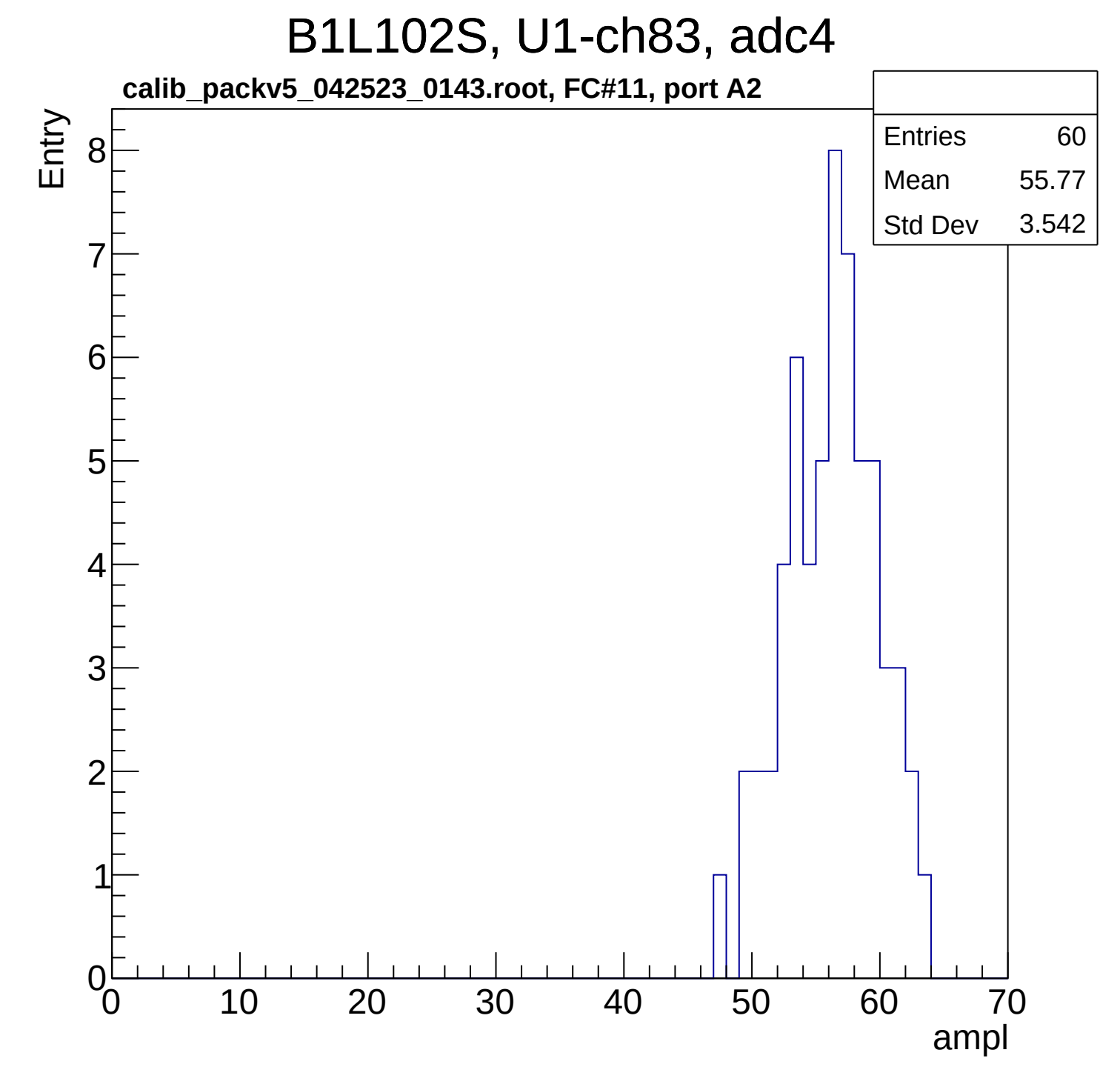
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

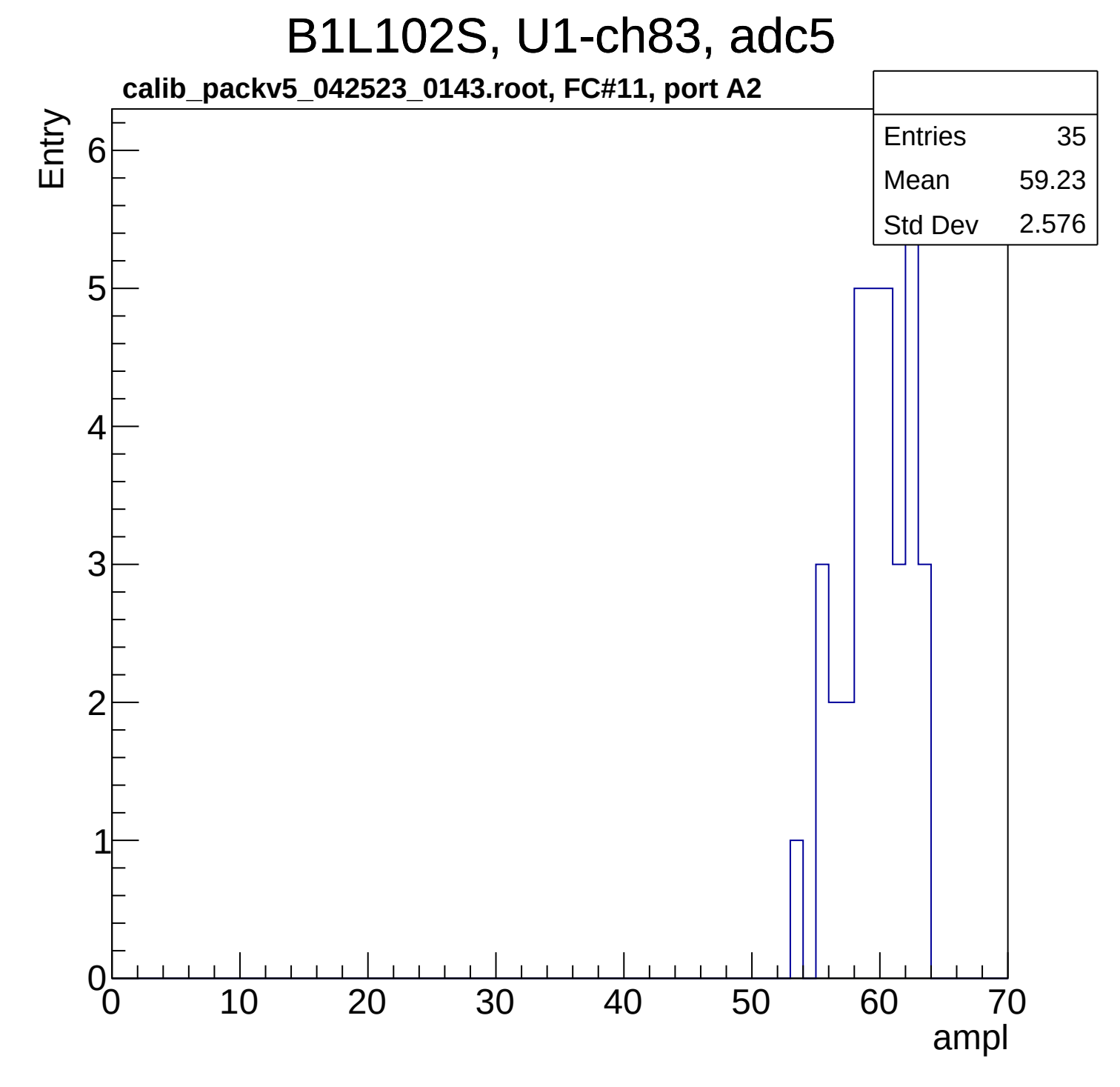

B1L102S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 28.29
Std Dev 5.351
Gaus mean : 29.8067
Gaus Width: 4.0956
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

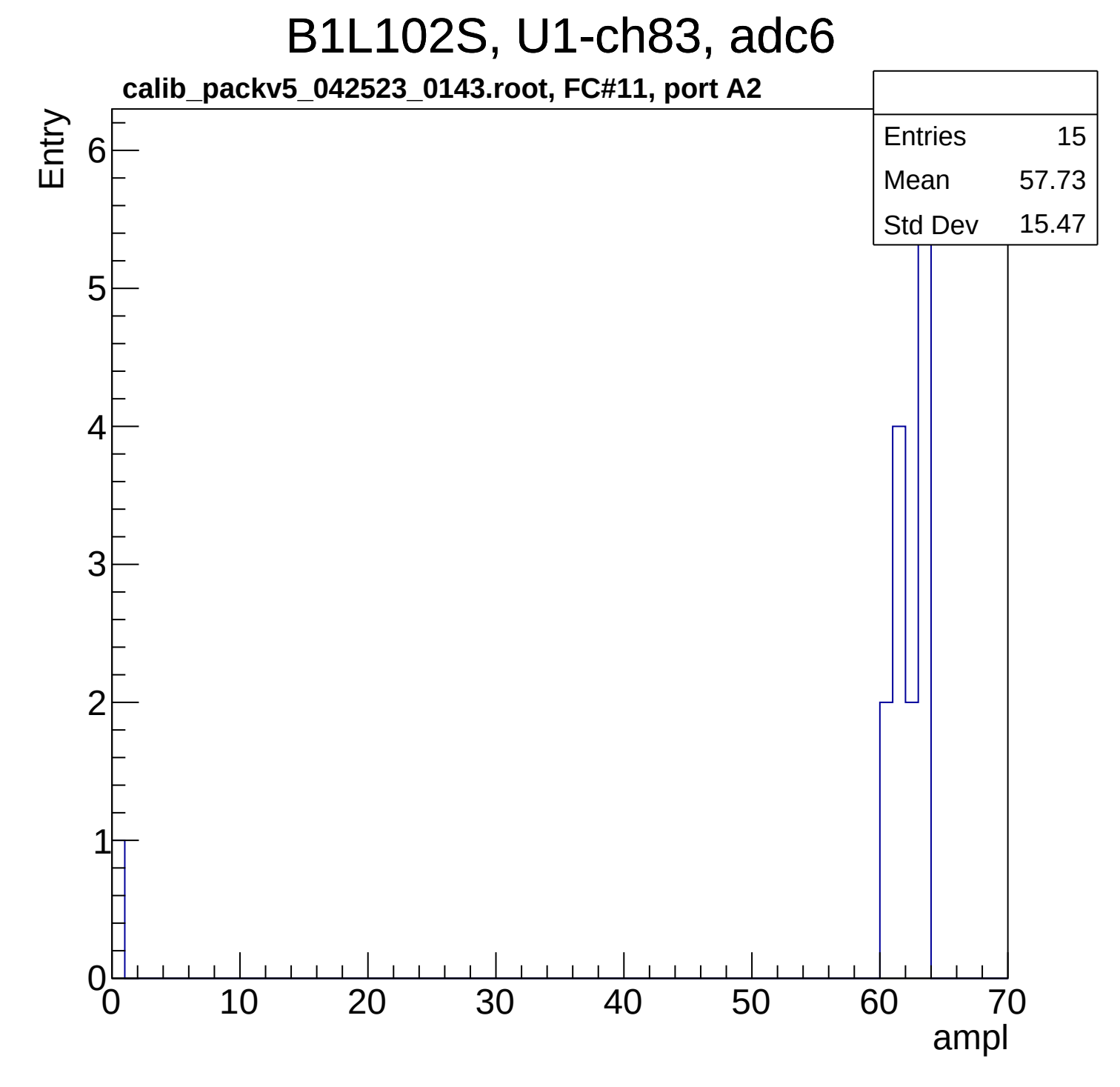

B1L102S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 35.09
Std Dev 3.509
Gaus mean : 35.6570
Gaus Width: 4.2093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

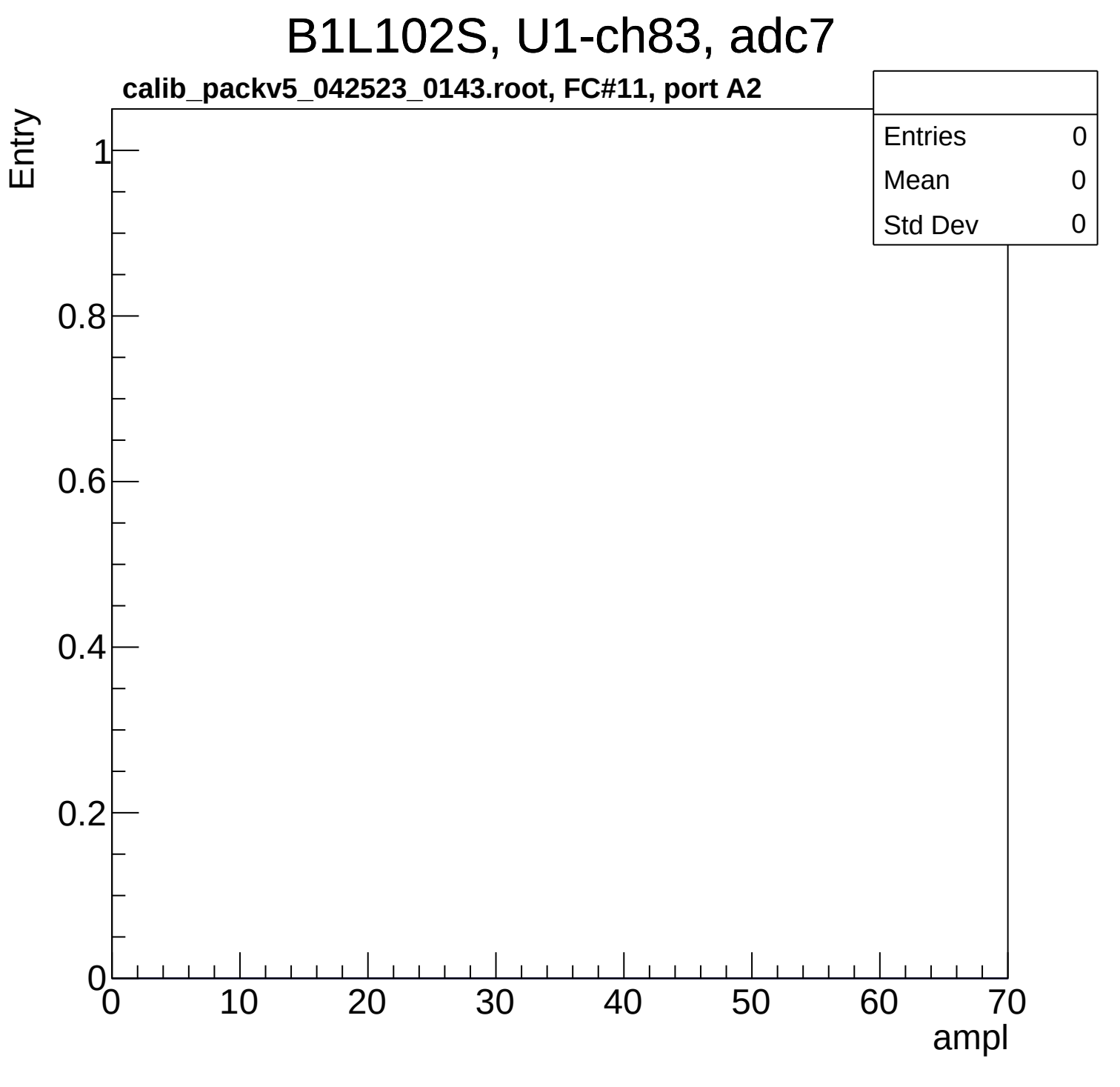

B1L102S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.59
Std Dev 3.187
Gaus mean : 43.4046
Gaus Width: 3.0432
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

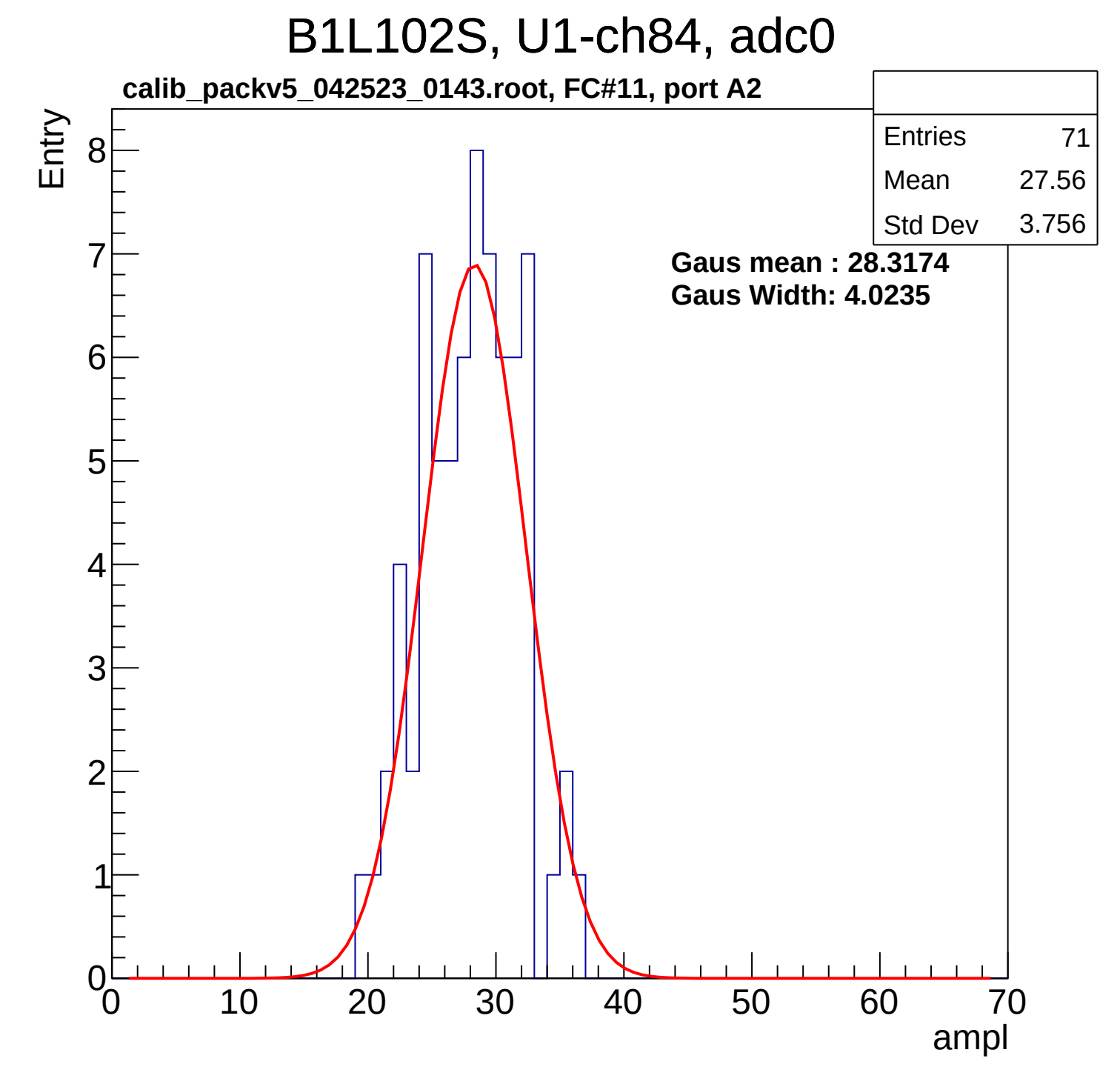

B1L102S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 49.73
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

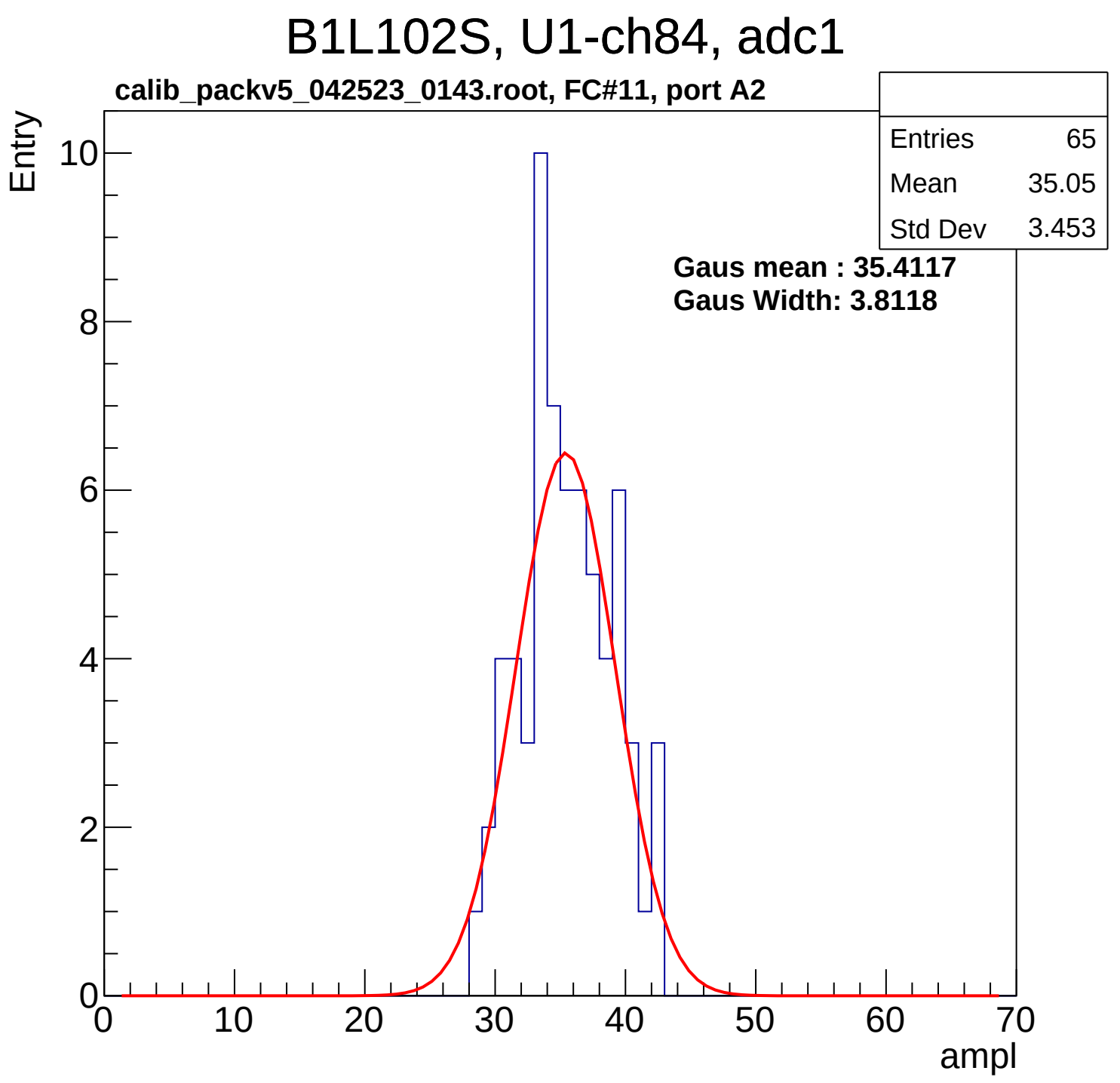

B1L102S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 55.77
Std Dev 3.542
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

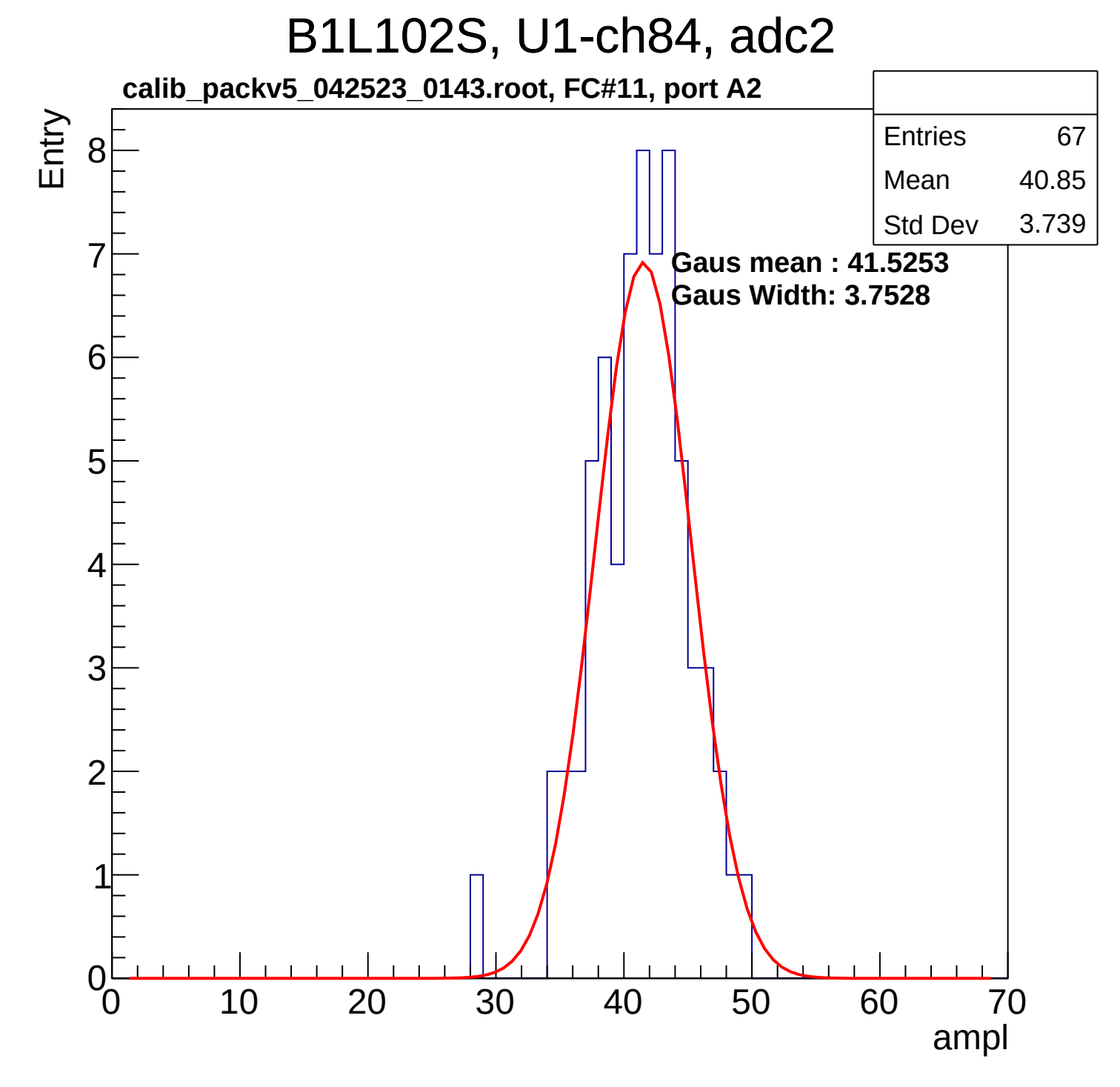

B1L102S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 59.23
Std Dev 2.576
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

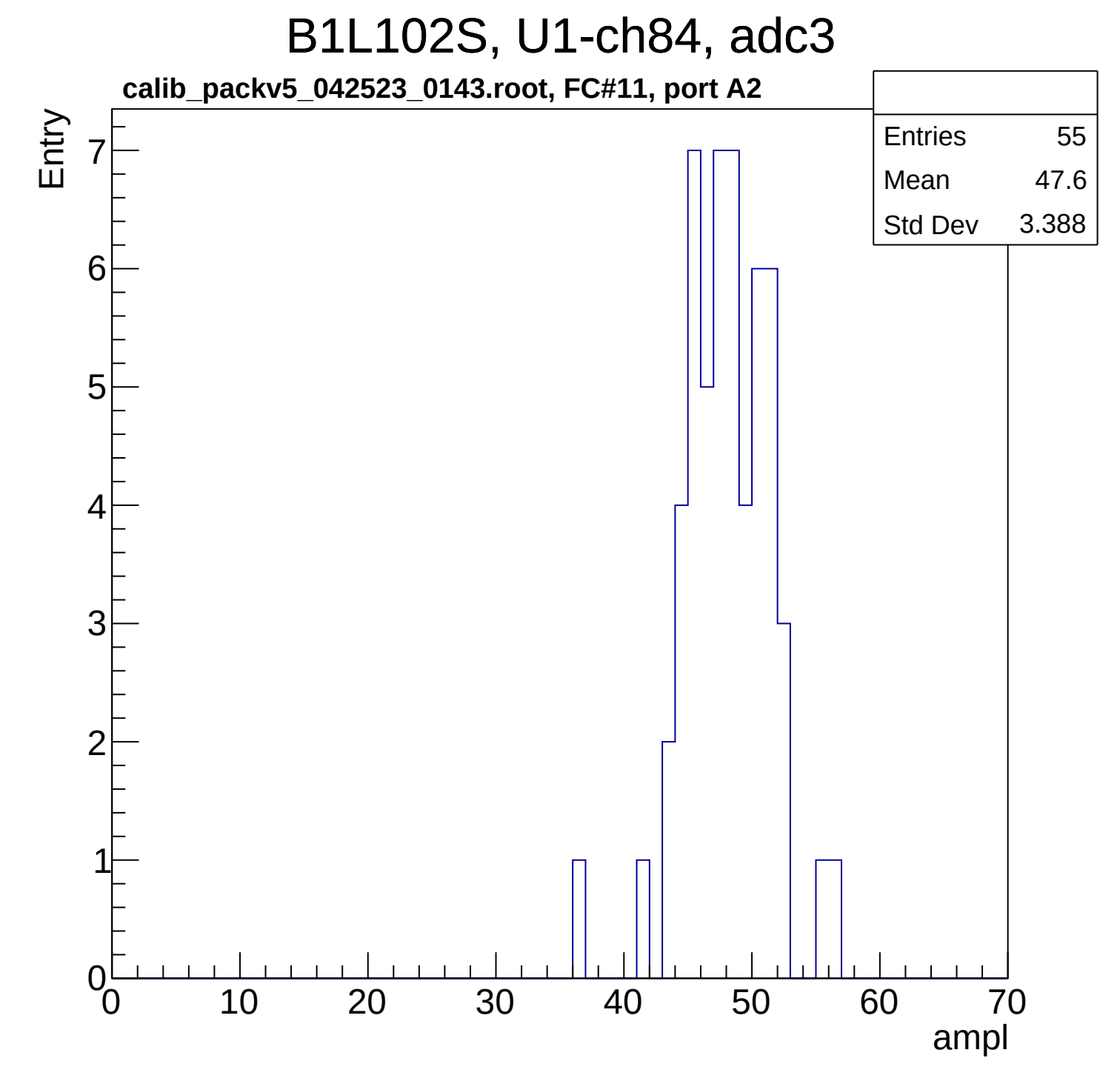

B1L102S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 57.73
Std Dev 15.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

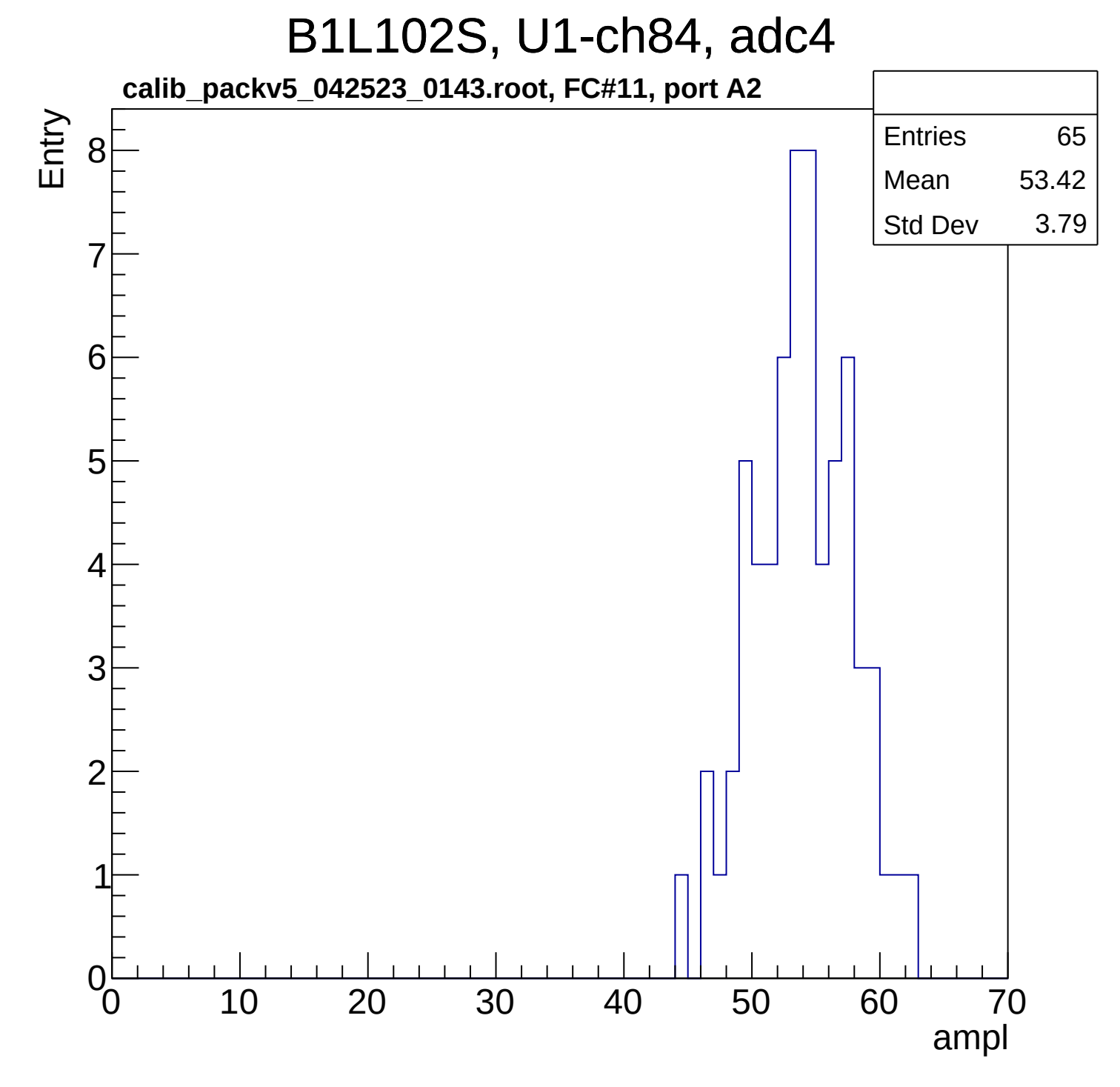

B1L102S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

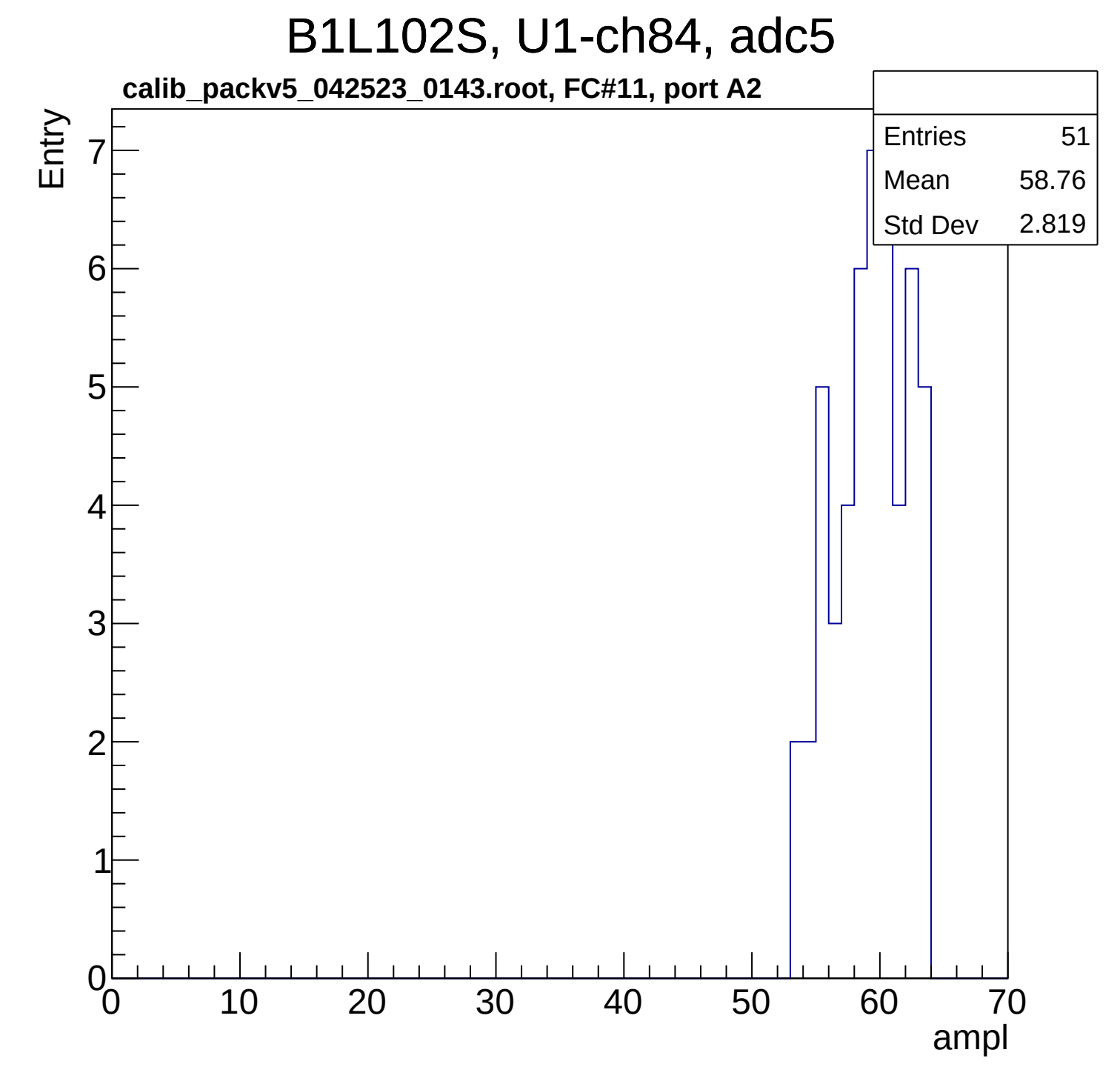

B1L102S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 27.56
Std Dev 3.756
Gaus mean : 28.3174
Gaus Width: 4.0235
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

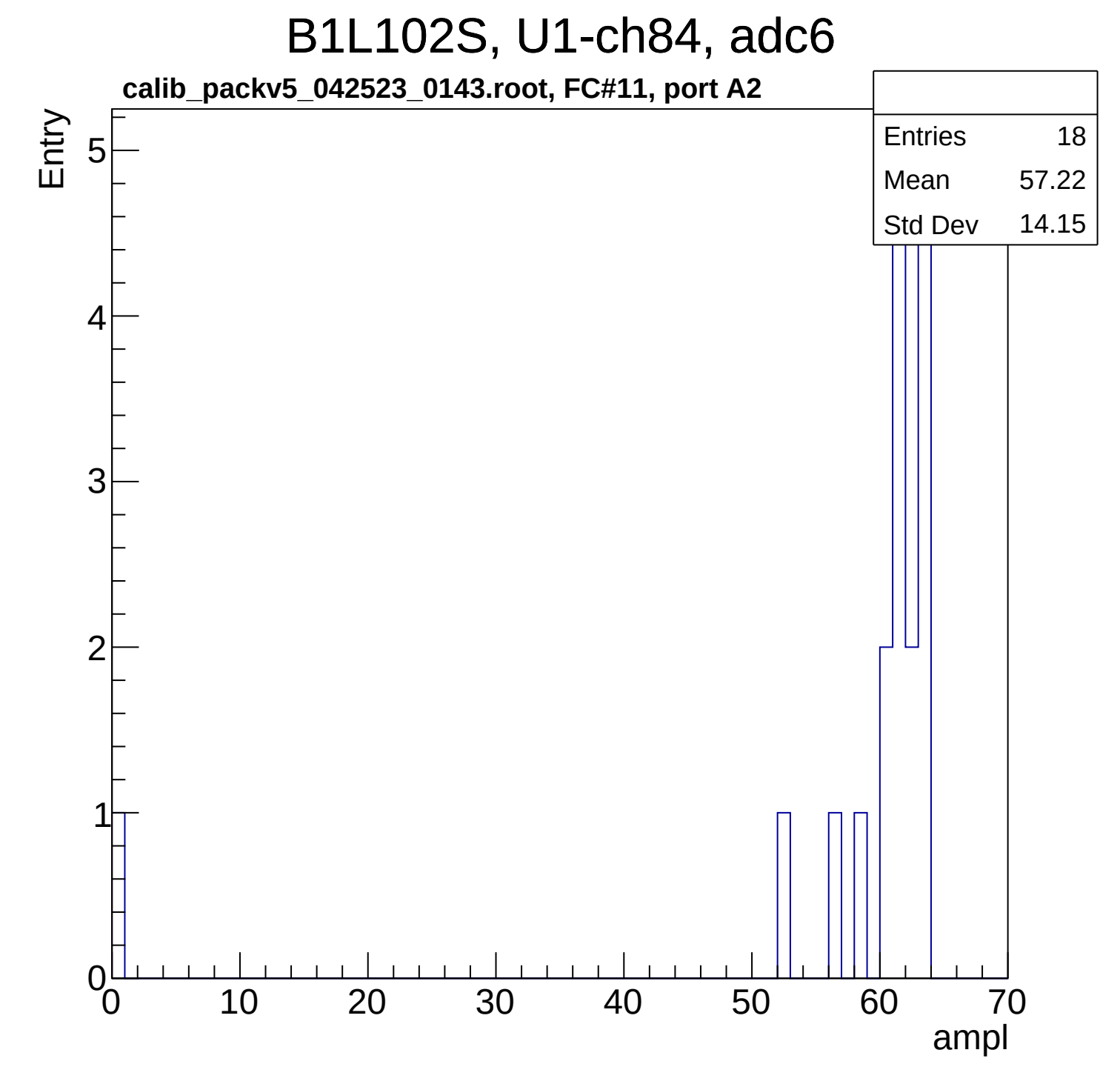

B1L102S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.05
Std Dev 3.453
Gaus mean : 35.4117
Gaus Width: 3.8118
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

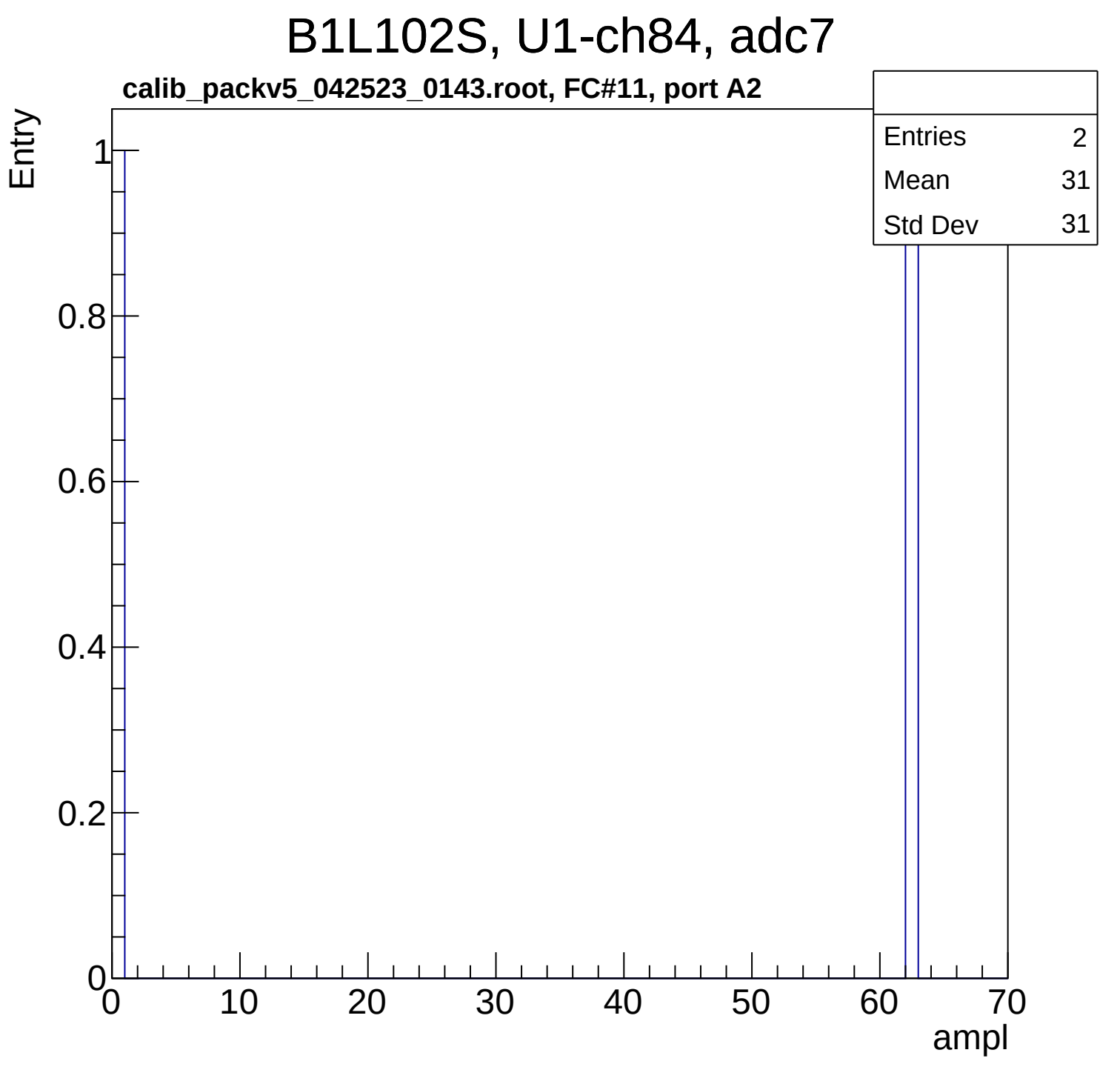

B1L102S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 40.85
Std Dev 3.739
Gaus mean : 41.5253
Gaus Width: 3.7528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 47.6
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 53.42
Std Dev 3.79
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58.76
Std Dev 2.819
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 18
Mean 57.22
Std Dev 14.15
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
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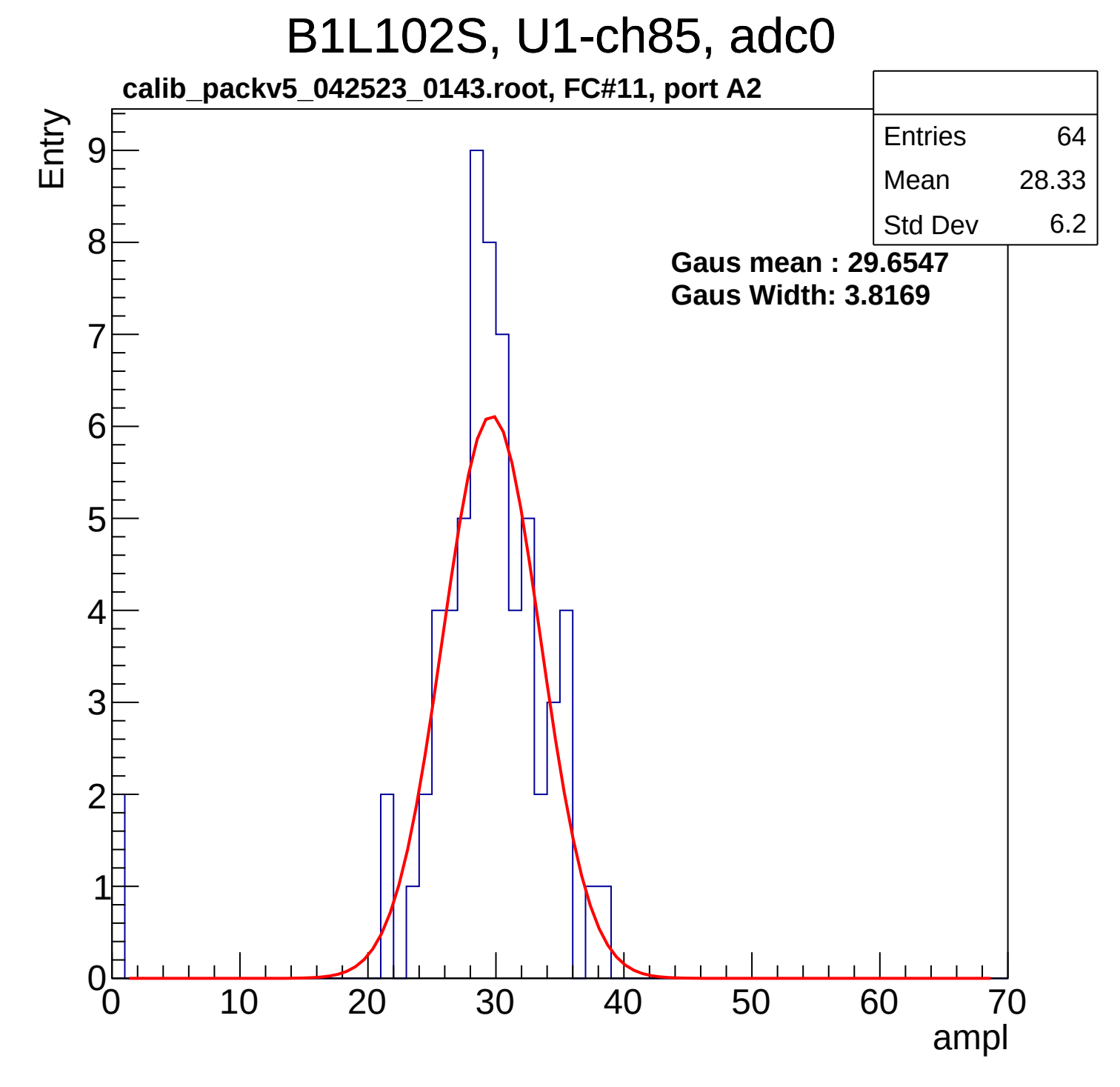
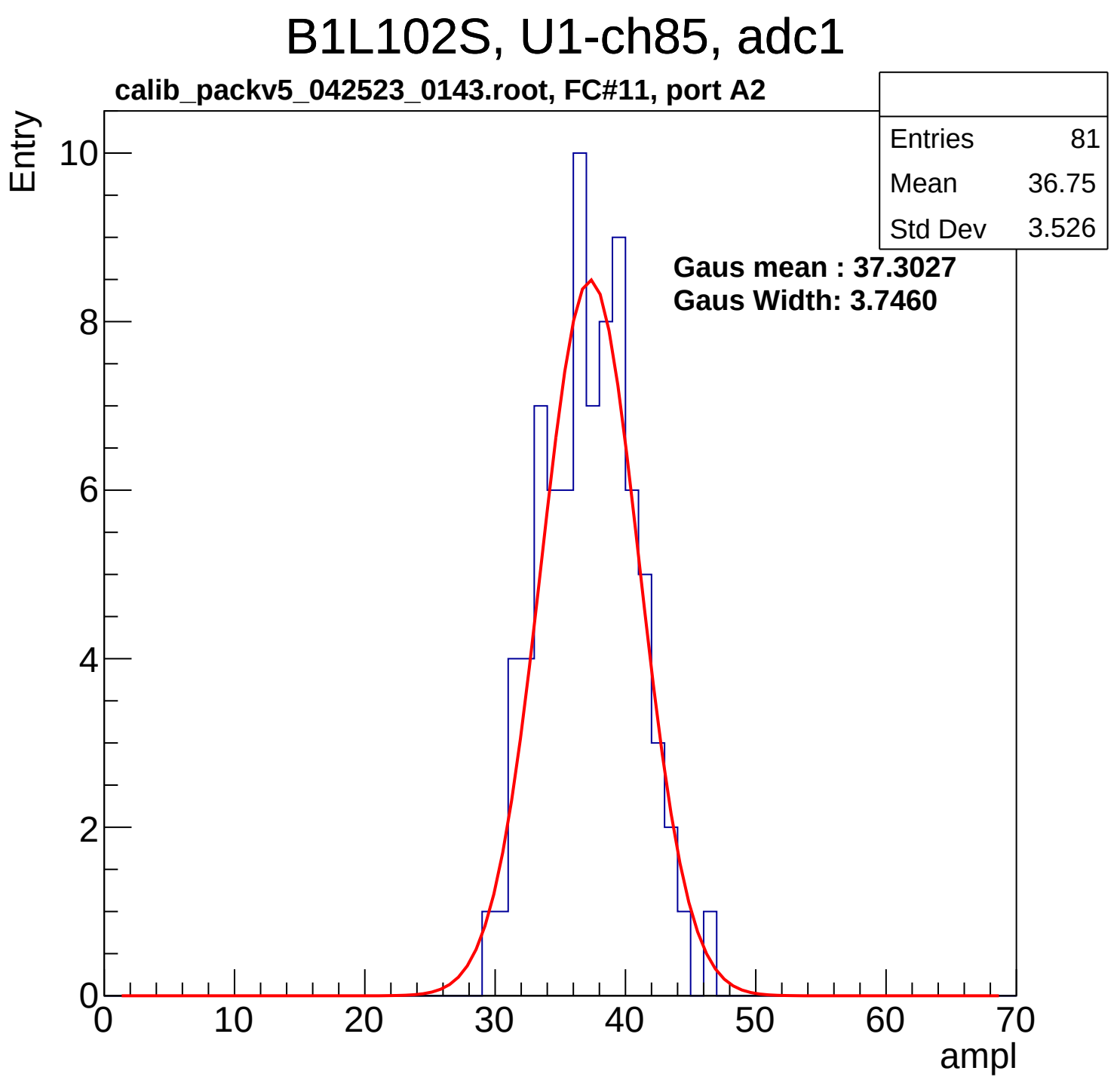
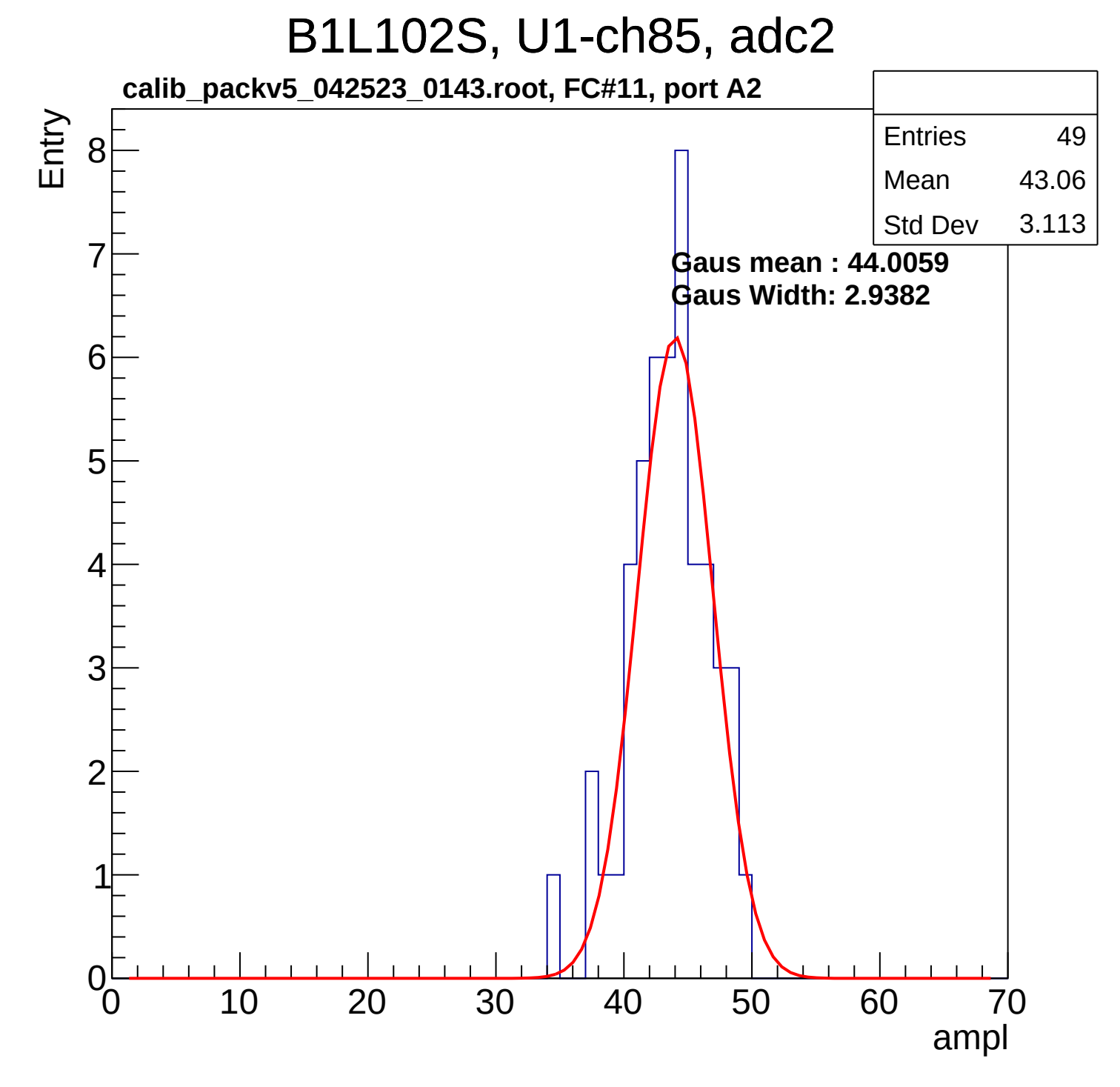
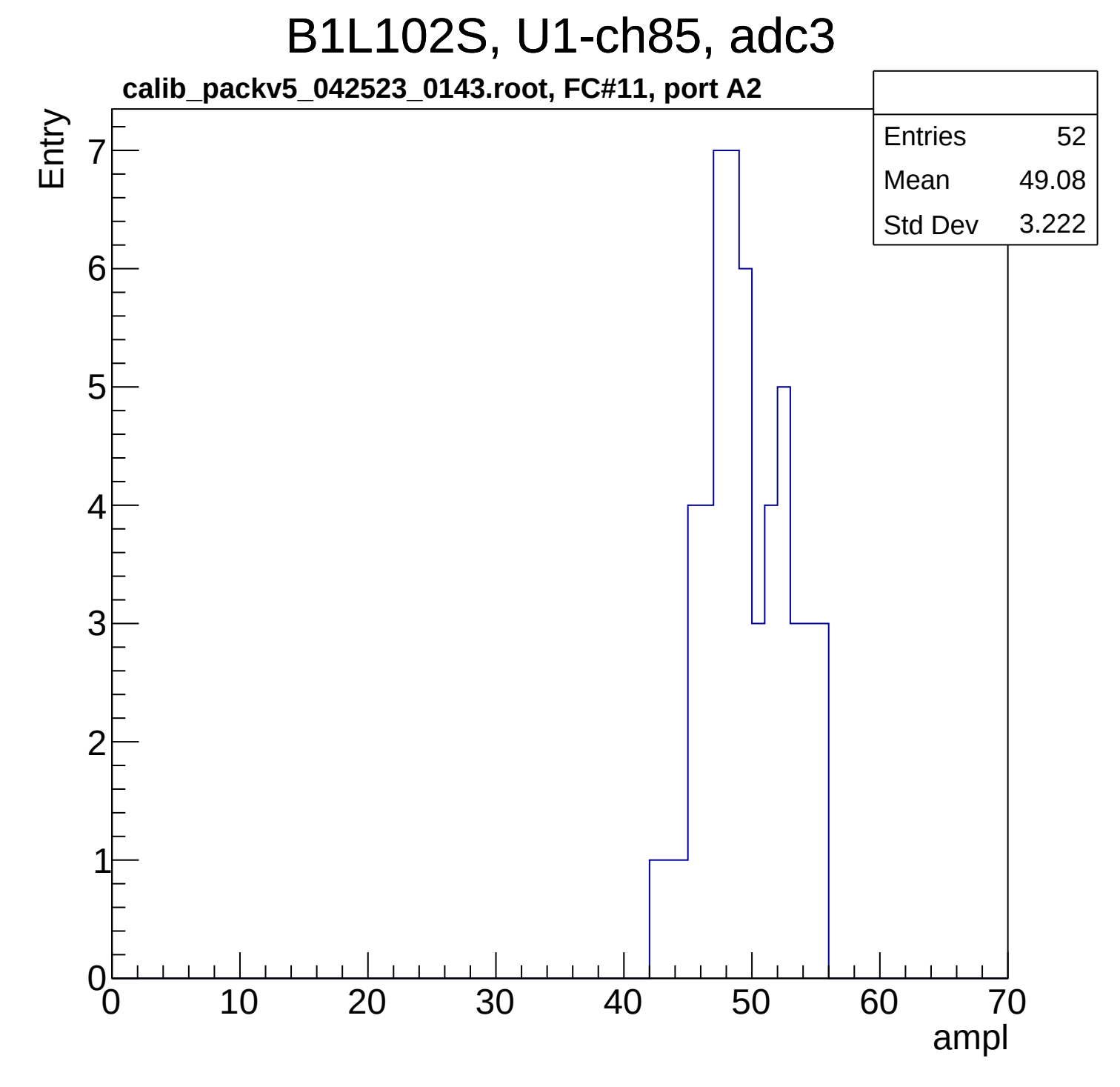
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 28.33
Std Dev 6.2
Gaus mean : 29.6547
Gaus Width: 3.8169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 81
Mean 36.75
Std Dev 3.526
Gaus mean : 37.3027
Gaus Width: 3.7460
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 43.06
Std Dev 3.113
Gaus mean : 44.0059
Gaus Width: 2.9382
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 49.08
Std Dev 3.222
Entry
ampl
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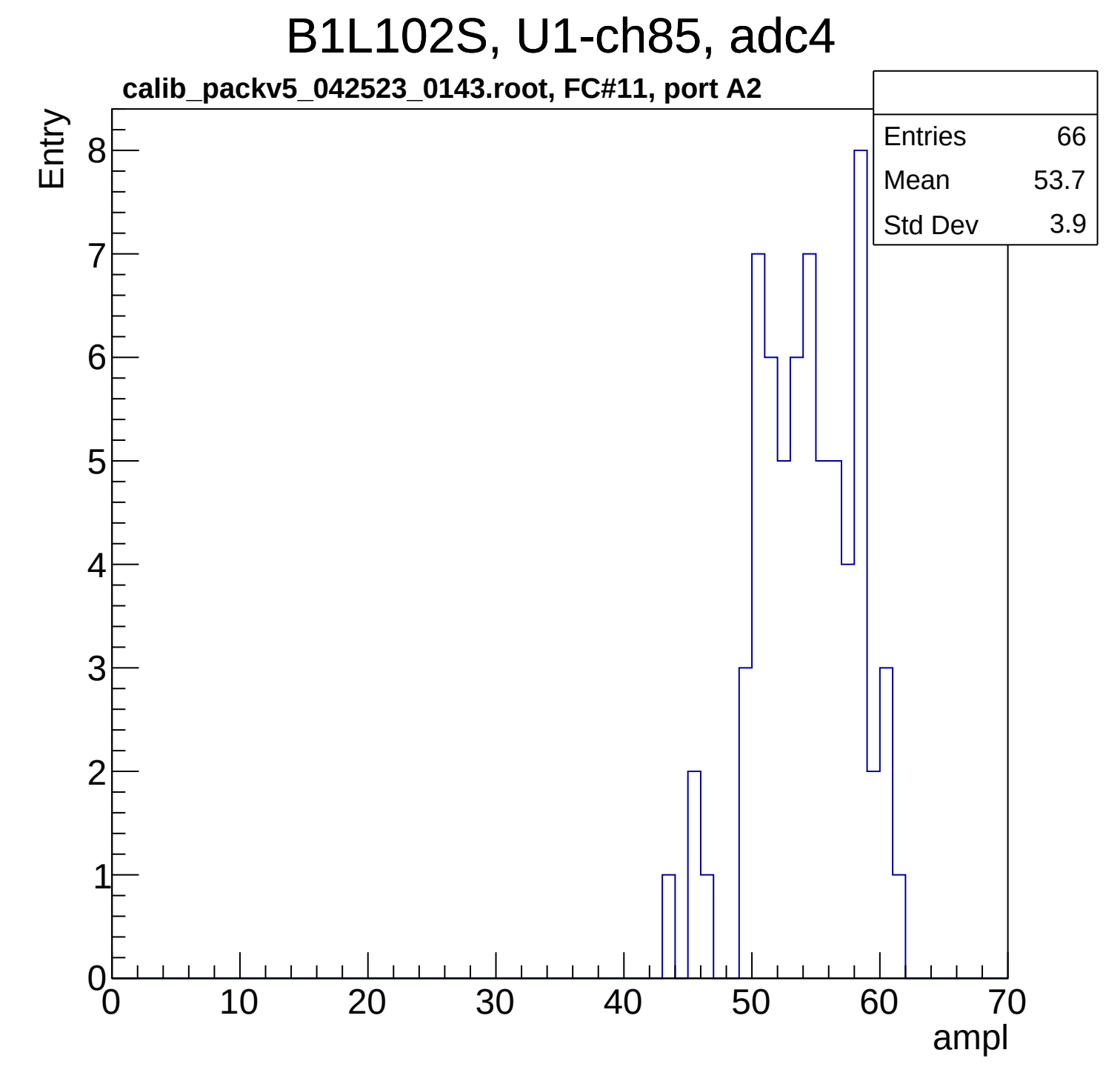
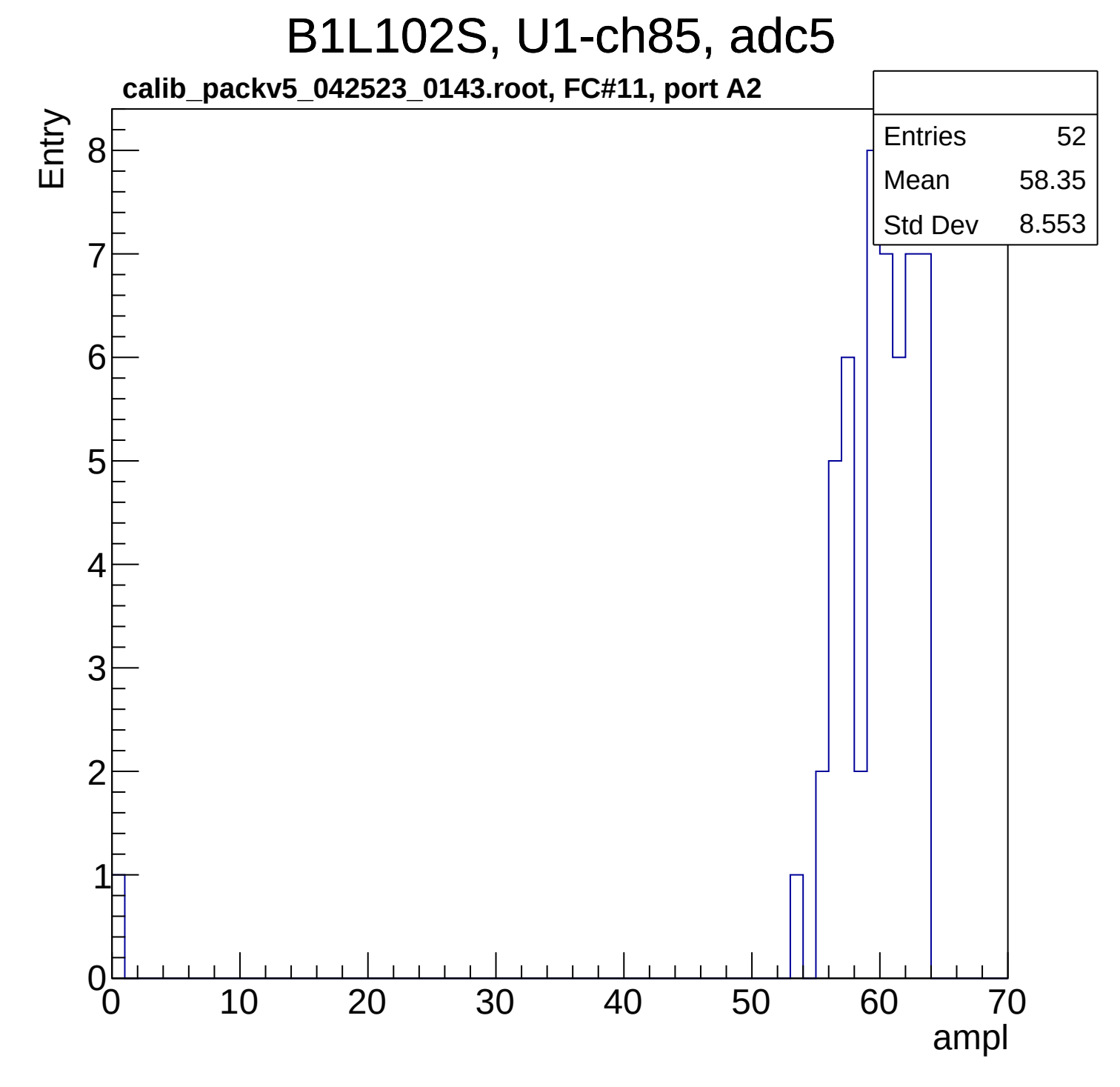
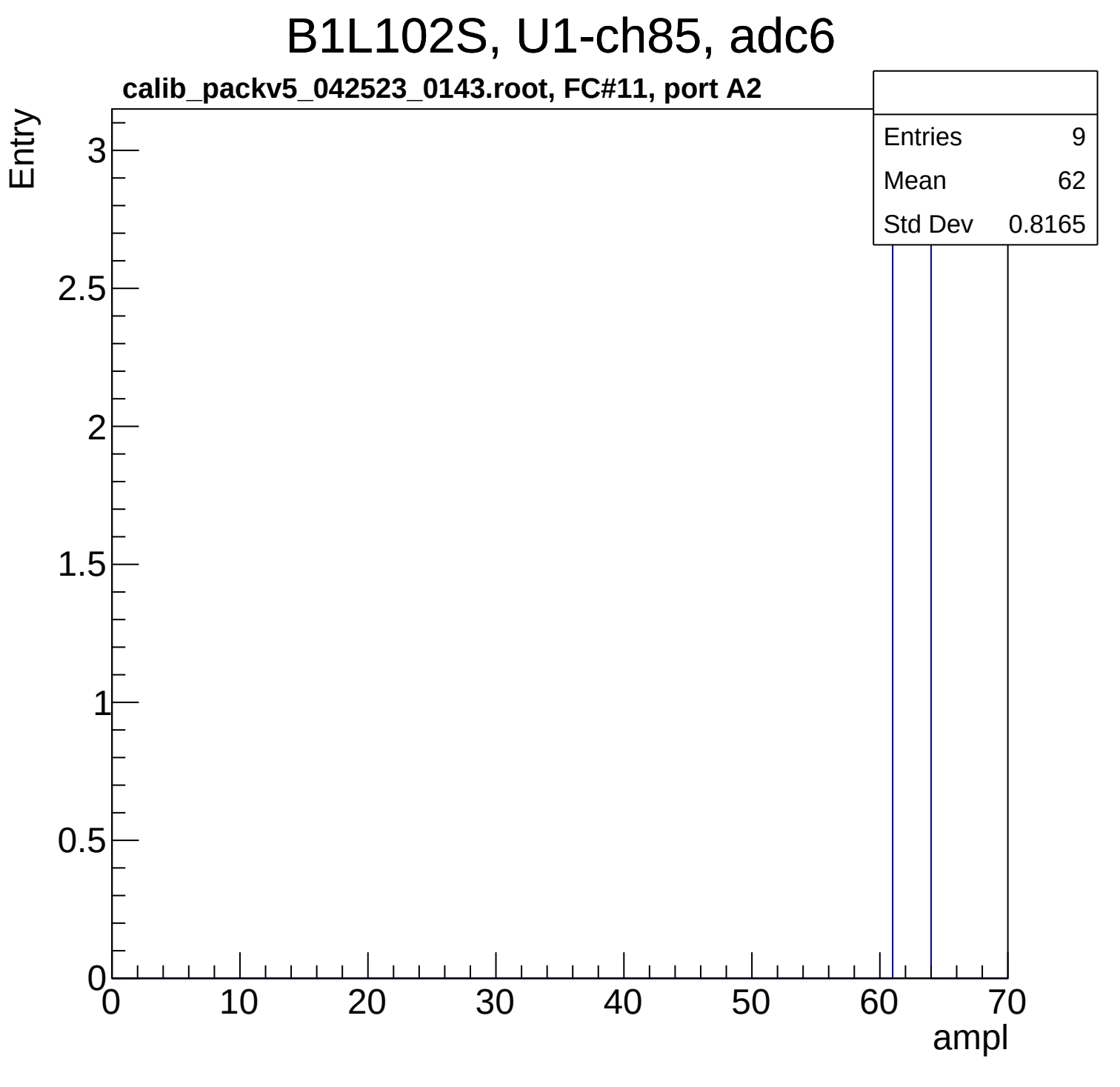
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 53.7
Std Dev 3.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 58.35
Std Dev 8.553
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62
Std Dev 0.8165
Entry
ampl
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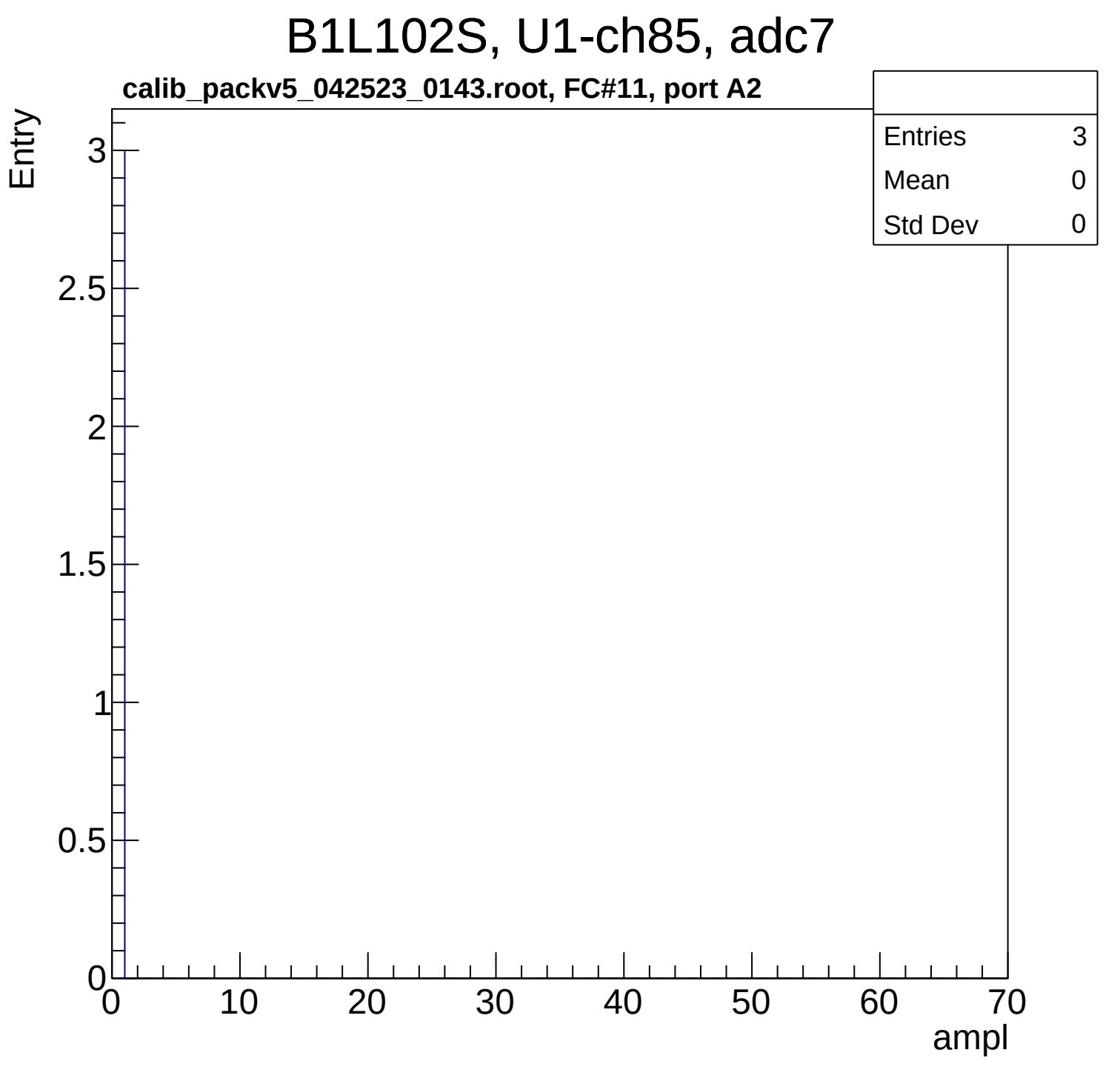
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 0
Std Dev 0
Entry
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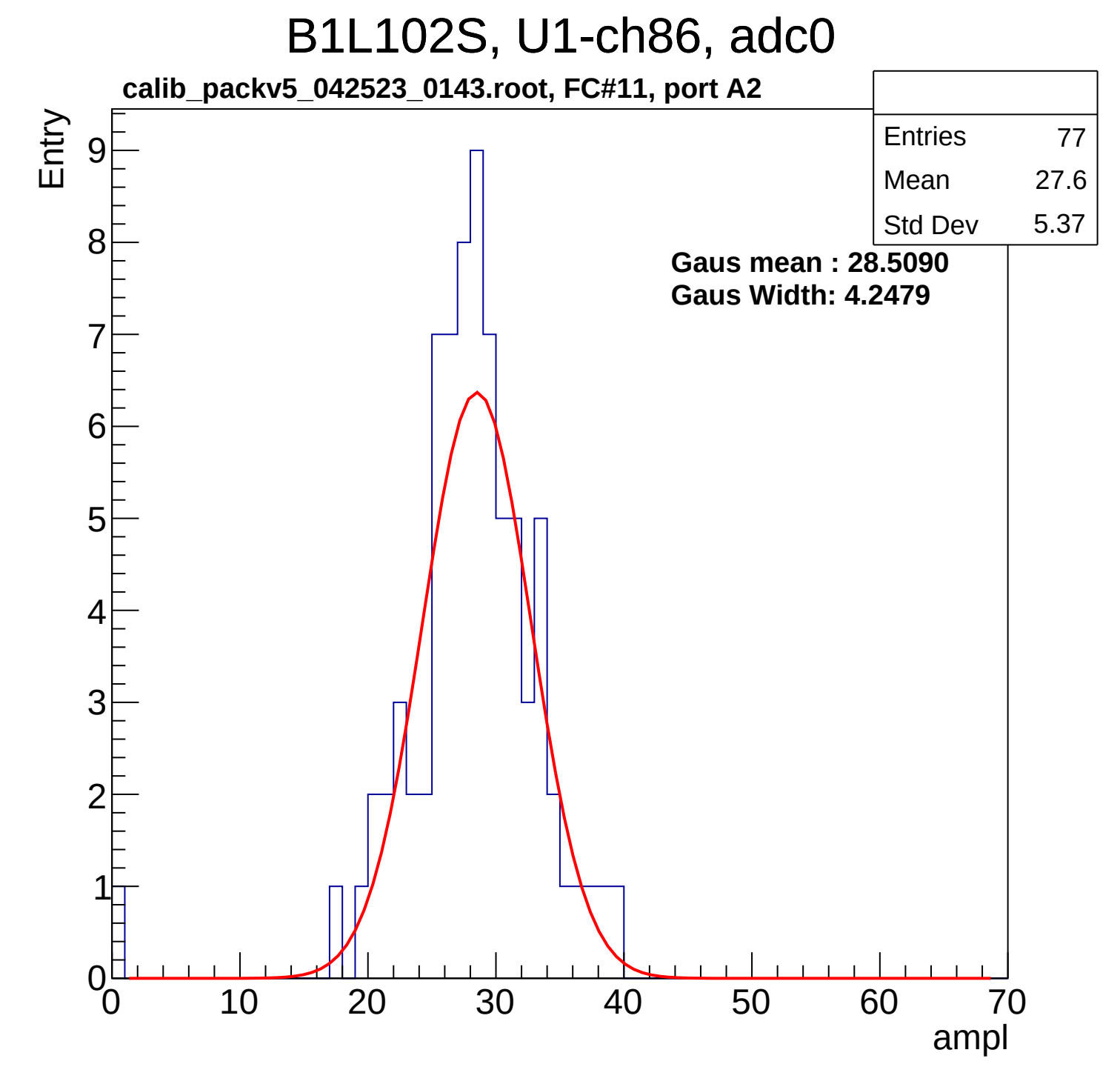
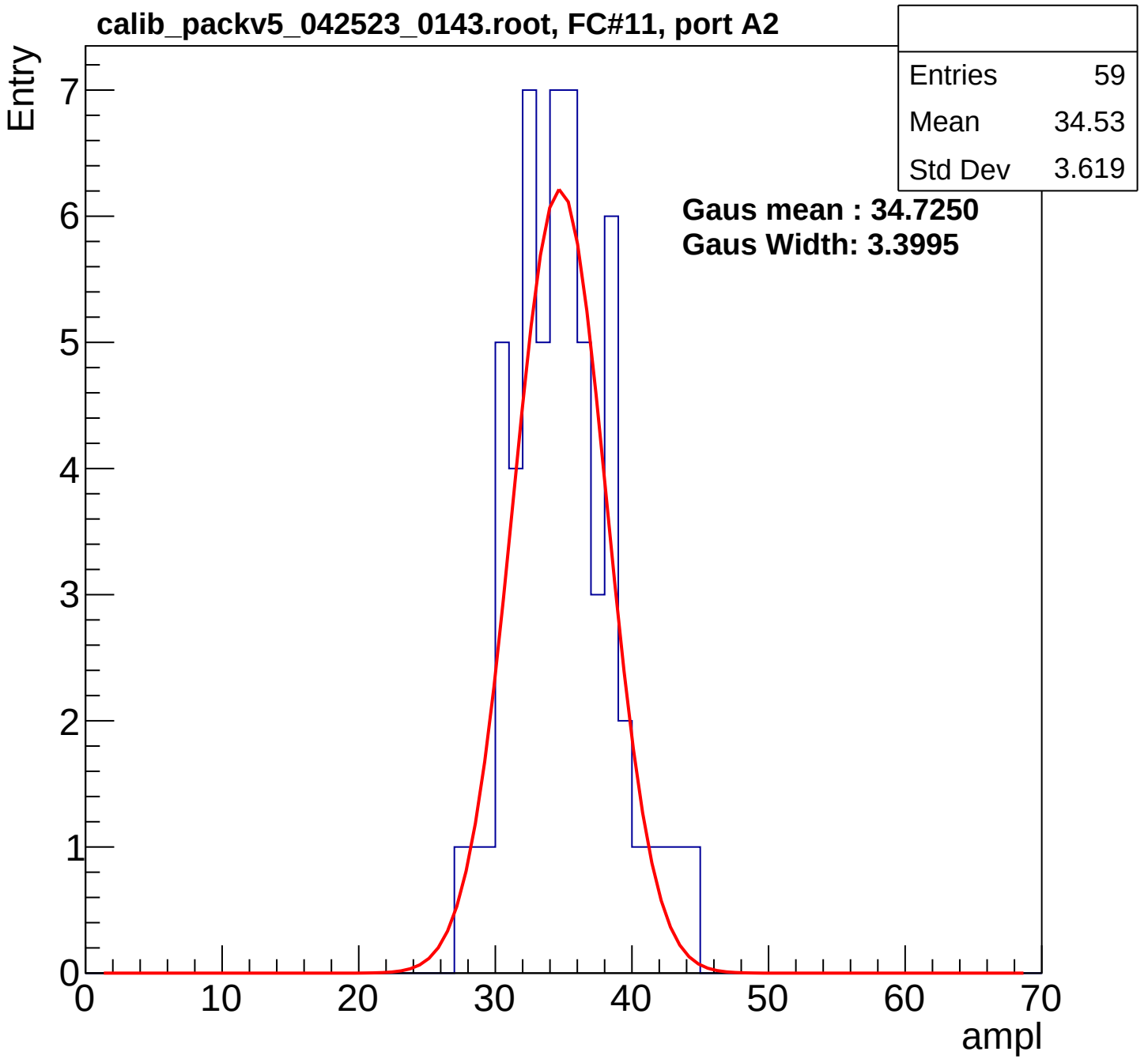
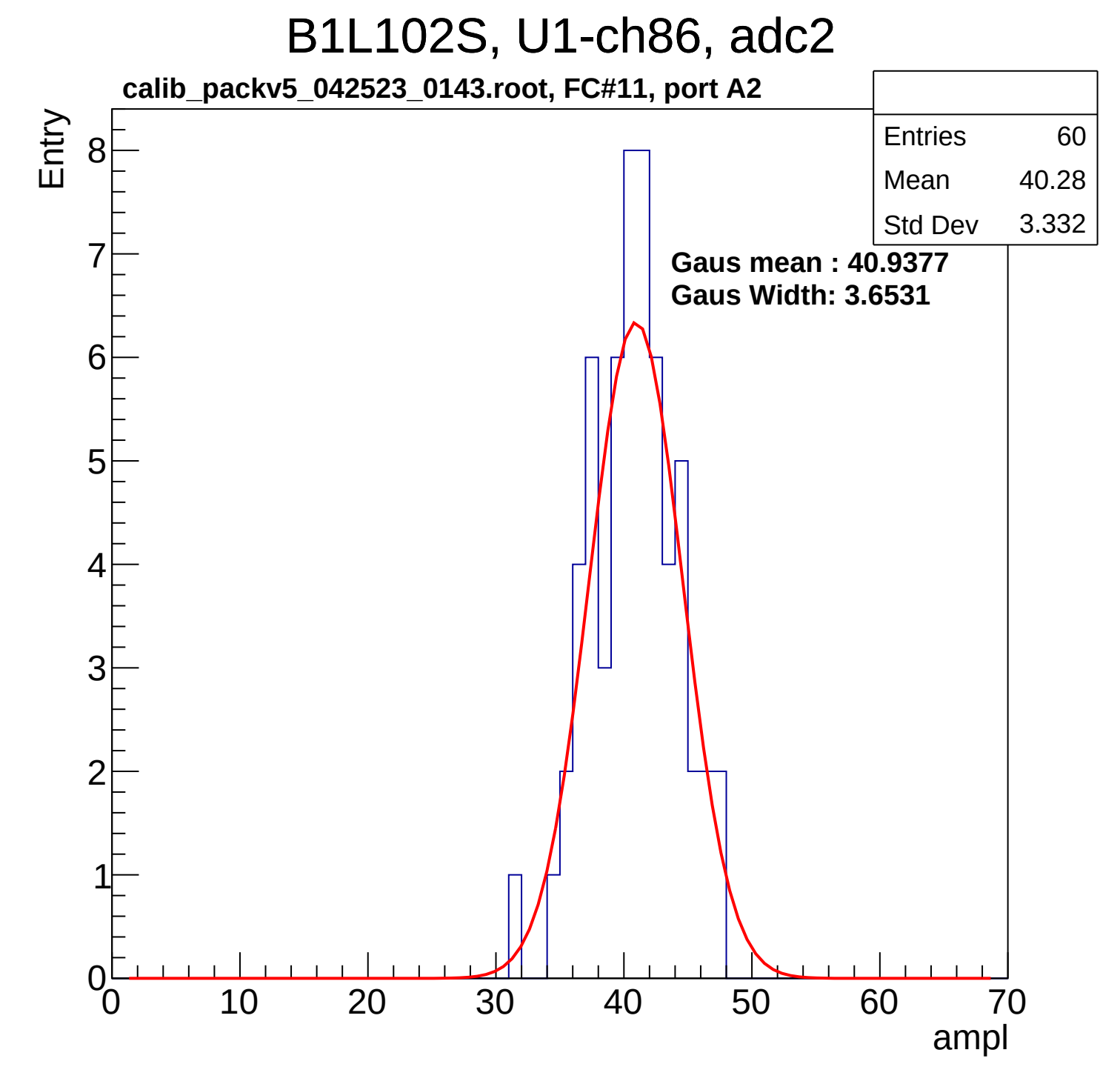
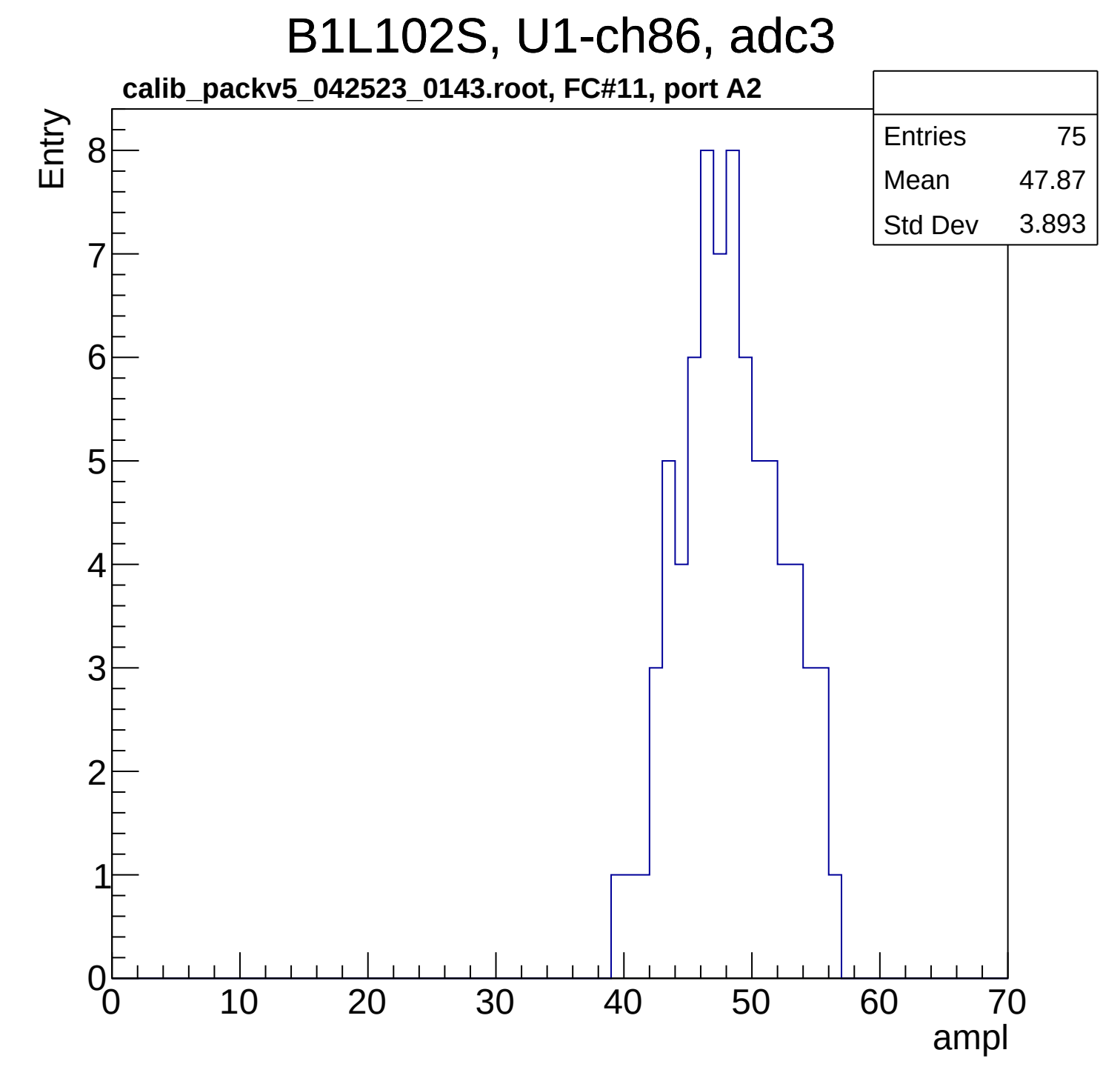
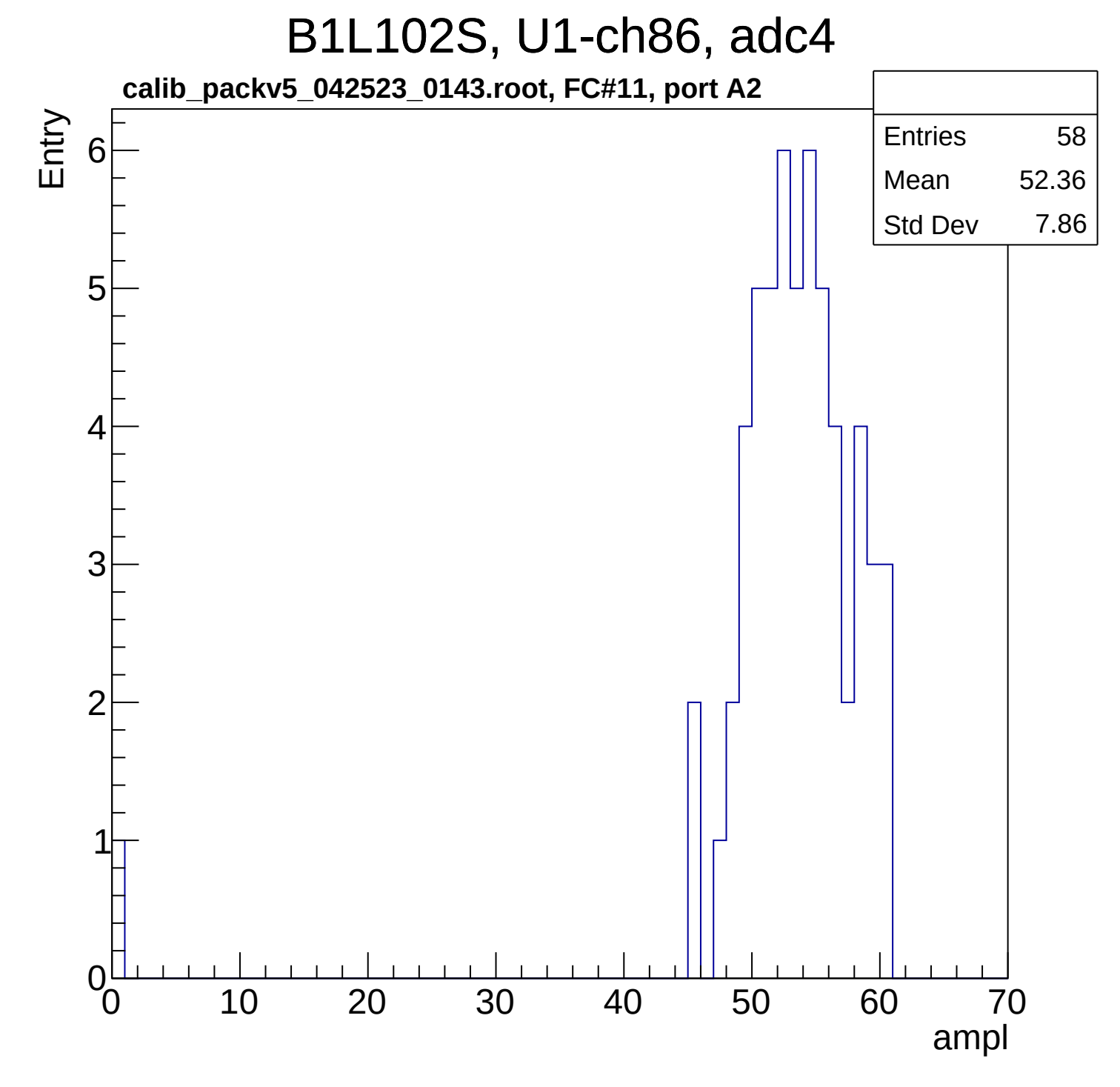
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

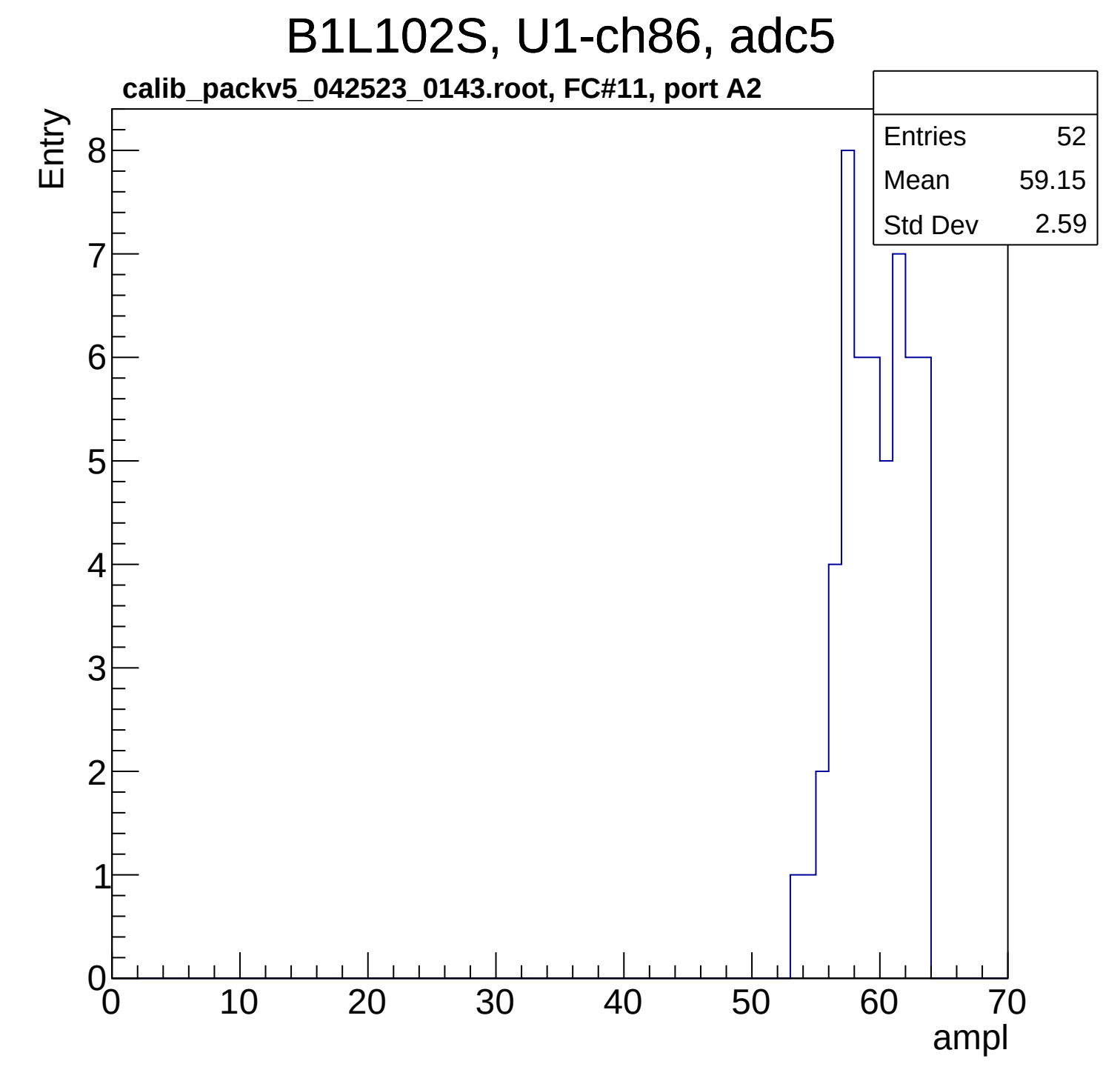

B1L102S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 27.6
Std Dev 5.37
Gaus mean : 28.5090
Gaus Width: 4.2479
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

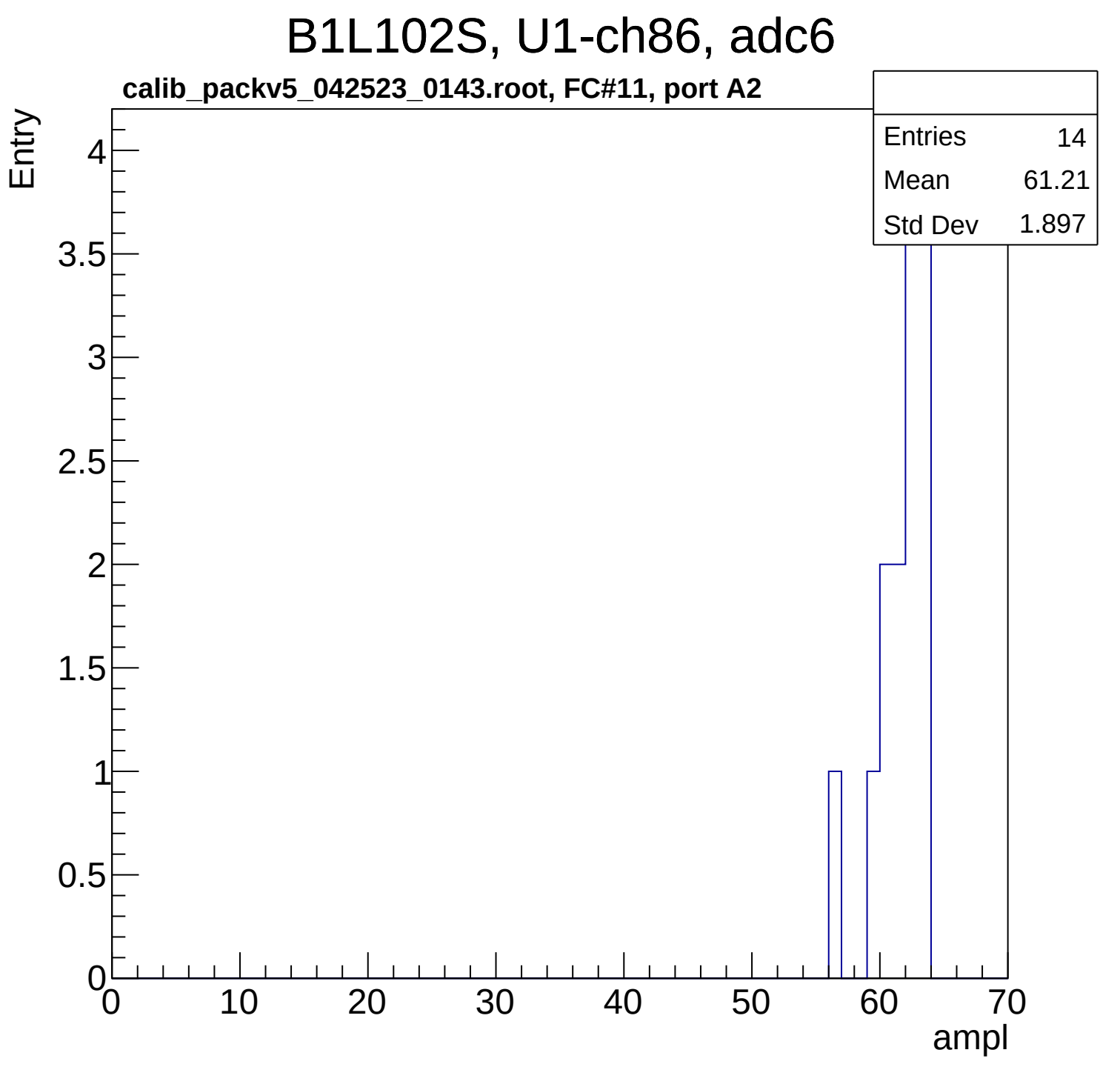

B1L102S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 34.53
Std Dev 3.619
Gaus mean : 34.7250
Gaus Width: 3.3995
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

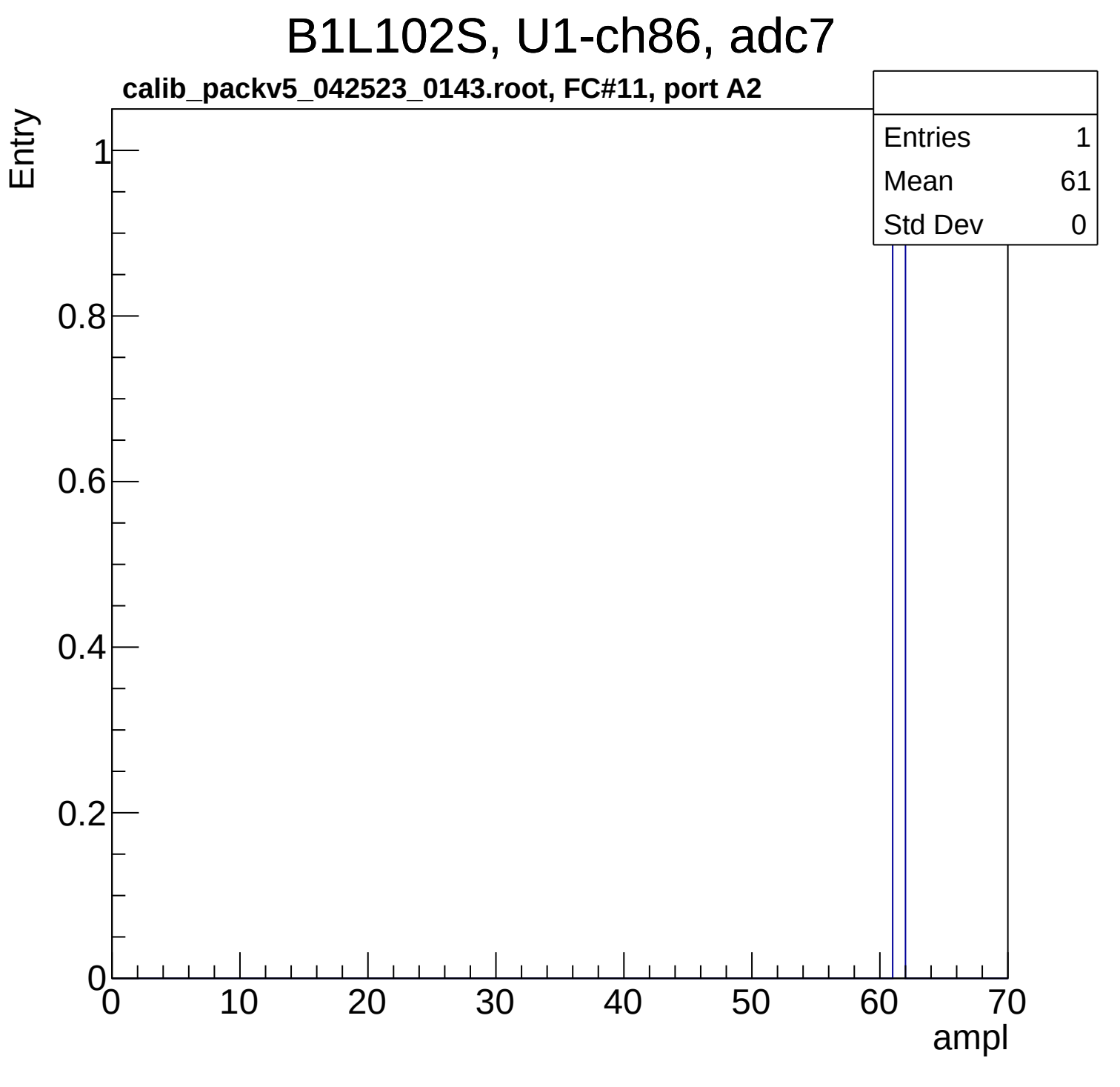

B1L102S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 40.28
Std Dev 3.332
Gaus mean : 40.9377
Gaus Width: 3.6531
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

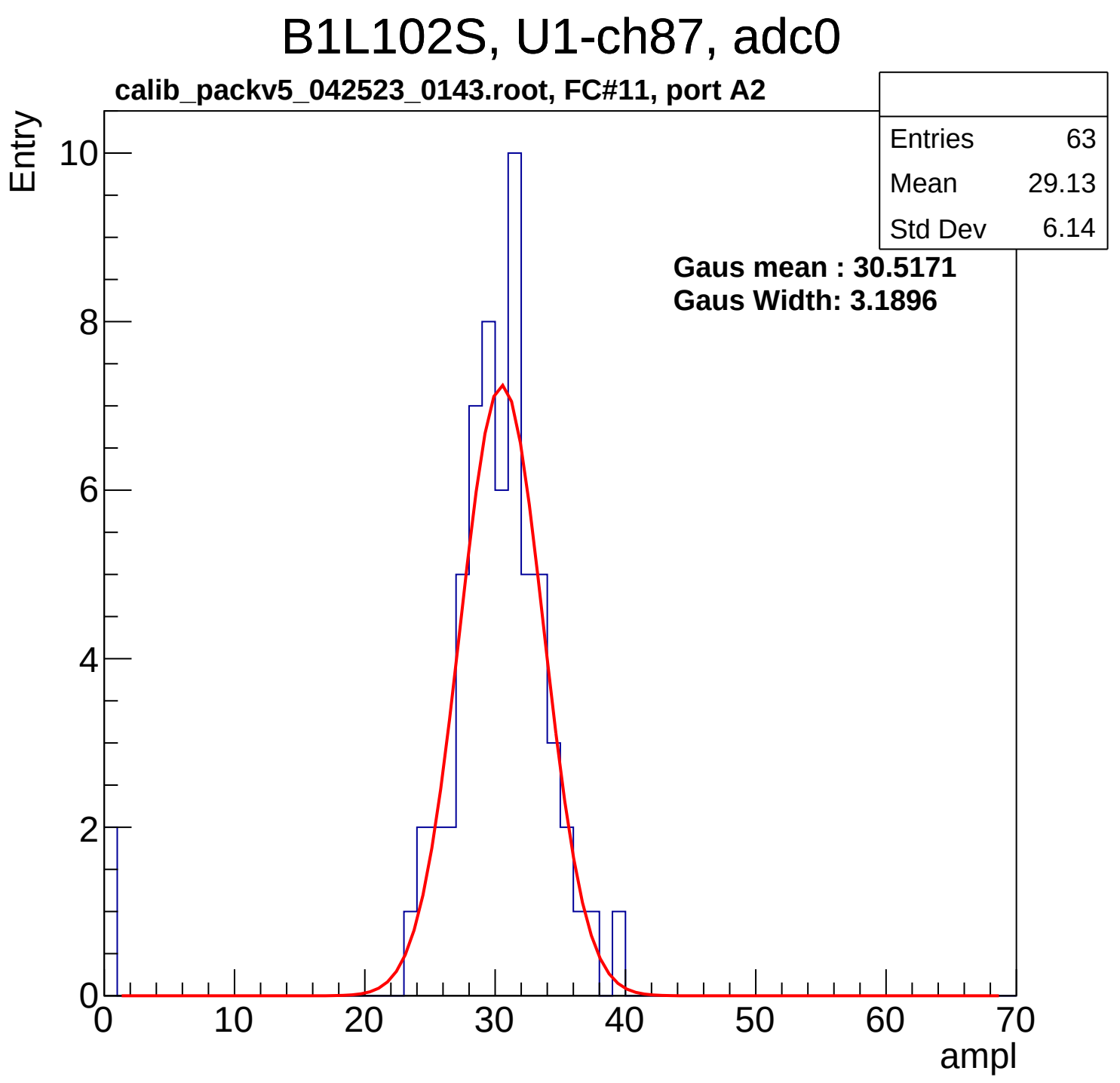

B1L102S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 47.87
Std Dev 3.893
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

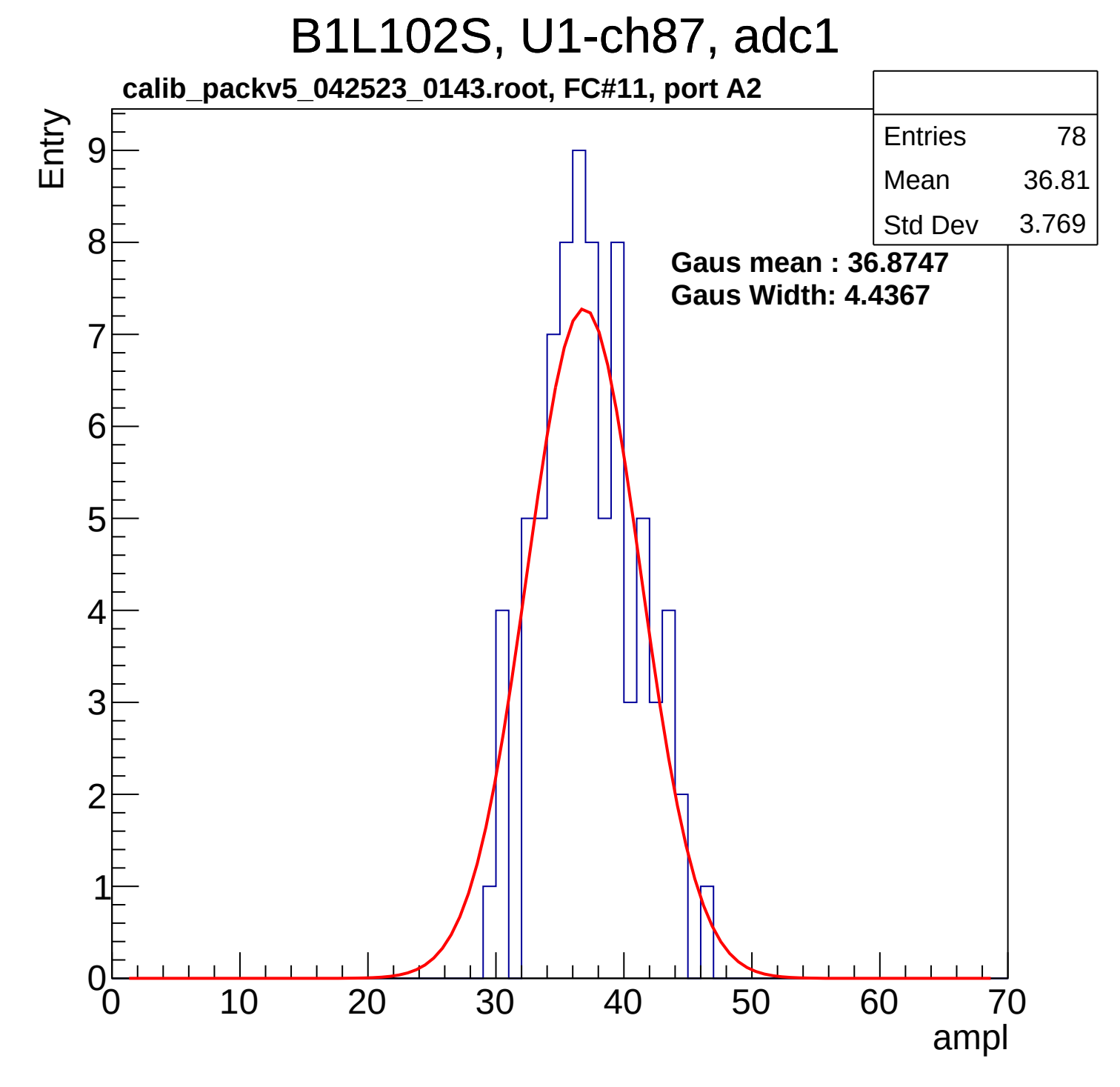

B1L102S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 52.36
Std Dev 7.86
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

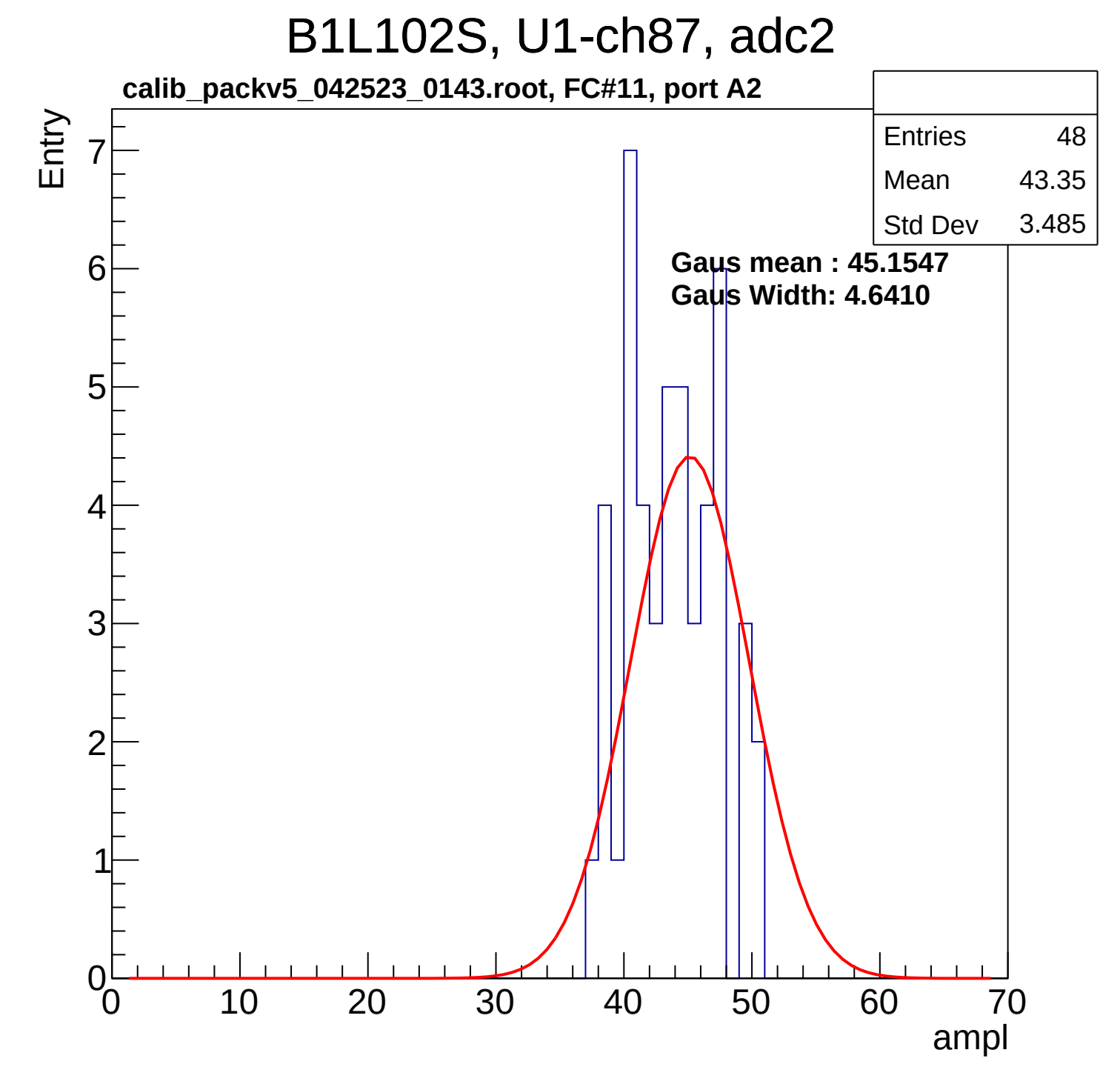

B1L102S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 59.15
Std Dev 2.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

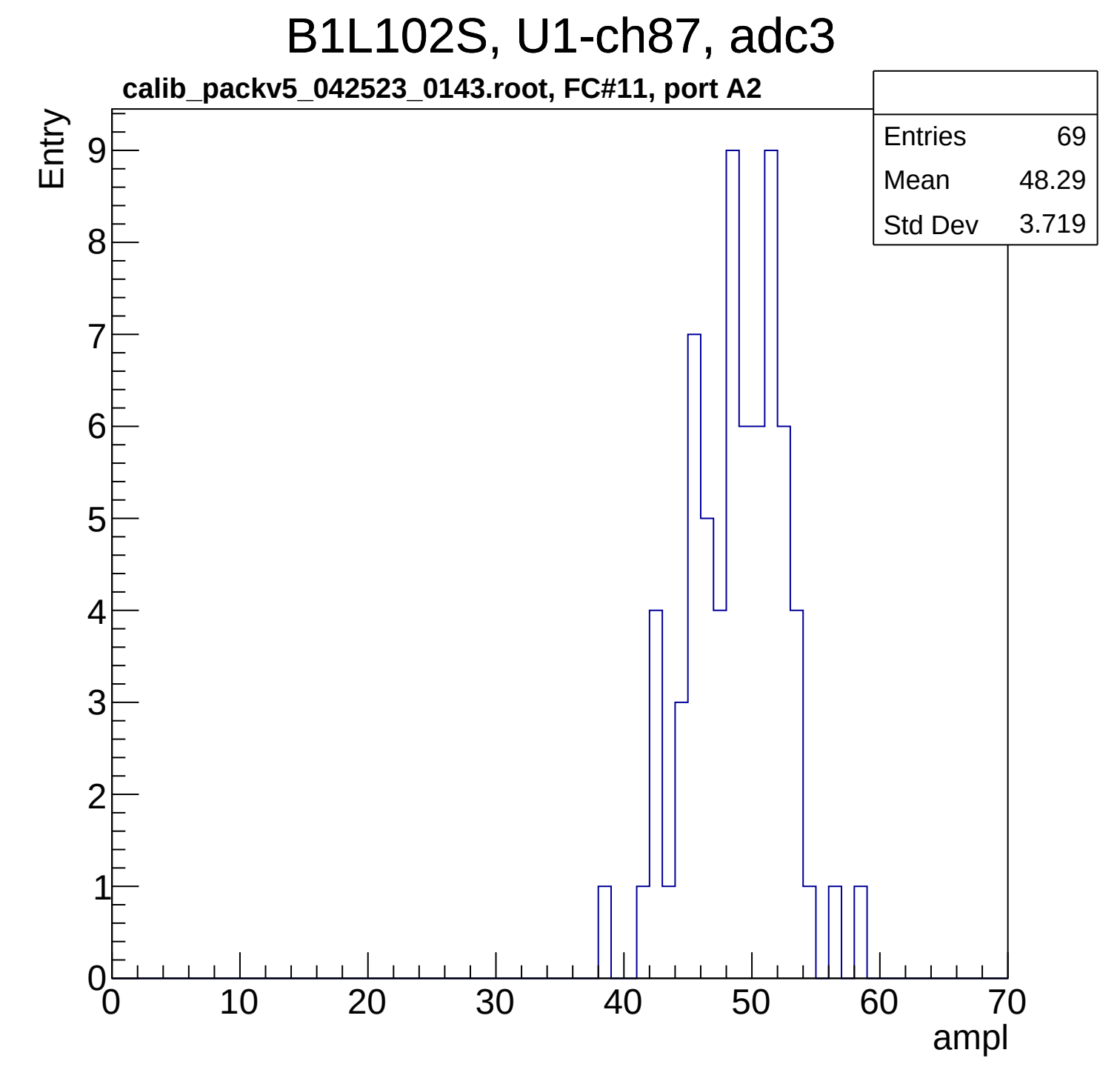

B1L102S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61.21
Std Dev 1.897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

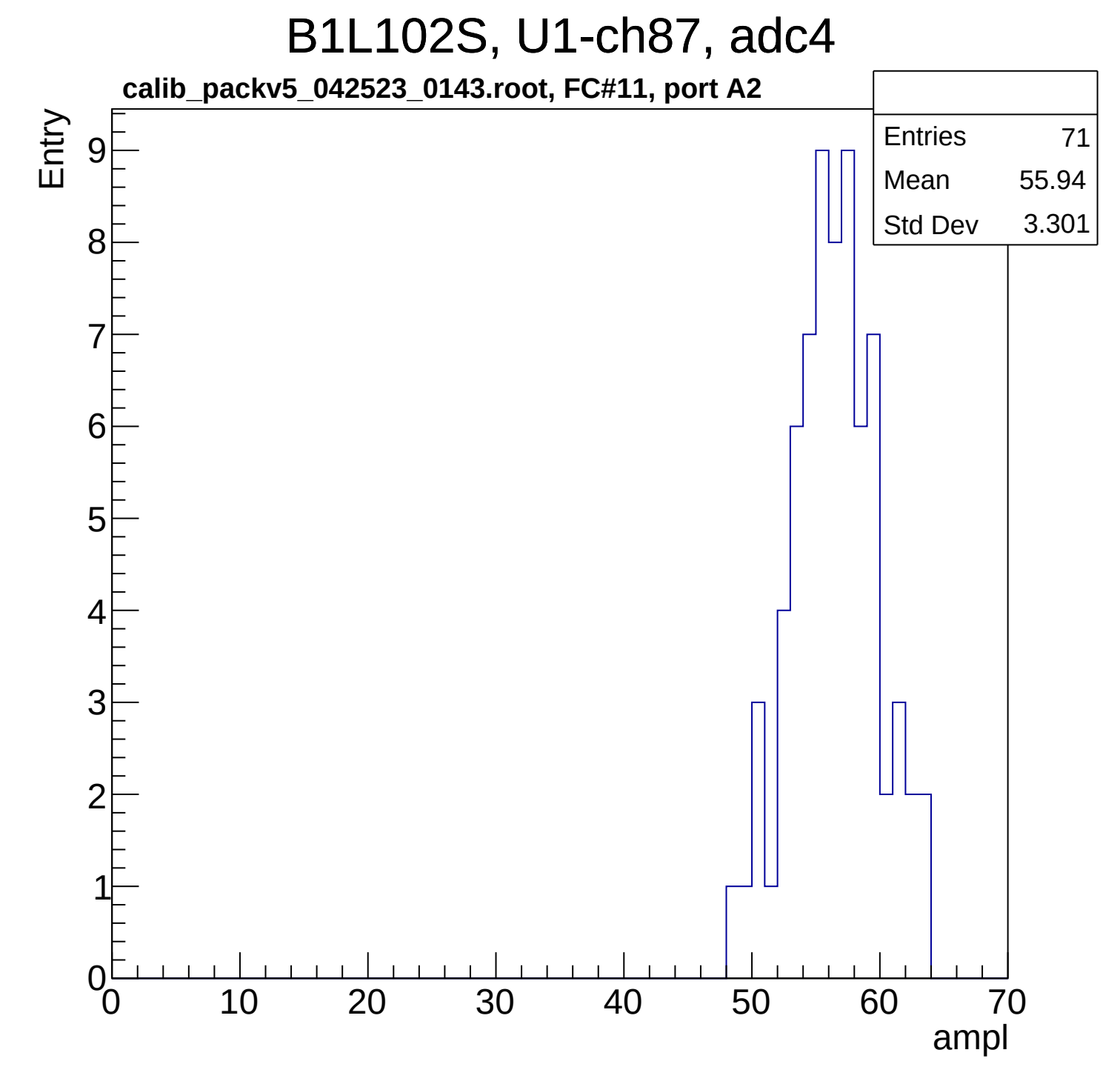

B1L102S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

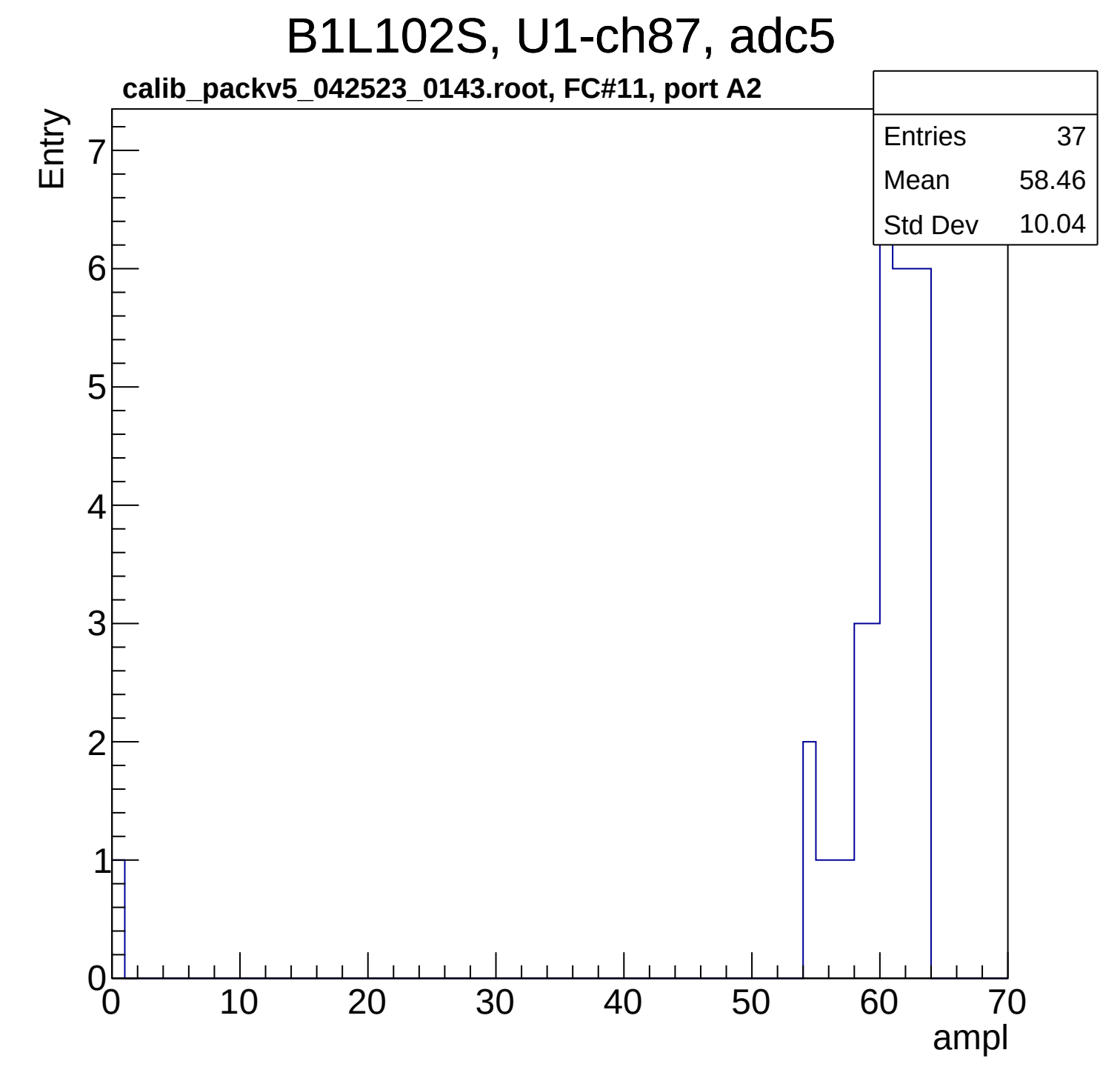

B1L102S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 29.13
Std Dev 6.14
Gaus mean : 30.5171
Gaus Width: 3.1896
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

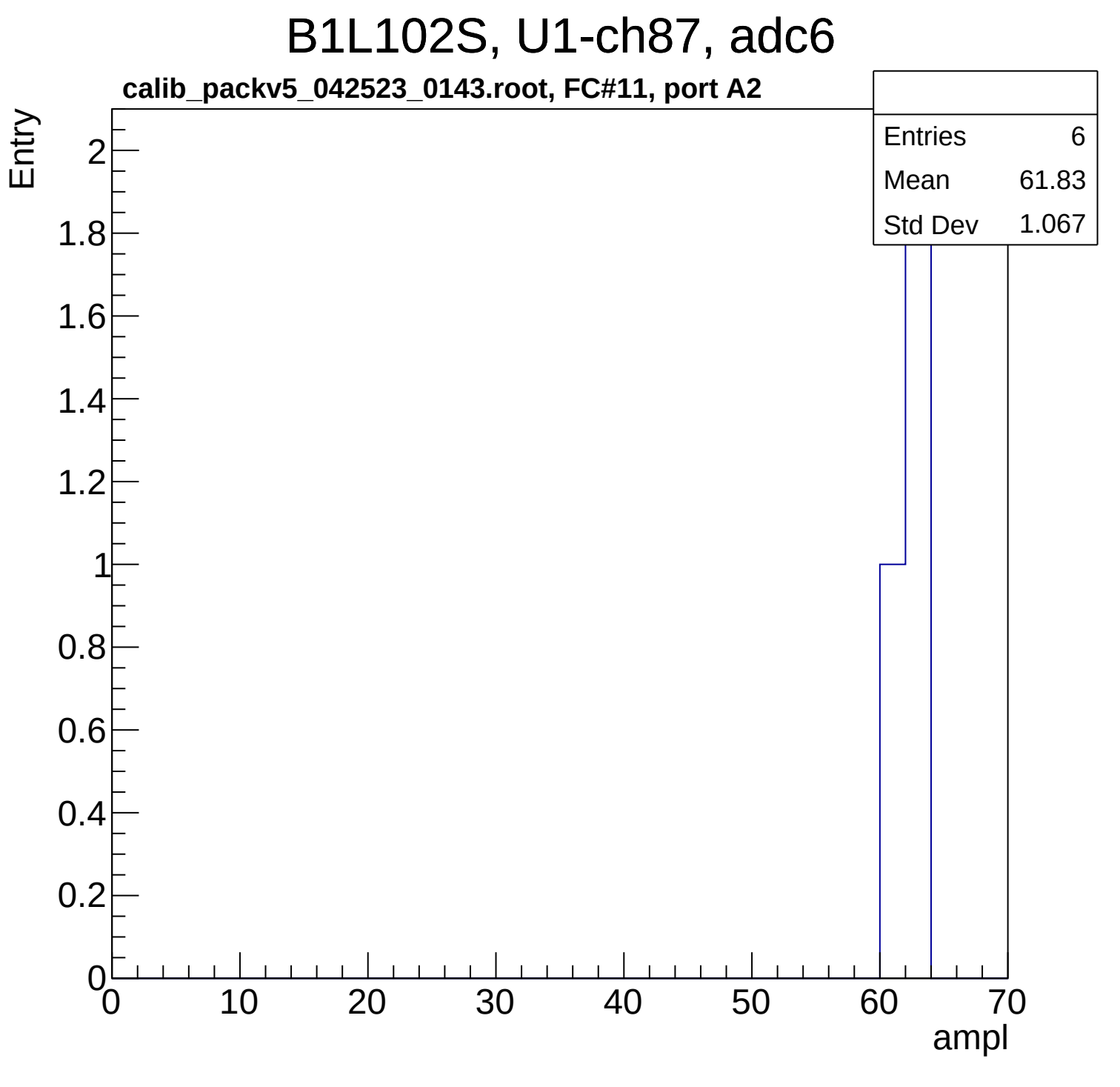

B1L102S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 36.81
Std Dev 3.769
Gaus mean : 36.8747
Gaus Width: 4.4367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

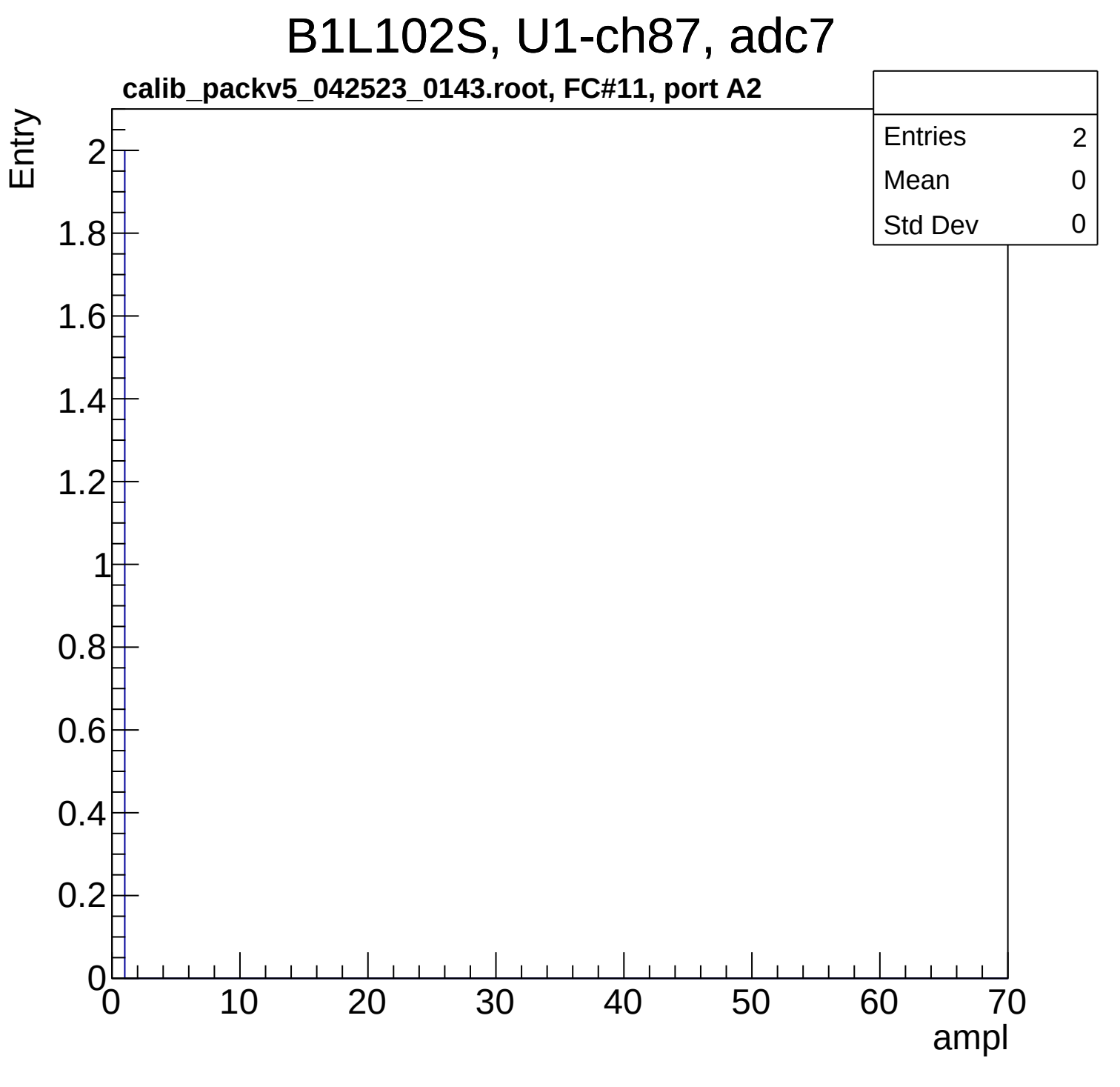

B1L102S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 43.35
Std Dev 3.485
Gaus mean : 45.1547
Gaus Width: 4.6410
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

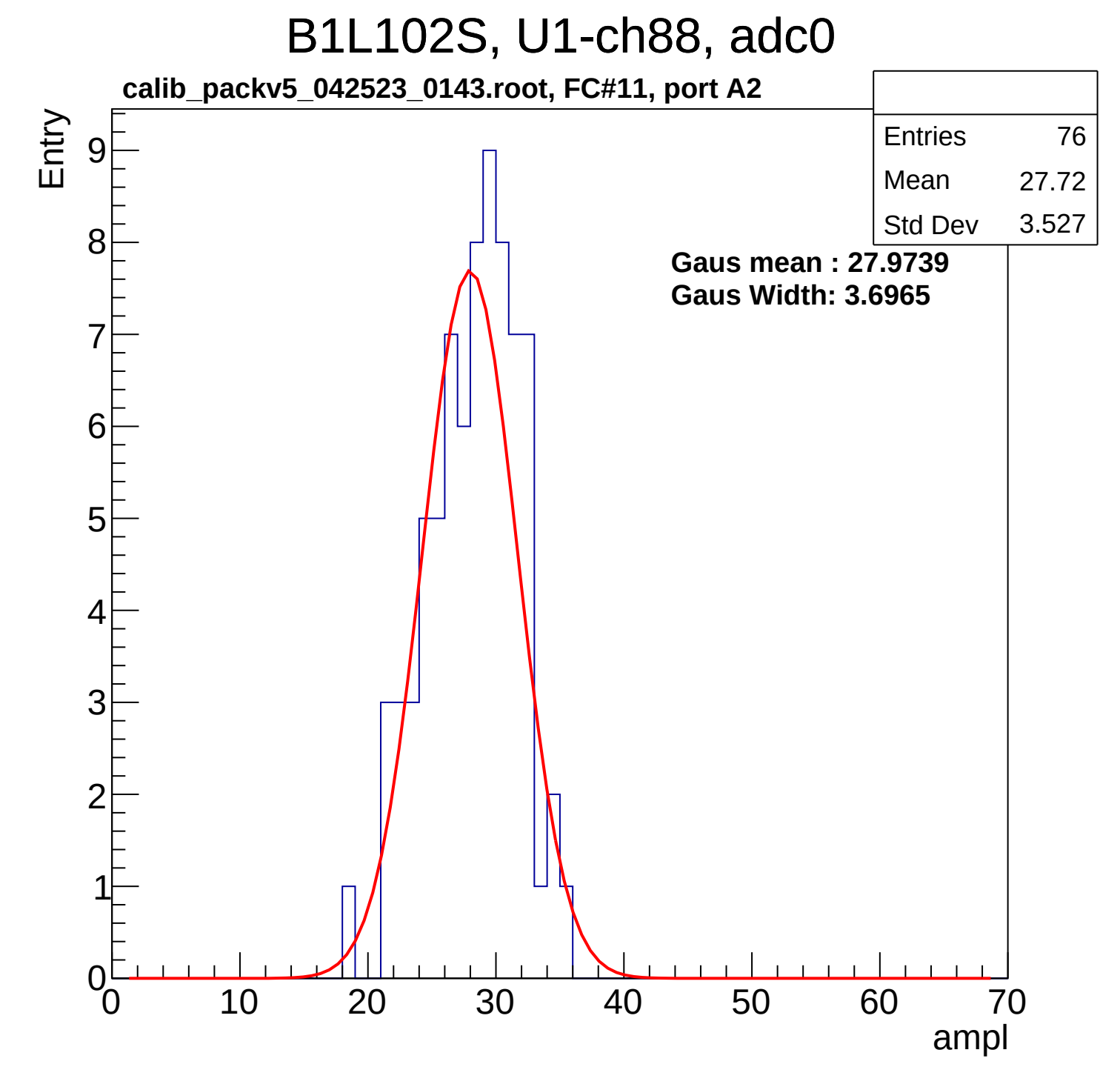

B1L102S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 48.29
Std Dev 3.719
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

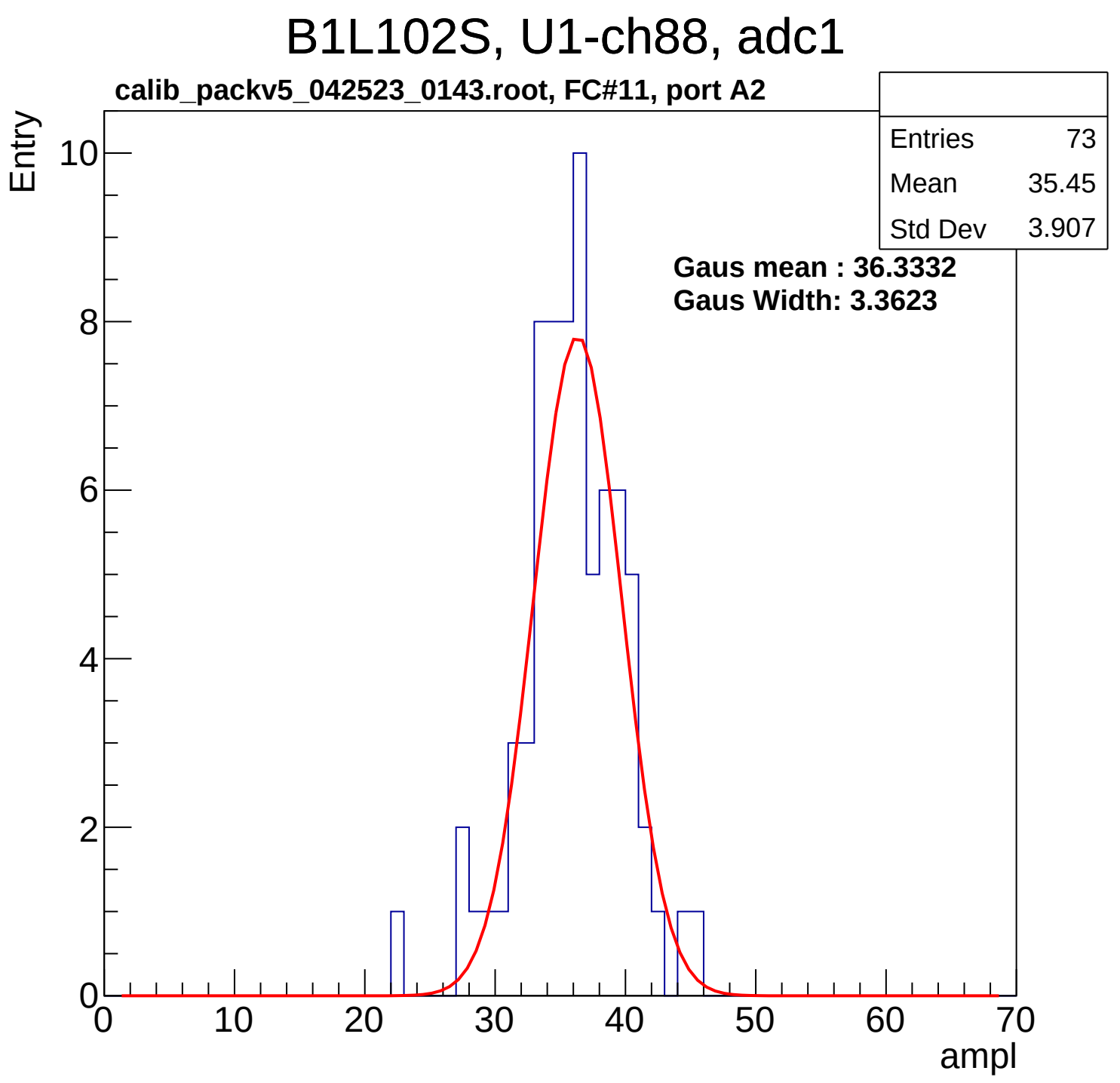

B1L102S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 55.94
Std Dev 3.301
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

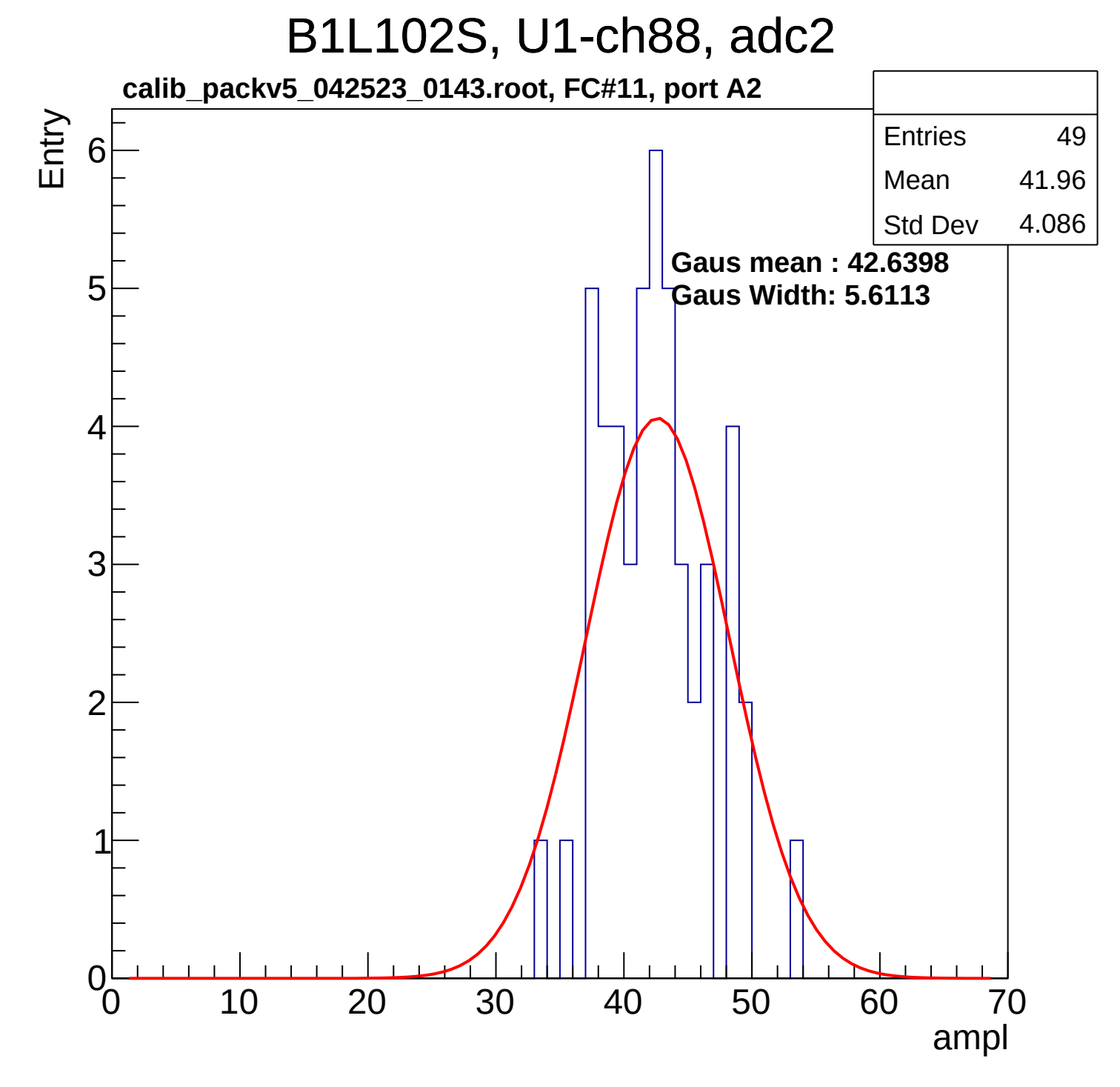

B1L102S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 58.46
Std Dev 10.04
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

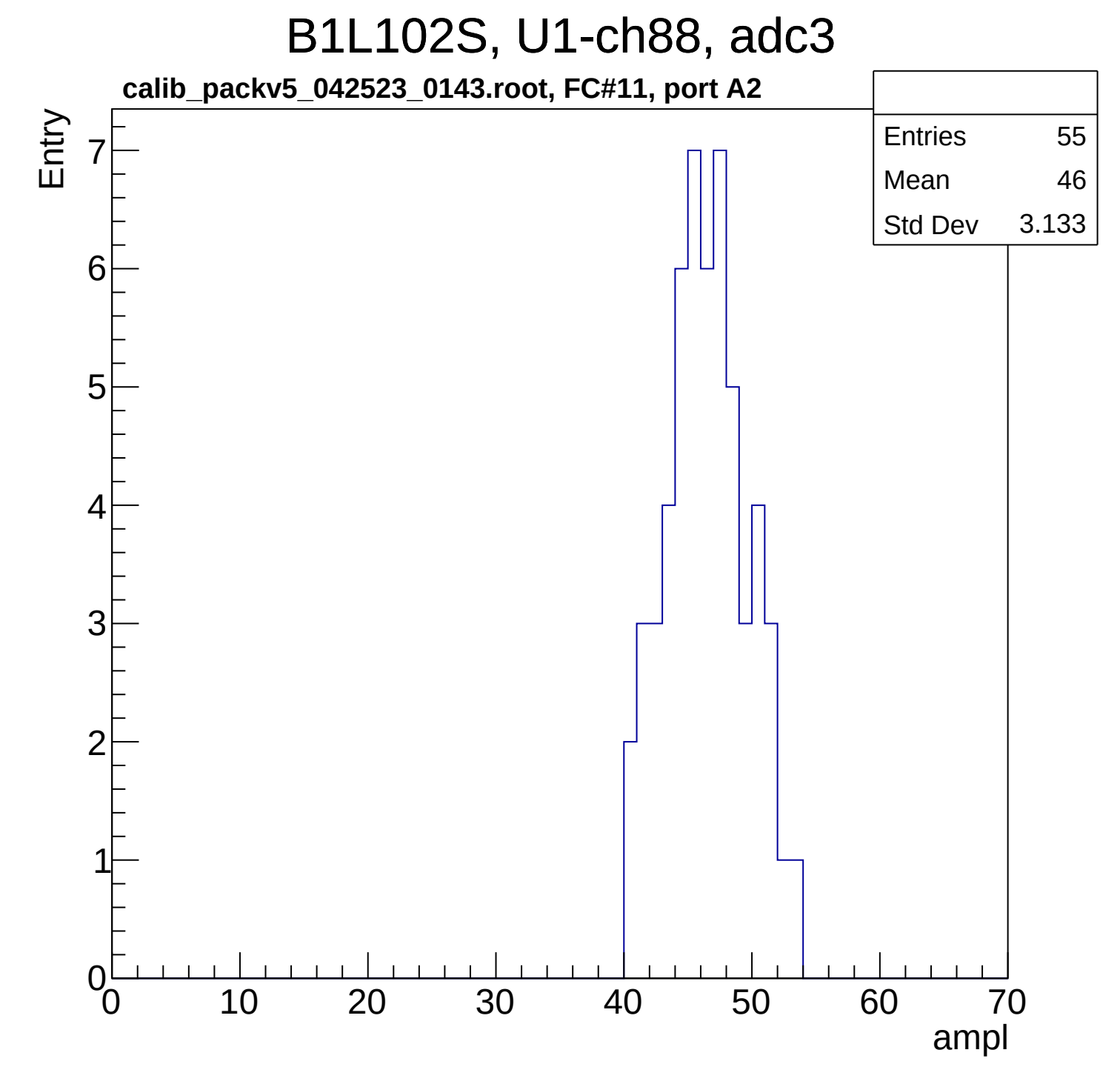

B1L102S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

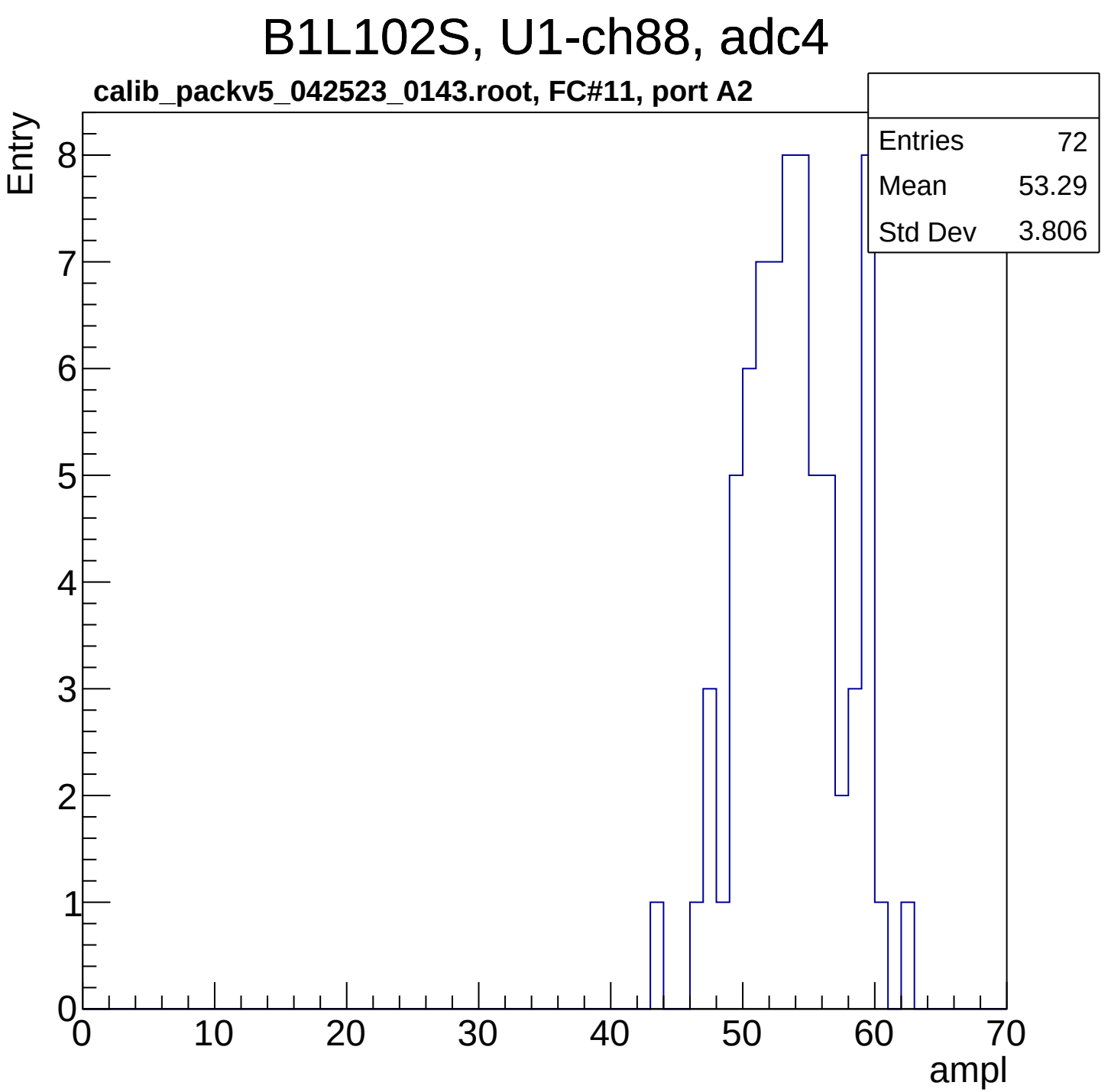

B1L102S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

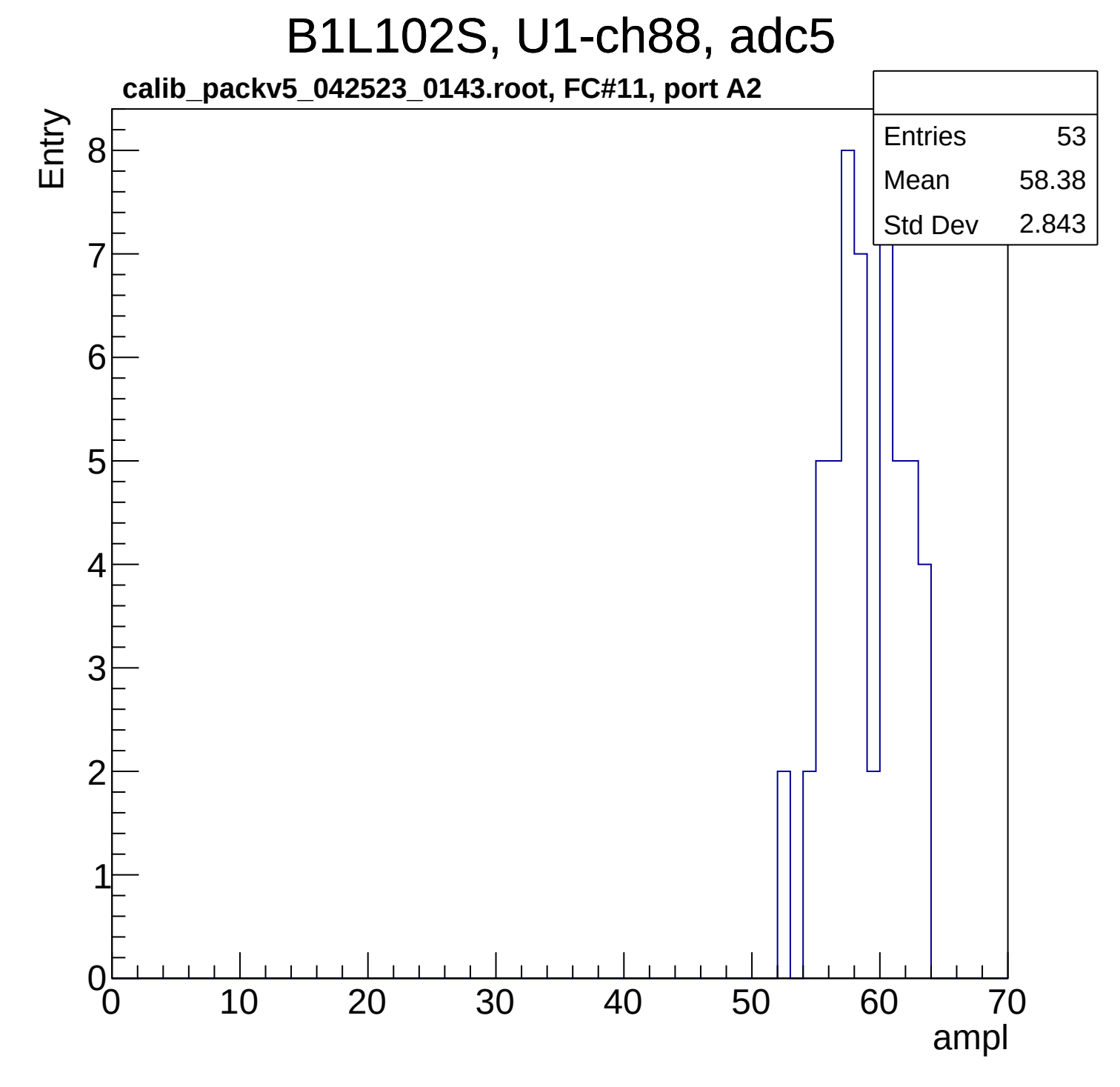

B1L102S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 27.72
Std Dev 3.527
Gaus mean : 27.9739
Gaus Width: 3.6965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

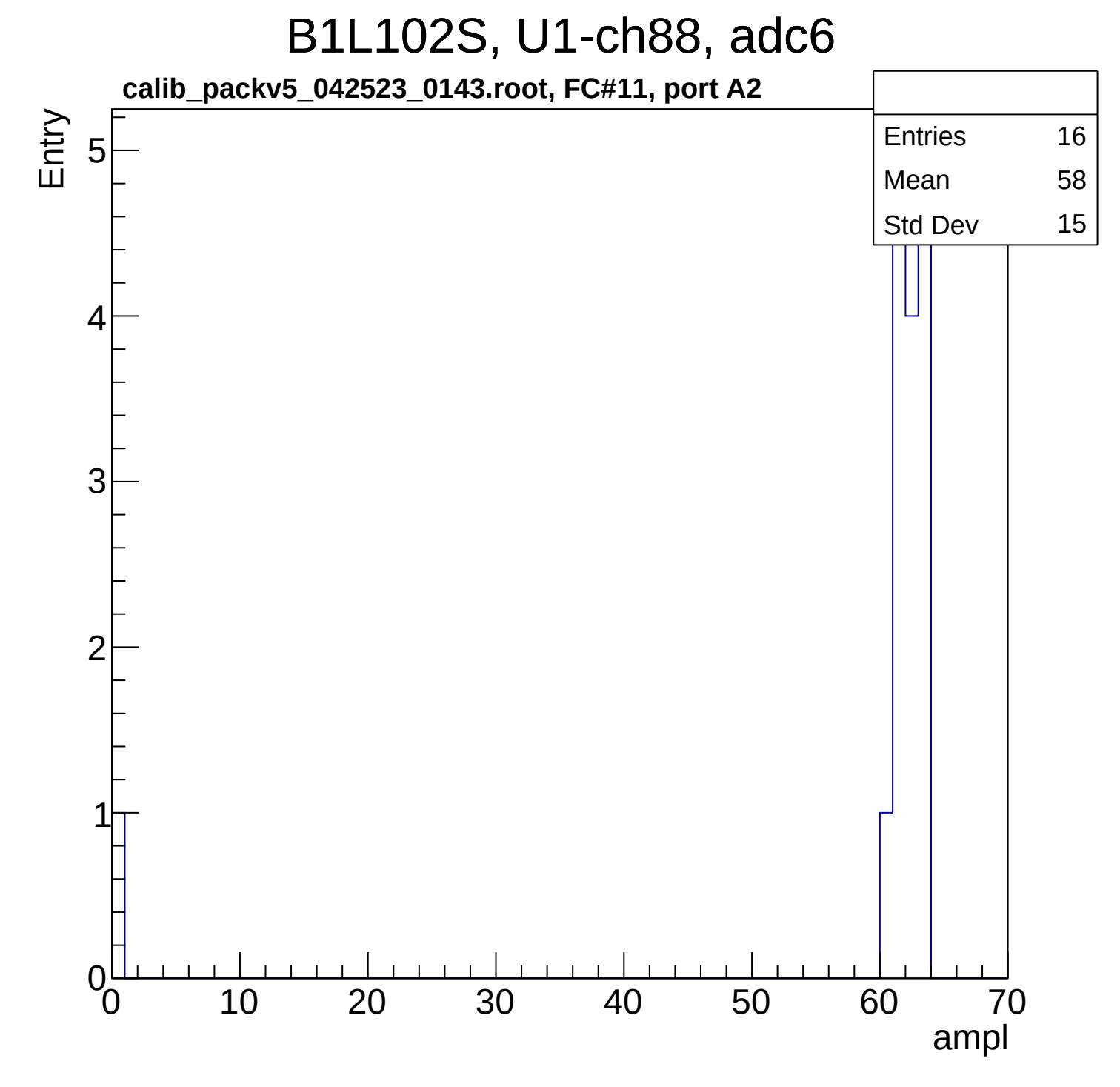

B1L102S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 35.45
Std Dev 3.907
Gaus mean : 36.3332
Gaus Width: 3.3623
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

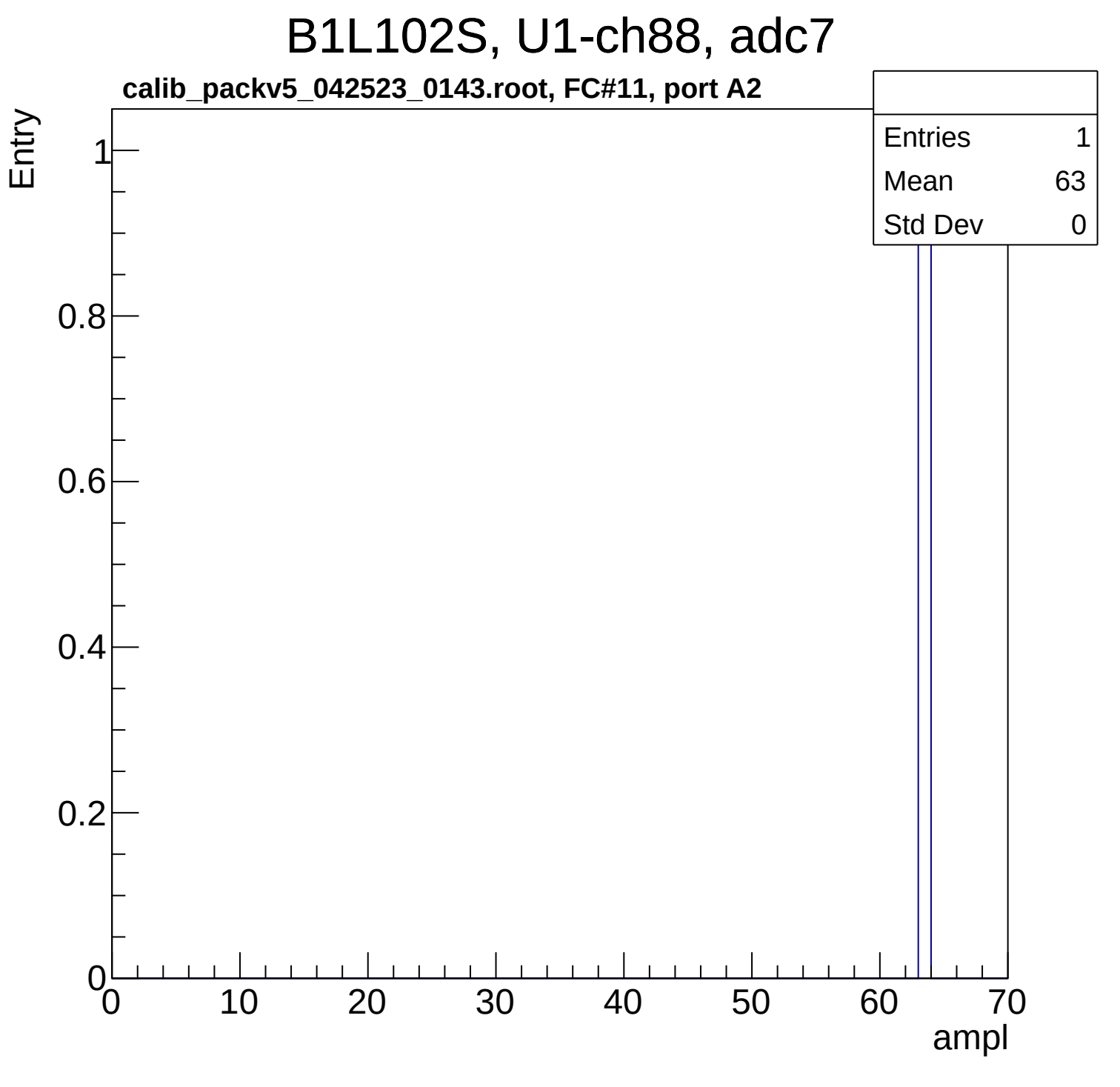

B1L102S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 41.96
Std Dev 4.086
Gaus mean : 42.6398
Gaus Width: 5.6113
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

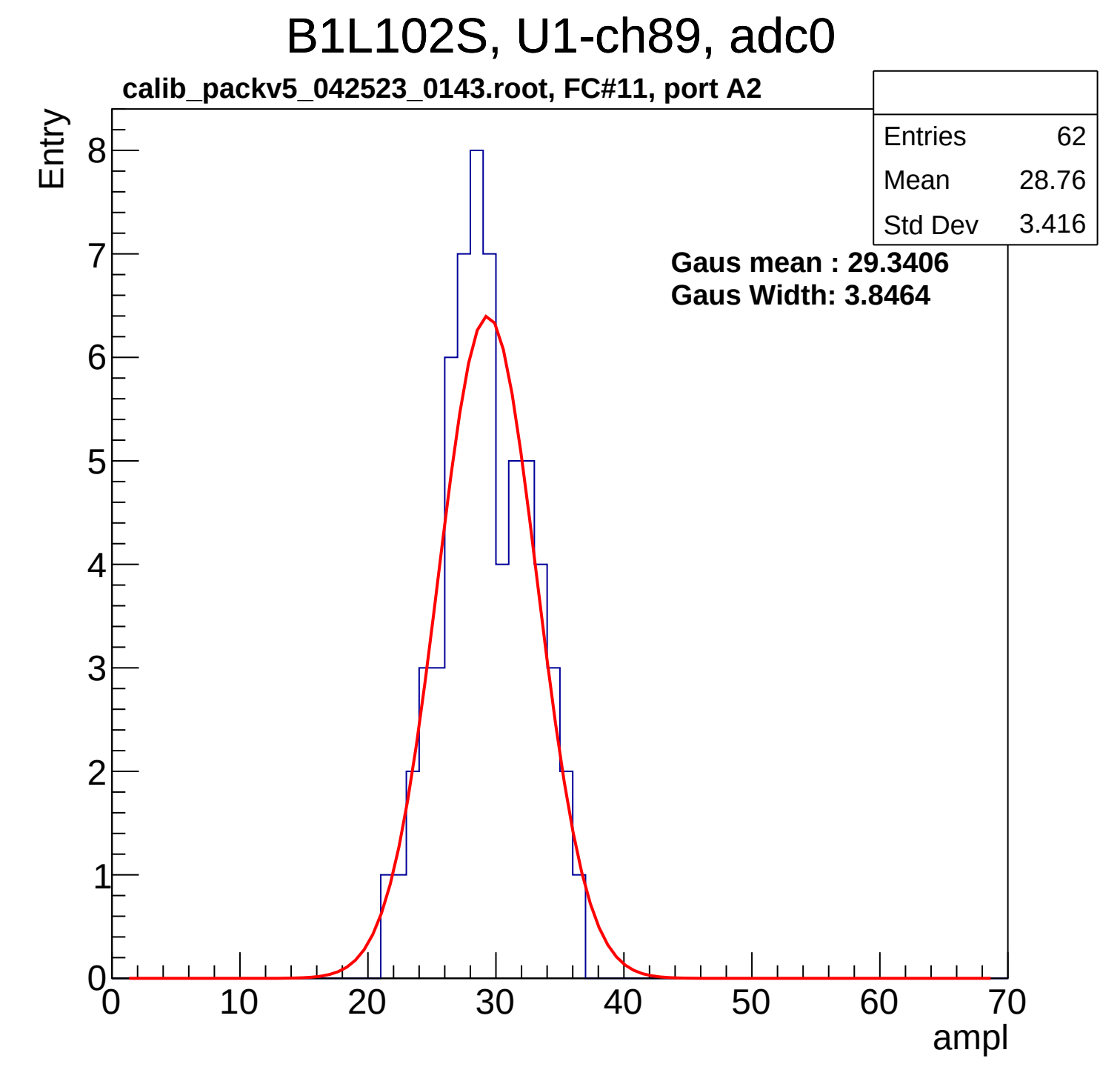

B1L102S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 46
Std Dev 3.133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

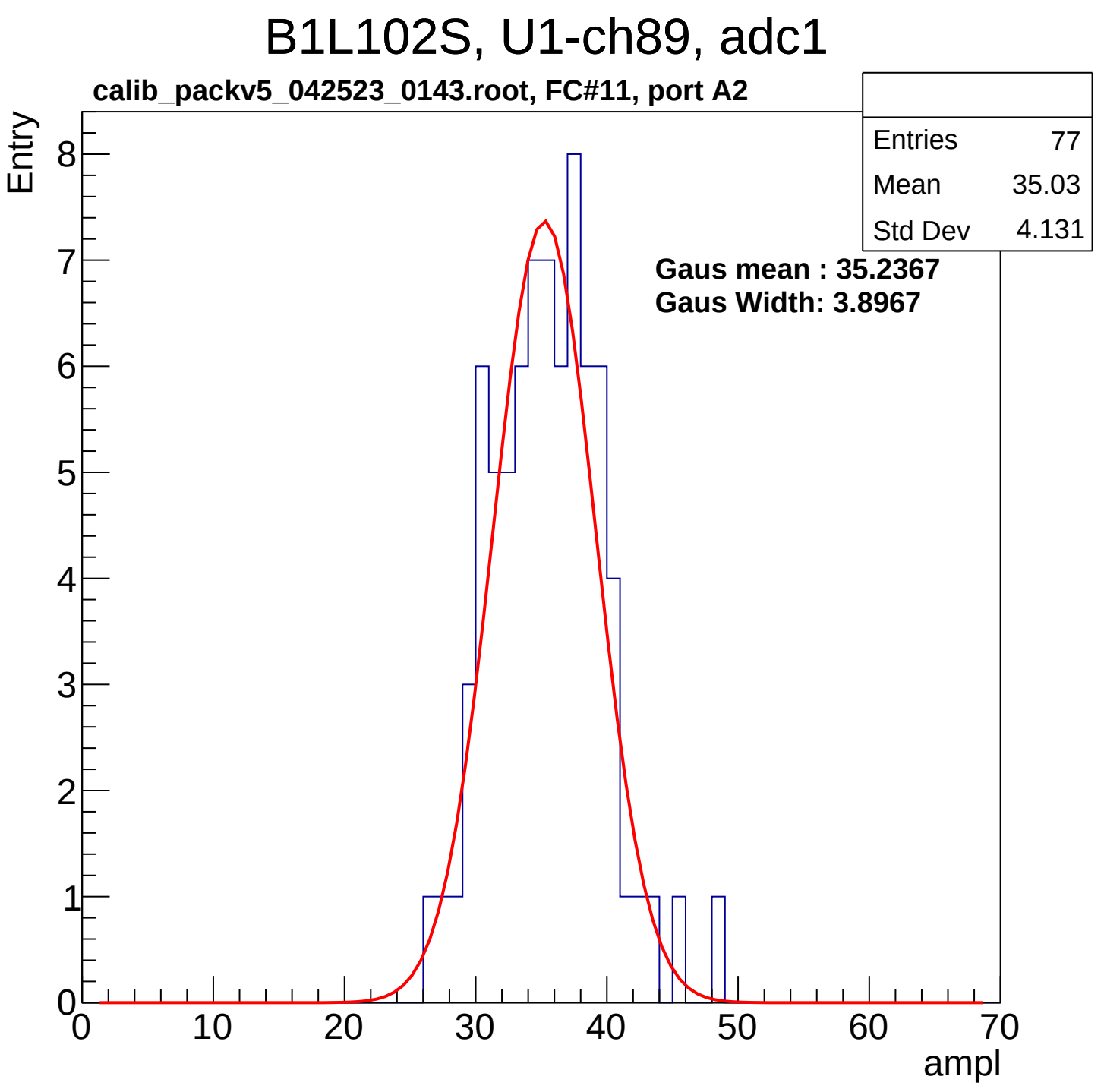

B1L102S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 53.29
Std Dev 3.806
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

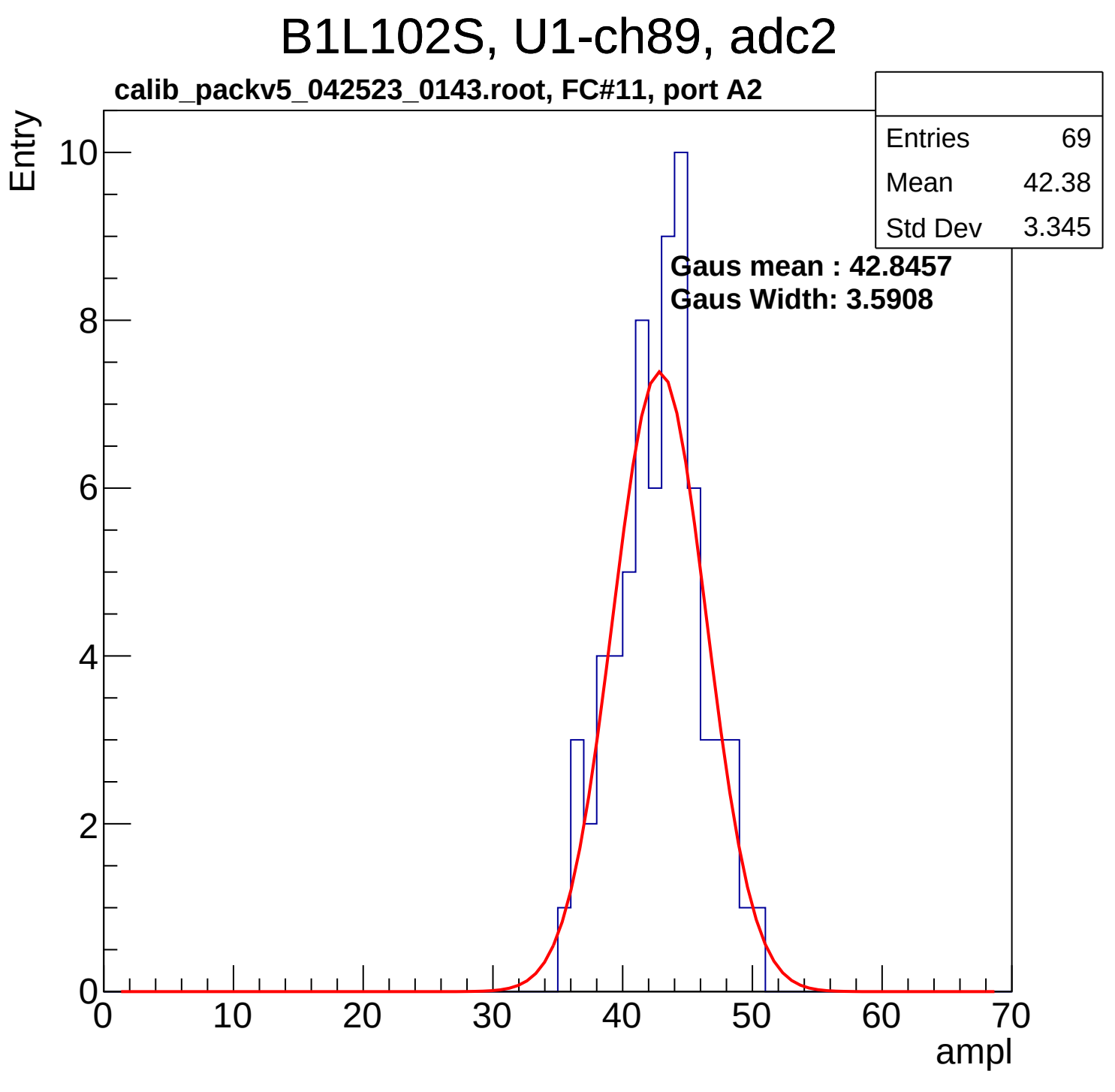

B1L102S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 58.38
Std Dev 2.843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

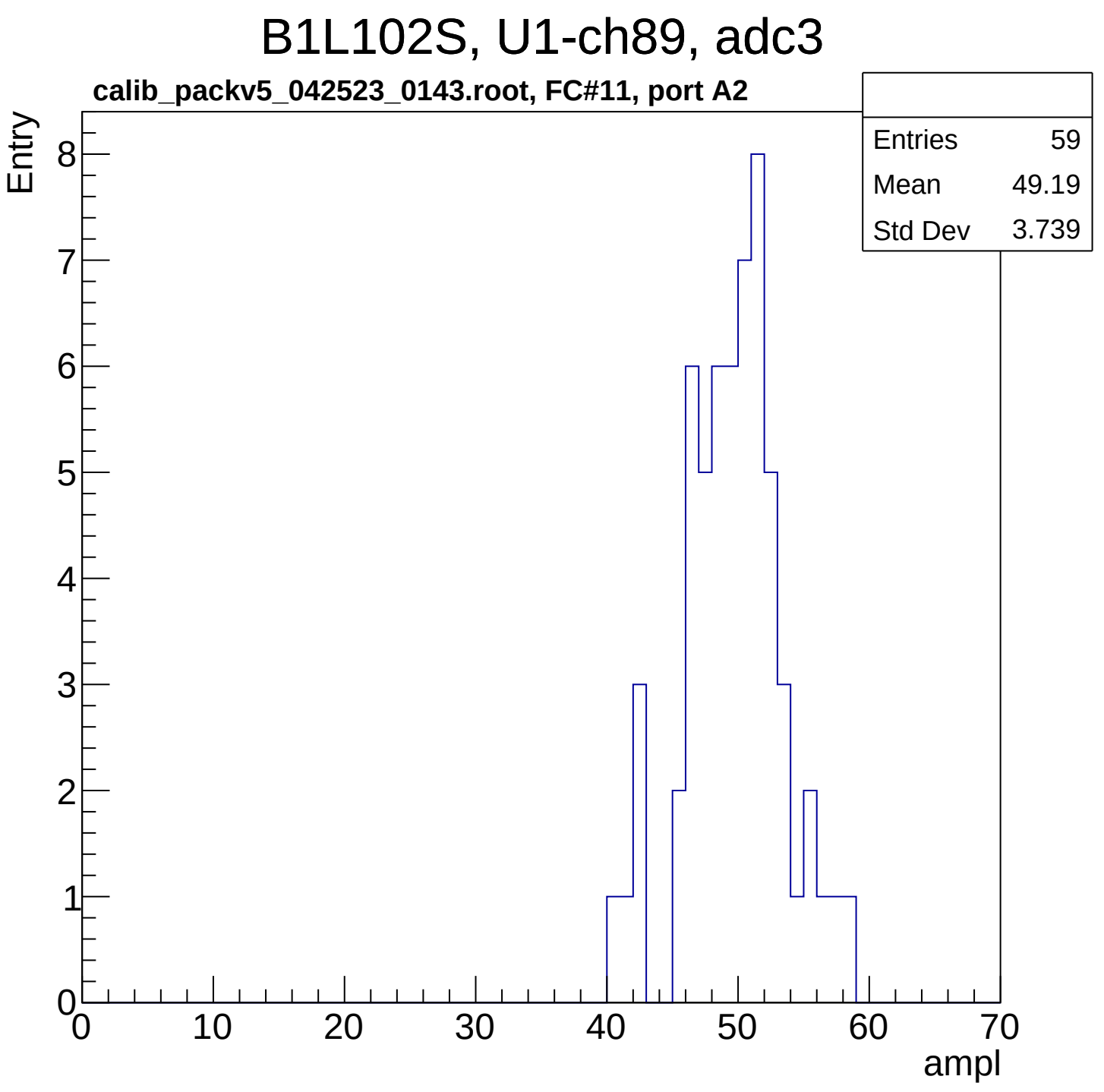

B1L102S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 58
Std Dev 15
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

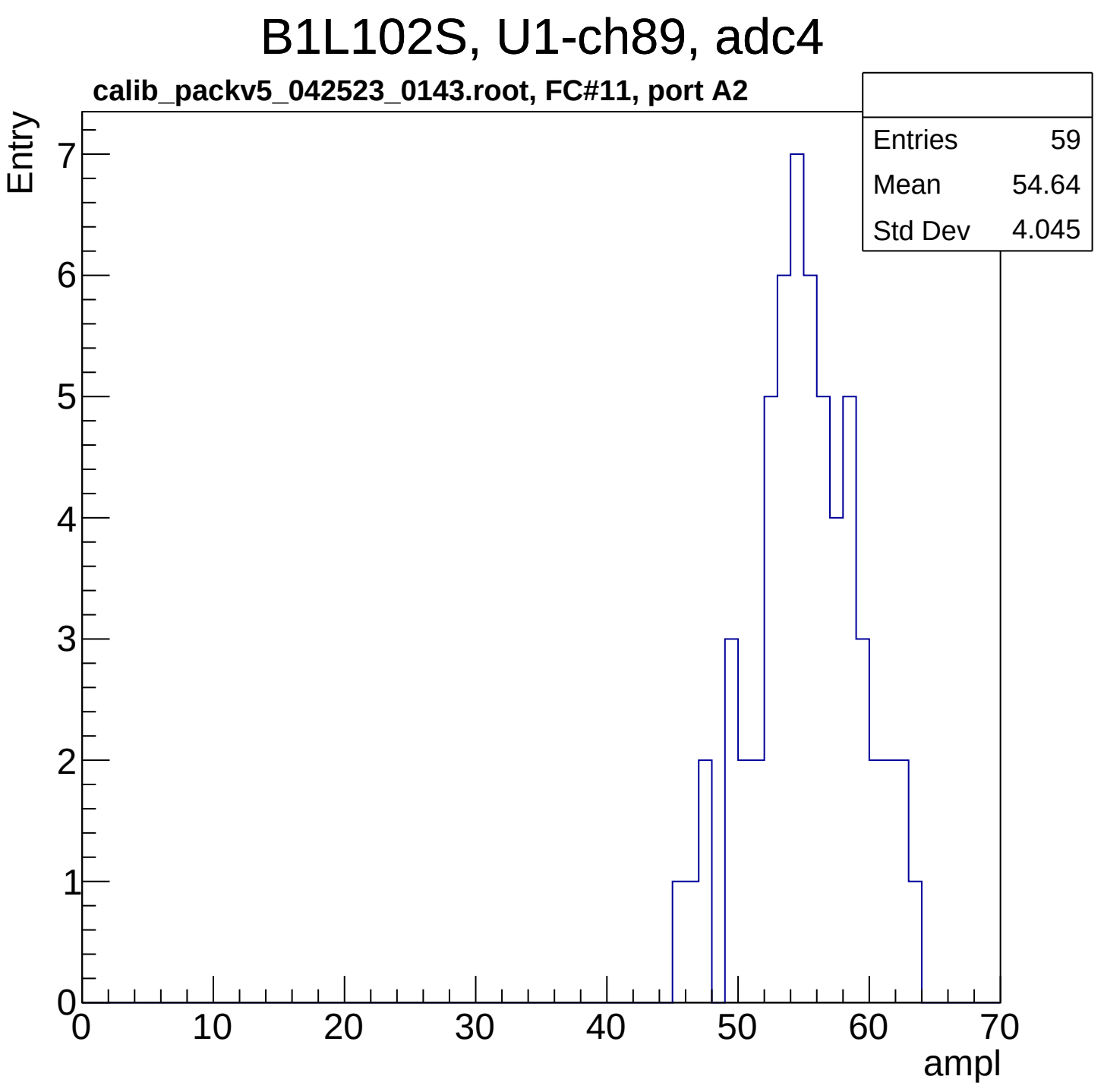

B1L102S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

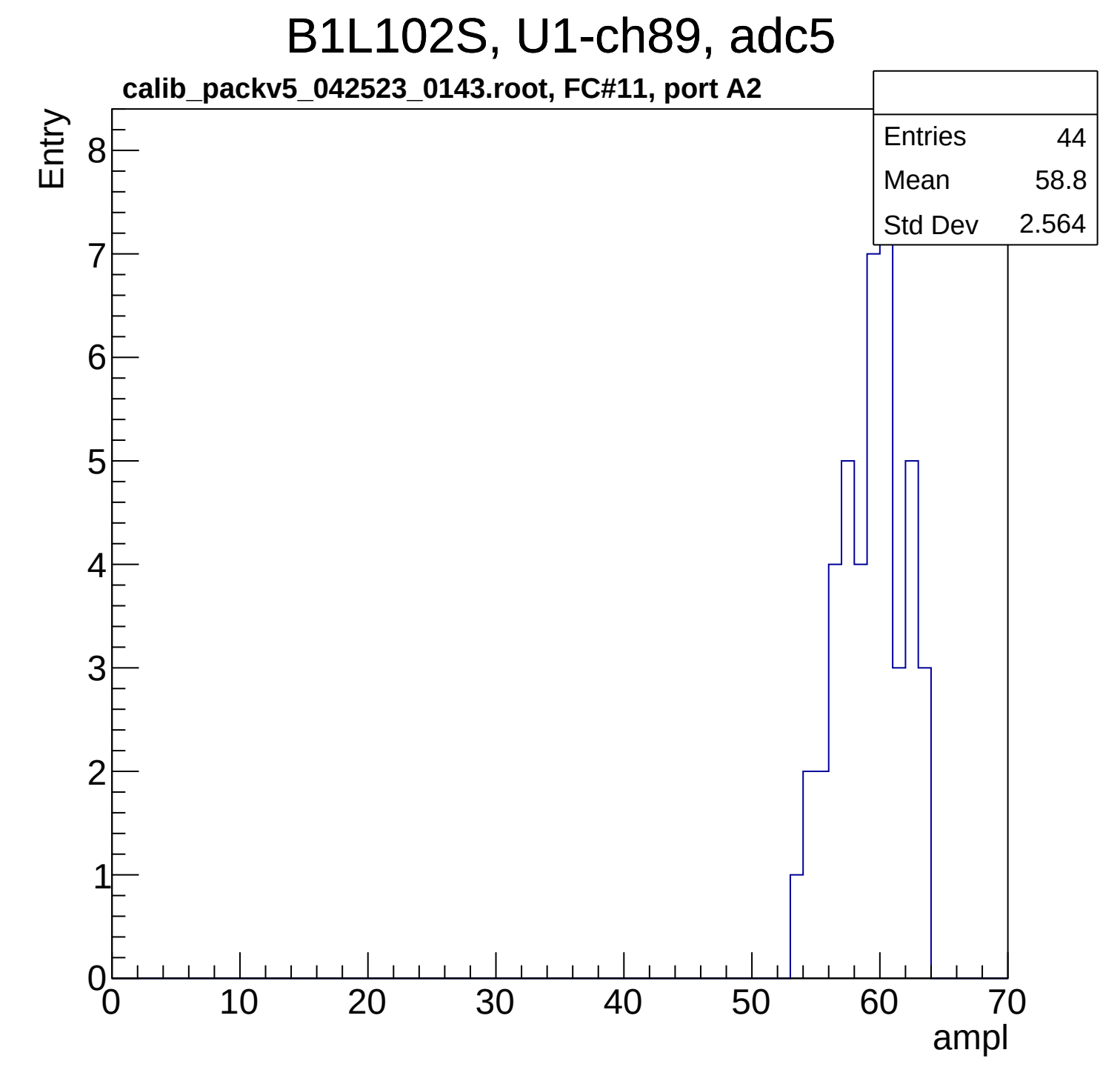

B1L102S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 28.76
Std Dev 3.416
Gaus mean : 29.3406
Gaus Width: 3.8464
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

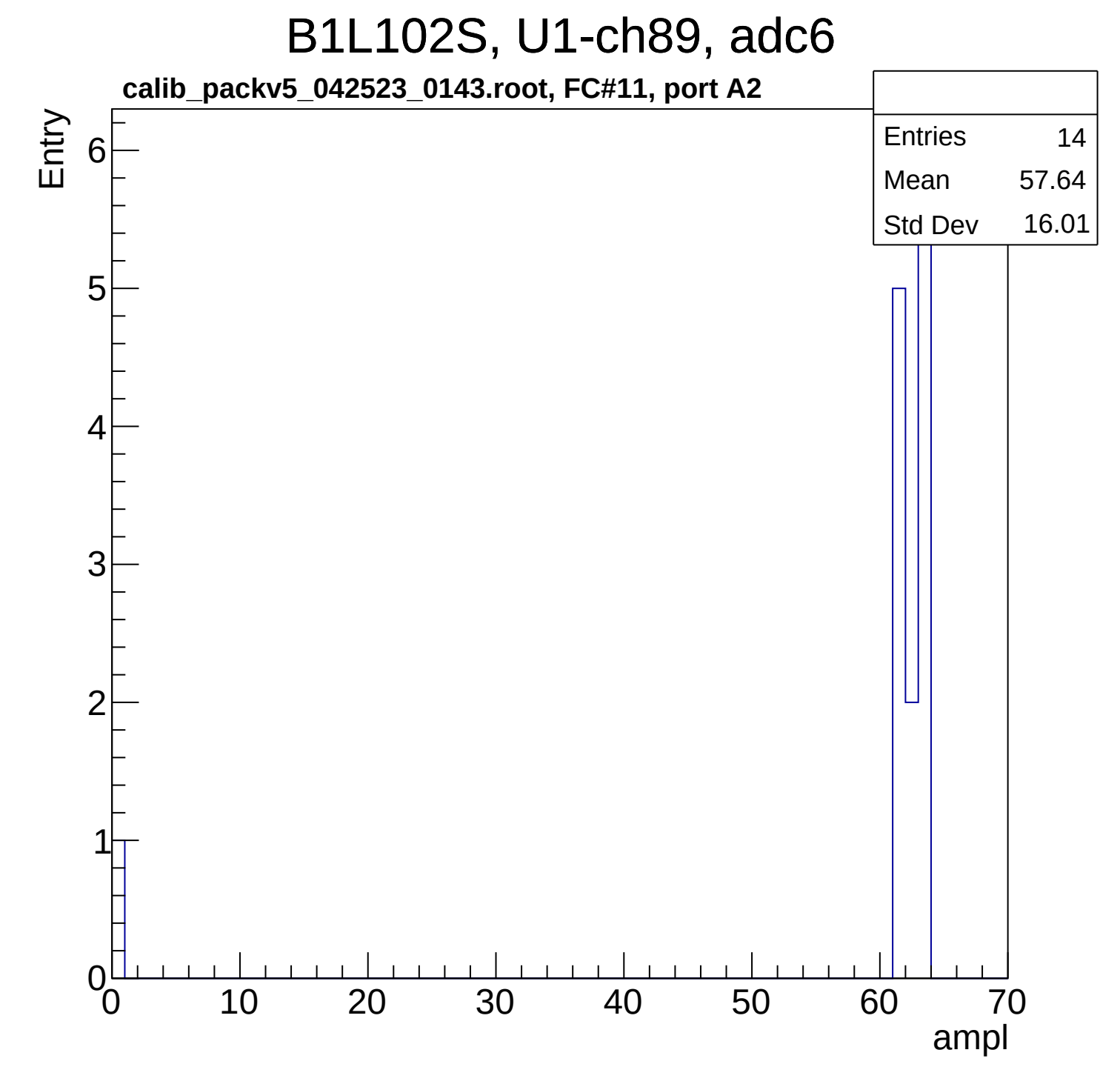

B1L102S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 35.03
Std Dev 4.131
Gaus mean : 35.2367
Gaus Width: 3.8967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

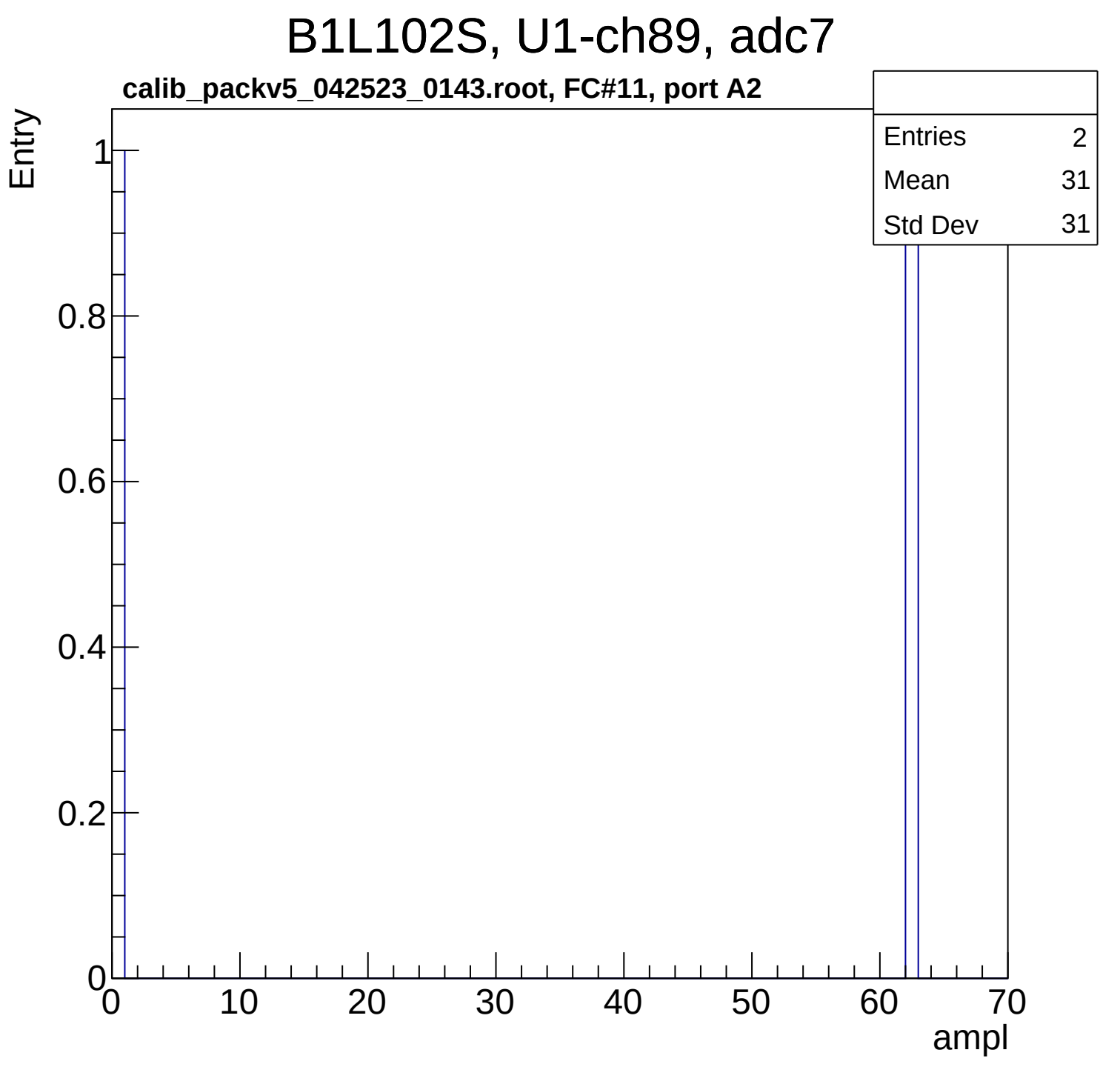

B1L102S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 42.38
Std Dev 3.345
Gaus mean : 42.8457
Gaus Width: 3.5908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

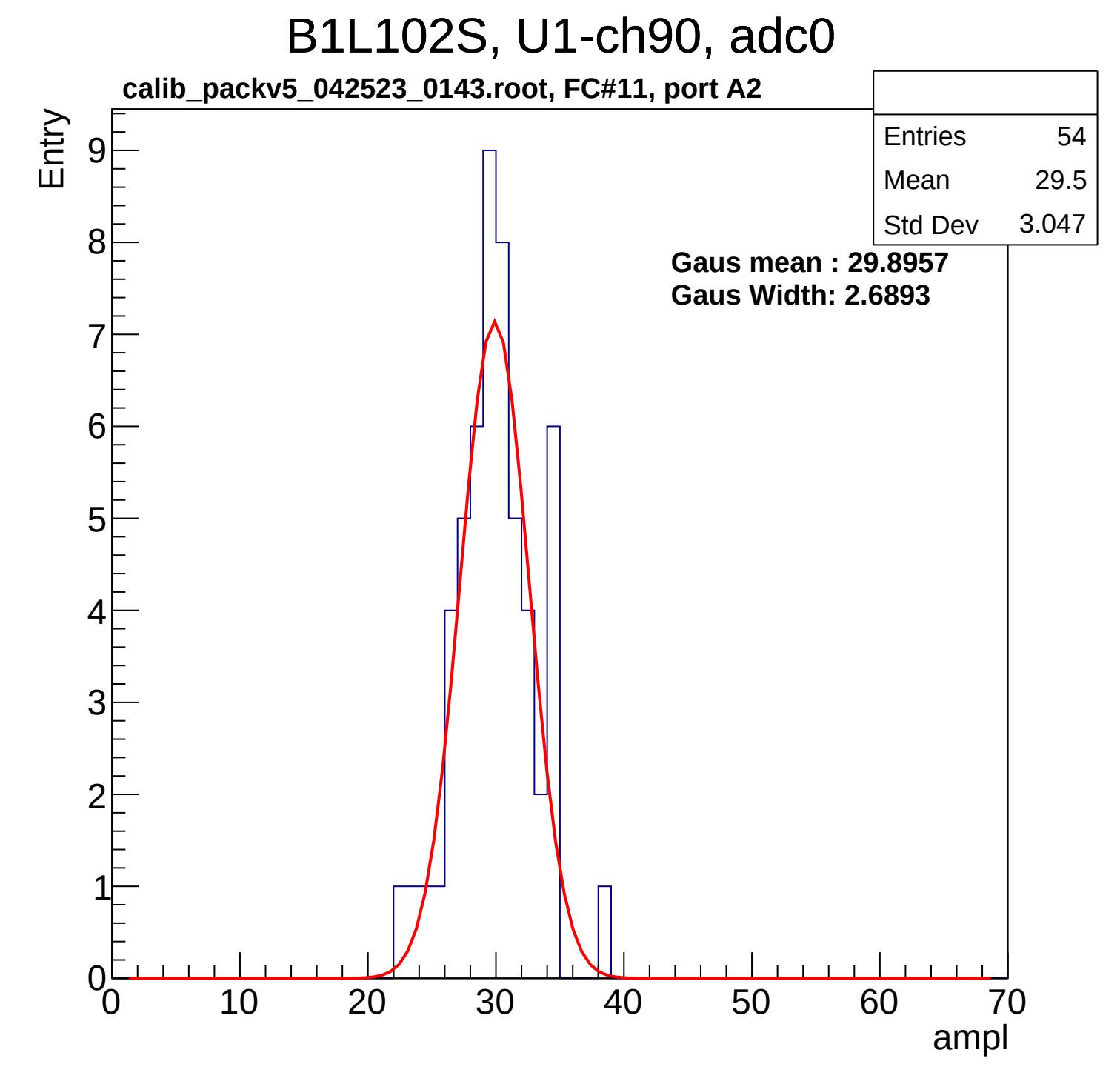

B1L102S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 49.19
Std Dev 3.739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 54.64
Std Dev 4.045
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 58.8
Std Dev 2.564
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 57.64
Std Dev 16.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 29.5
Std Dev 3.047
Gaus mean : 29.8957
Gaus Width: 2.6893
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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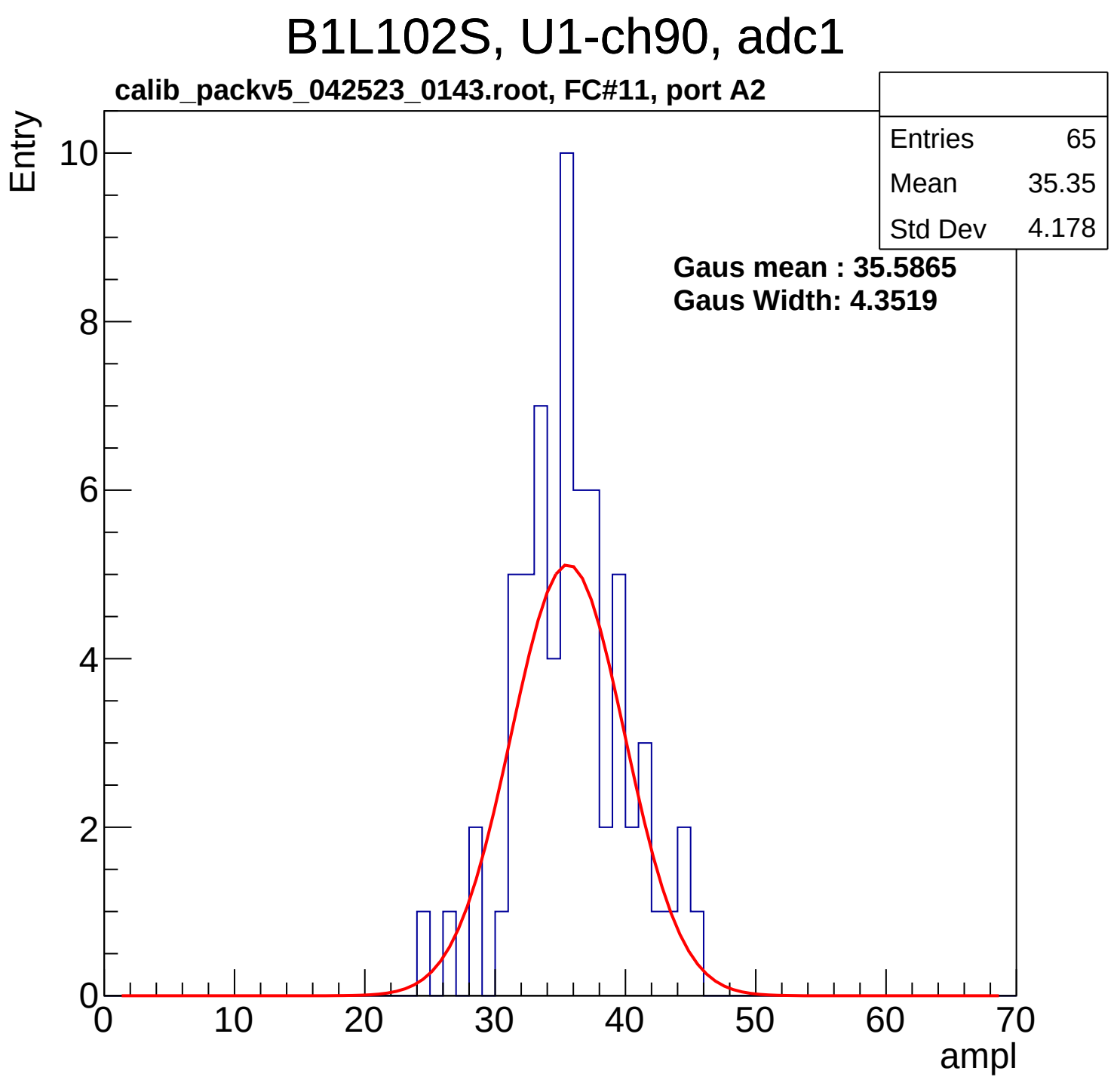
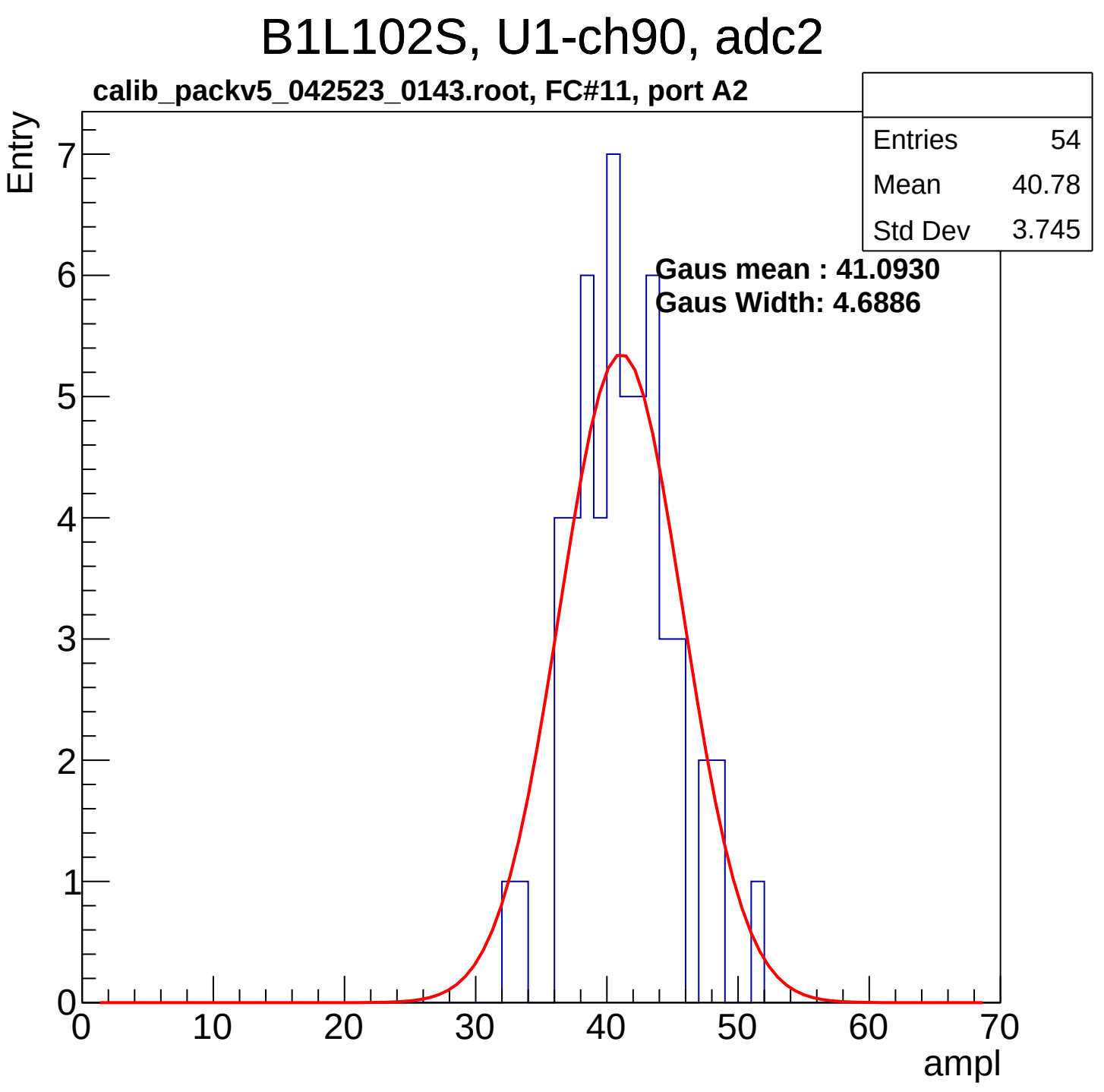
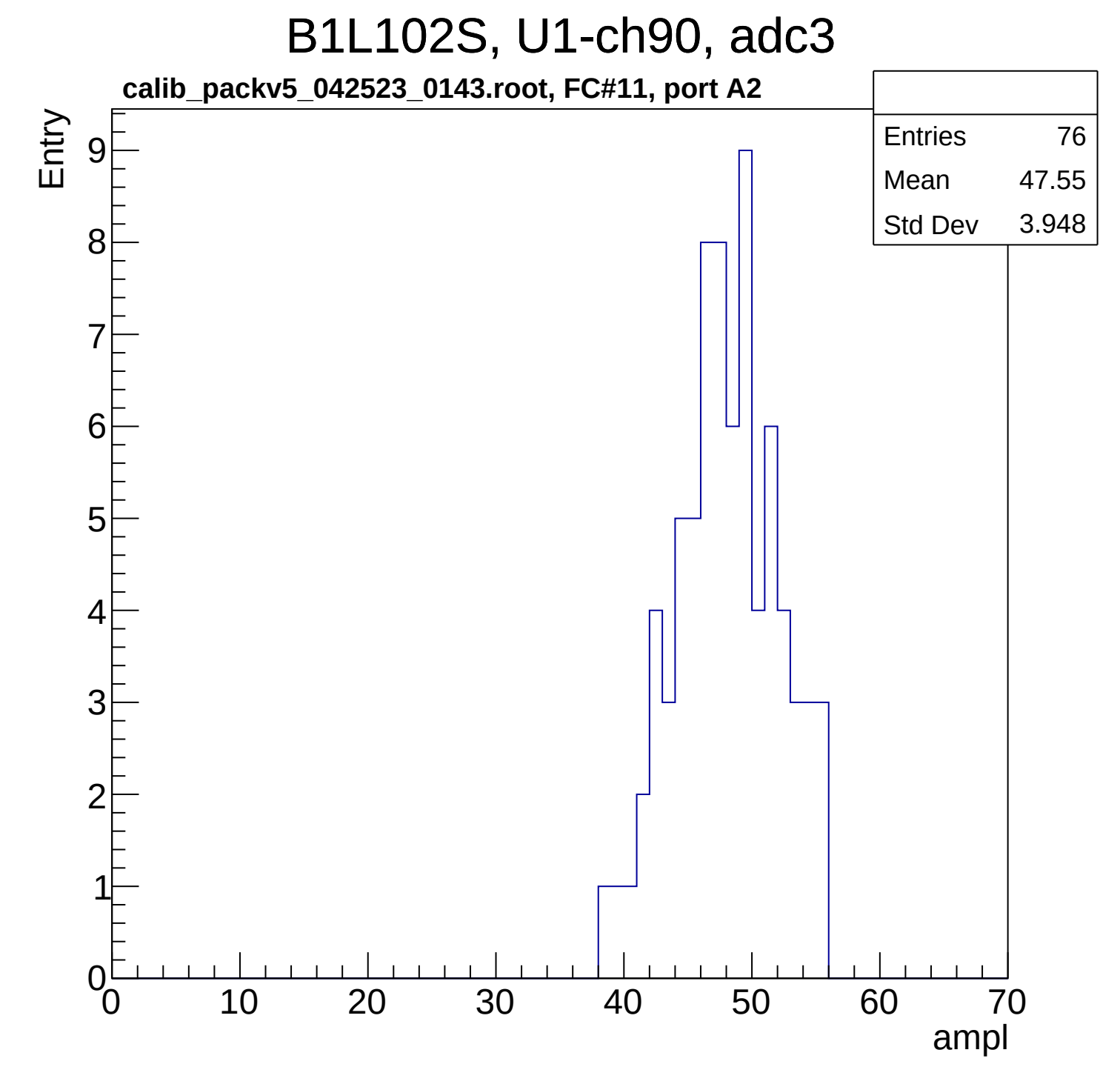
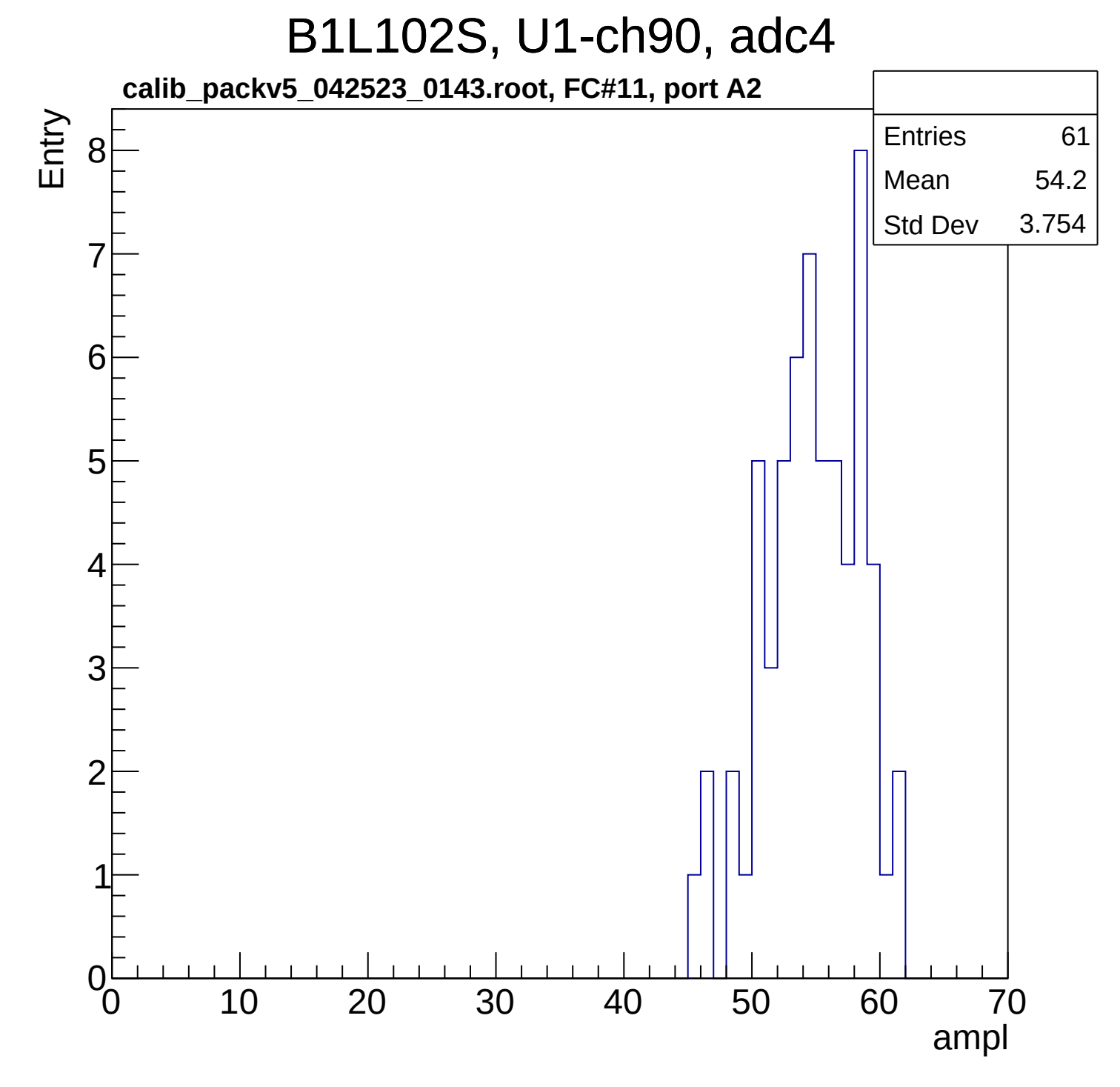
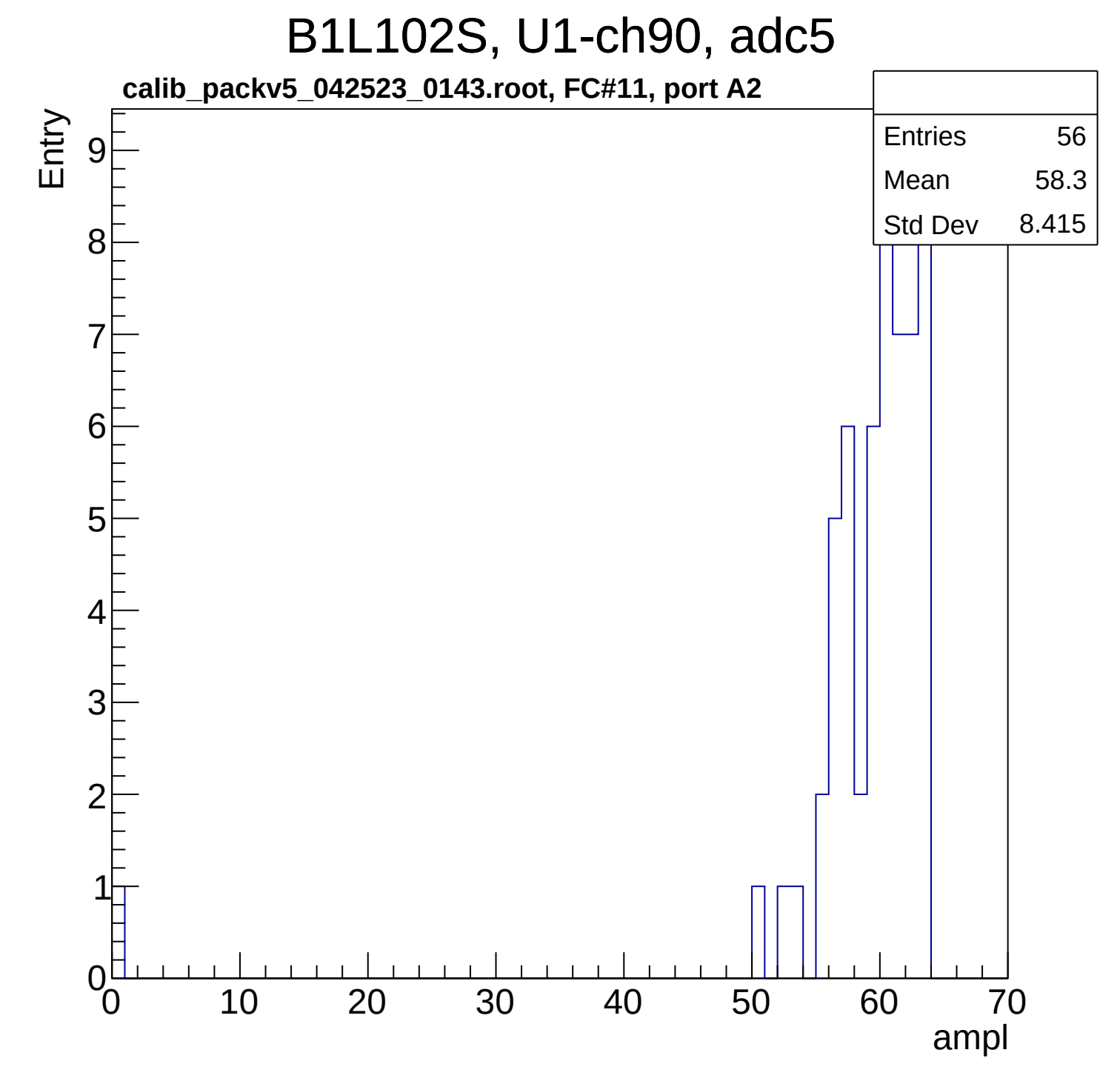
60
70


B1L102S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 35.35
Std Dev 4.178
Gaus mean : 35.5865
Gaus Width: 4.3519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 40.78
Std Dev 3.745
Gaus mean : 41.0930
Gaus Width: 4.6886
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 47.55
Std Dev 3.948
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 54.2
Std Dev 3.754
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 58.3
Std Dev 8.415
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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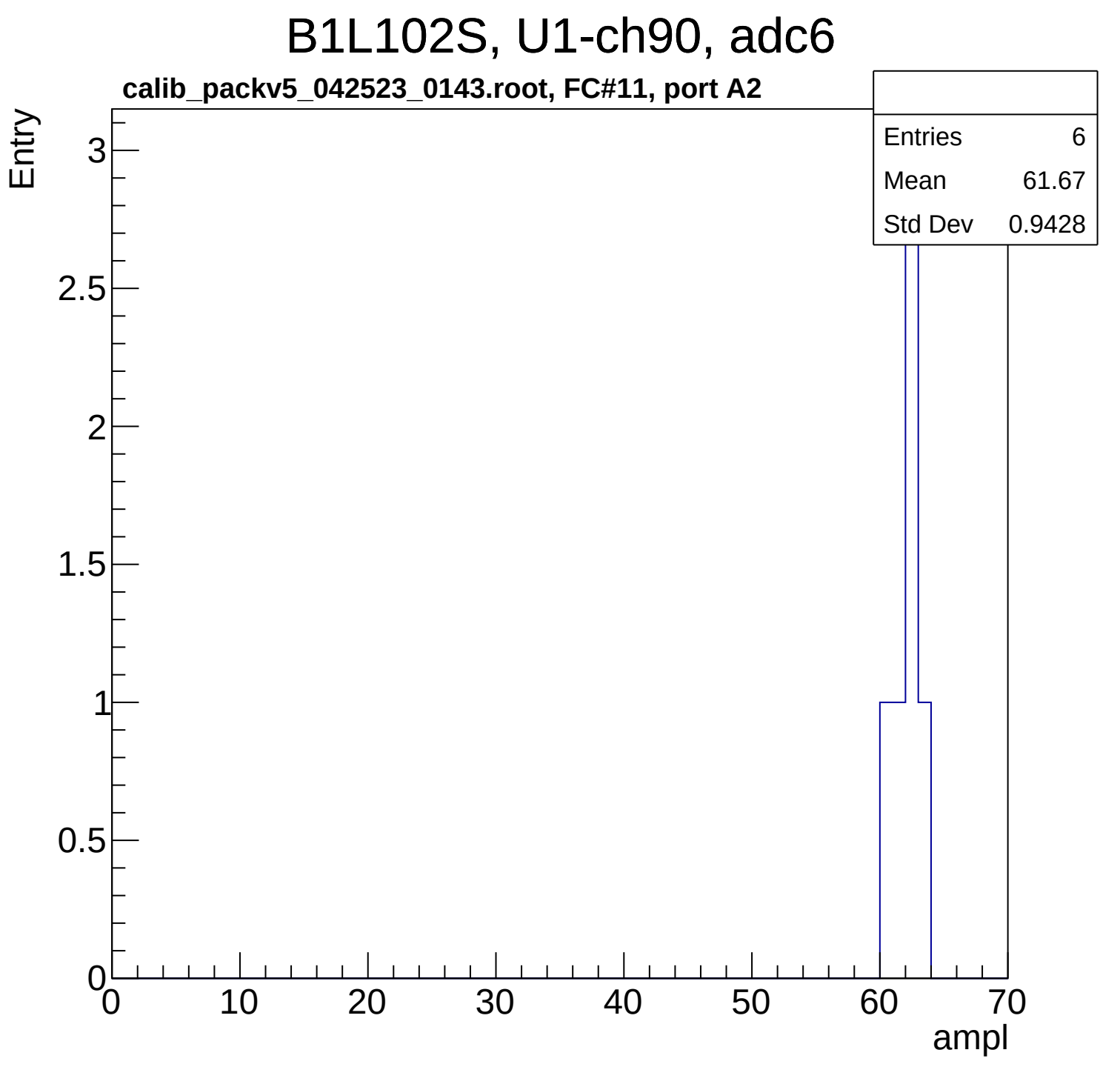
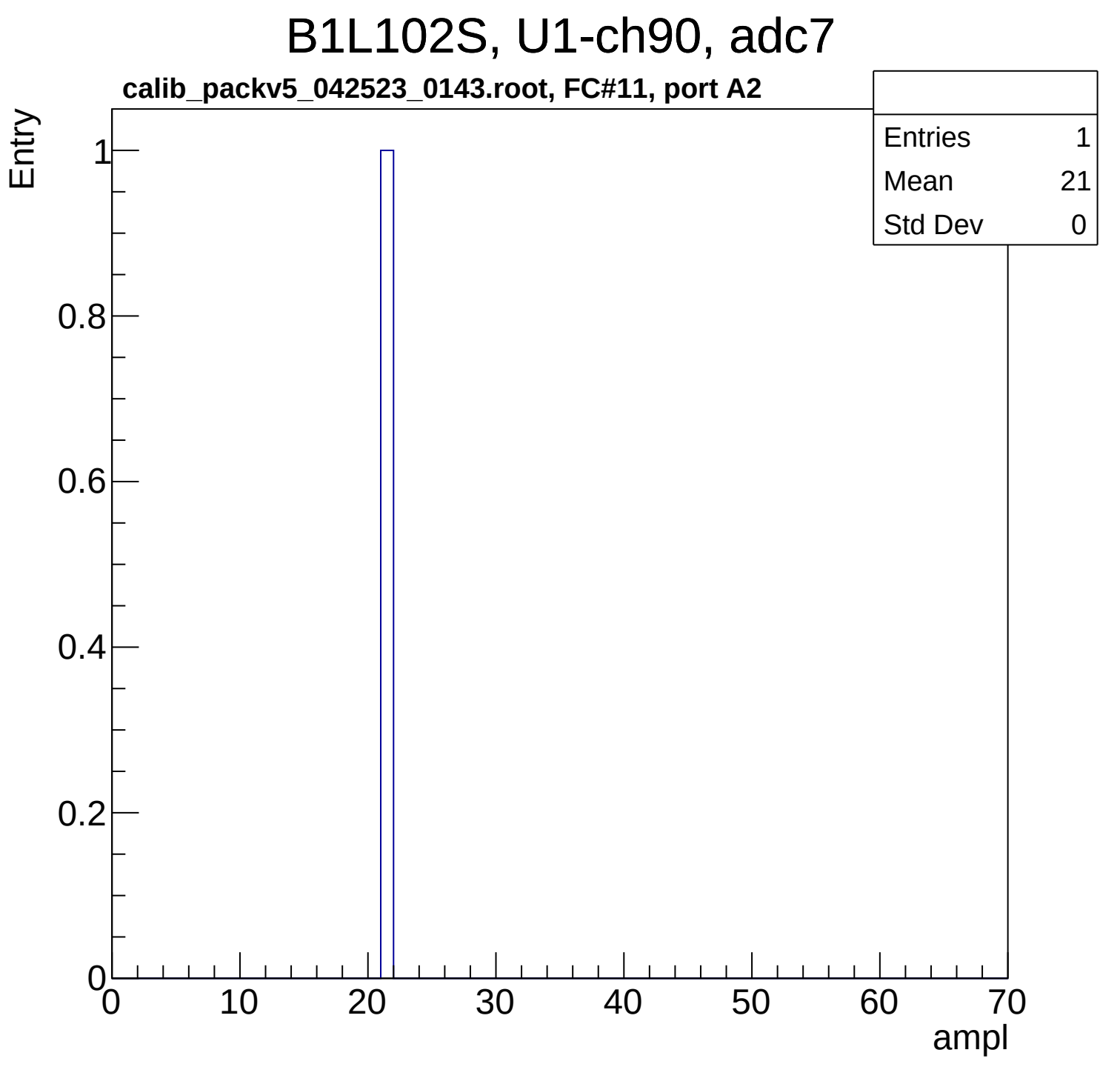
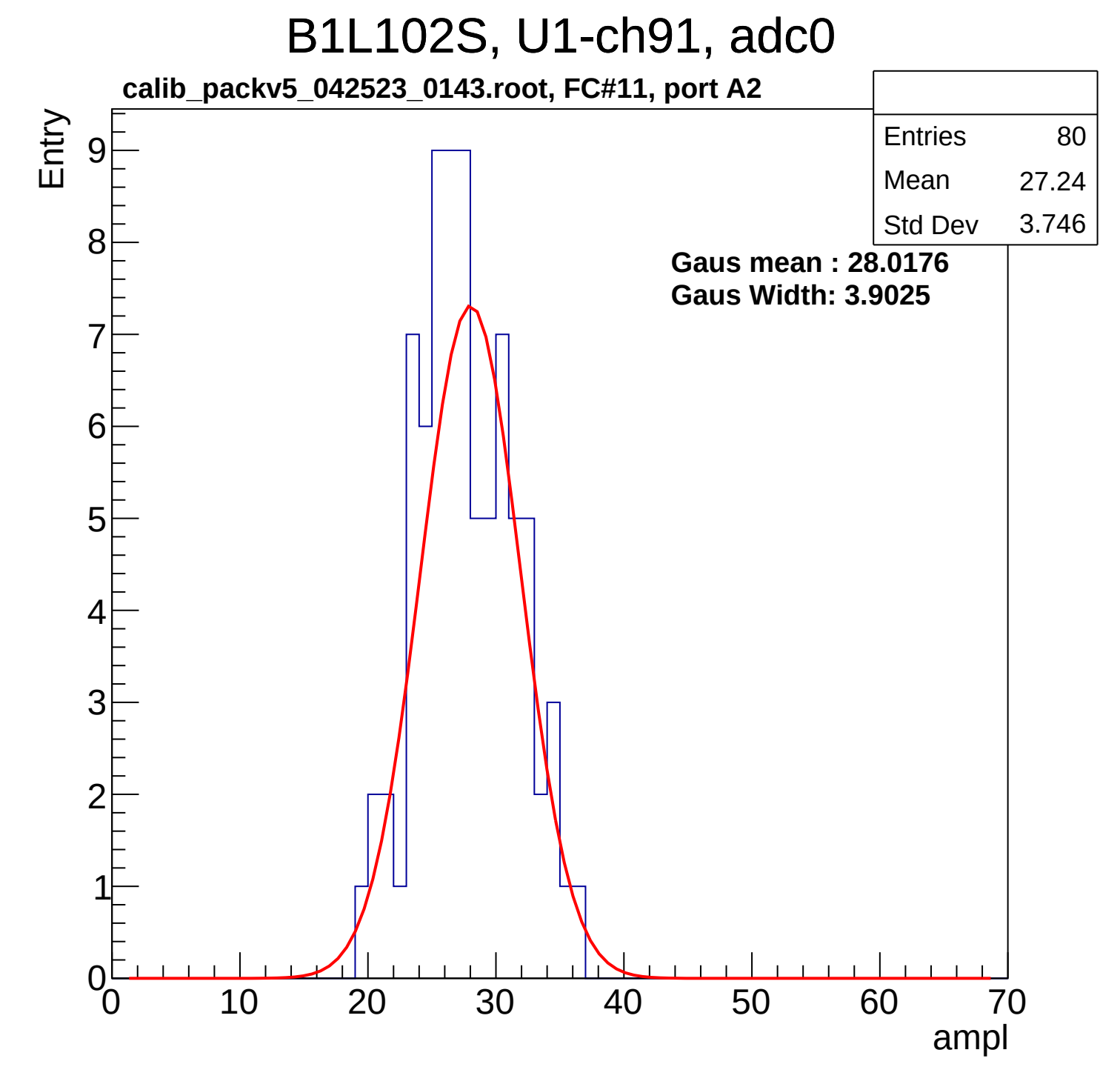
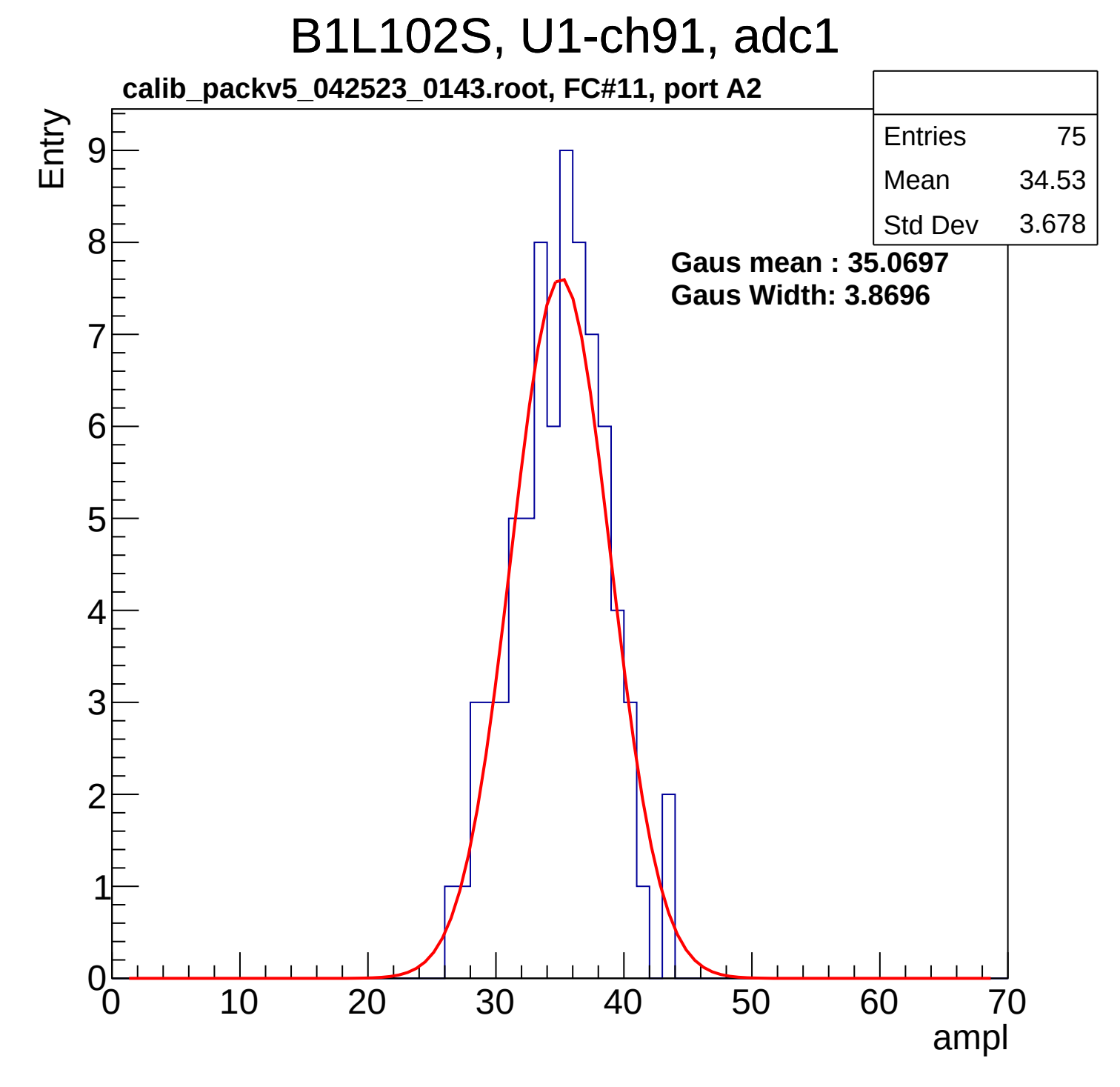
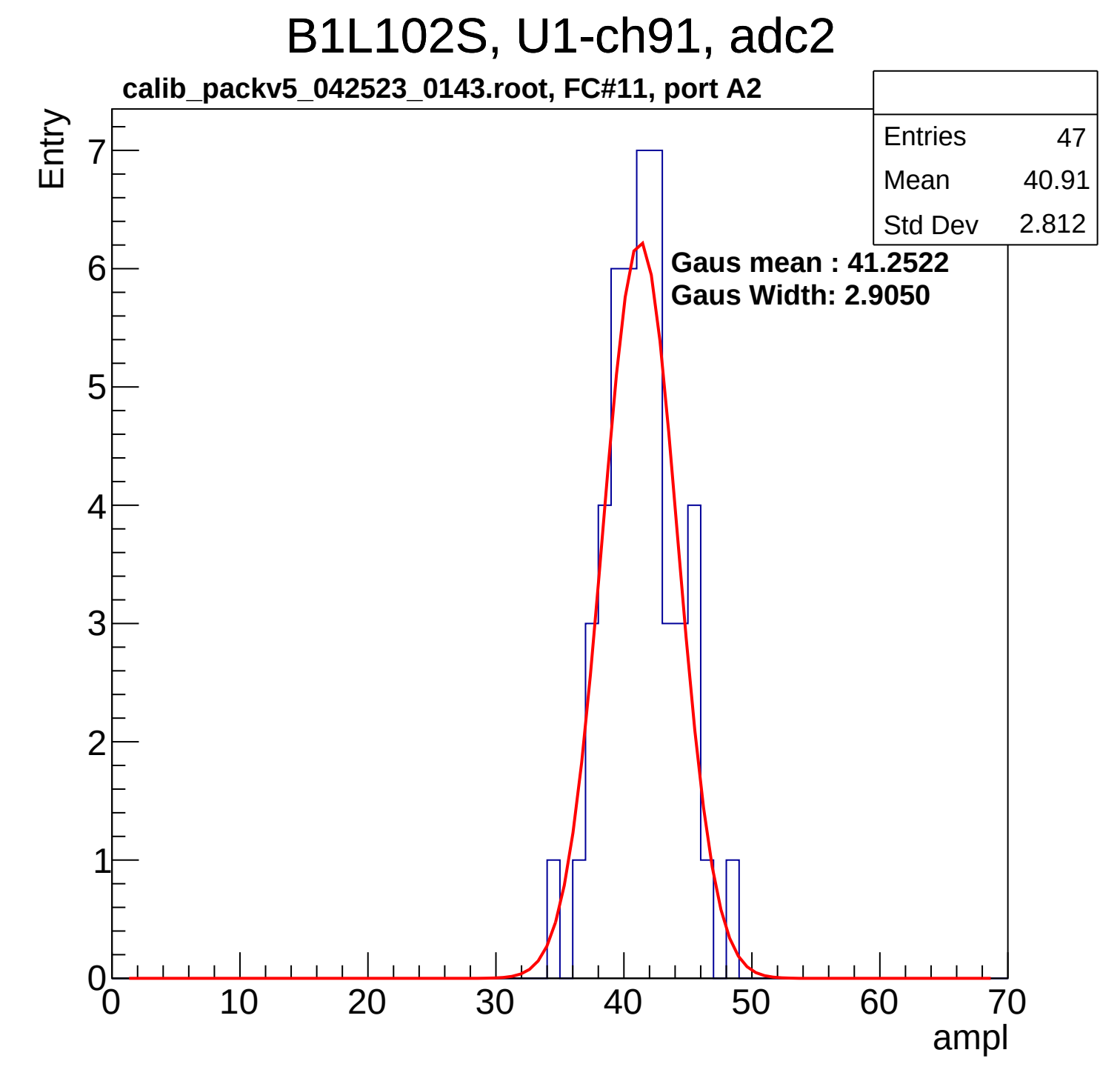
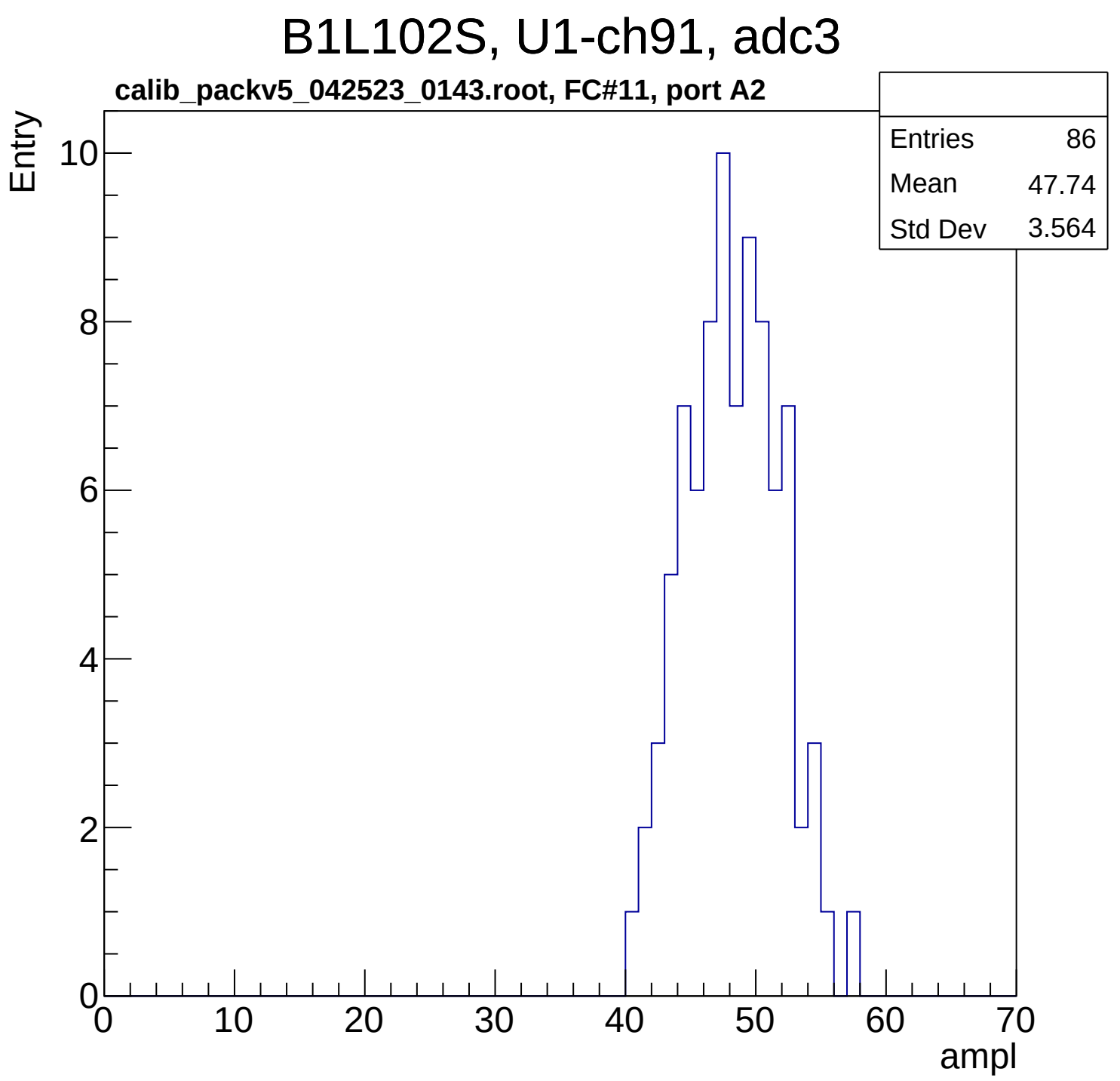

B1L102S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

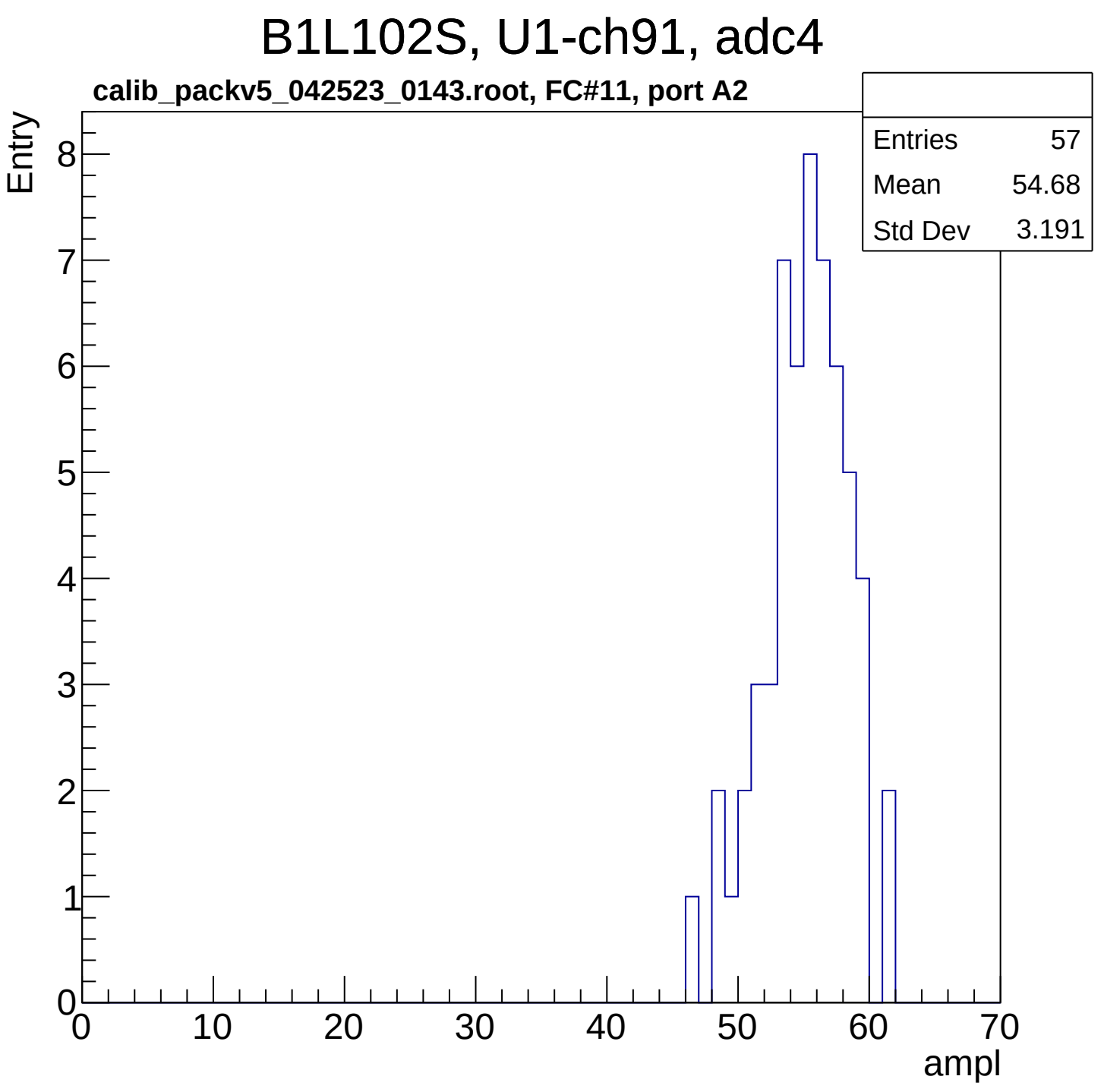

B1L102S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

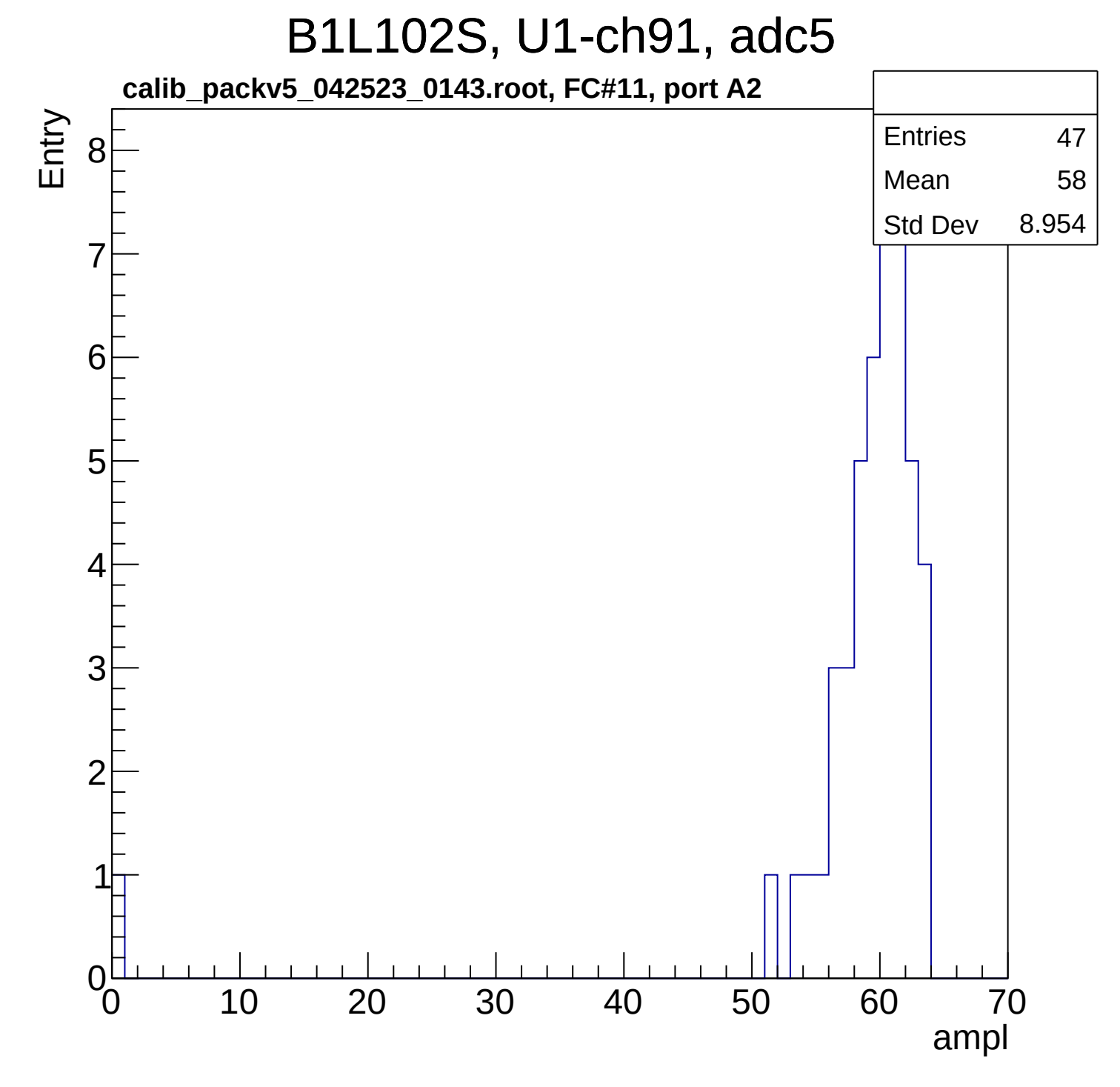

B1L102S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 80
Mean 27.24
Std Dev 3.746
Gaus mean : 28.0176
Gaus Width: 3.9025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 34.53
Std Dev 3.678
Gaus mean : 35.0697
Gaus Width: 3.8696
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 40.91
Std Dev 2.812
Gaus mean : 41.2522
Gaus Width: 2.9050
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 86
Mean 47.74
Std Dev 3.564
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 54.68
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 58
Std Dev 8.954
Entry
ampl
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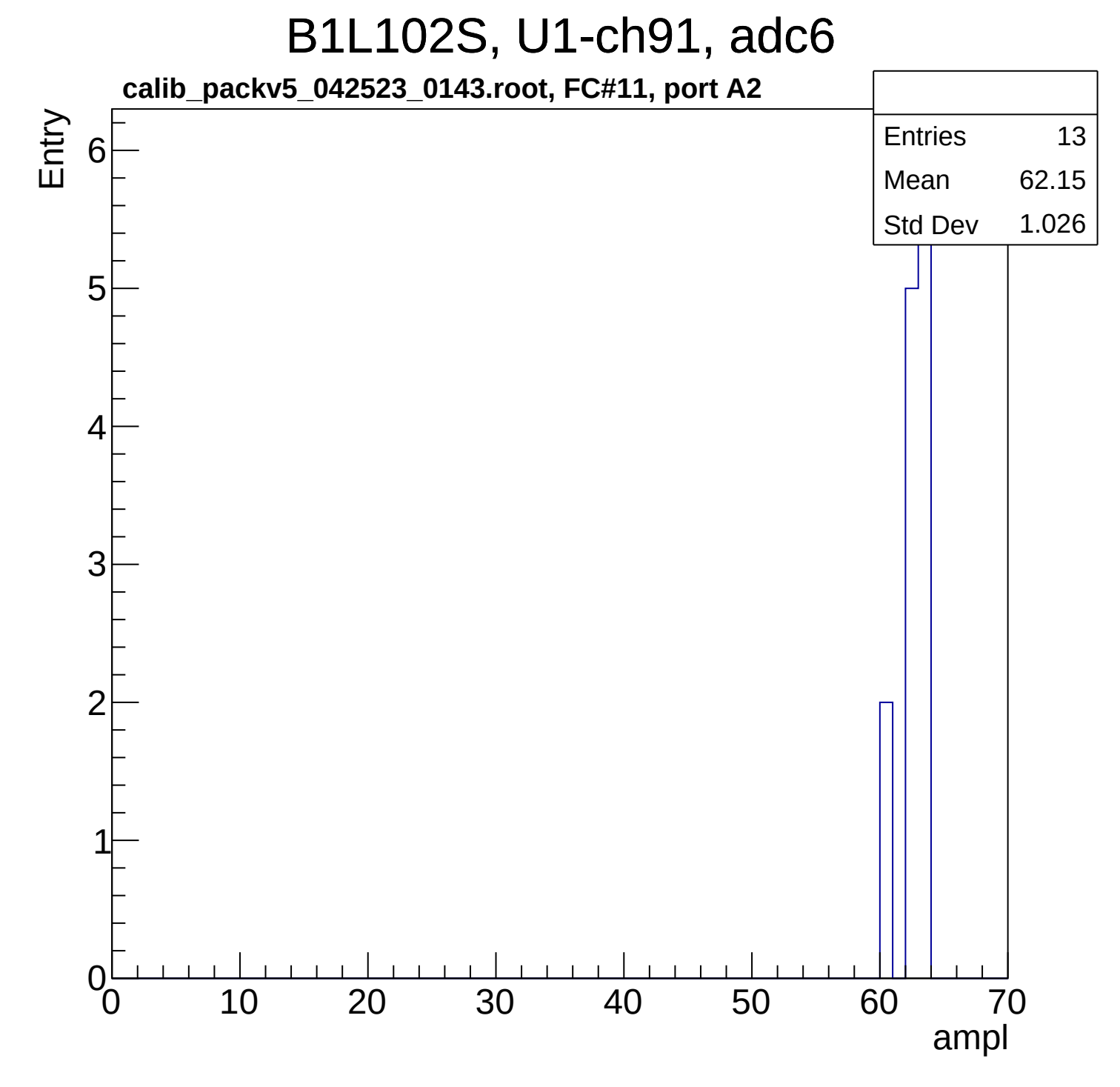
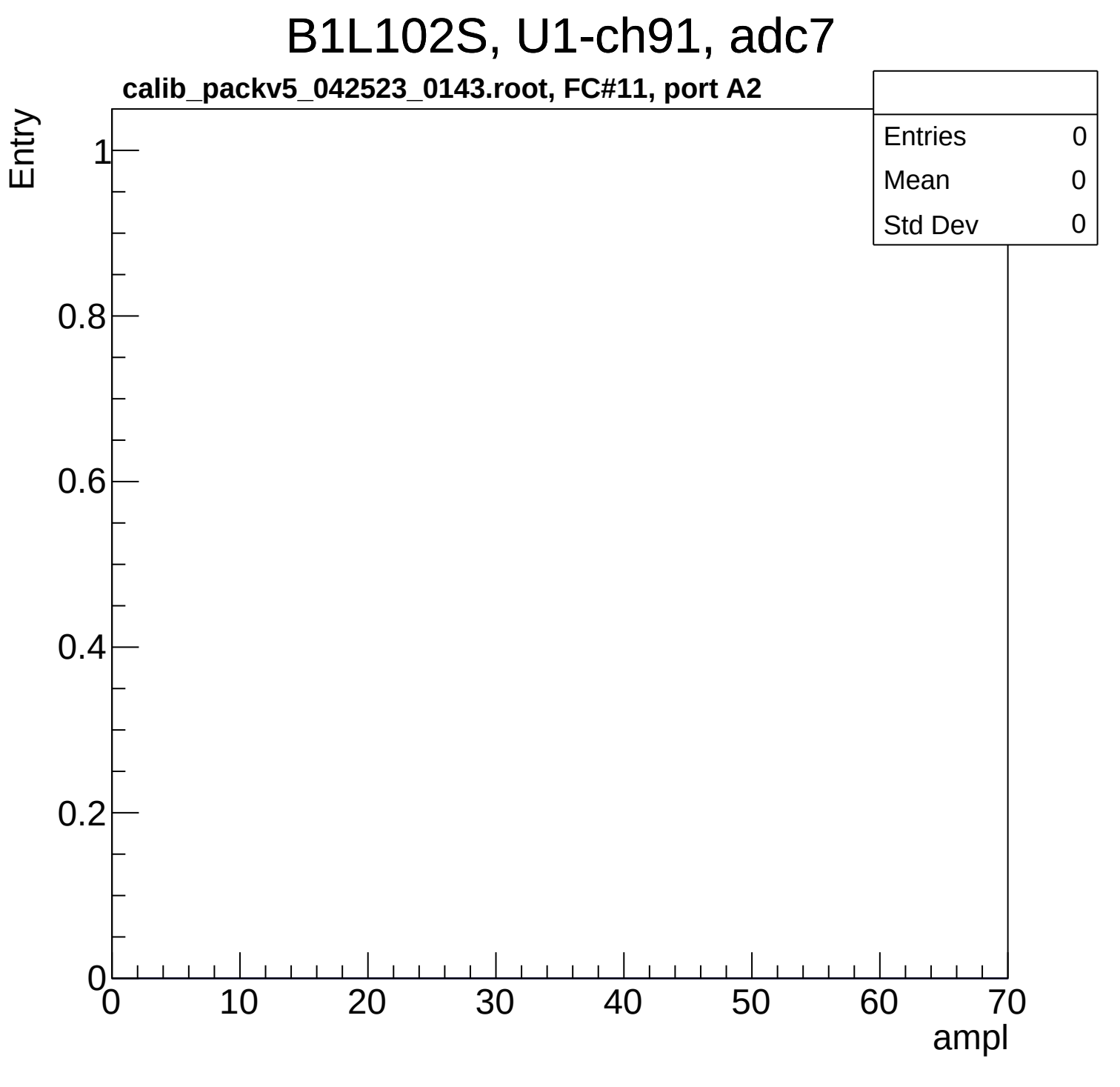
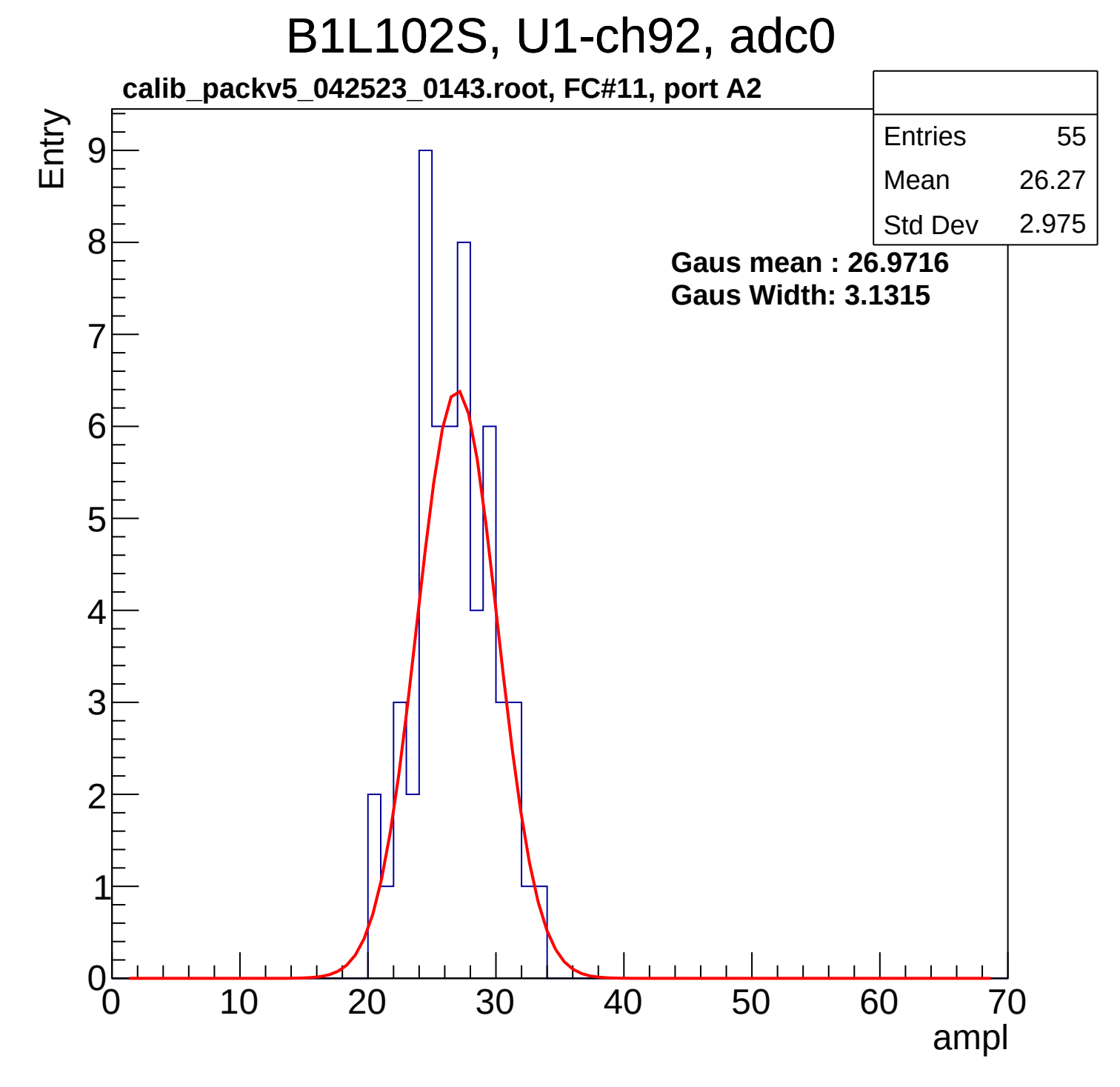
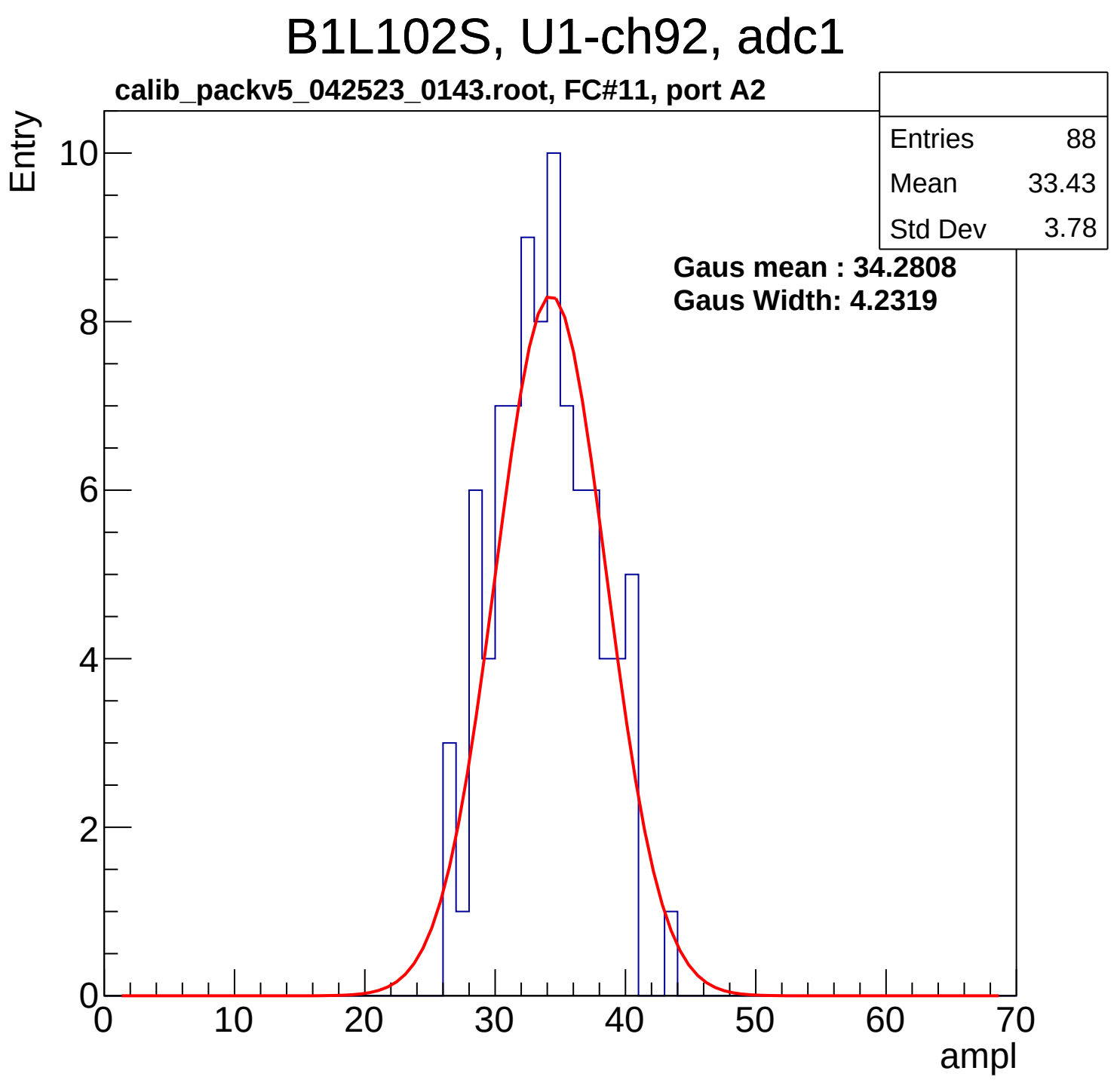
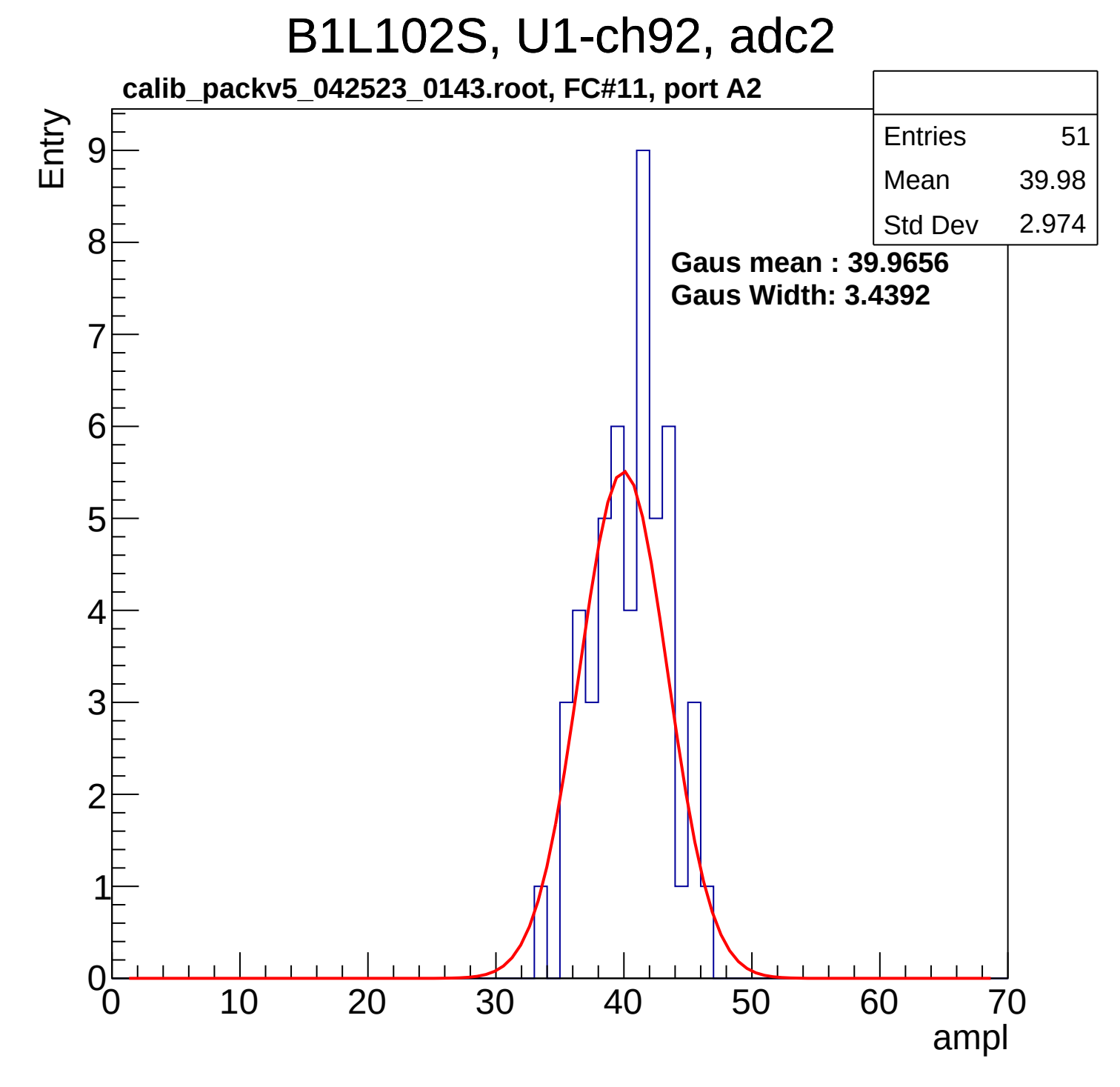
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

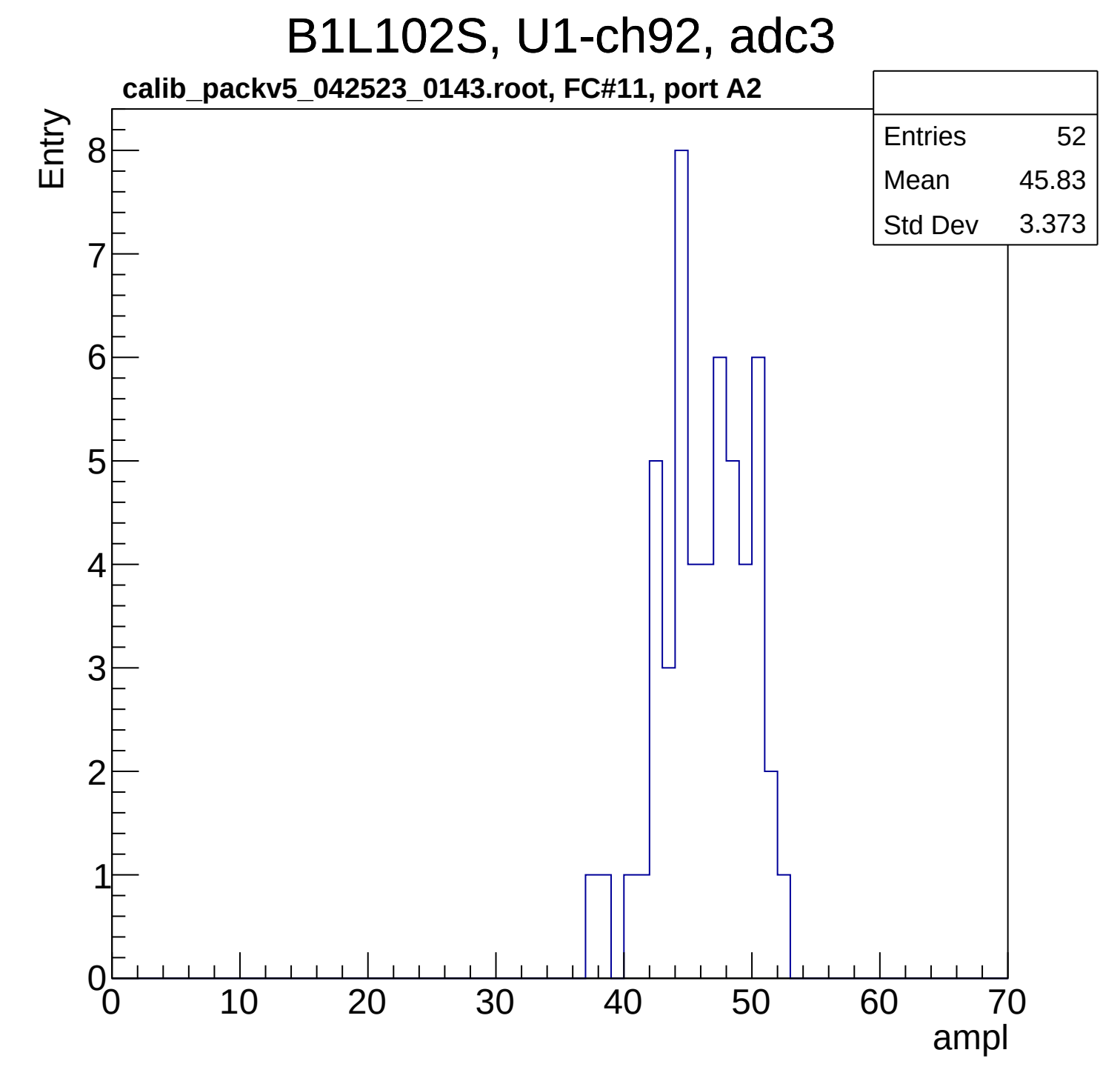

B1L102S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 62.15
Std Dev 1.026
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 26.27
Std Dev 2.975
Gaus mean : 26.9716
Gaus Width: 3.1315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 33.43
Std Dev 3.78
Gaus mean : 34.2808
Gaus Width: 4.2319
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 39.98
Std Dev 2.974
Gaus mean : 39.9656
Gaus Width: 3.4392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 45.83
Std Dev 3.373
Entry
ampl
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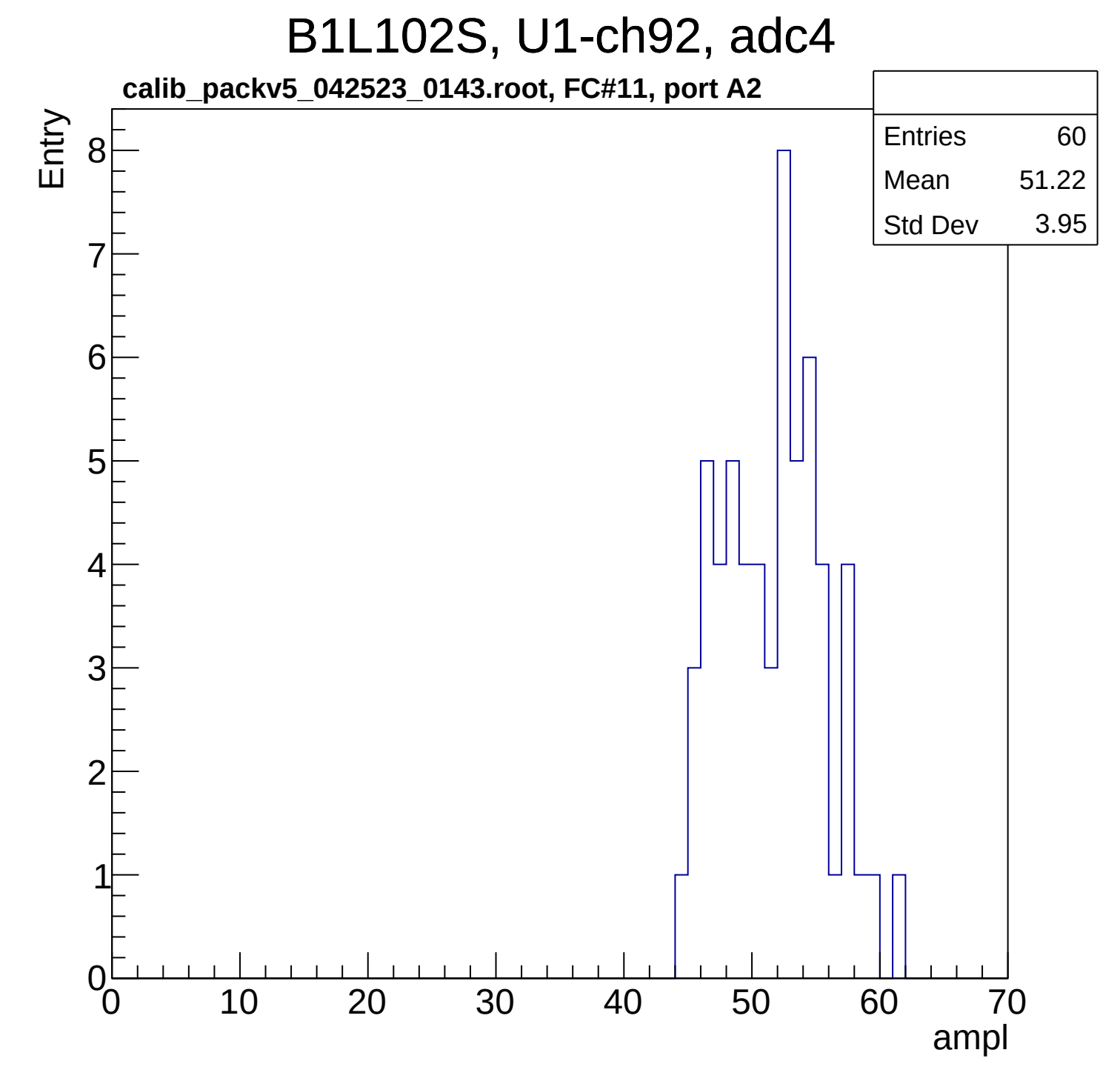
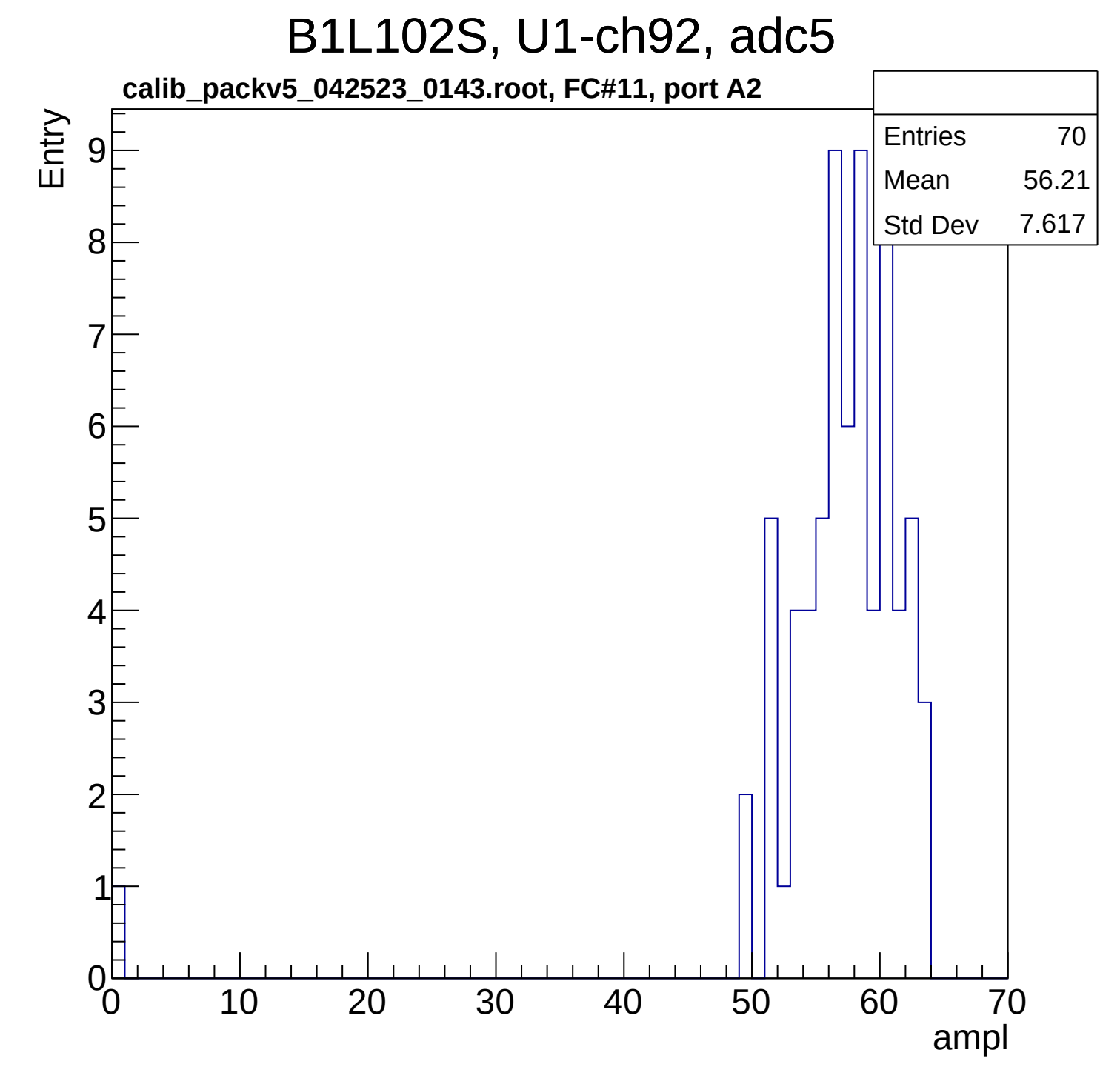
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 51.22
Std Dev 3.95
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 56.21
Std Dev 7.617
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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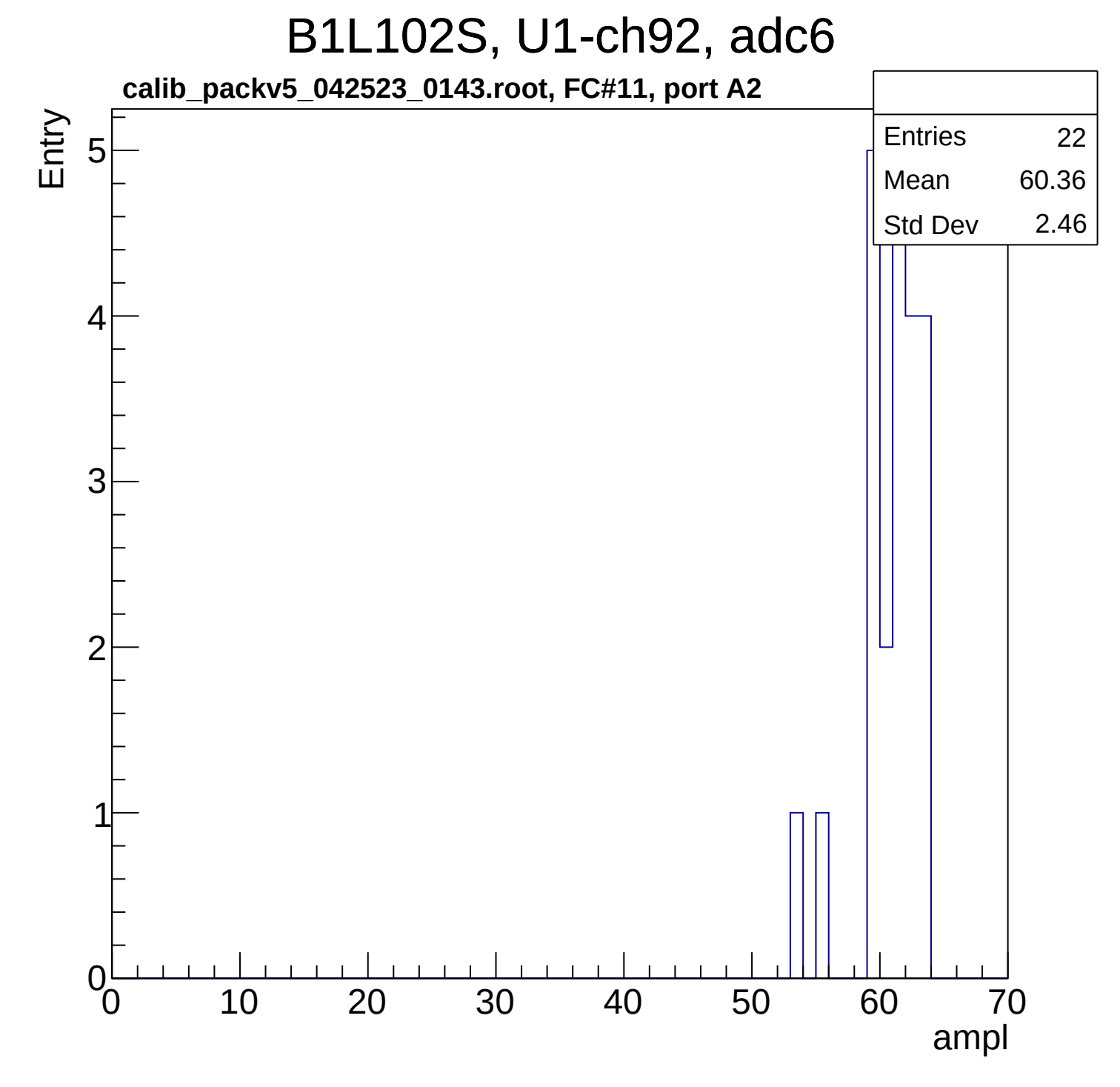
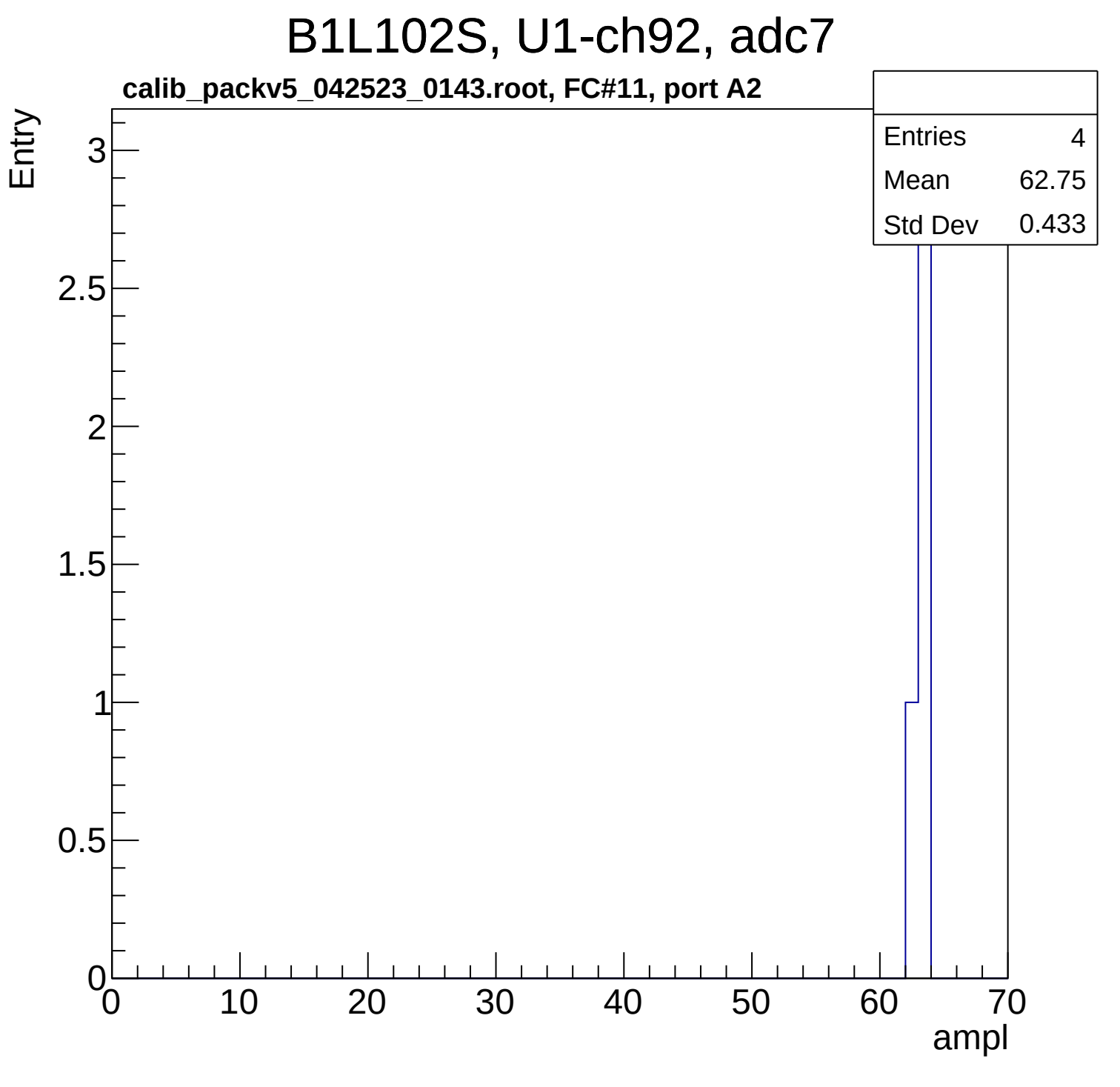
40
50
60
70


B1L102S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 22
Mean 60.36
Std Dev 2.46
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L102S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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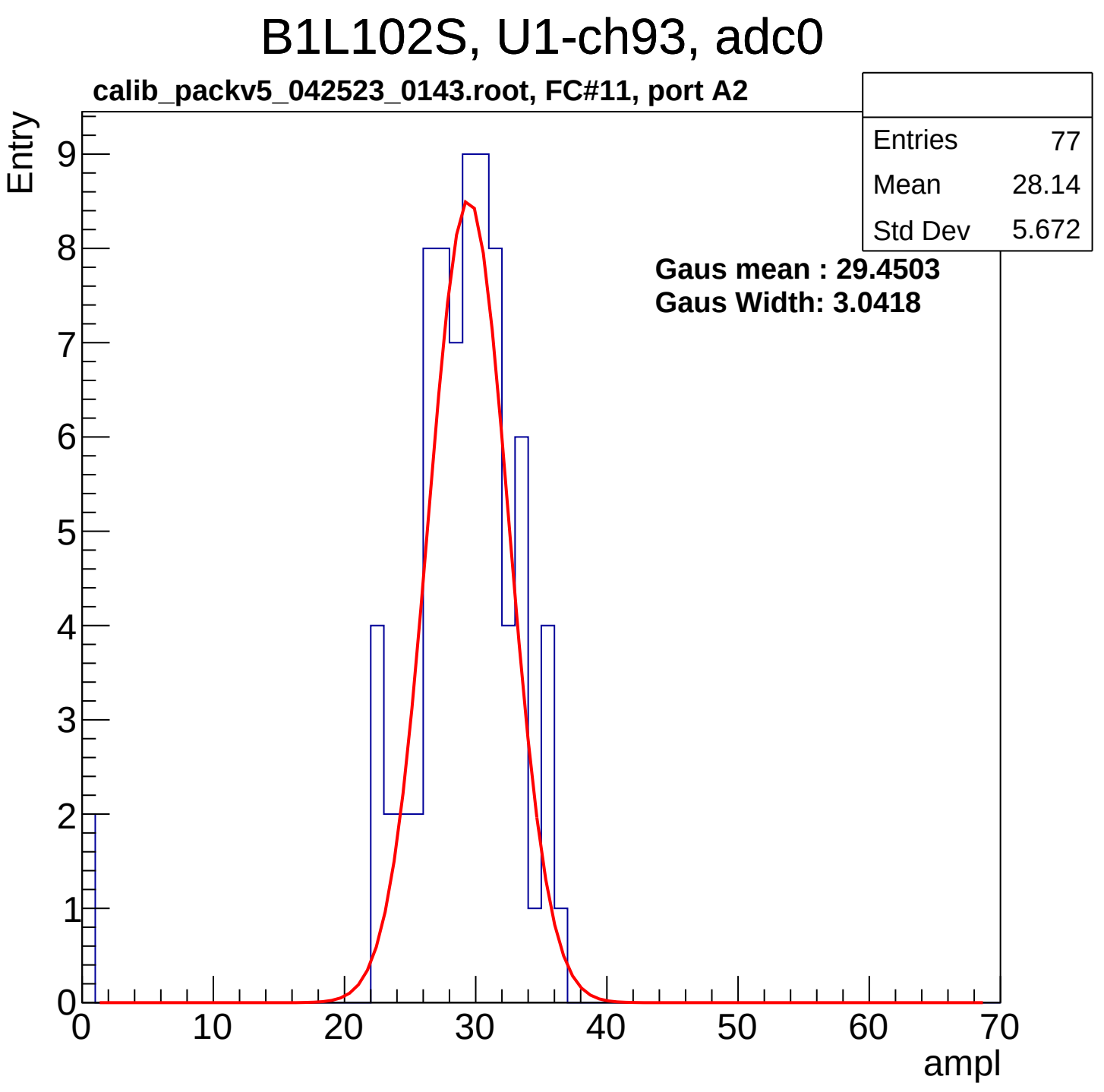
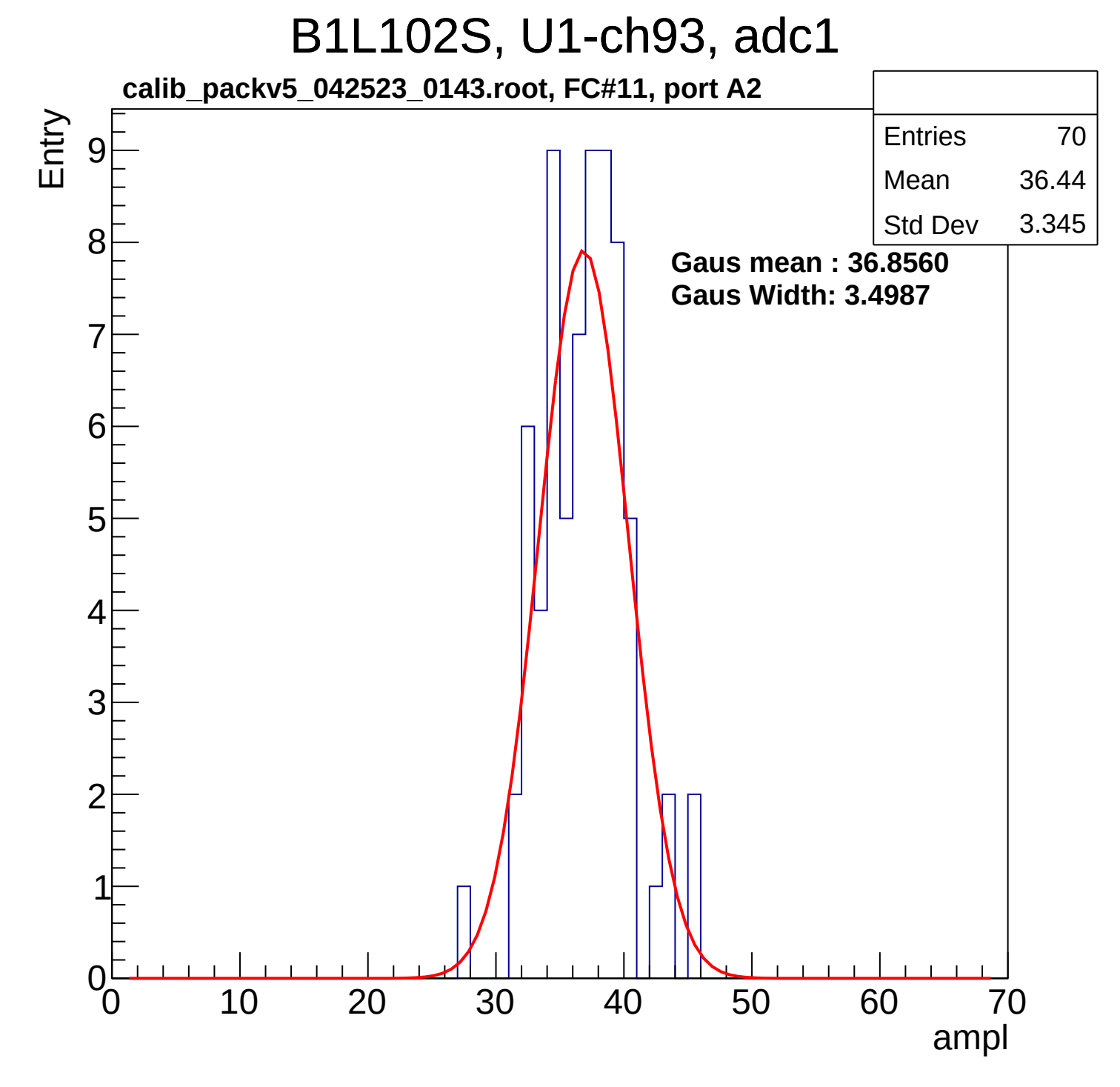
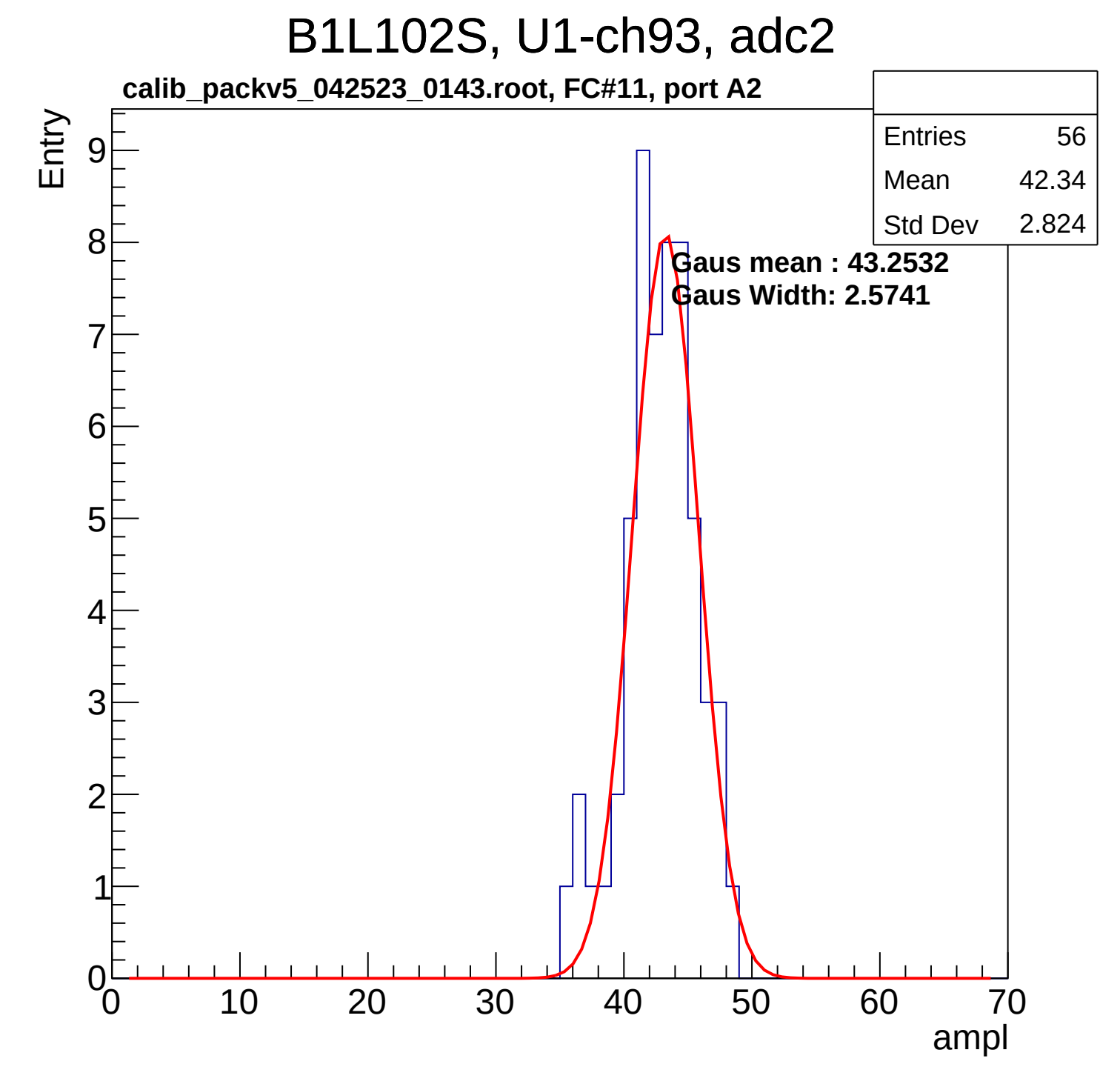
40
50
60
70


B1L102S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 28.14
Std Dev 5.672
Gaus mean : 29.4503
Gaus Width: 3.0418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 36.44
Std Dev 3.345
Gaus mean : 36.8560
Gaus Width: 3.4987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 42.34
Std Dev 2.824
Gaus mean : 43.2532
Gaus Width: 2.5741
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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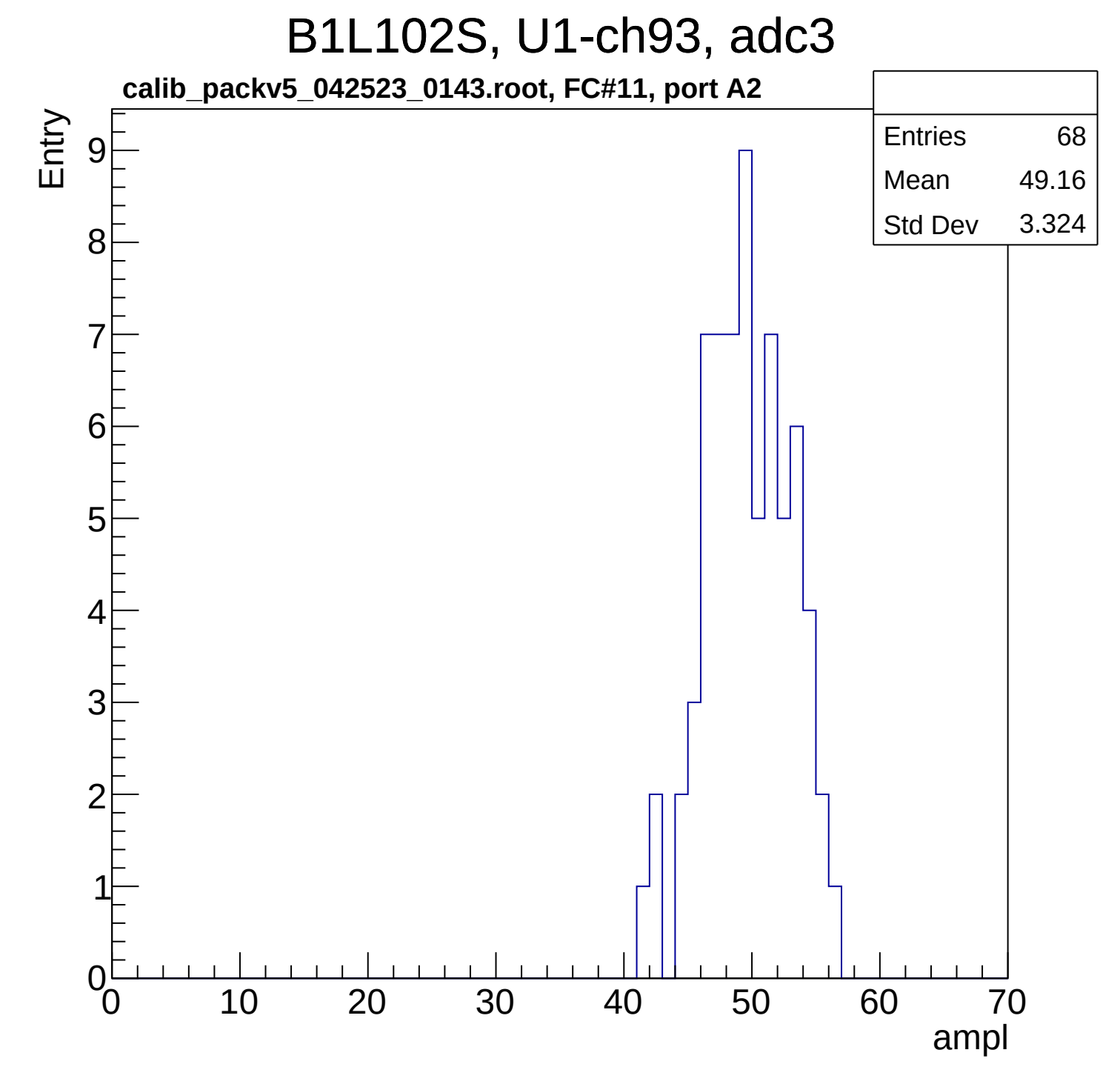
B1L102S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 49.16
Std Dev 3.324
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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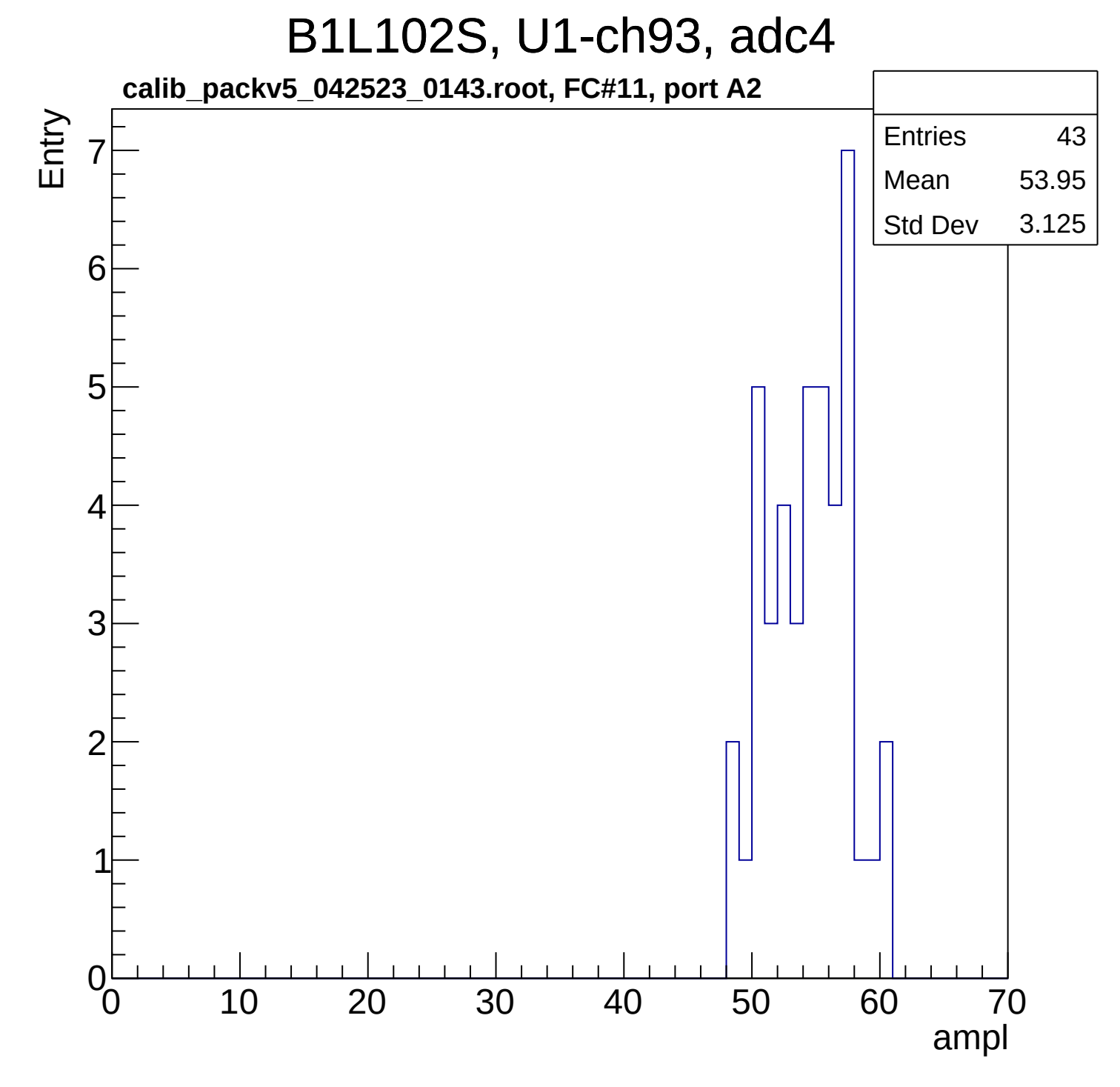
B1L102S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 53.95
Std Dev 3.125
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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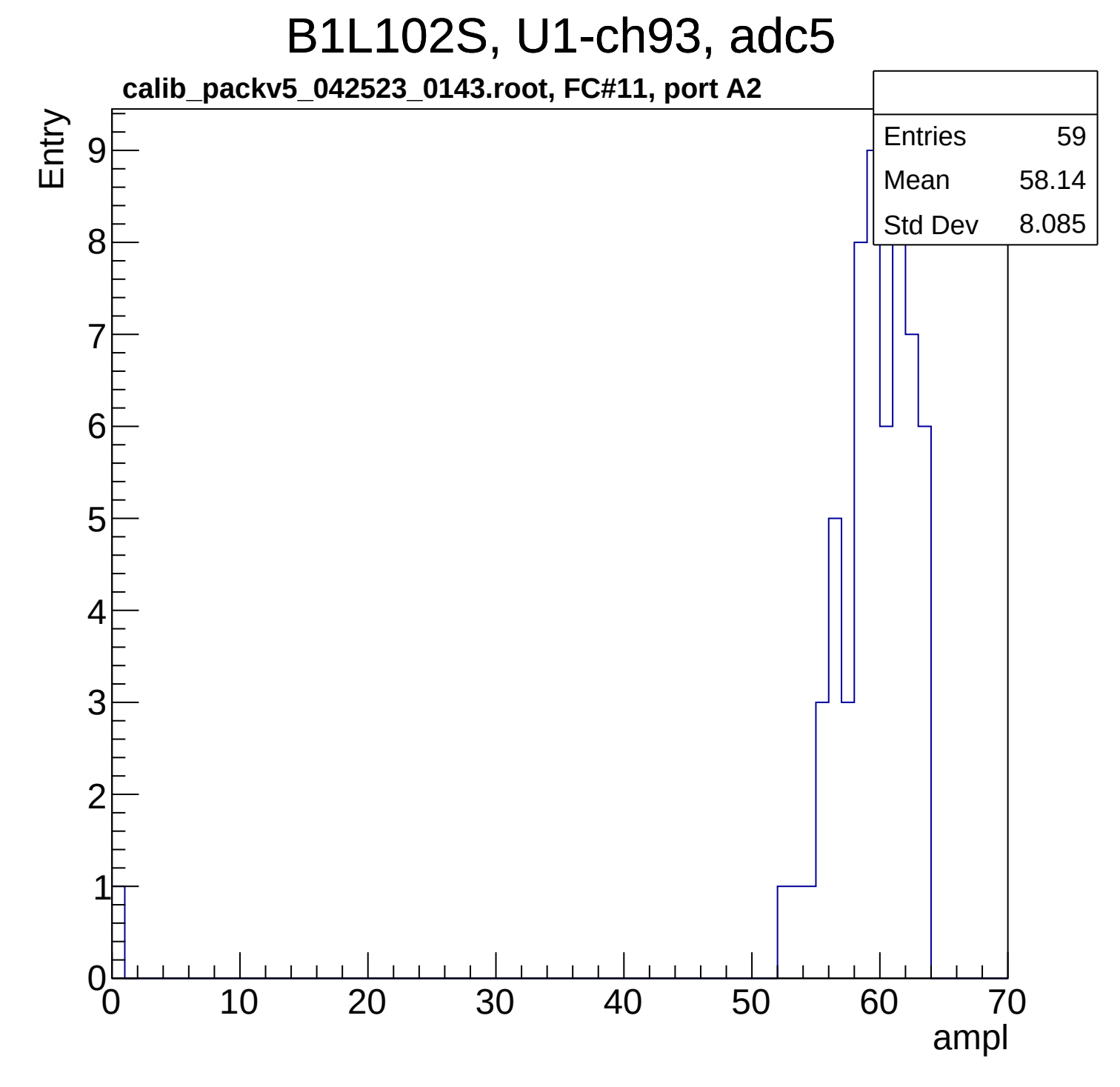
40
50
60
70


B1L102S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 58.14
Std Dev 8.085
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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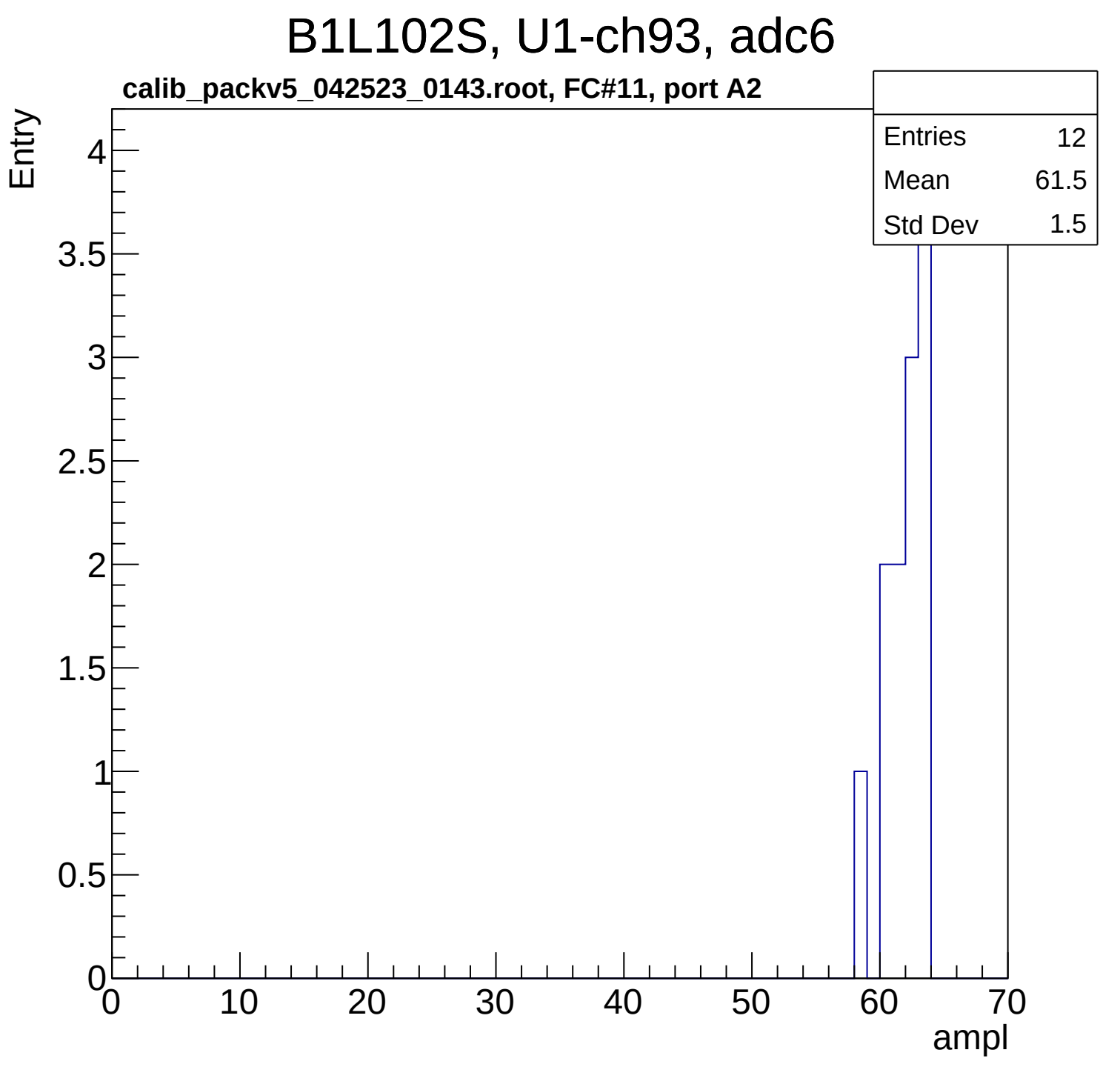
B1L102S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.5
Std Dev 1.5
Entry
ampl
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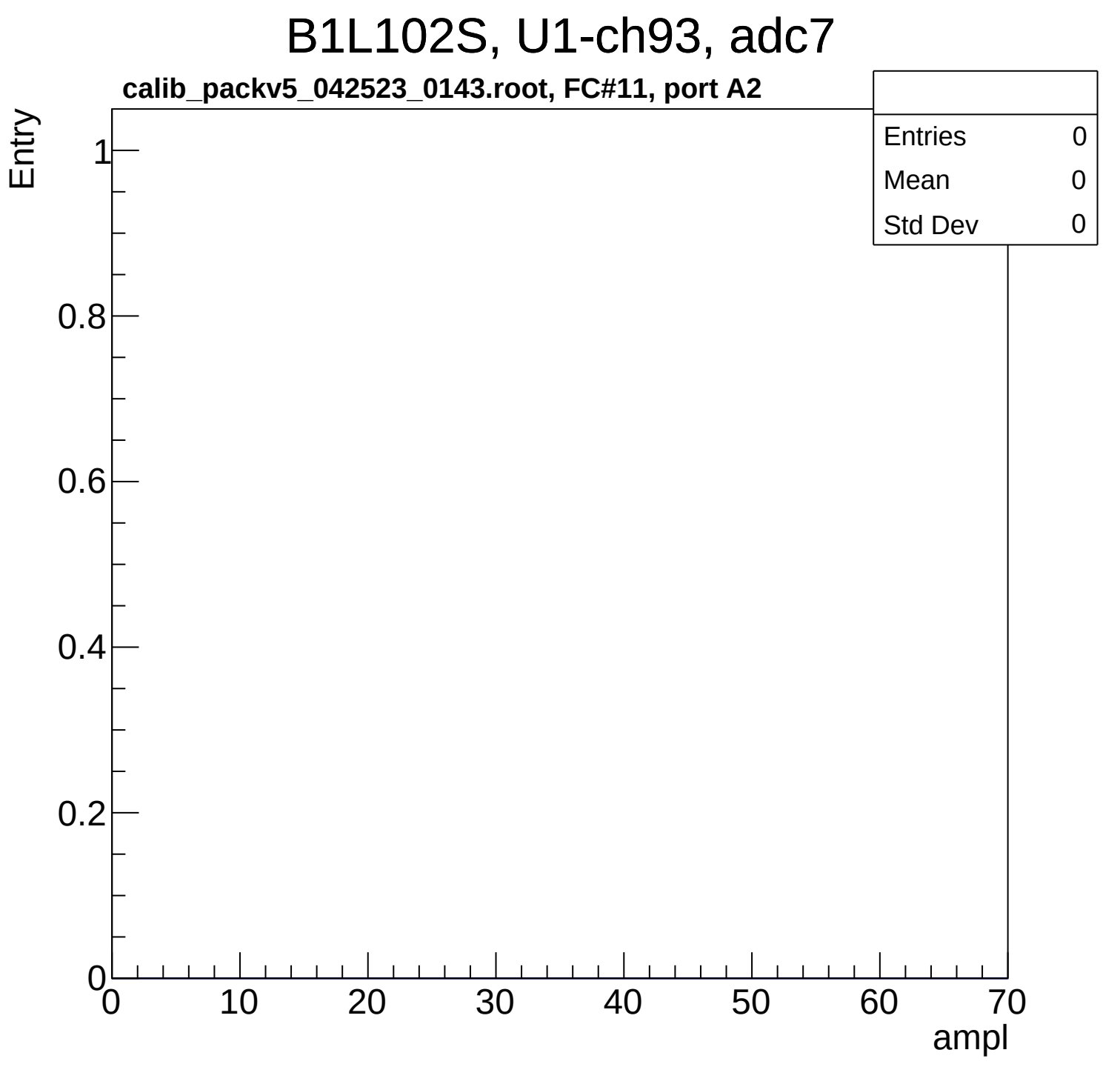
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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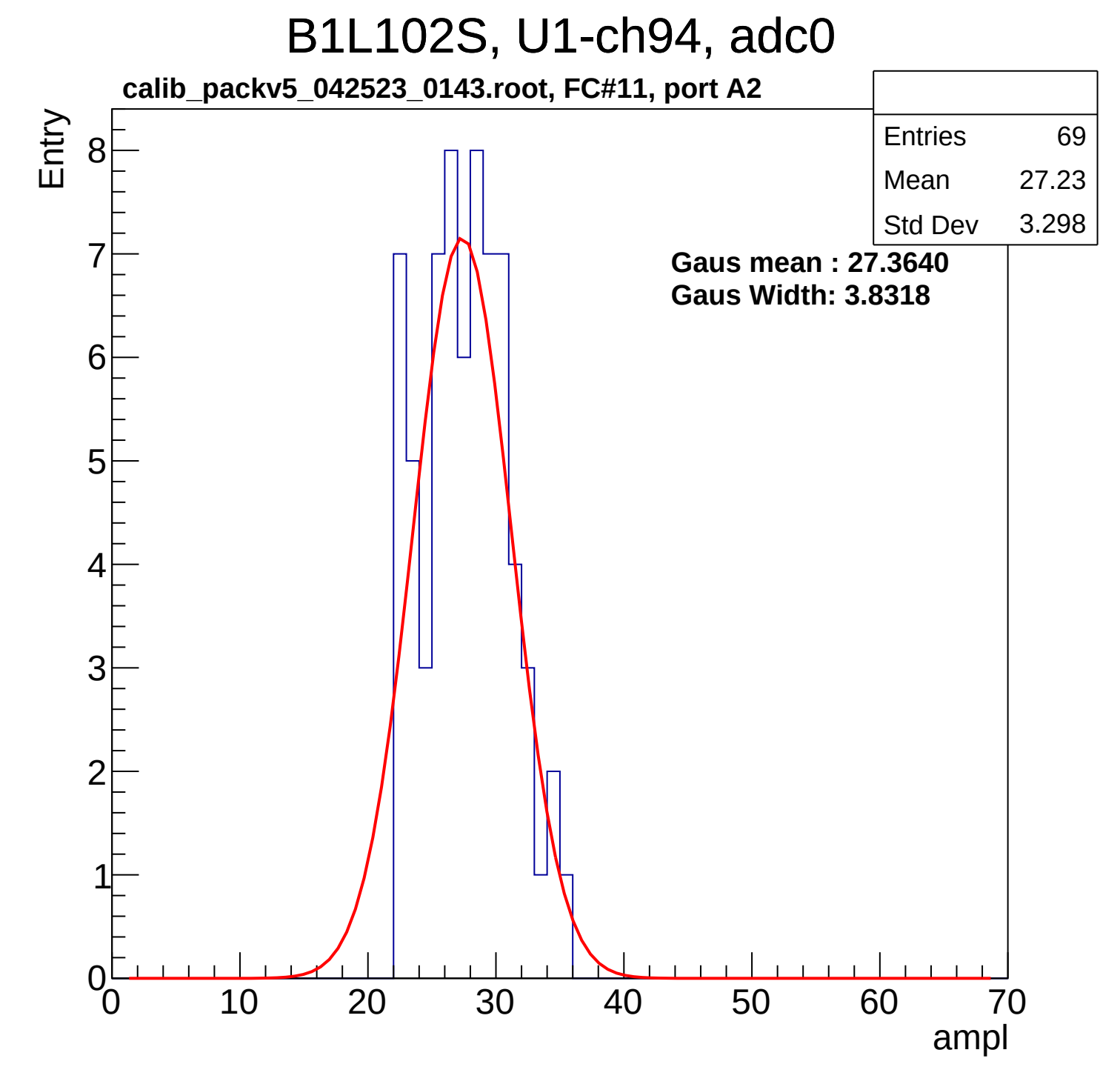
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 27.23
Std Dev 3.298
Gaus mean : 27.3640
Gaus Width: 3.8318
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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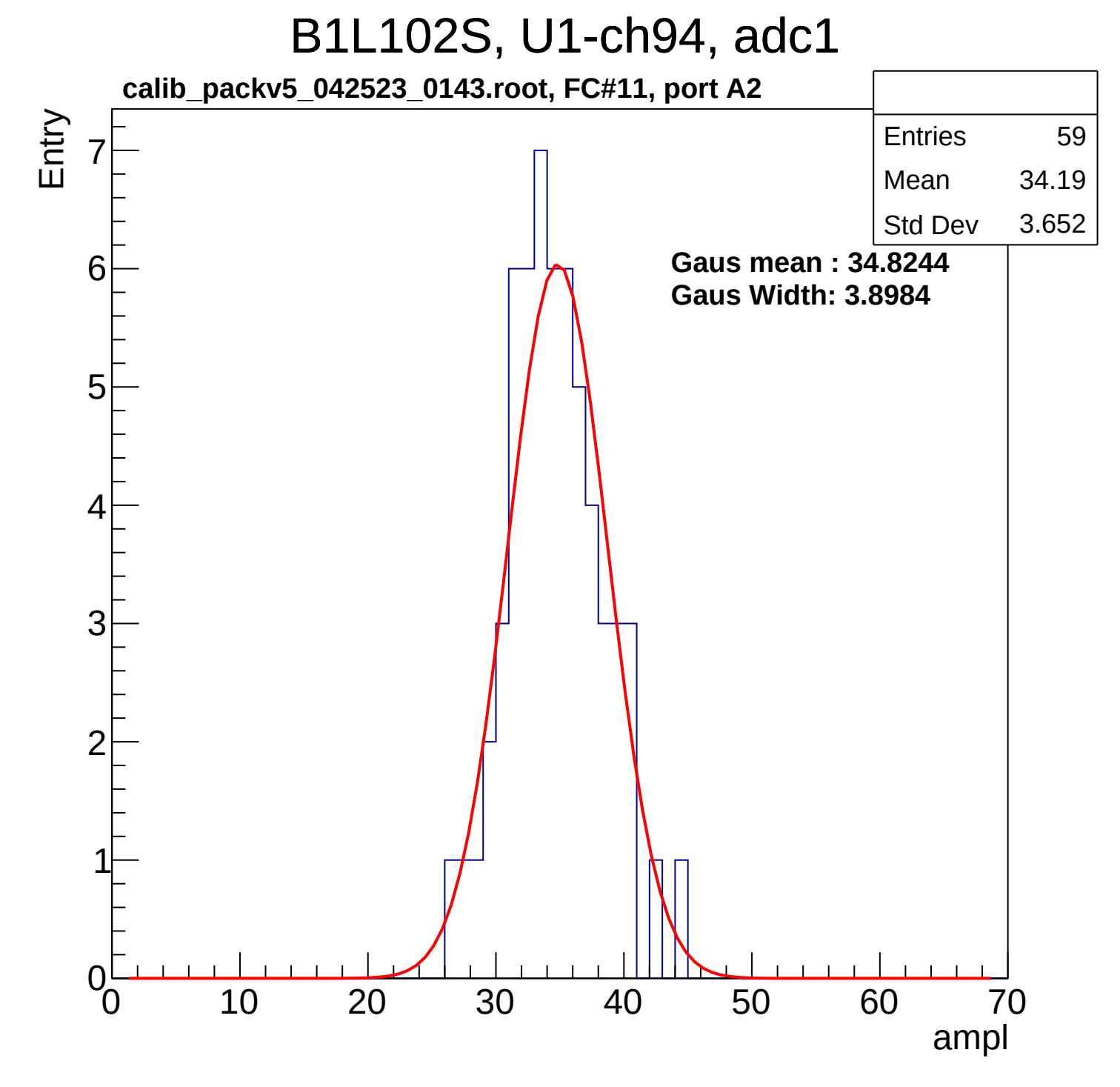
B1L102S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 34.19
Std Dev 3.652
Gaus mean : 34.8244
Gaus Width: 3.8984
Entry
ampl
0
1
2
3
4
5
6
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0
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30
40
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60
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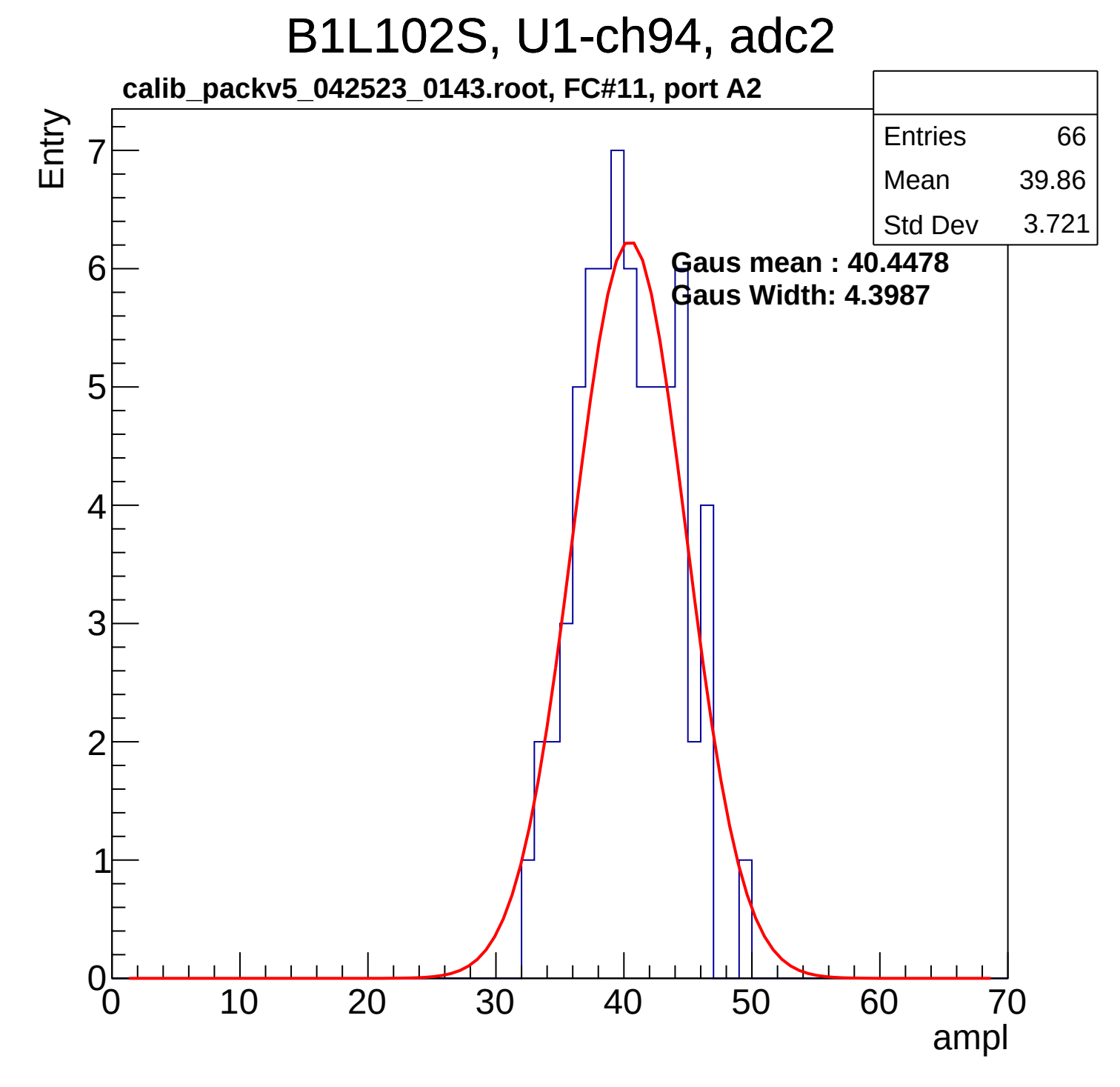
B1L102S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 39.86
Std Dev 3.721
Gaus mean : 40.4478
Gaus Width: 4.3987
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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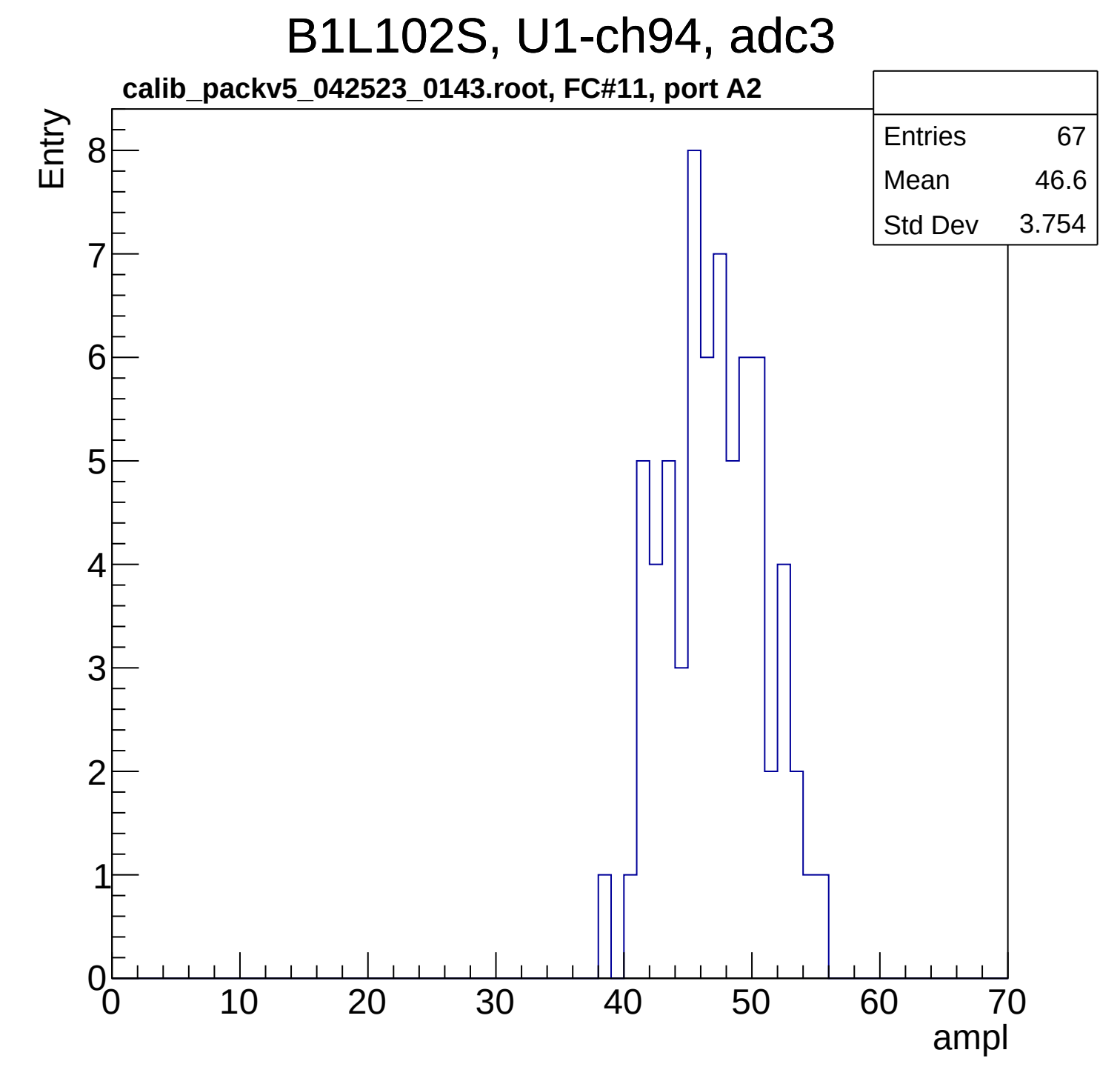
B1L102S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 46.6
Std Dev 3.754
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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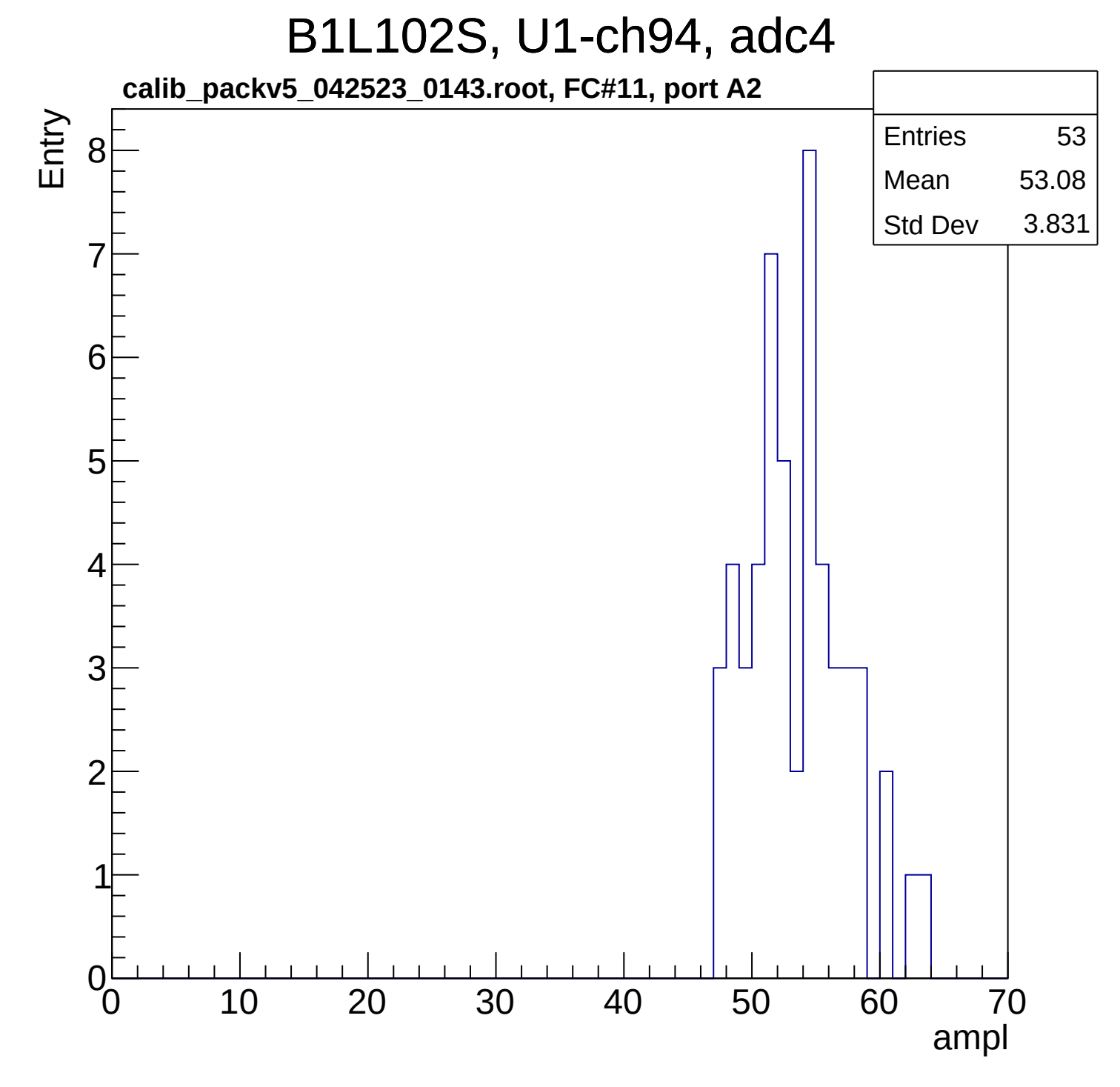
B1L102S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 53.08
Std Dev 3.831
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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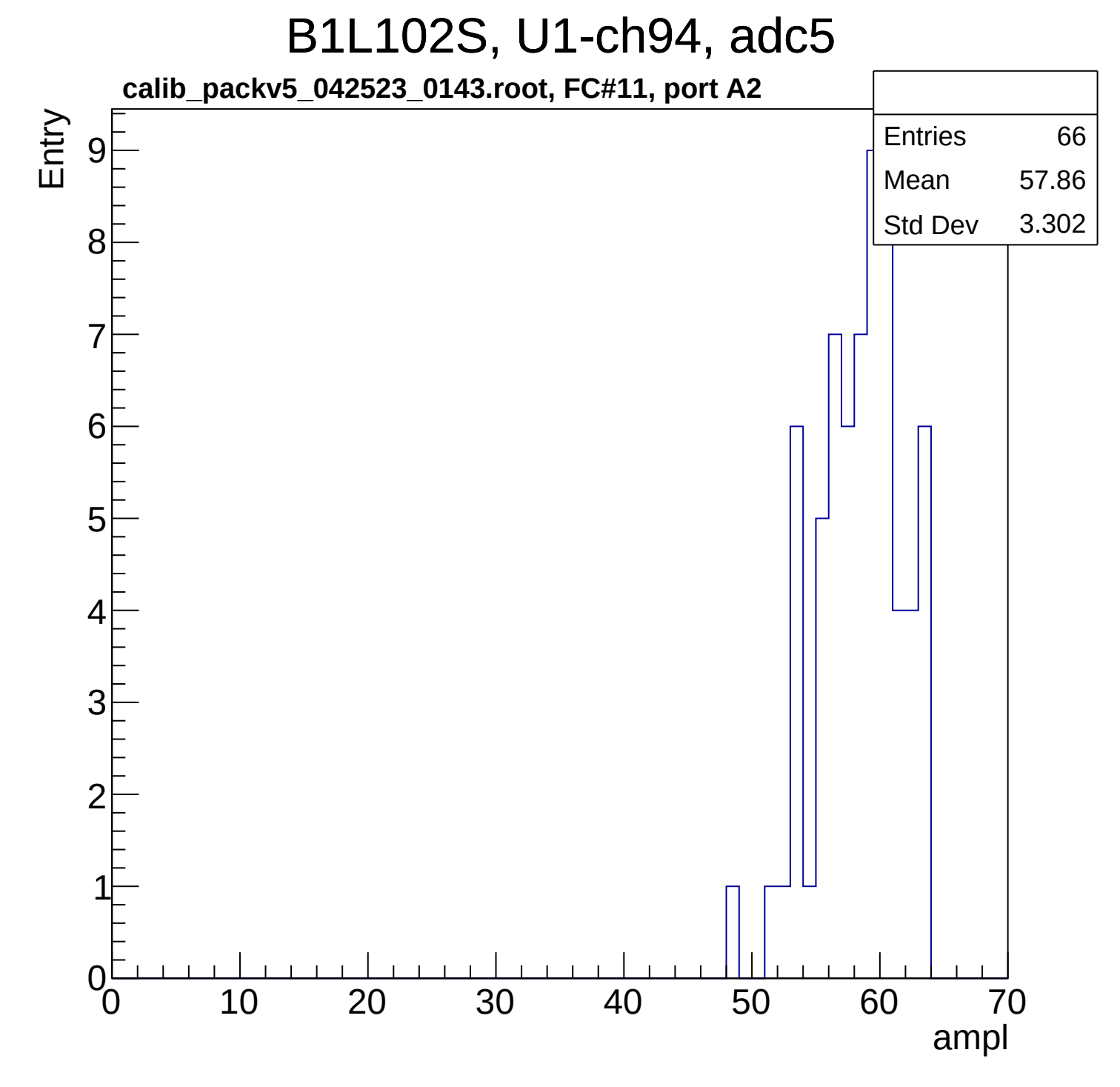
B1L102S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 57.86
Std Dev 3.302
Entry
ampl
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3
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0
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60
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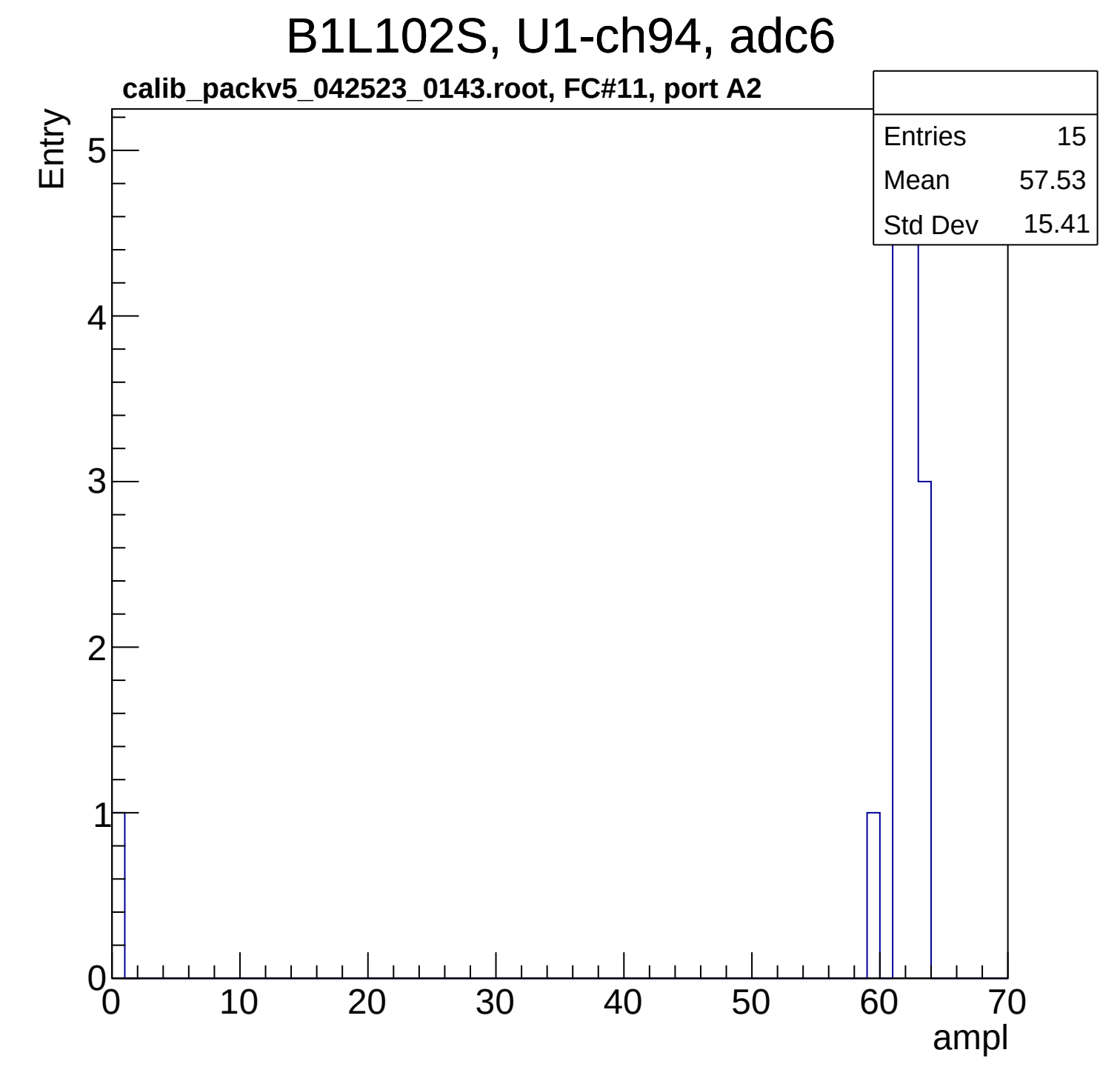
B1L102S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 15
Mean 57.53
Std Dev 15.41
Entry
0
1
2
3
4
5
0
10
20
30
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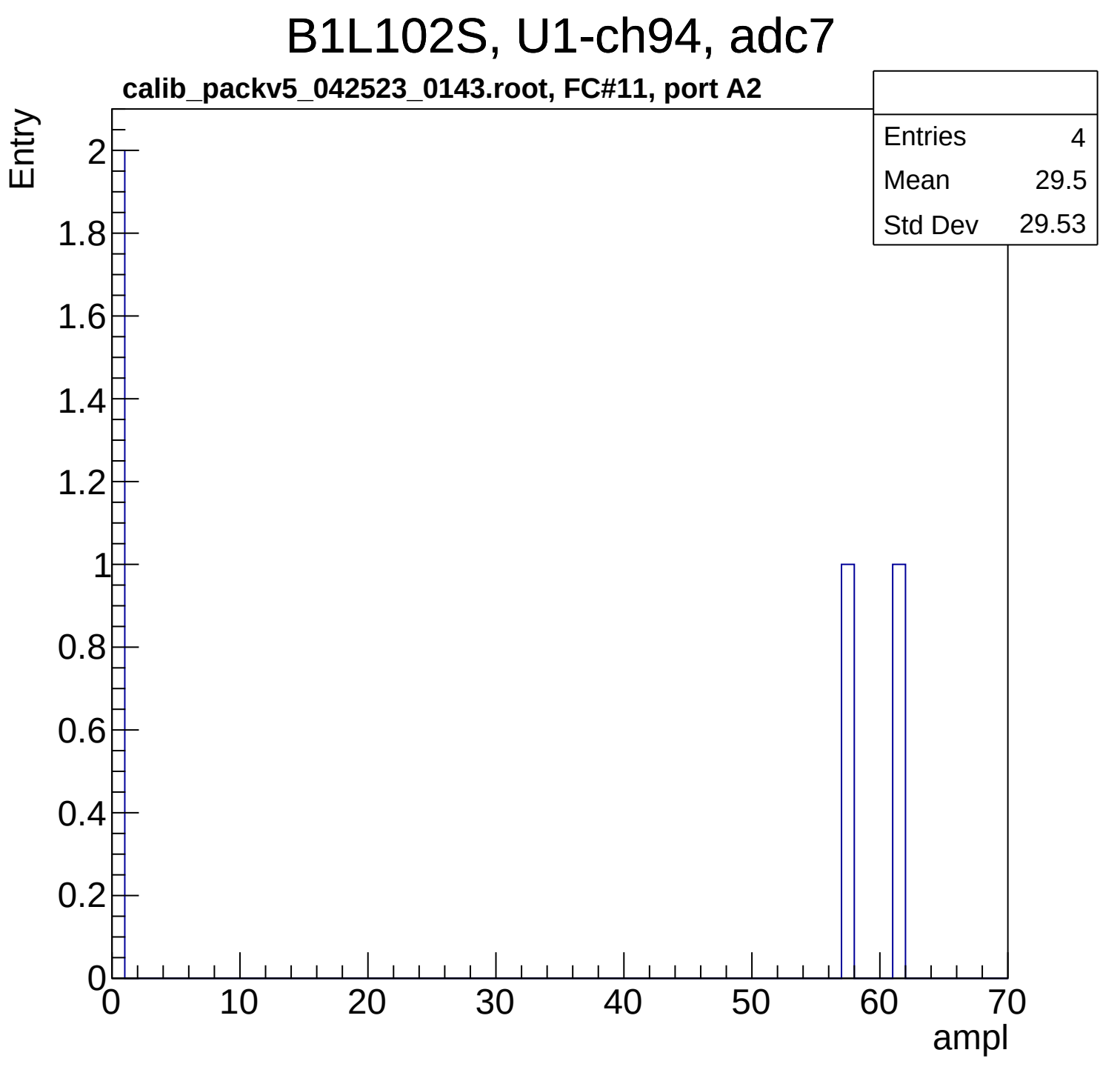
50
60
70
ampl


B1L102S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 29.5
Std Dev 29.53
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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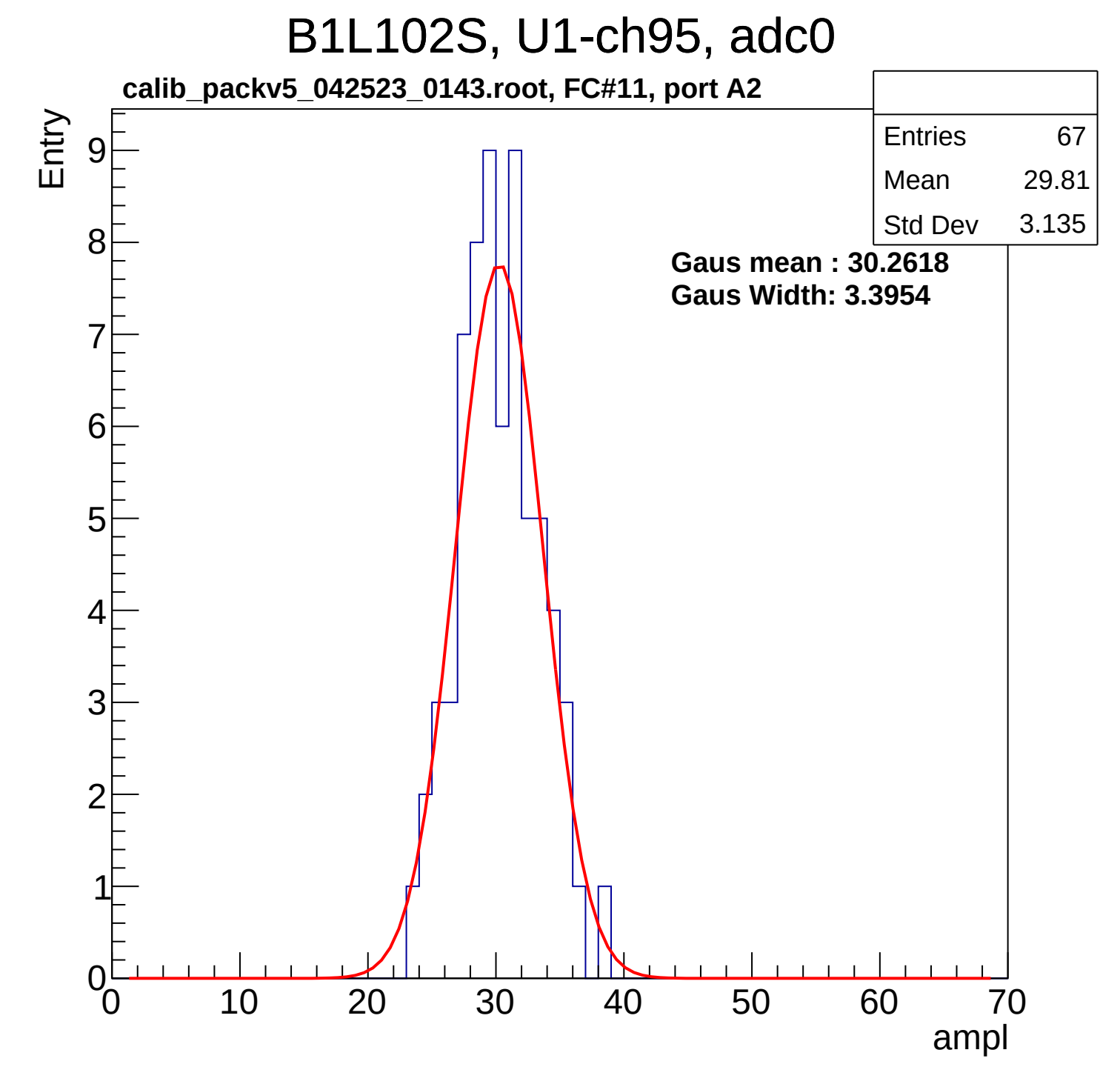
B1L102S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 29.81
Std Dev 3.135
Gaus mean : 30.2618
Gaus Width: 3.3954
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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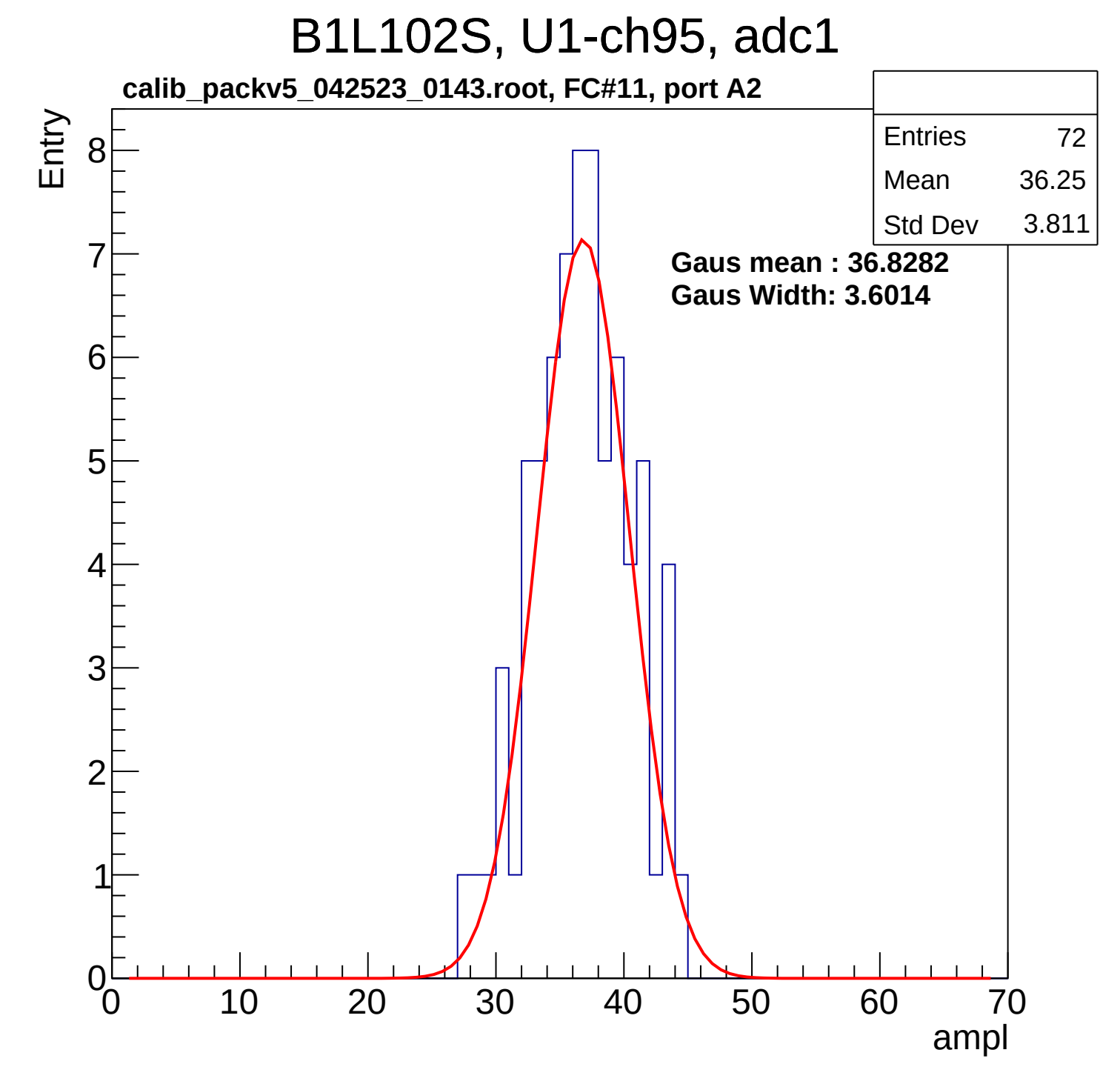
B1L102S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 36.25
Std Dev 3.811
Gaus mean : 36.8282
Gaus Width: 3.6014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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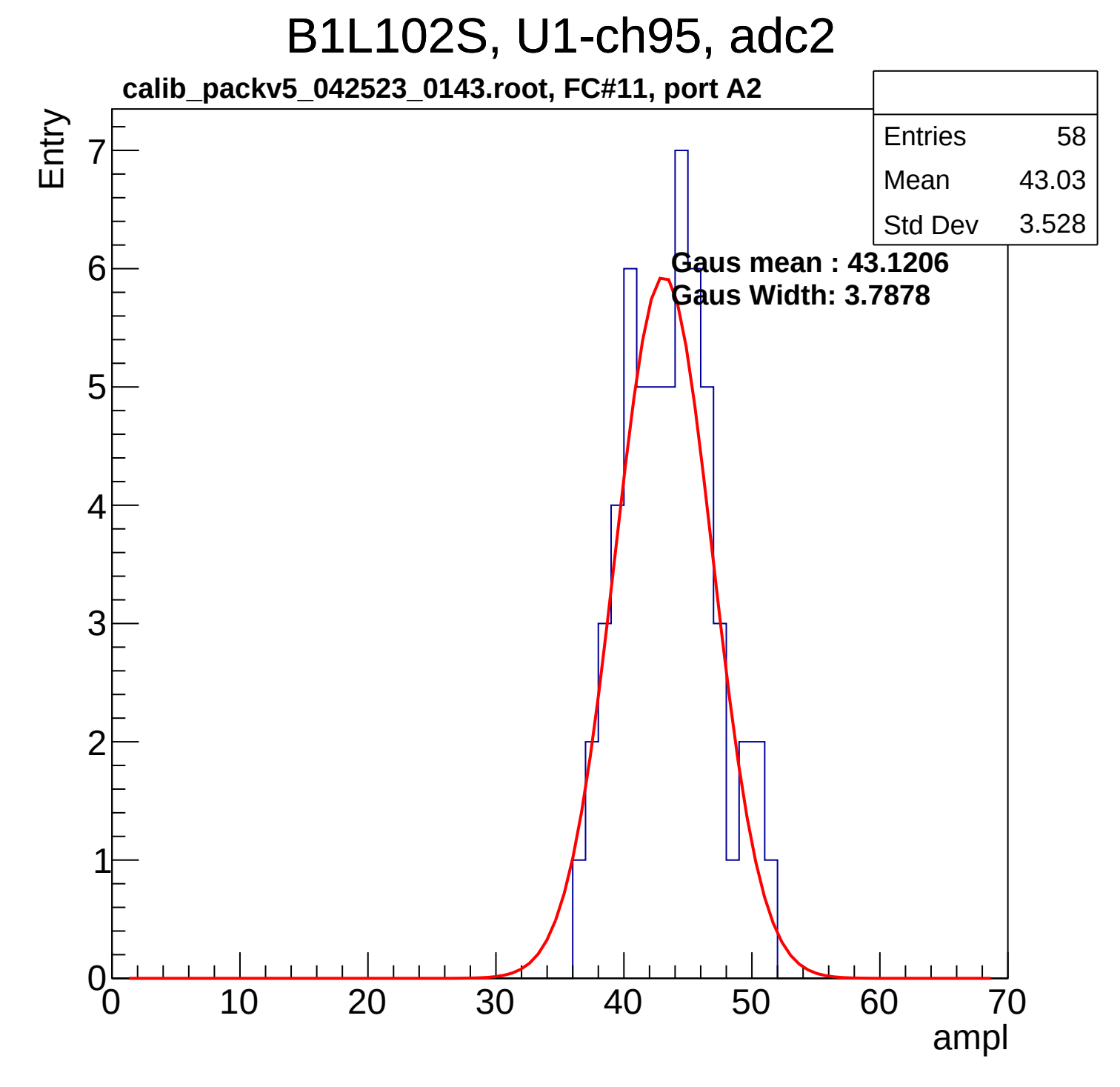
B1L102S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 43.03
Std Dev 3.528
Gaus mean : 43.1206
Gaus Width: 3.7878
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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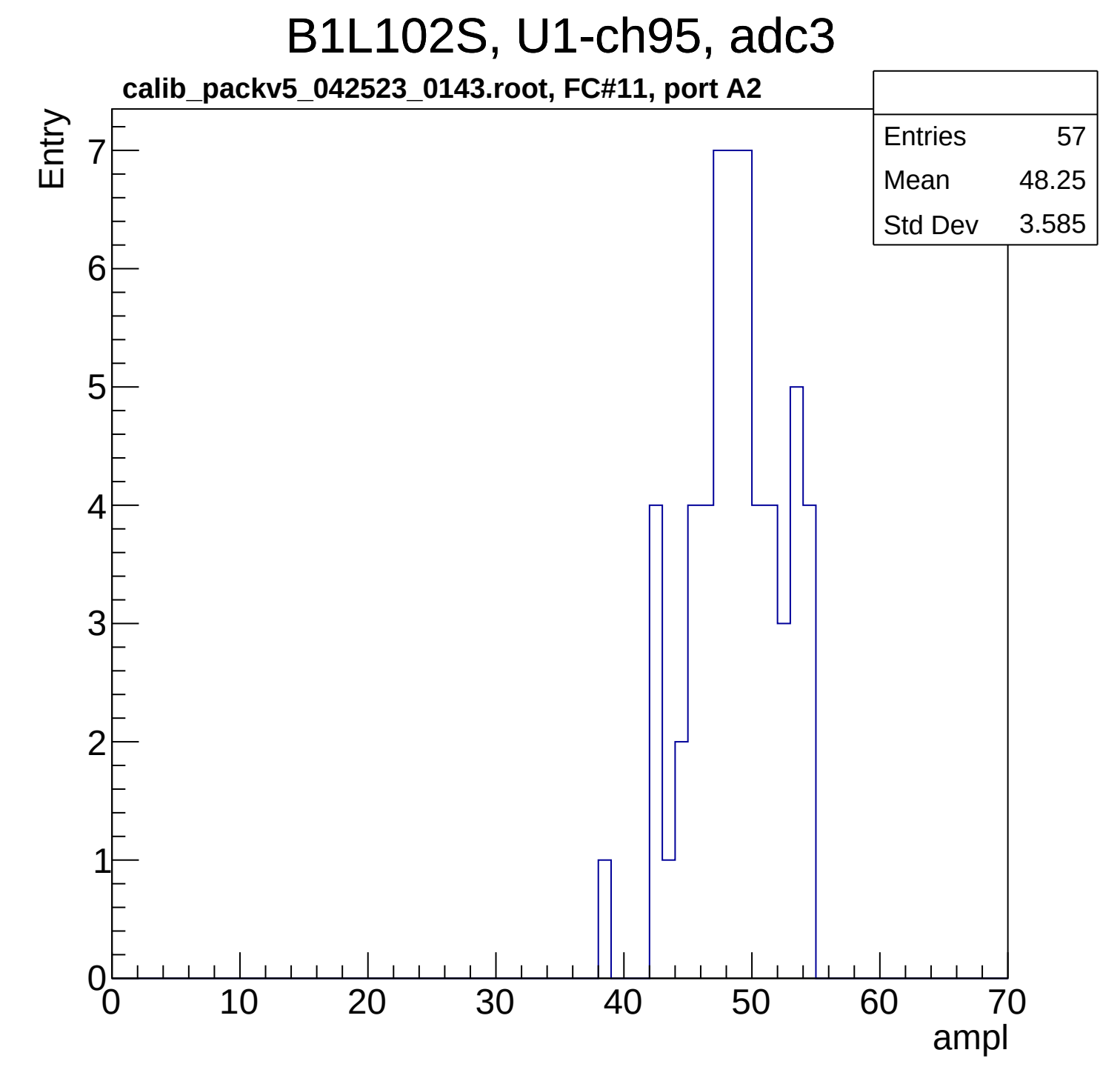
B1L102S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 48.25
Std Dev 3.585
Entry
ampl
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1
2
3
4
5
6
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0
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20
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60
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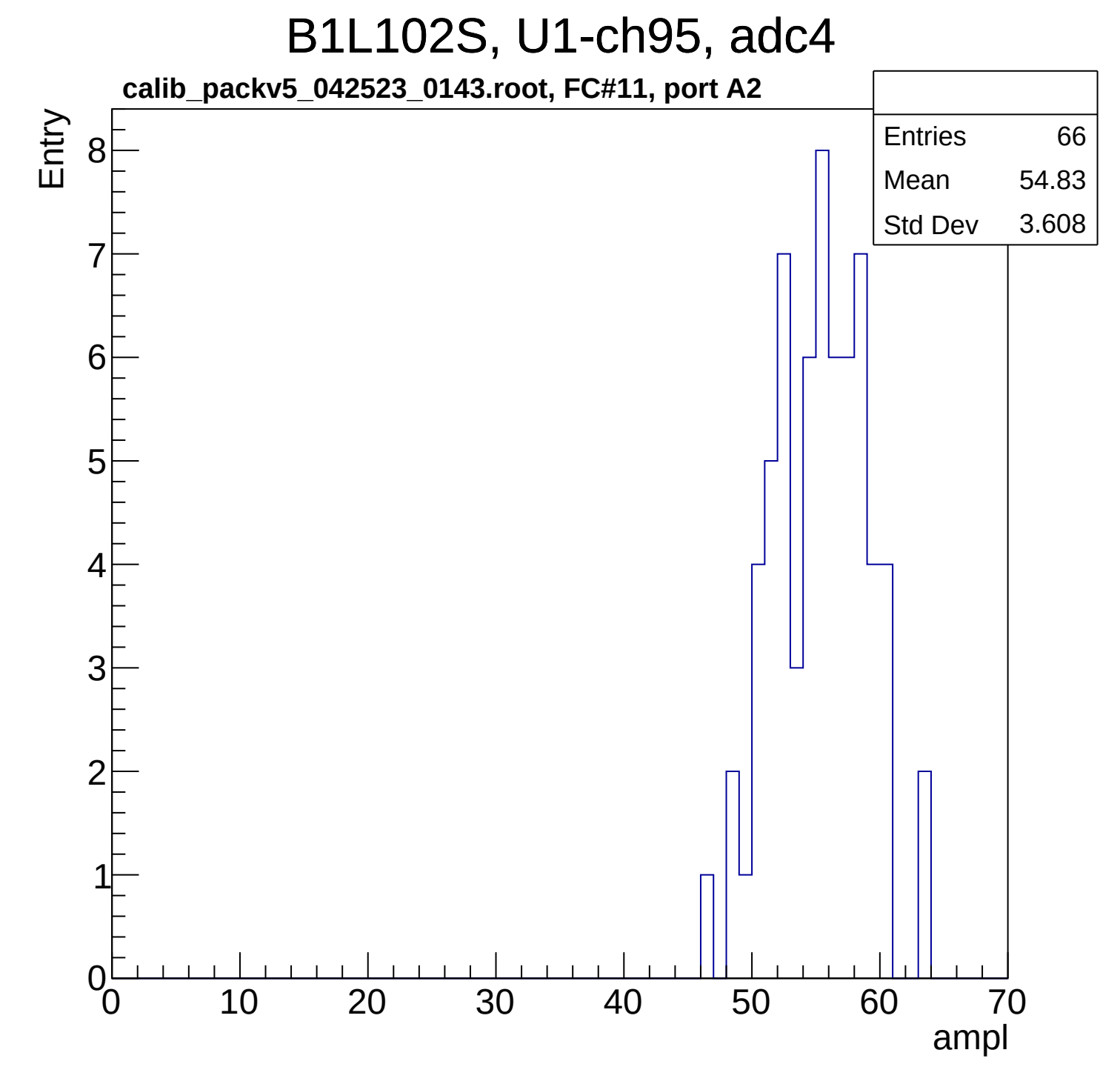
B1L102S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 54.83
Std Dev 3.608
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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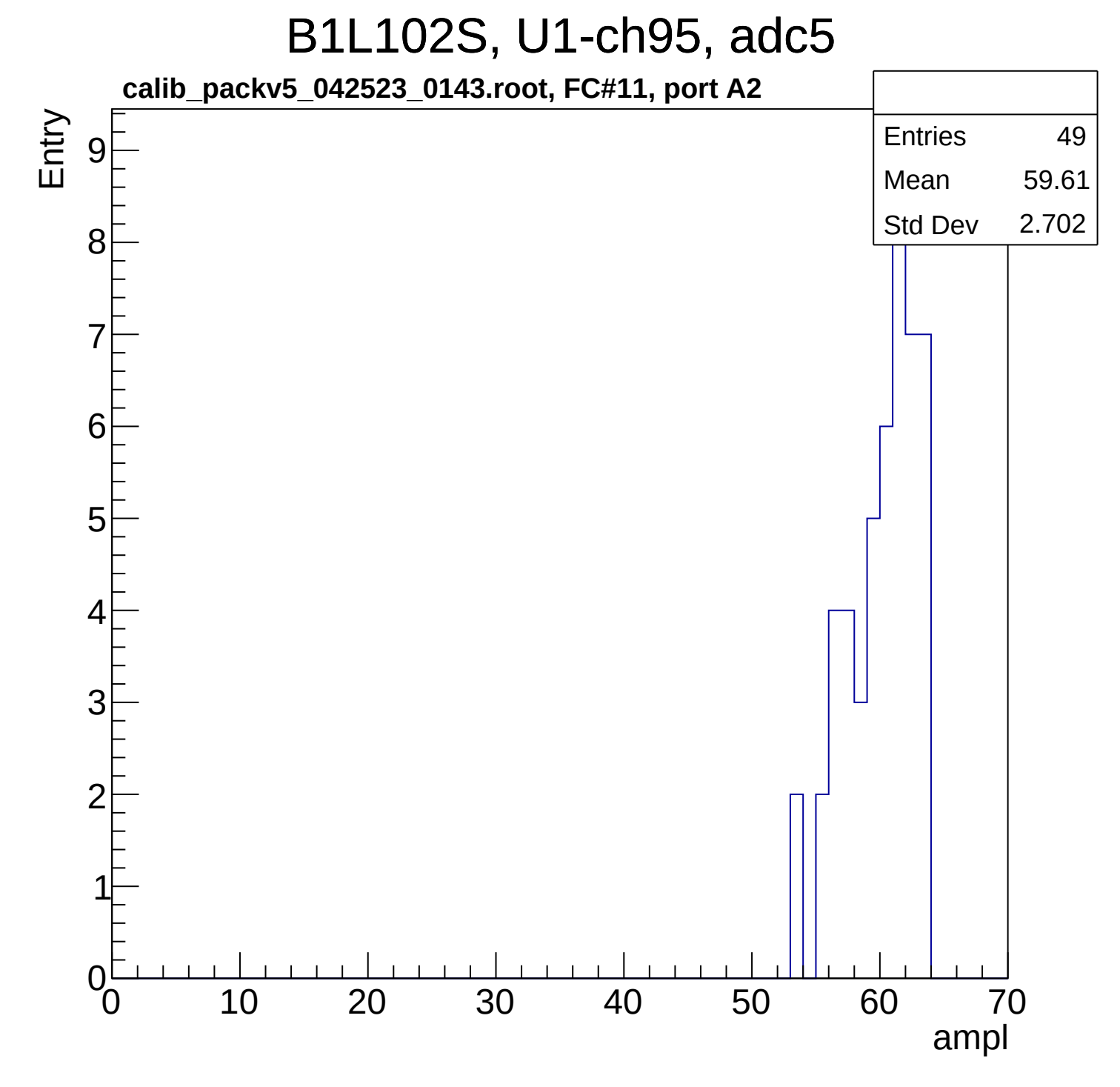
B1L102S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.61
Std Dev 2.702
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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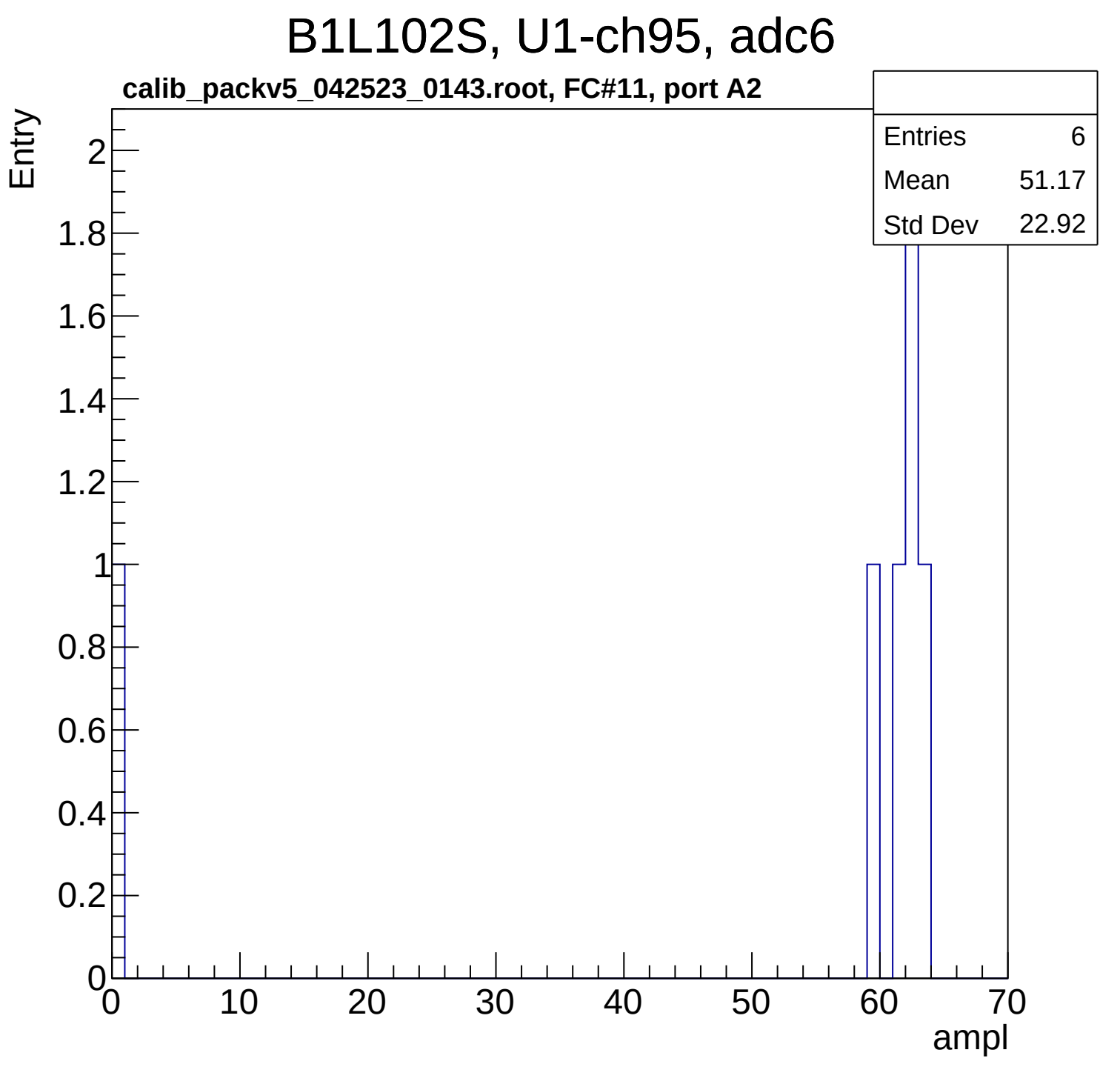
B1L102S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 51.17
Std Dev 22.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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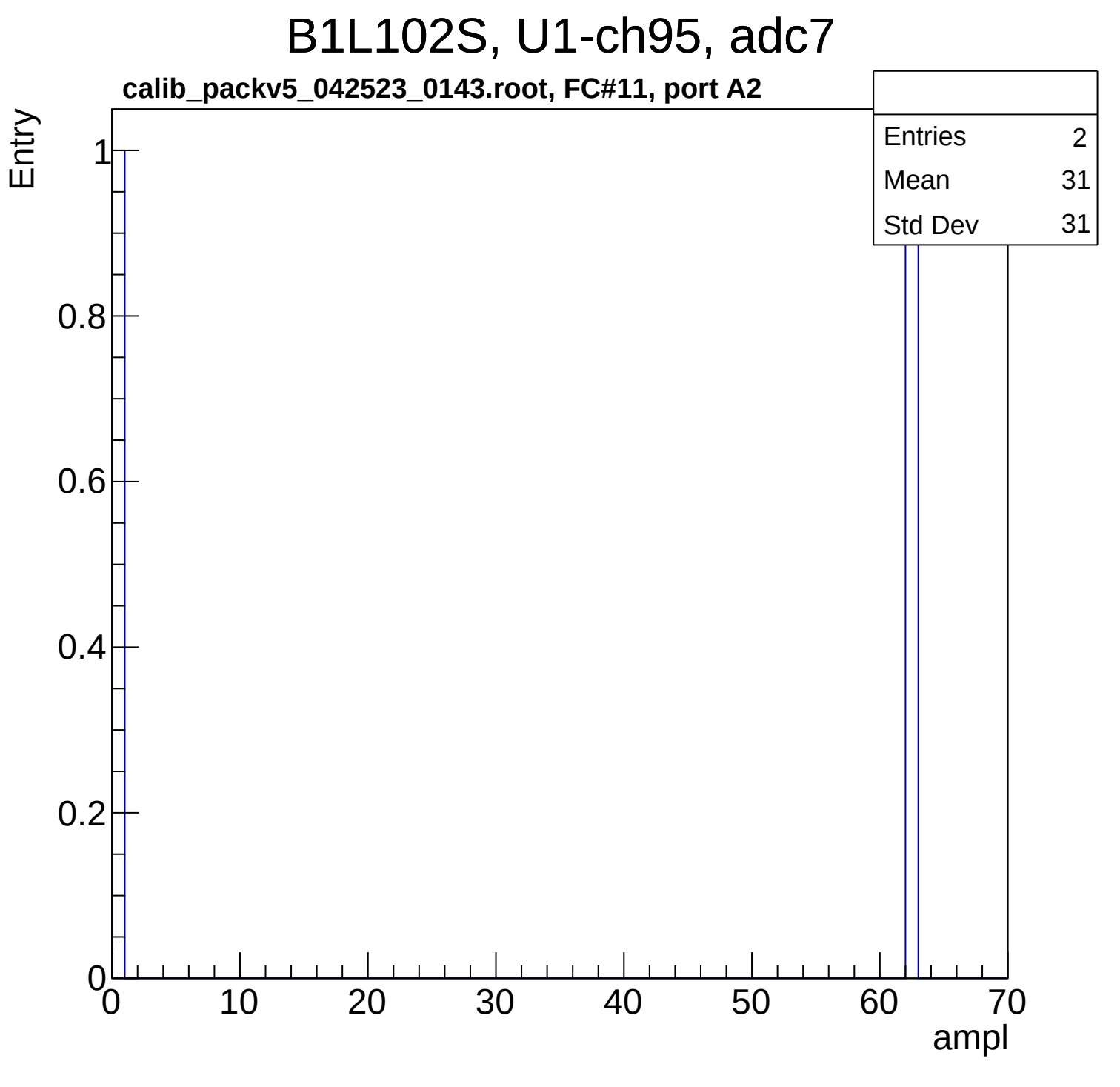
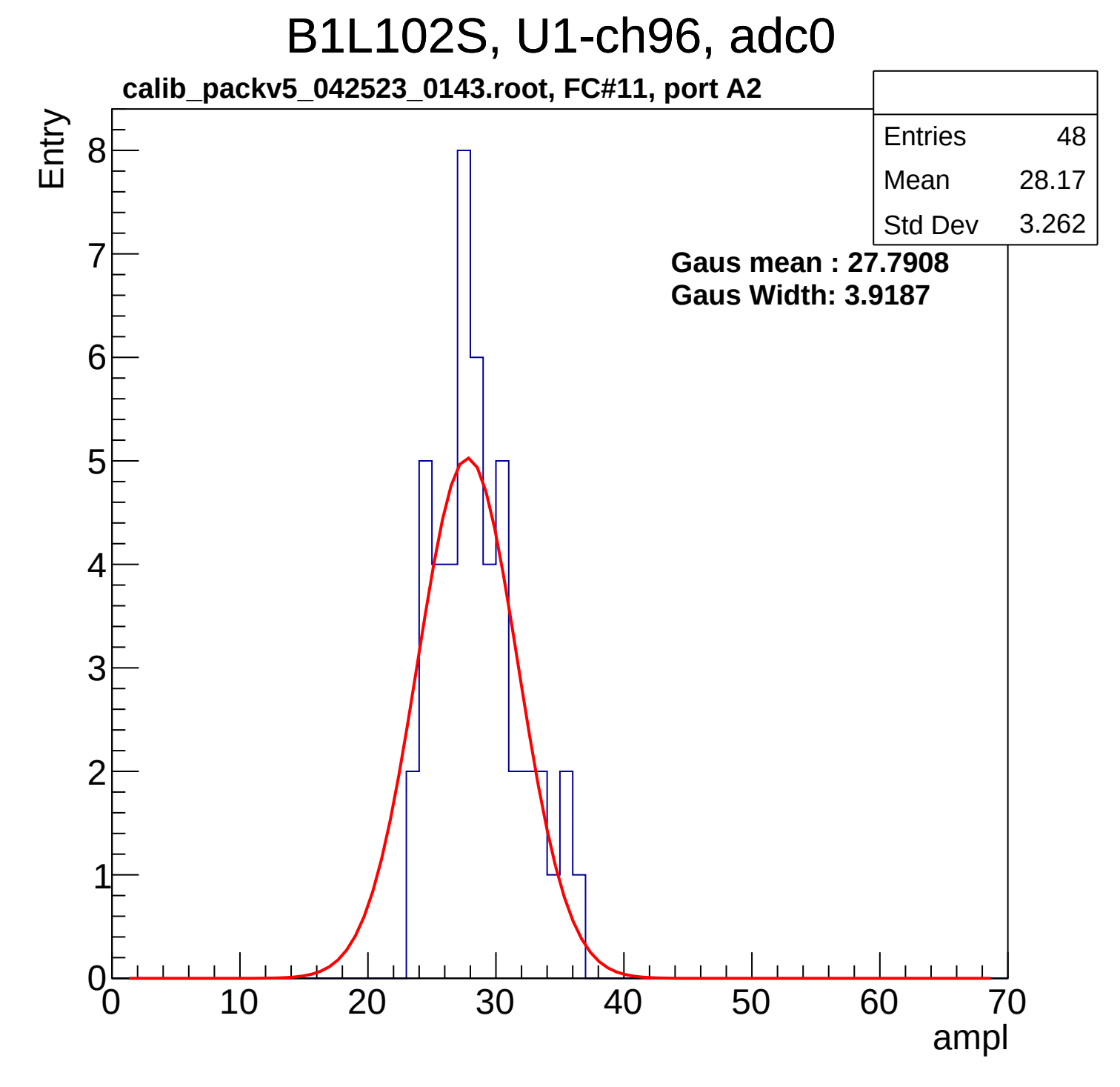

B1L102S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 28.17
Std Dev 3.262
Gaus mean : 27.7908
Gaus Width: 3.9187
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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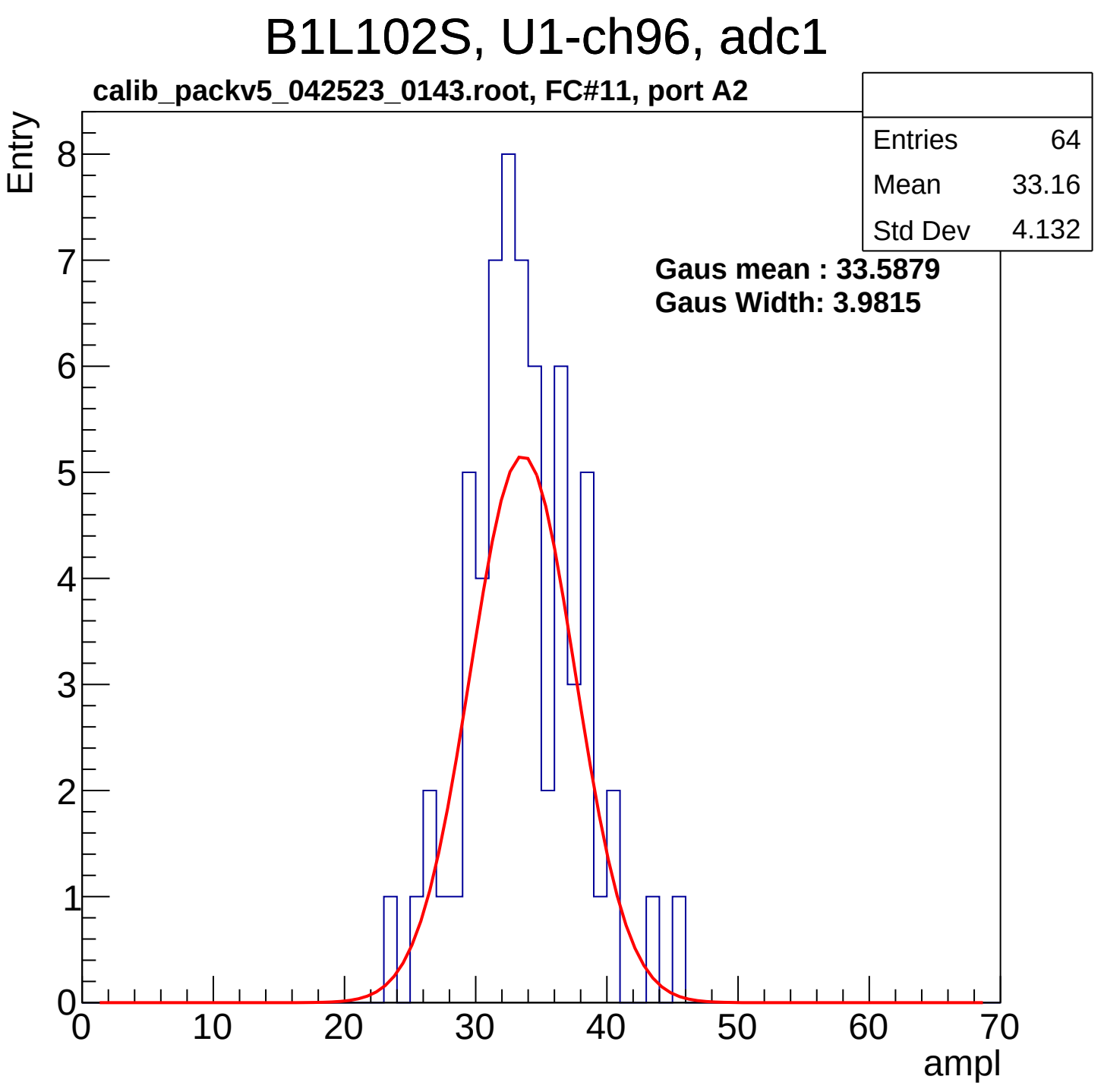
40
50
60
70


B1L102S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 33.16
Std Dev 4.132
Gaus mean : 33.5879
Gaus Width: 3.9815
Entry
ampl
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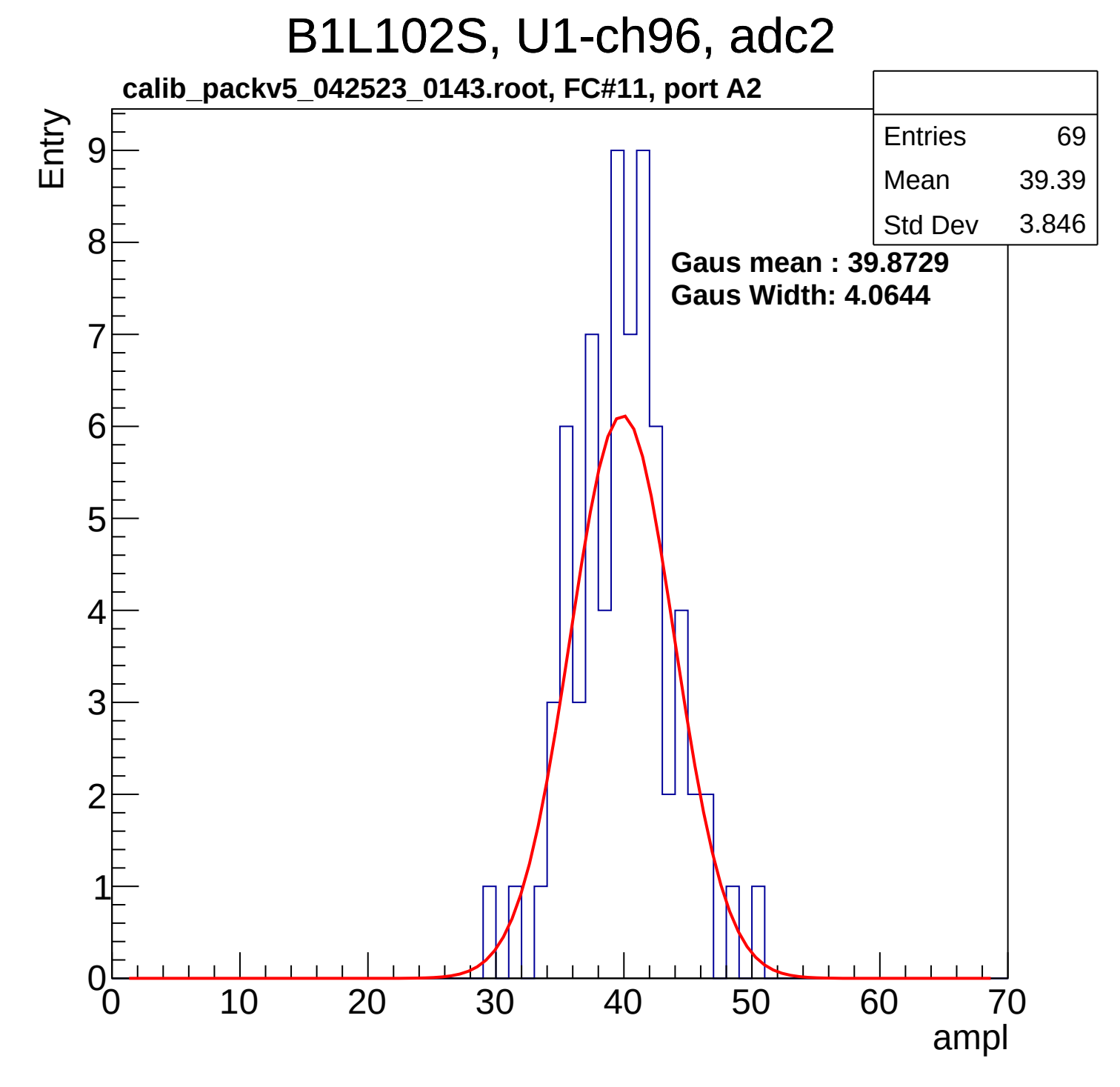
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 39.39
Std Dev 3.846
Gaus mean : 39.8729
Gaus Width: 4.0644
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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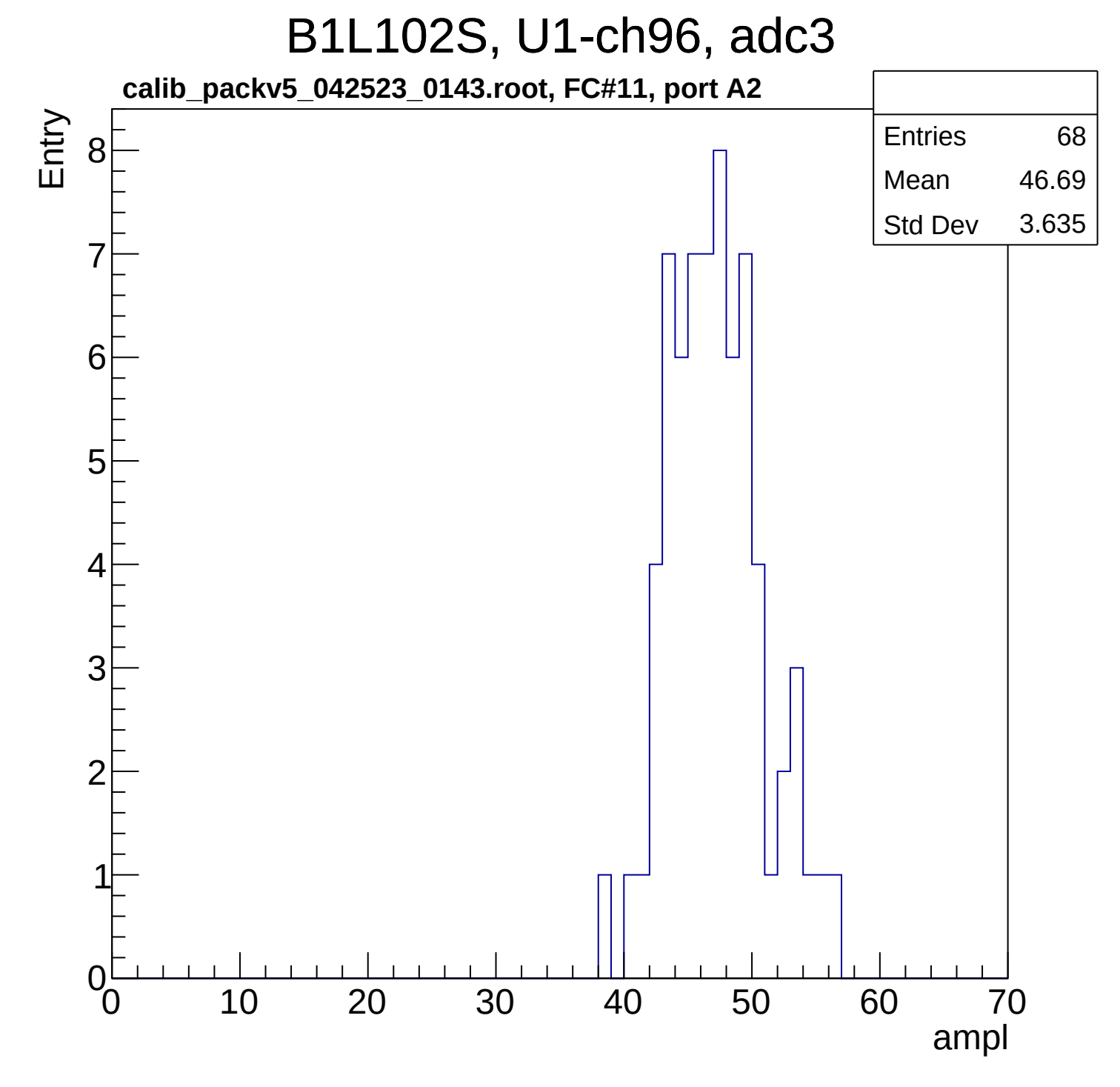
B1L102S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 46.69
Std Dev 3.635
Entry
ampl
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2
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4
5
6
7
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0
10
20
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40
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60
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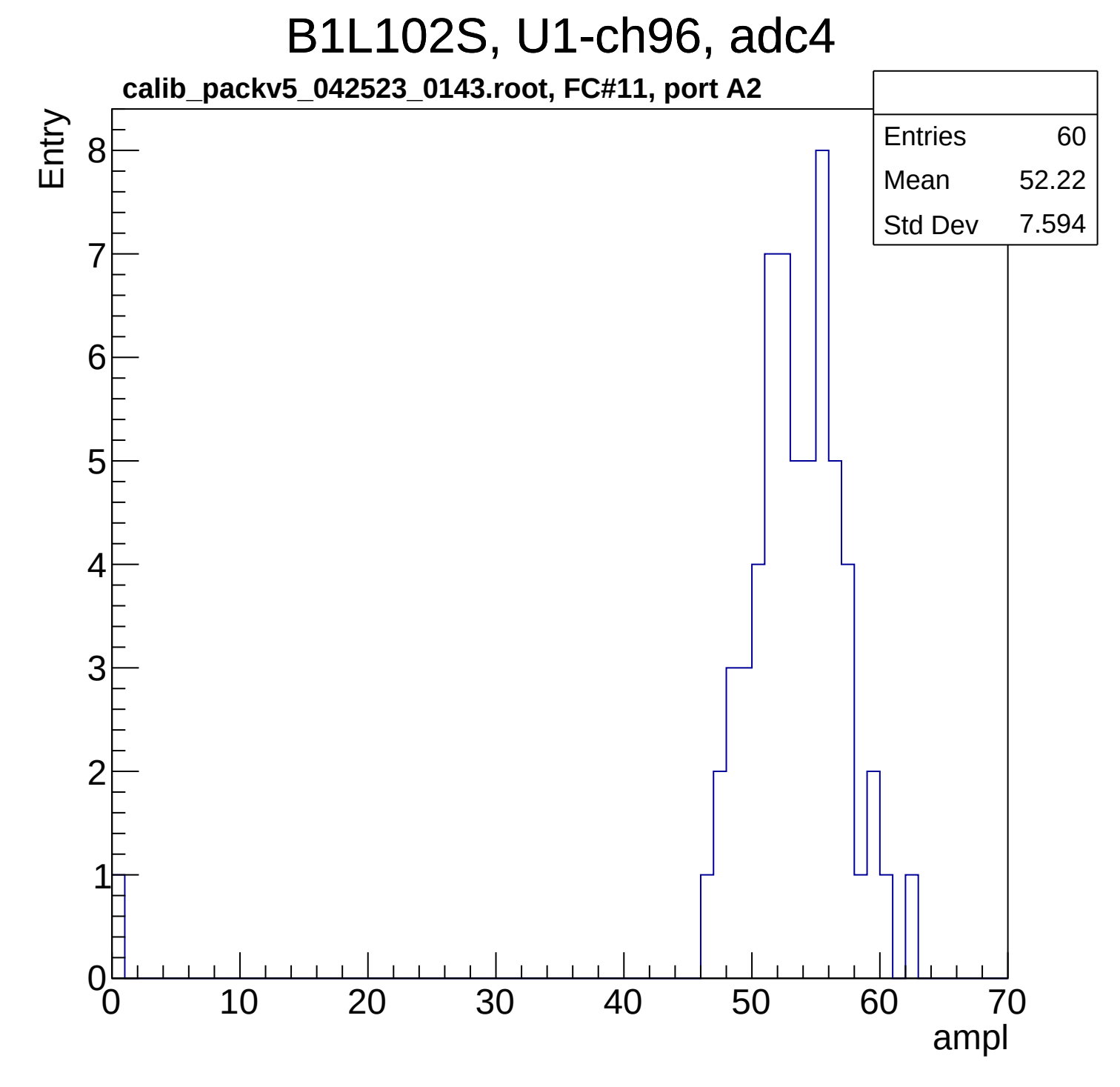
B1L102S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 52.22
Std Dev 7.594
Entry
ampl
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2
3
4
5
6
7
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0
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60
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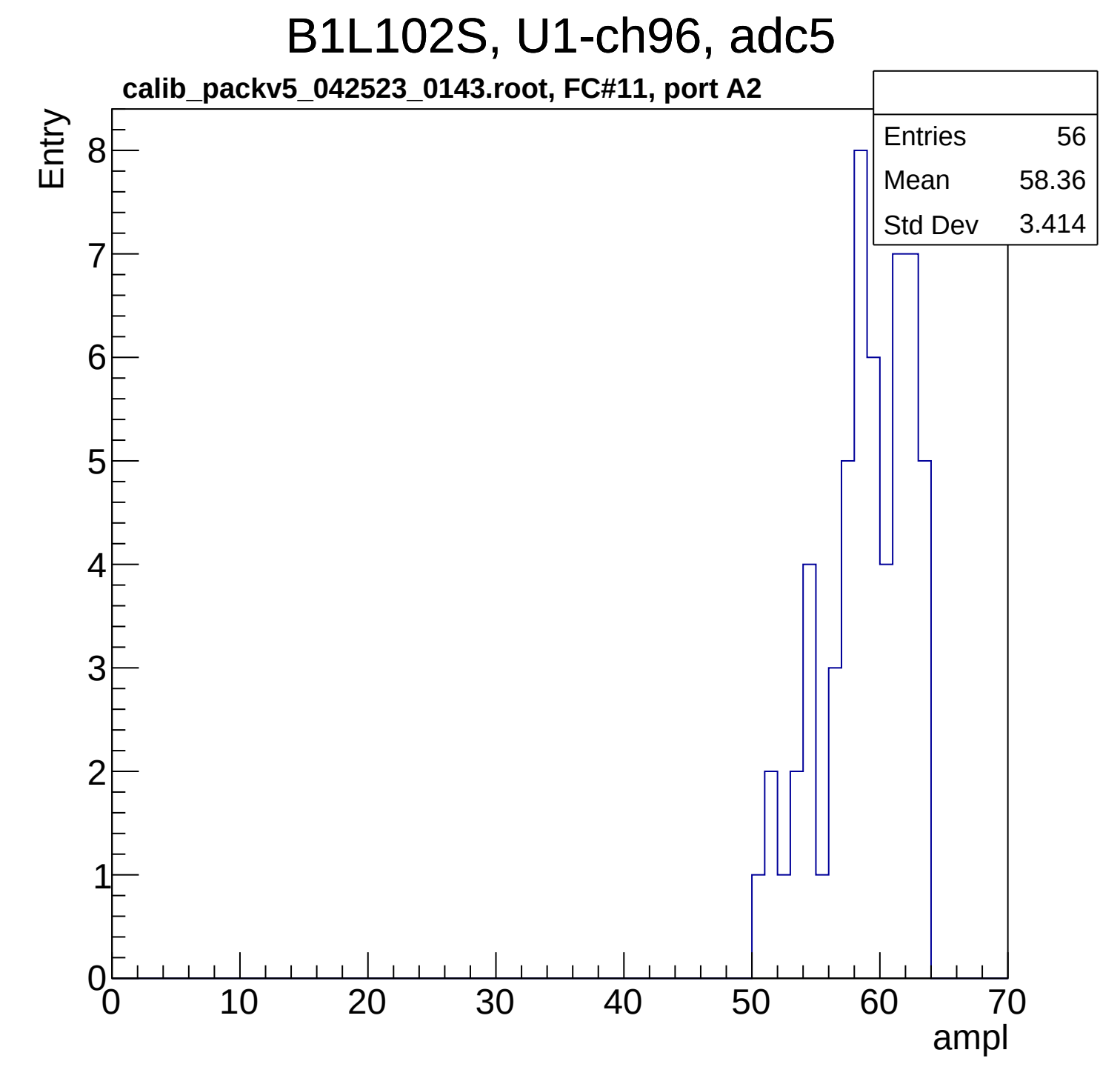
B1L102S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 58.36
Std Dev 3.414
Entry
ampl
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2
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4
5
6
7
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0
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60
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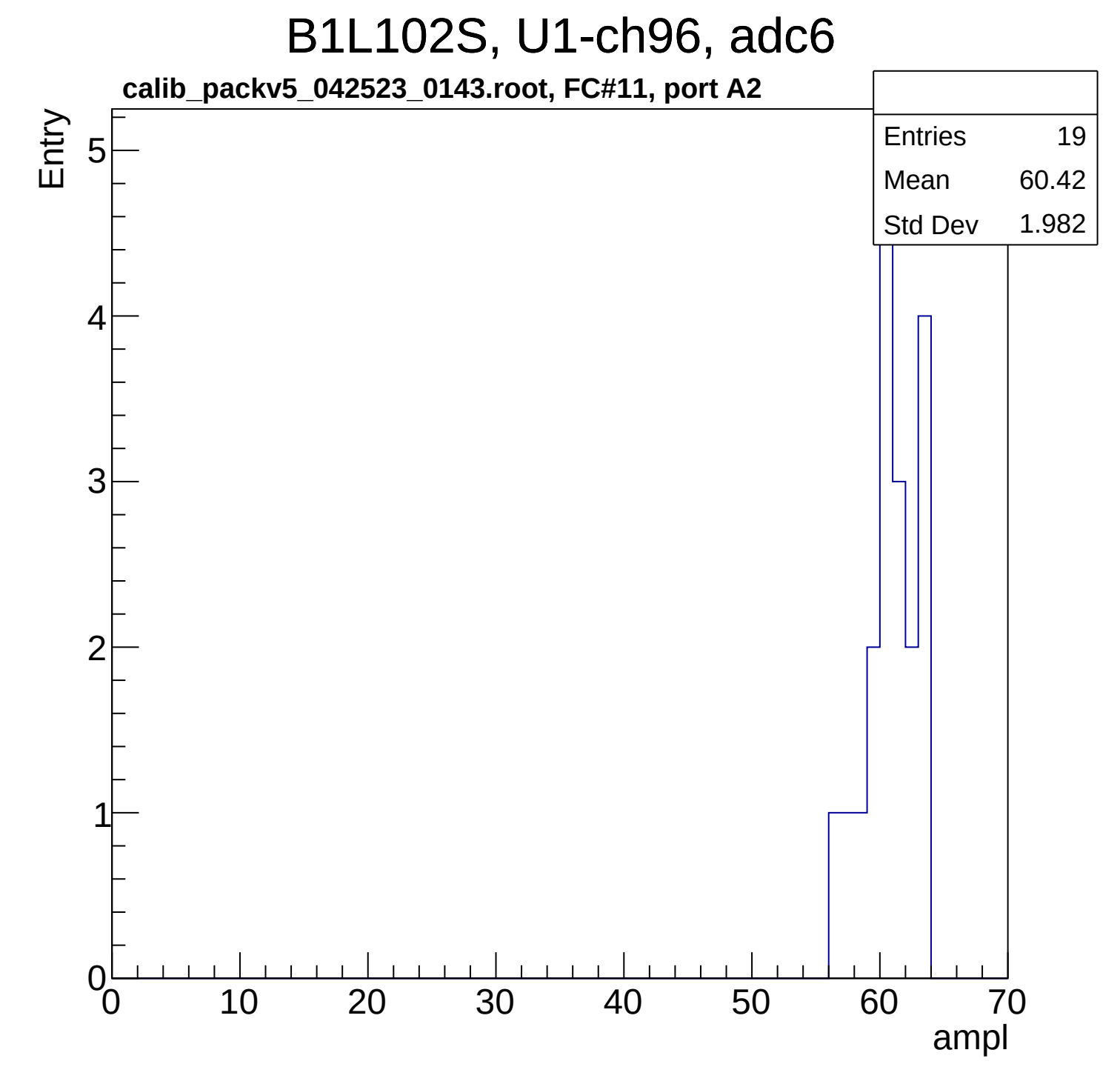
B1L102S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 19
Mean 60.42
Std Dev 1.982
Entry
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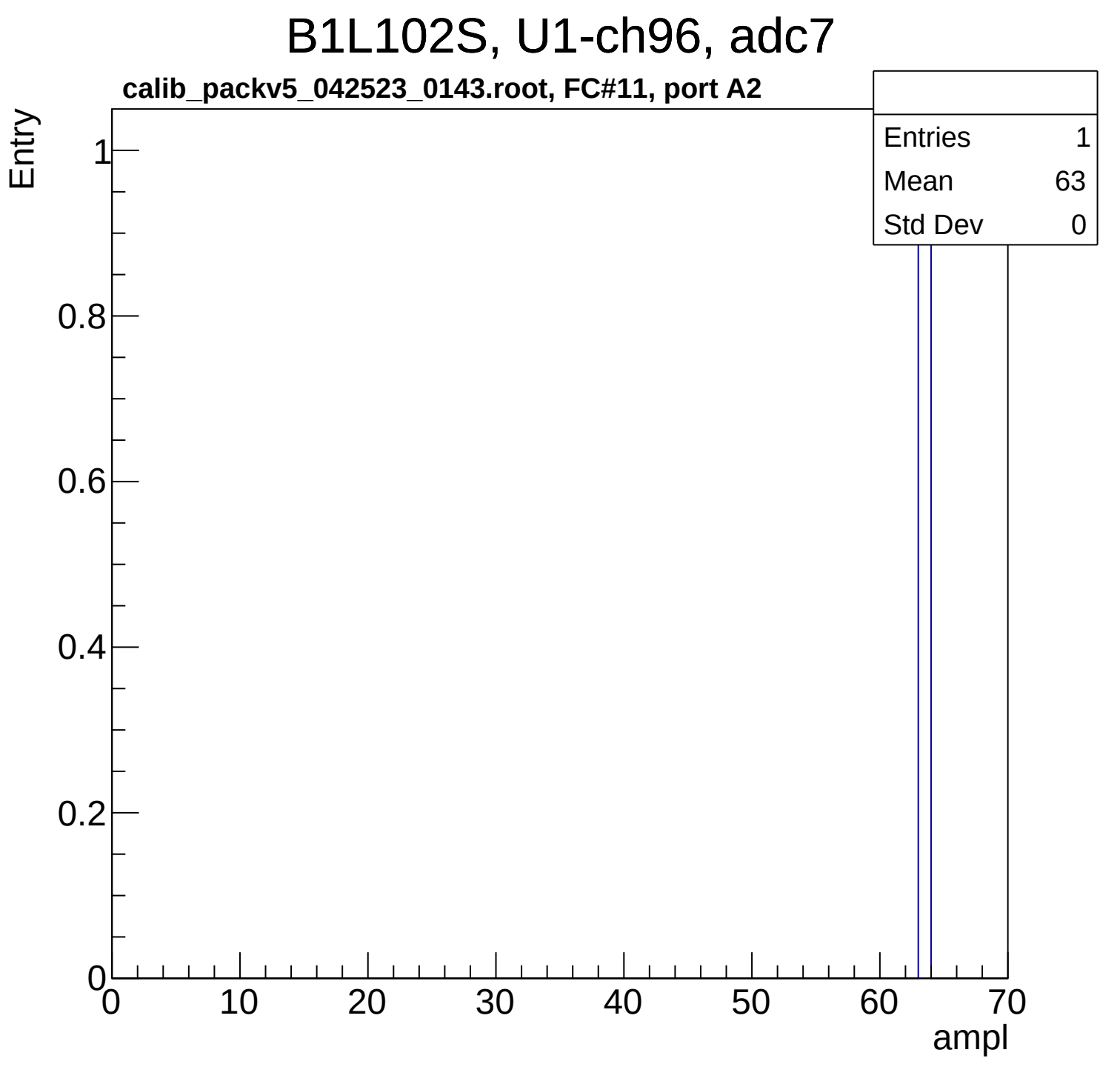
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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0.4
0.2
0
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10
20
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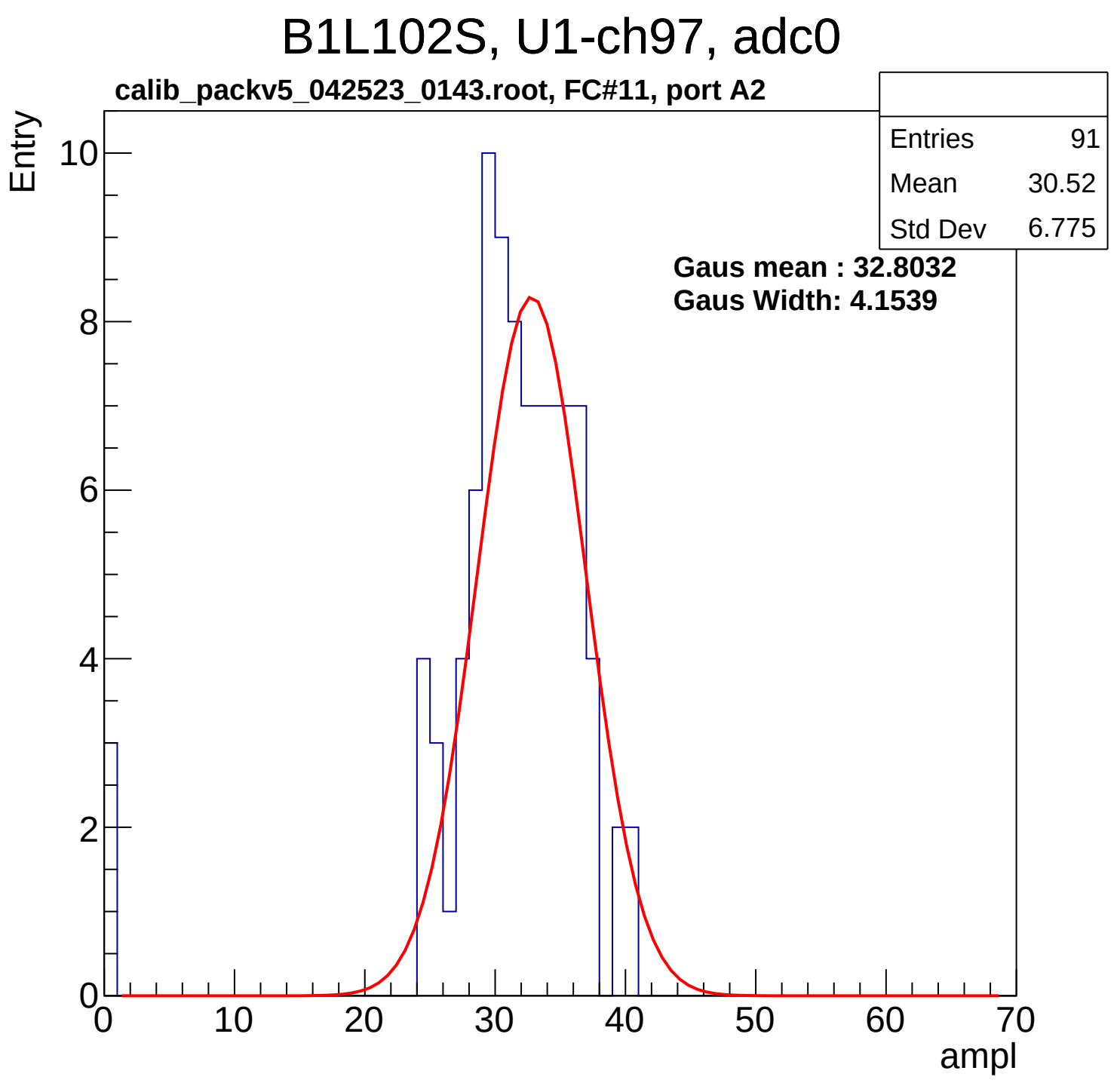
40
50
60
70
ampl


B1L102S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 91
Mean 30.52
Std Dev 6.775
Gaus mean : 32.8032
Gaus Width: 4.1539
Entry
ampl
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6
8
10
0
10
20
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40
50
60
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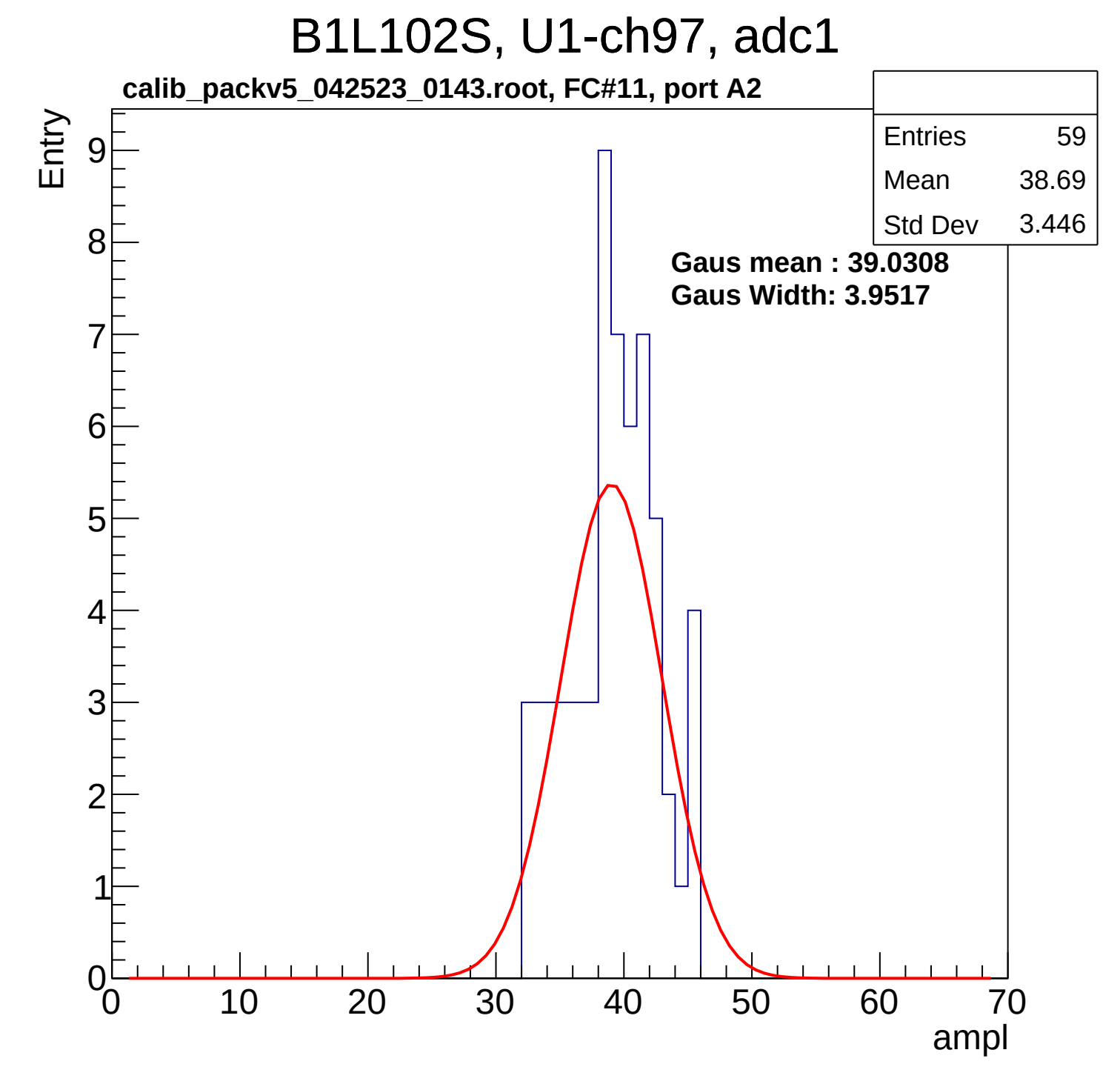
B1L102S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 38.69
Std Dev 3.446
Gaus mean : 39.0308
Gaus Width: 3.9517
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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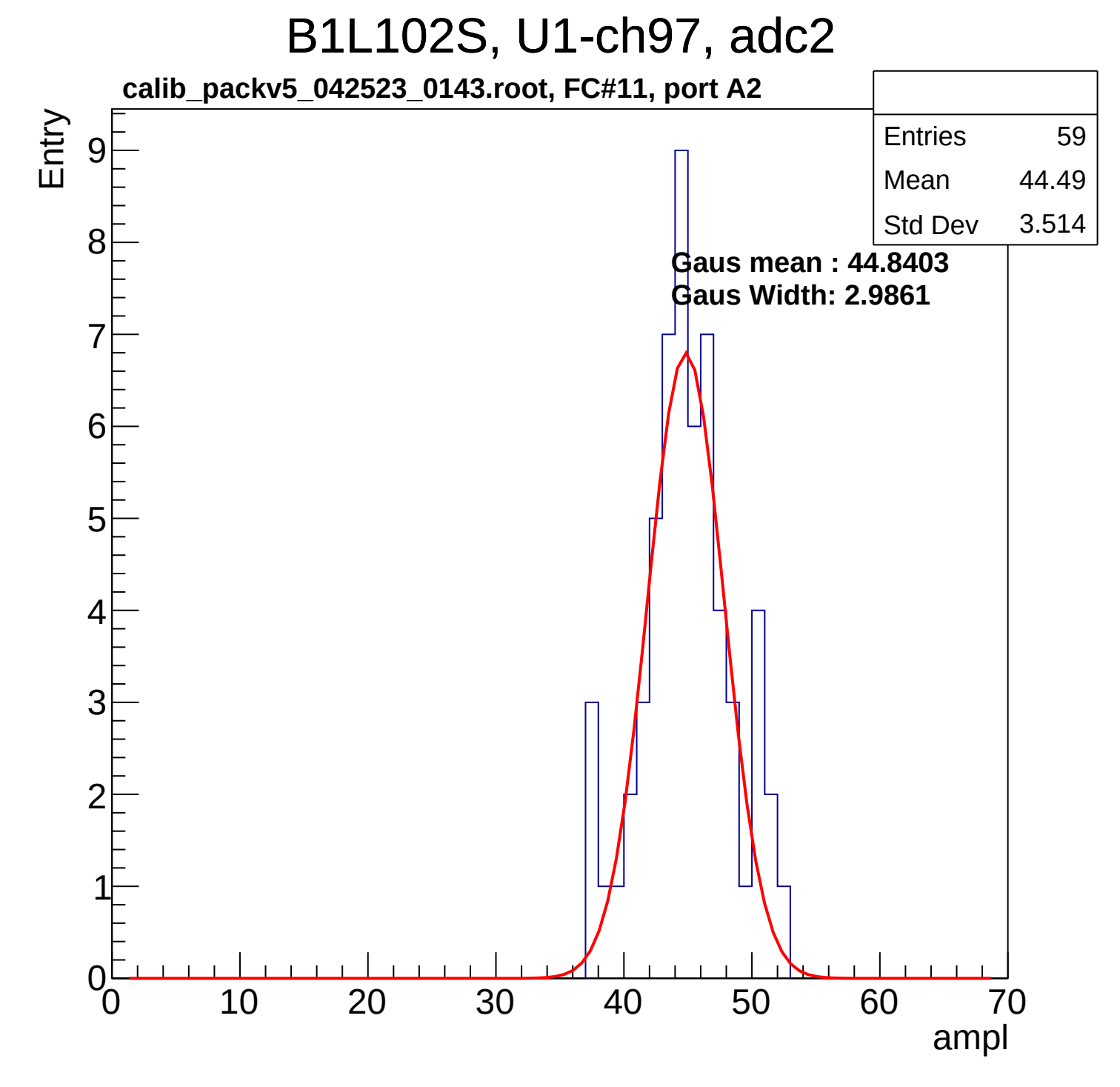
B1L102S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 44.49
Std Dev 3.514
Gaus mean : 44.8403
Gaus Width: 2.9861
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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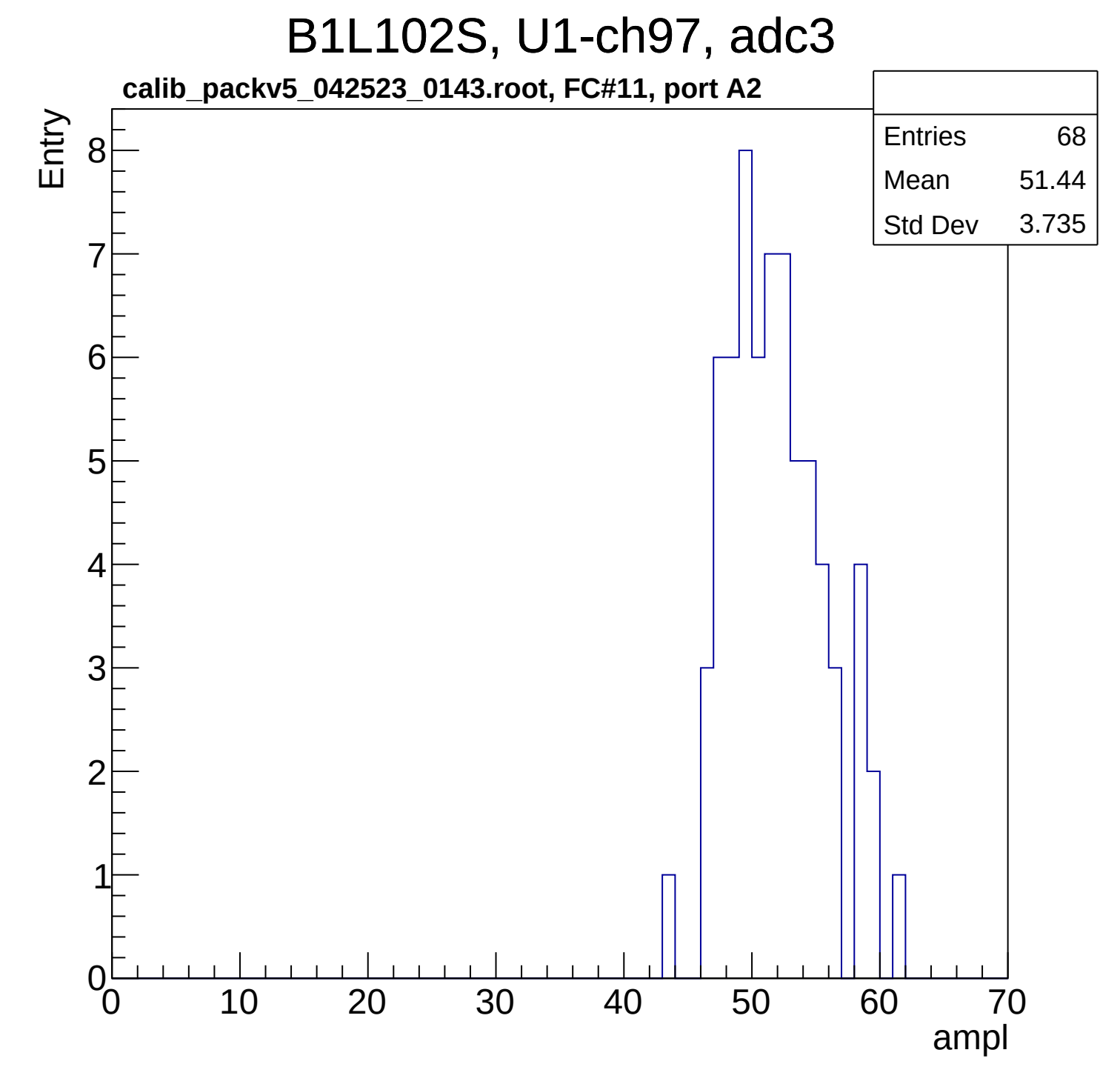
30
40
50
60
70


B1L102S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 51.44
Std Dev 3.735
Entry
ampl
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2
3
4
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6
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0
10
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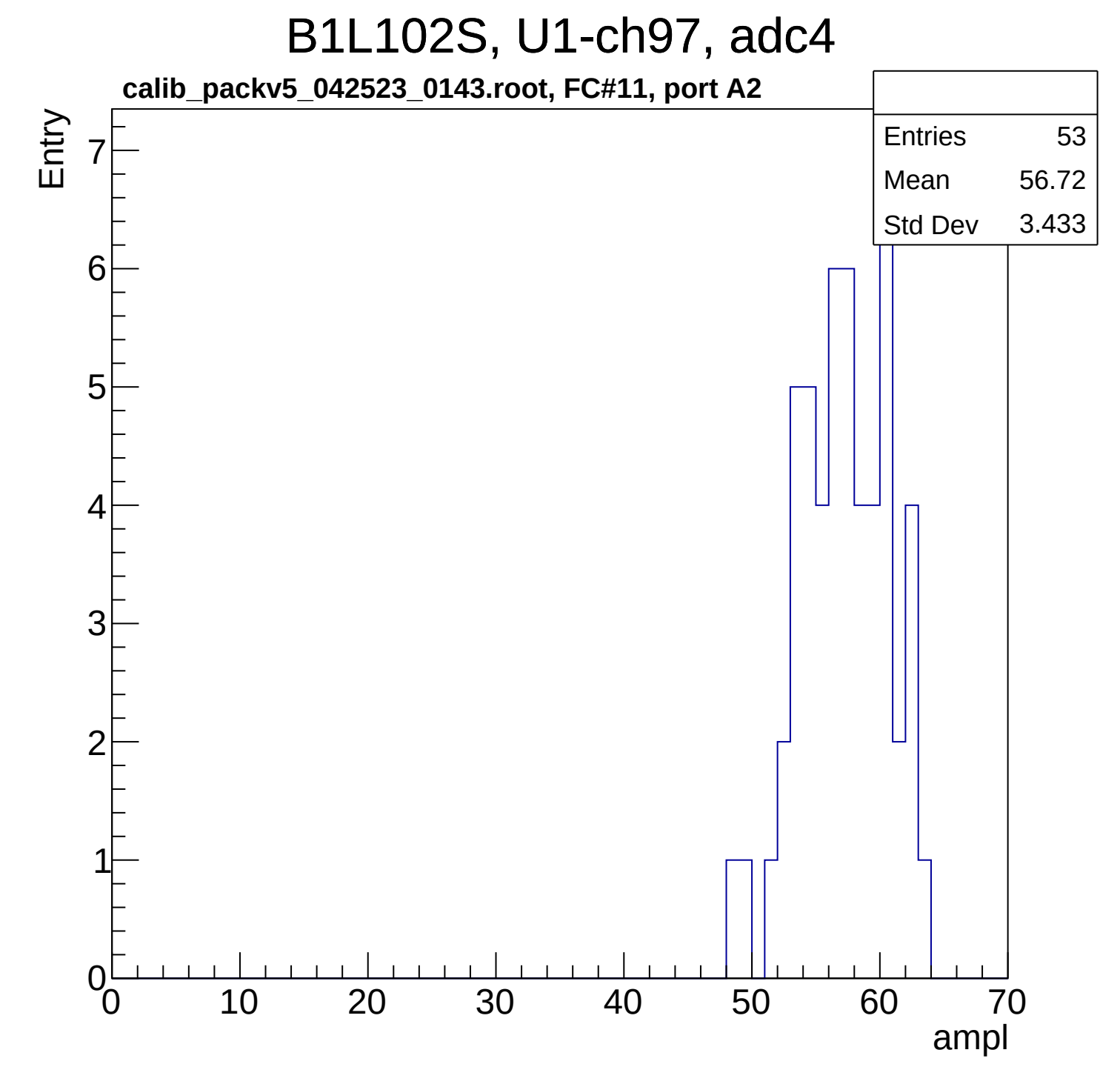
B1L102S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 56.72
Std Dev 3.433
Entry
ampl
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2
3
4
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6
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0
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40
50
60
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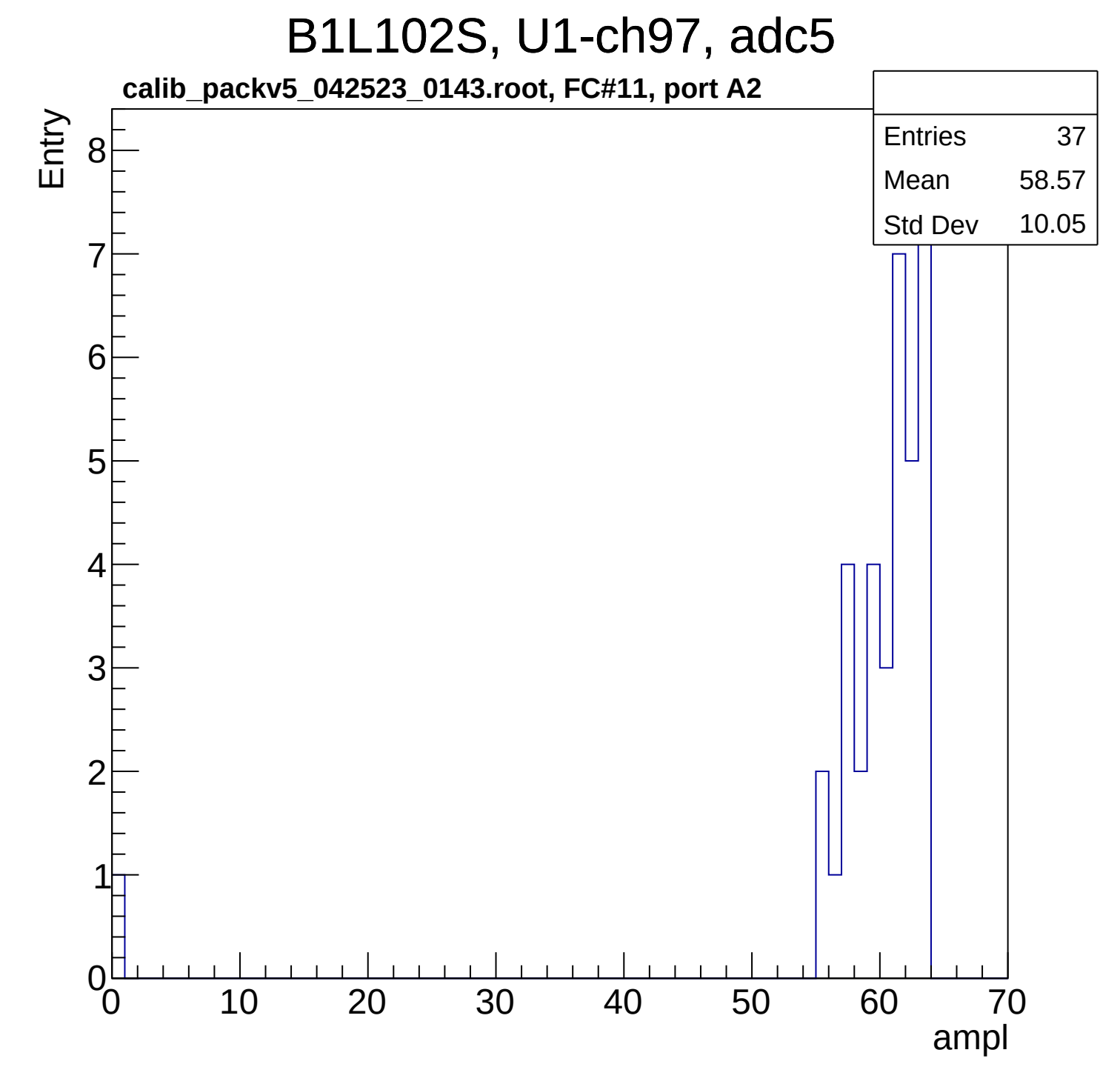
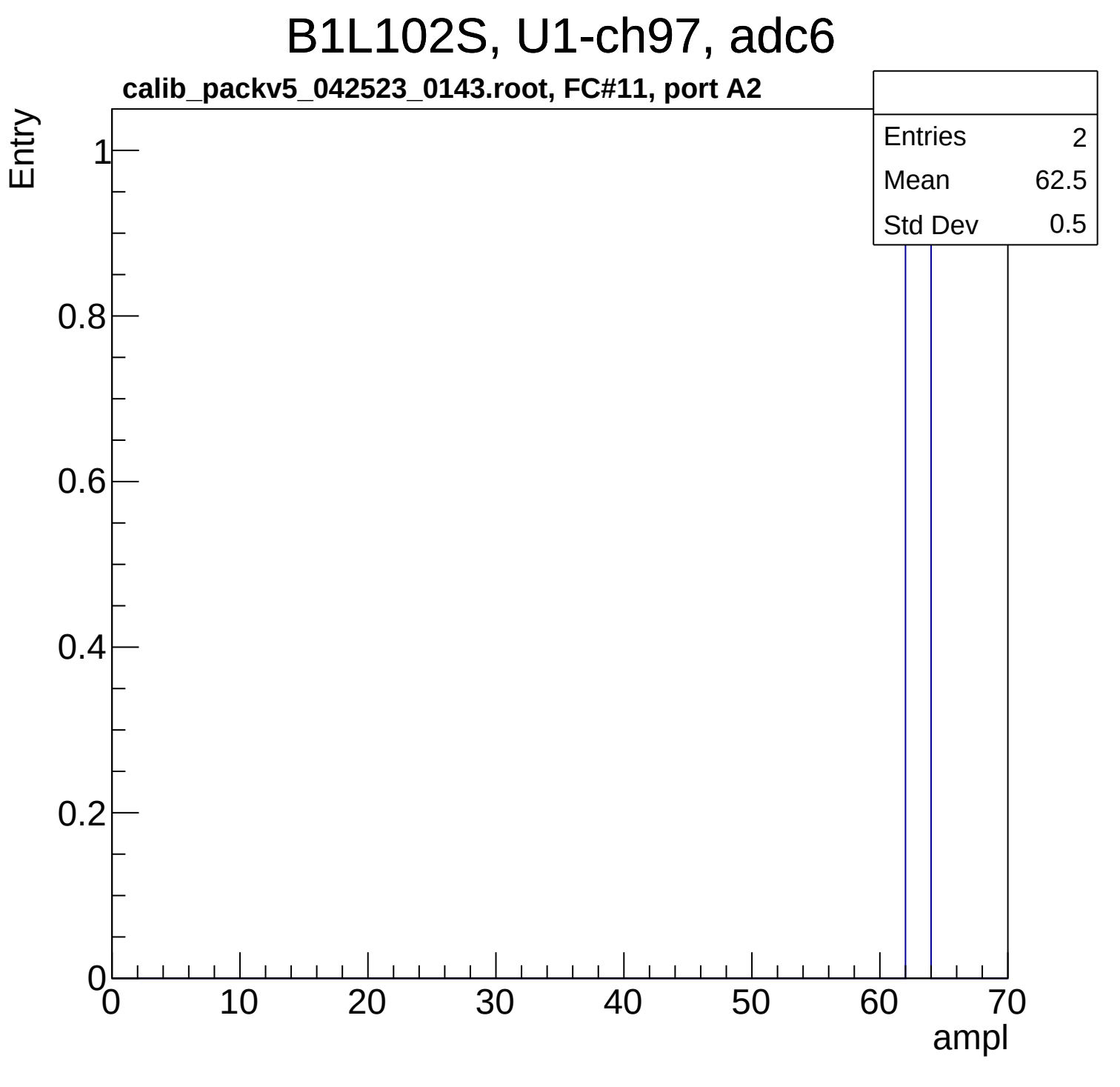
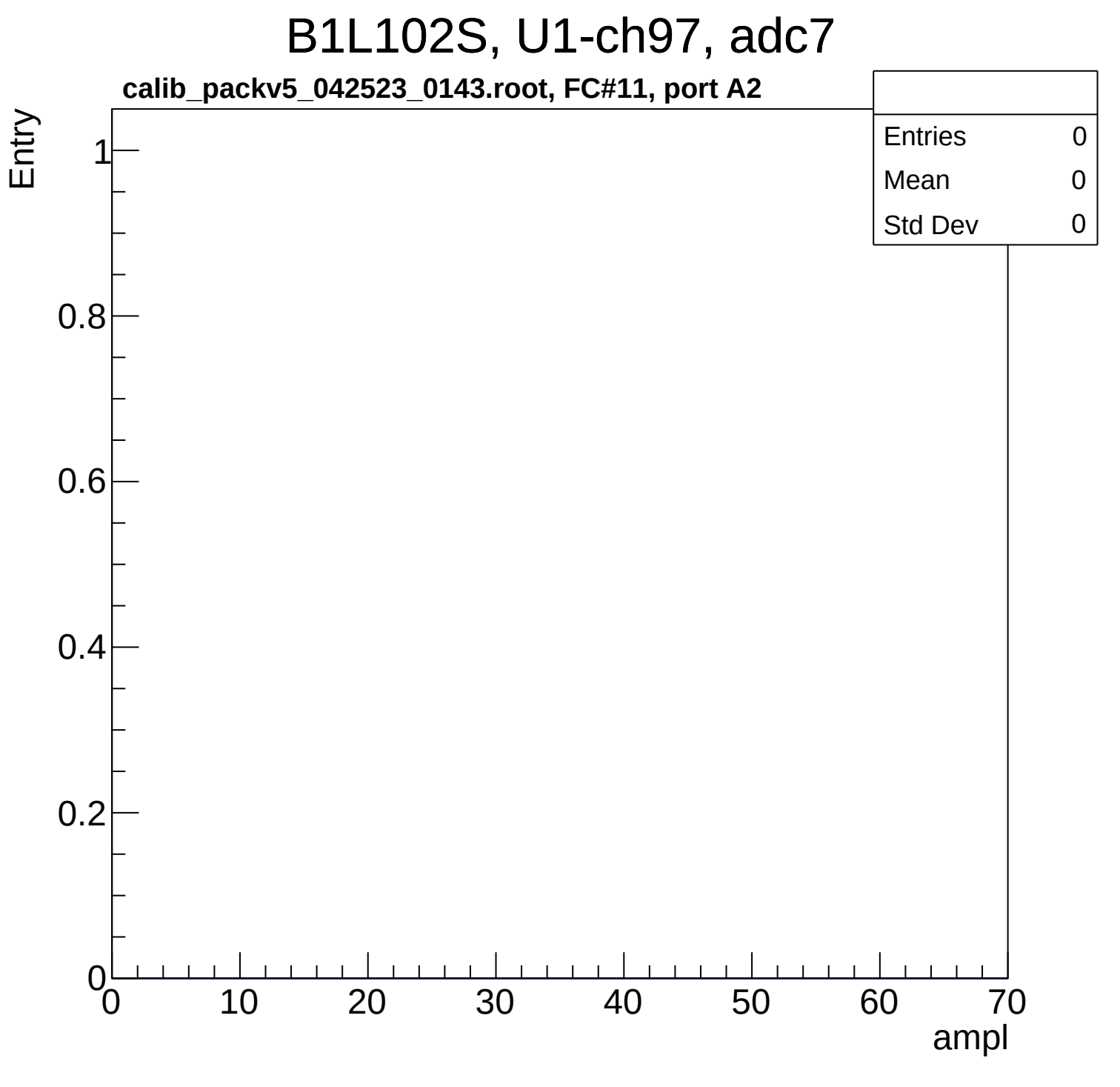
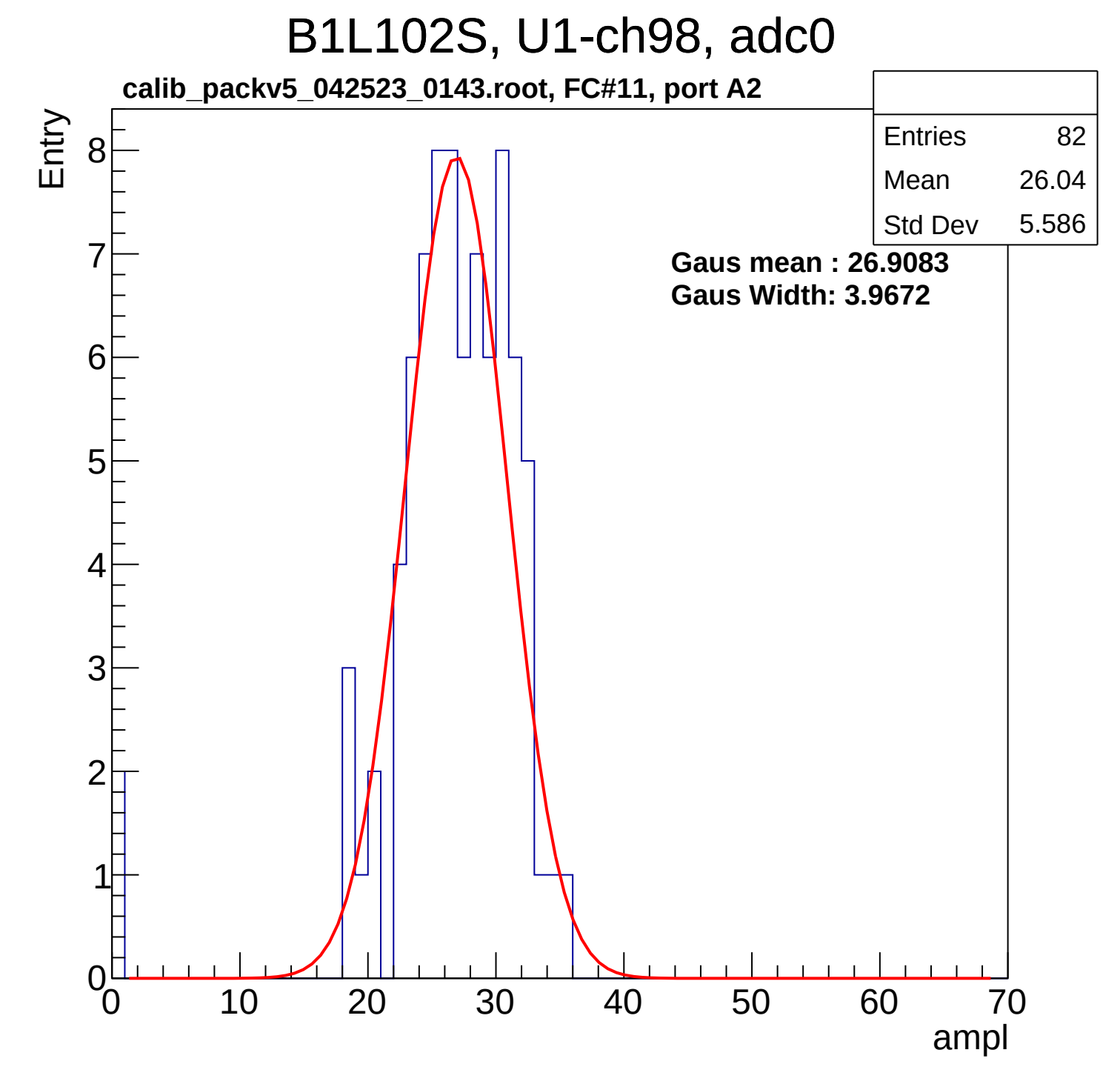
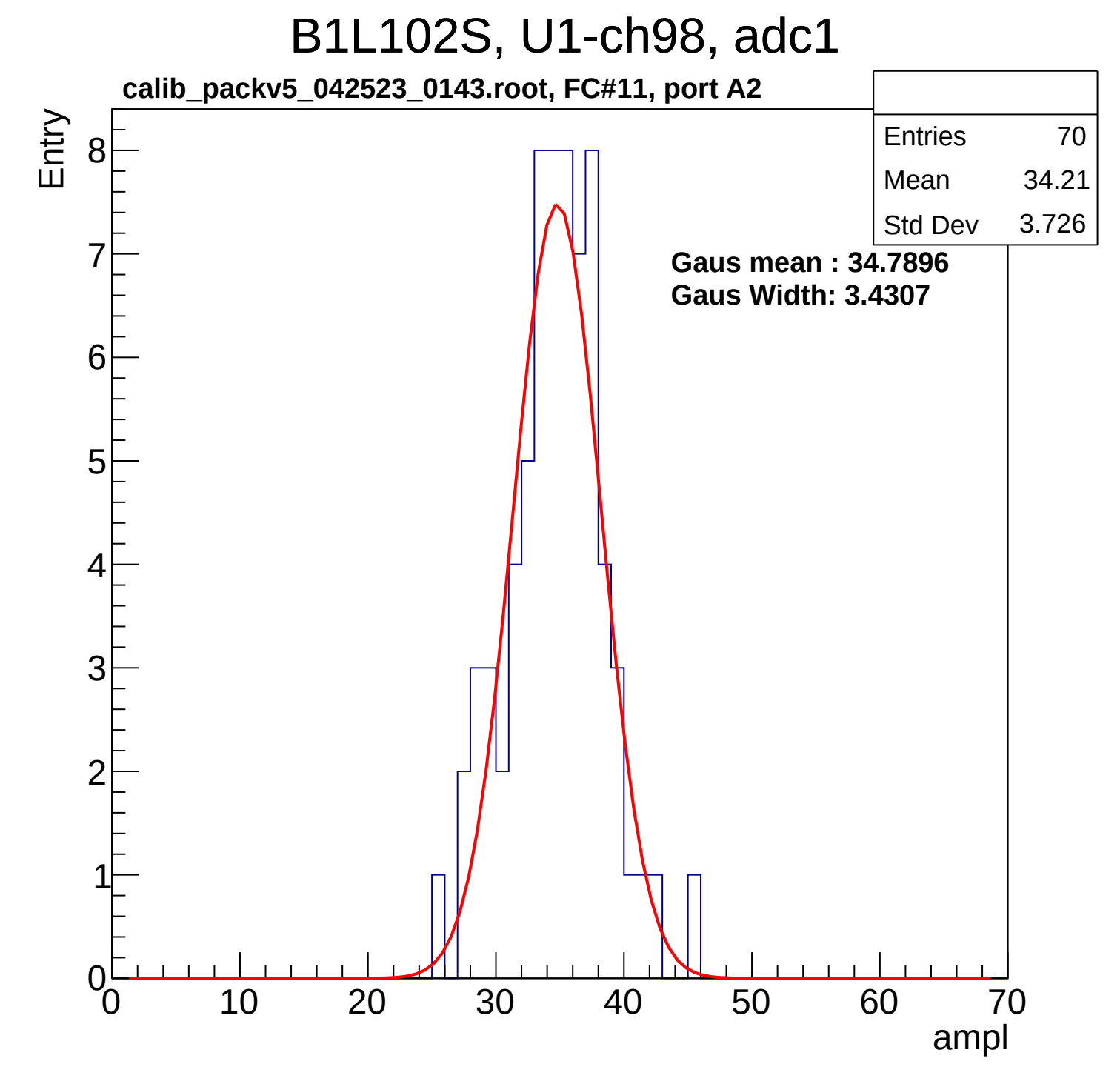
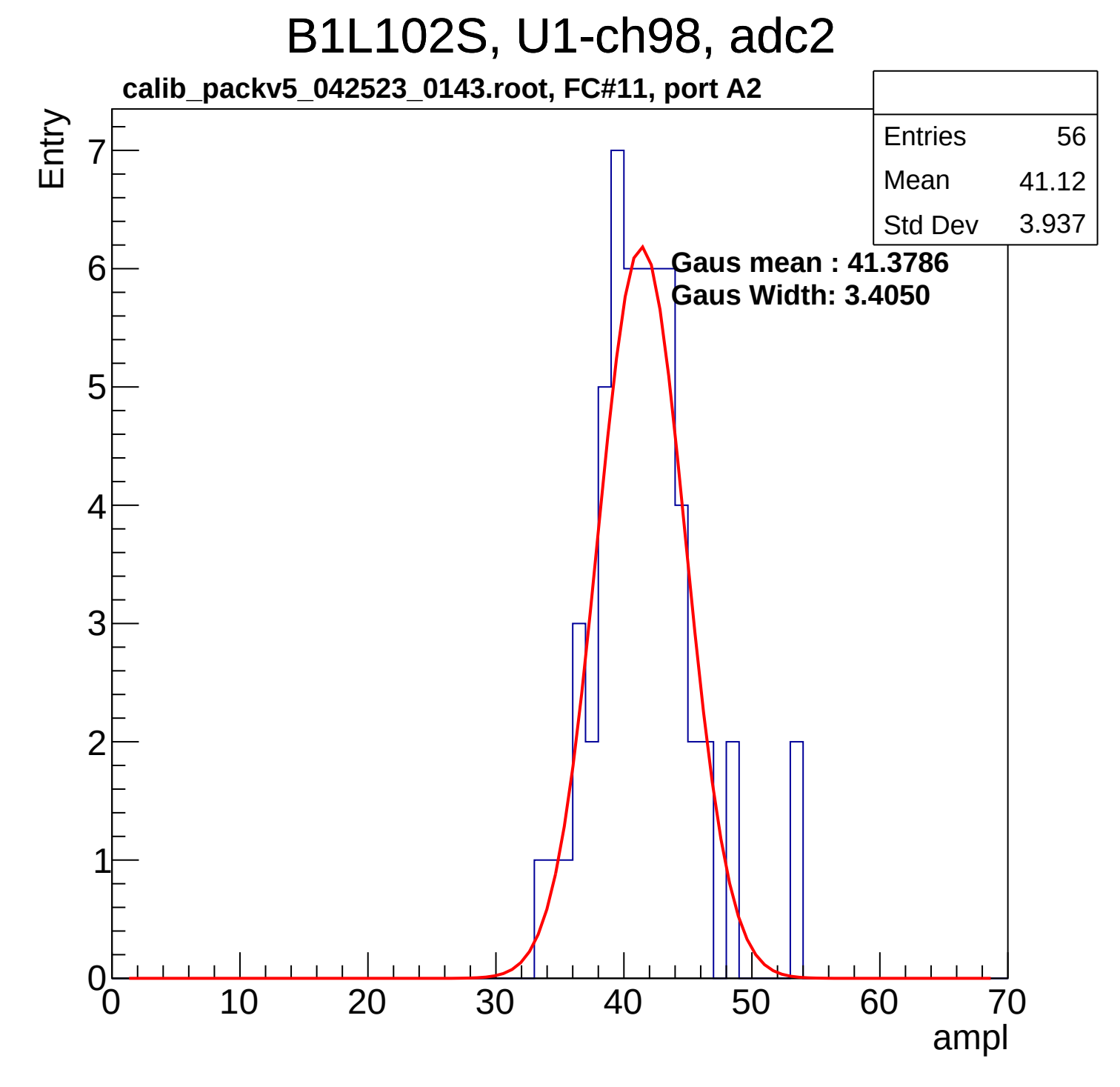

B1L102S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 37
Mean 58.57
Std Dev 10.05
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

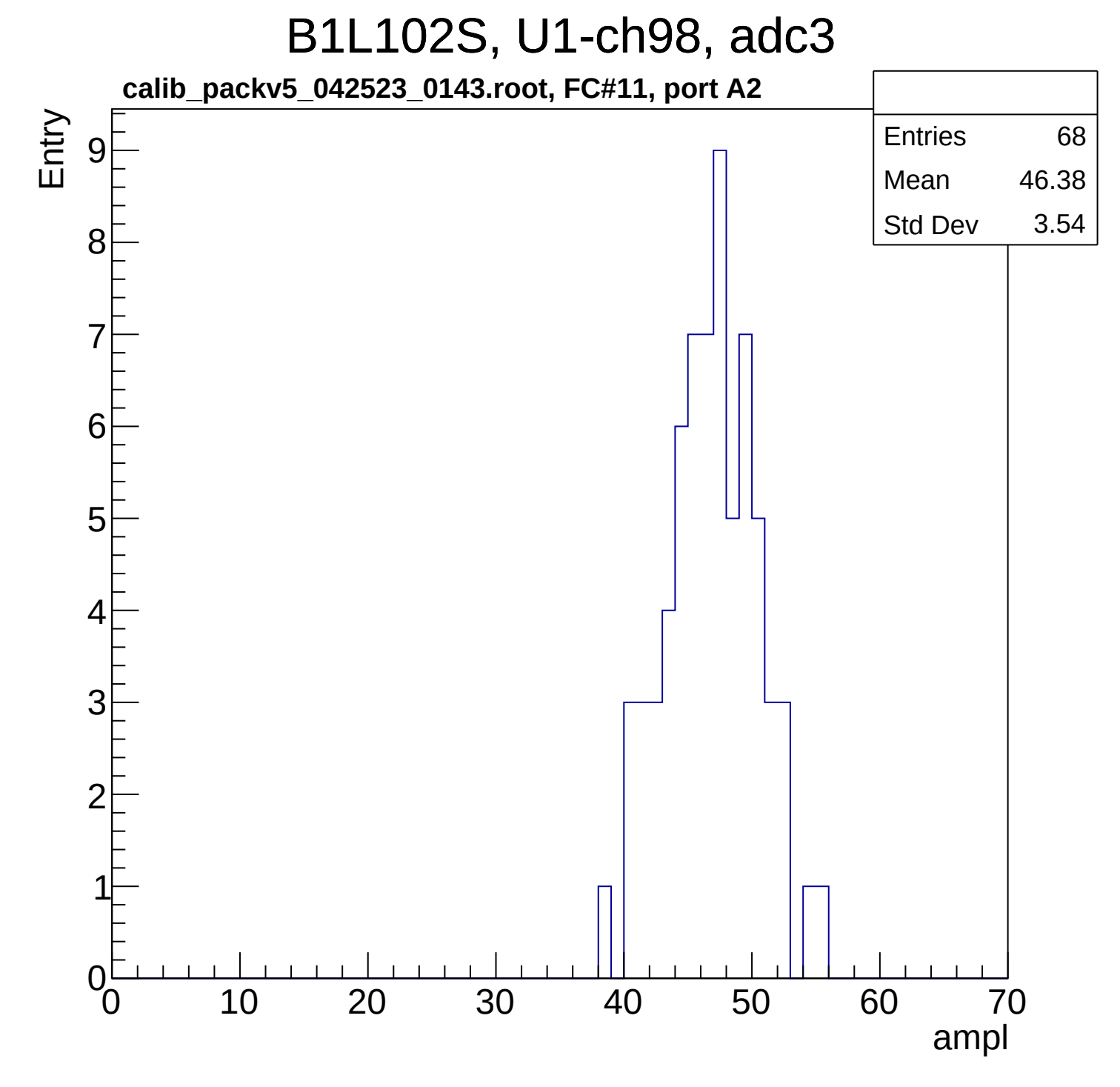

B1L102S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

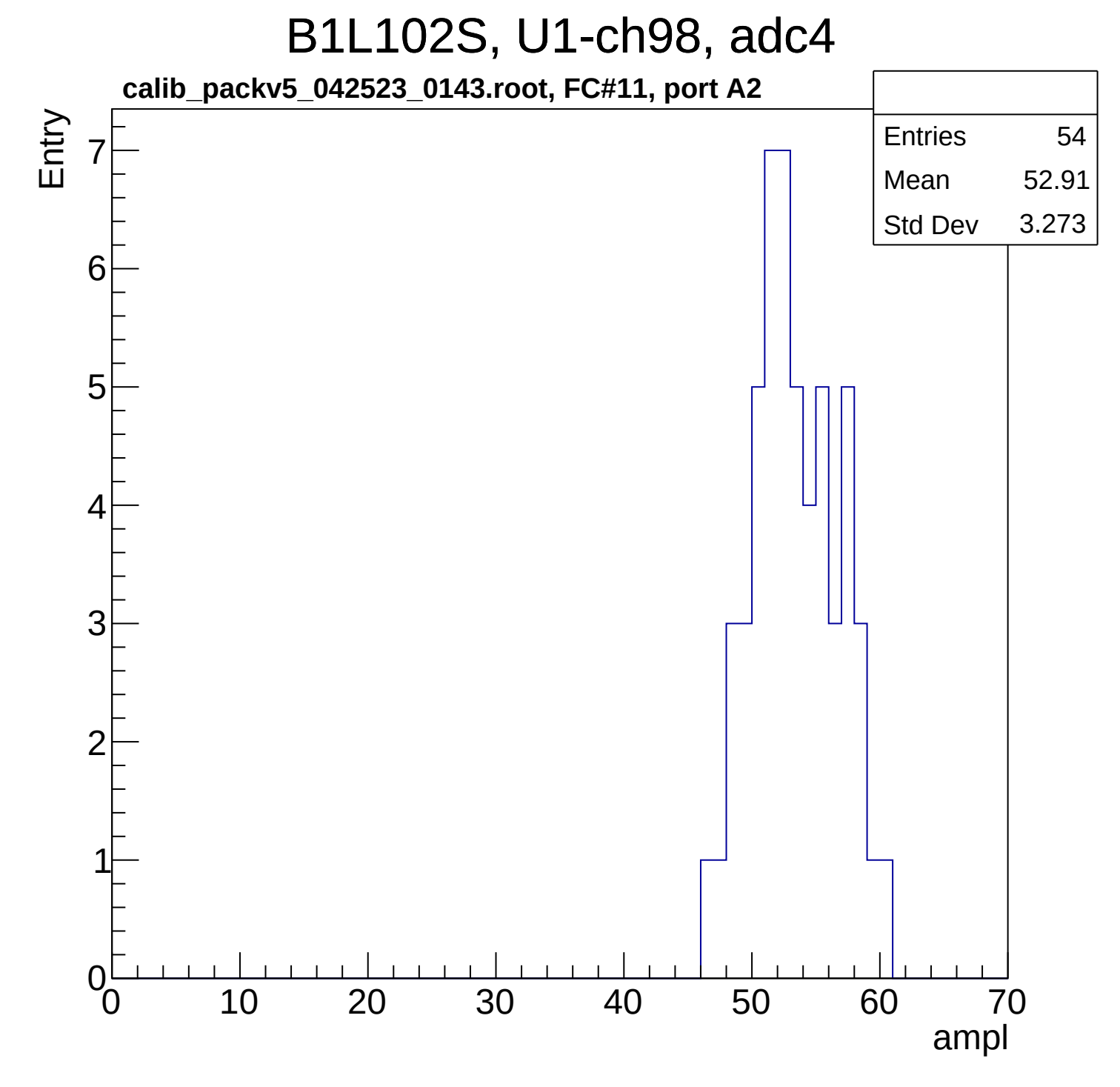

B1L102S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

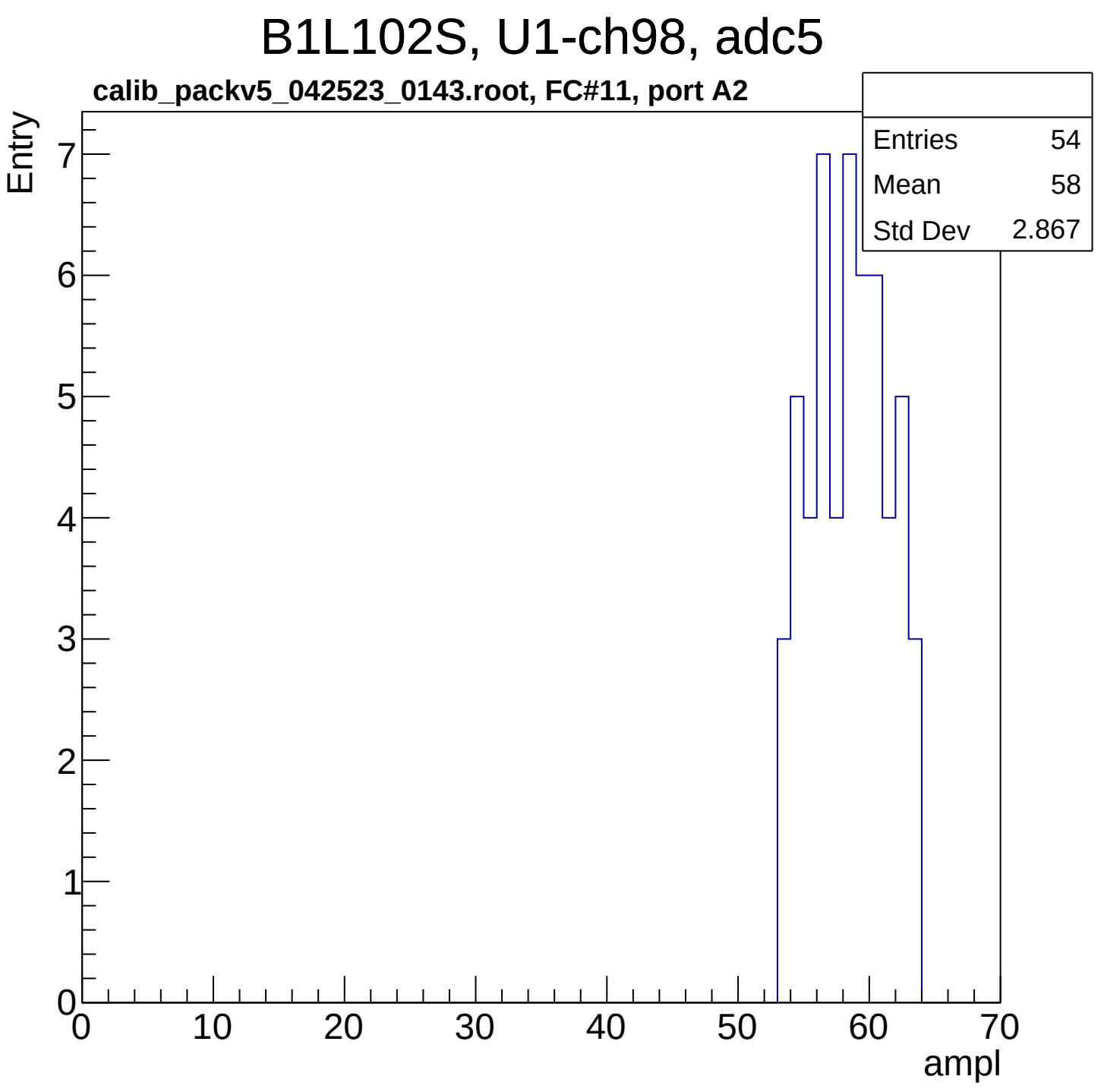

B1L102S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 26.04
Std Dev 5.586
Gaus mean : 26.9083
Gaus Width: 3.9672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

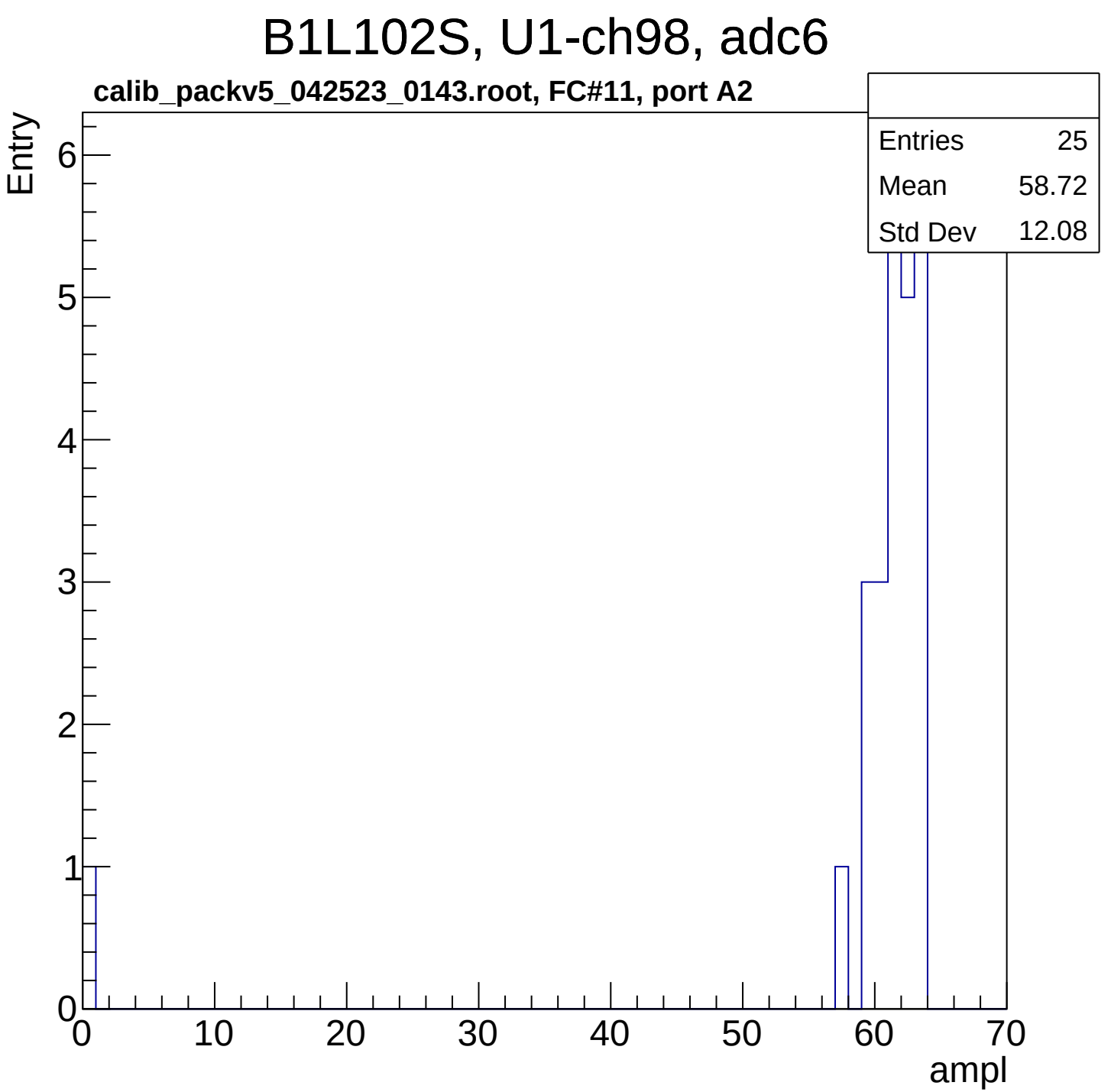

B1L102S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.21
Std Dev 3.726
Gaus mean : 34.7896
Gaus Width: 3.4307
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

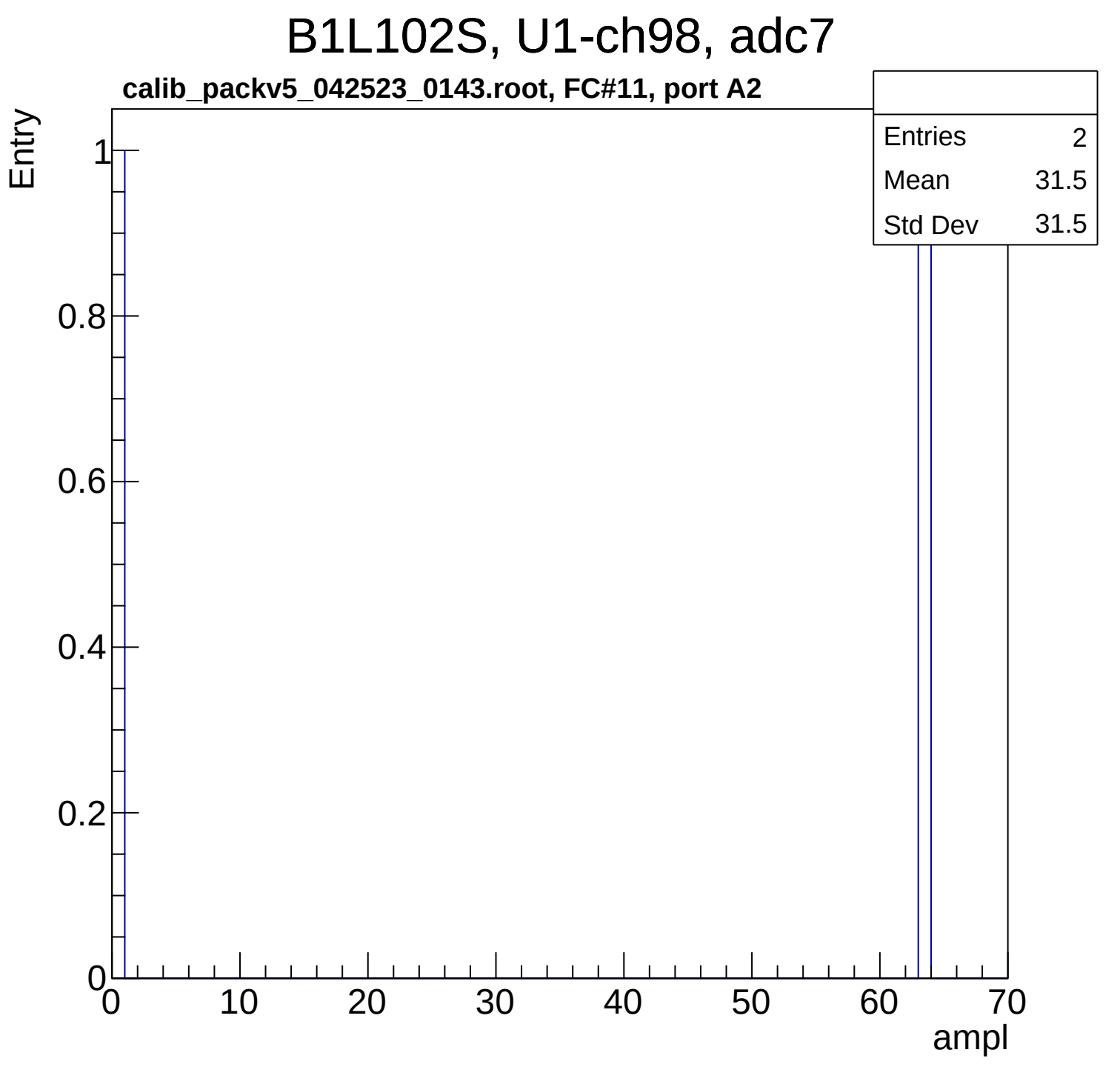

B1L102S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 41.12
Std Dev 3.937
Gaus mean : 41.3786
Gaus Width: 3.4050
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

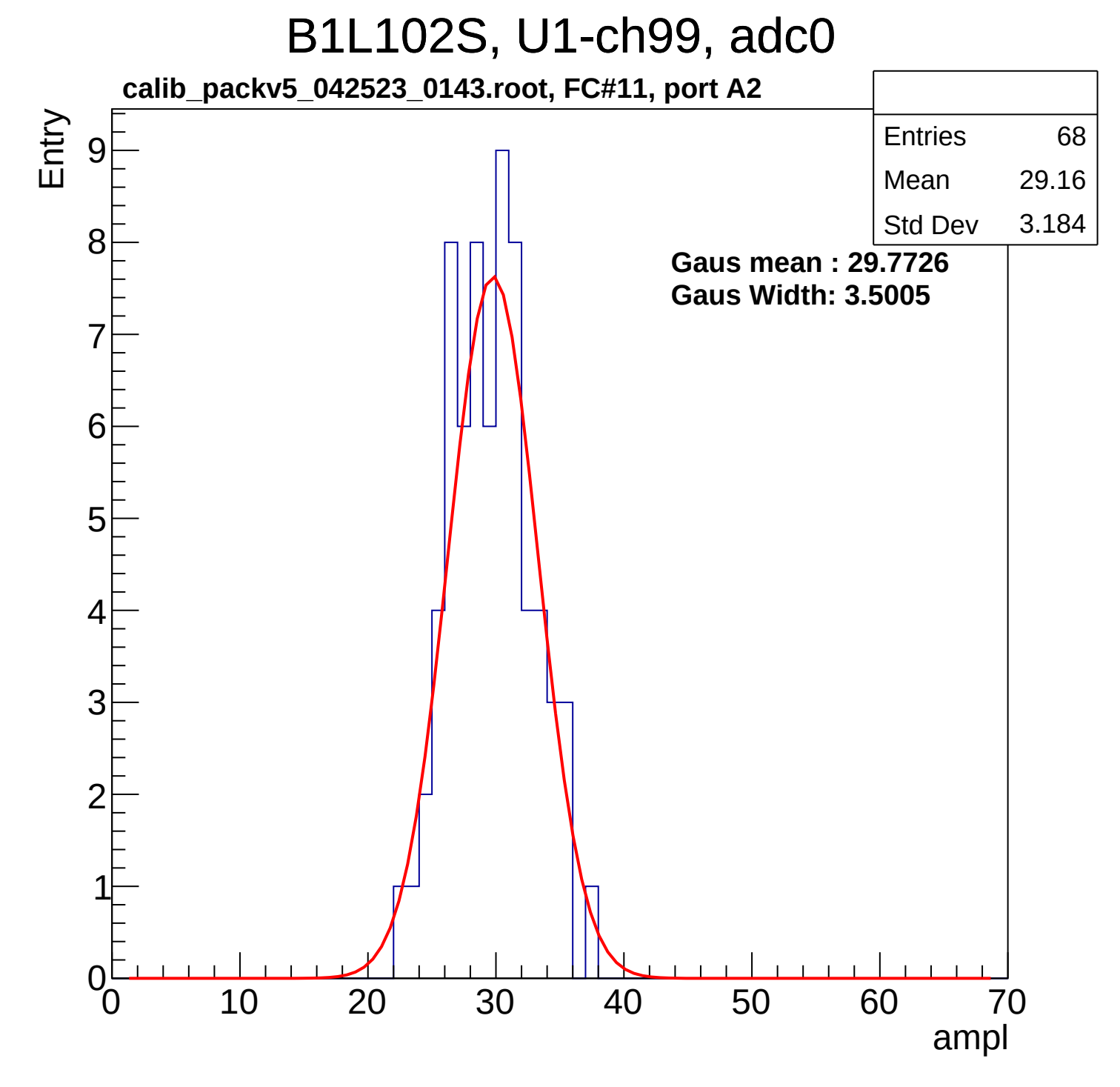

B1L102S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 46.38
Std Dev 3.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

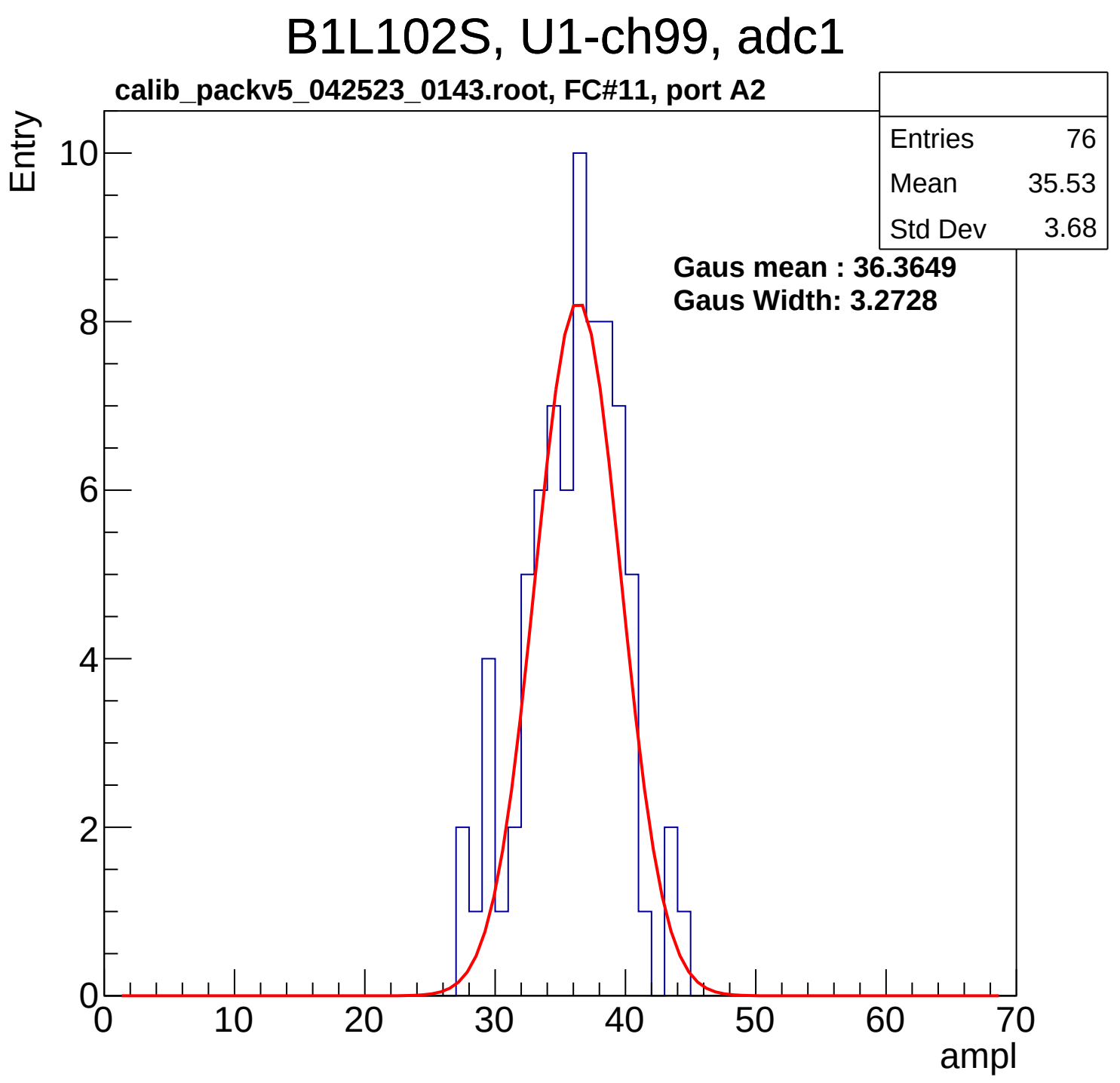

B1L102S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 52.91
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

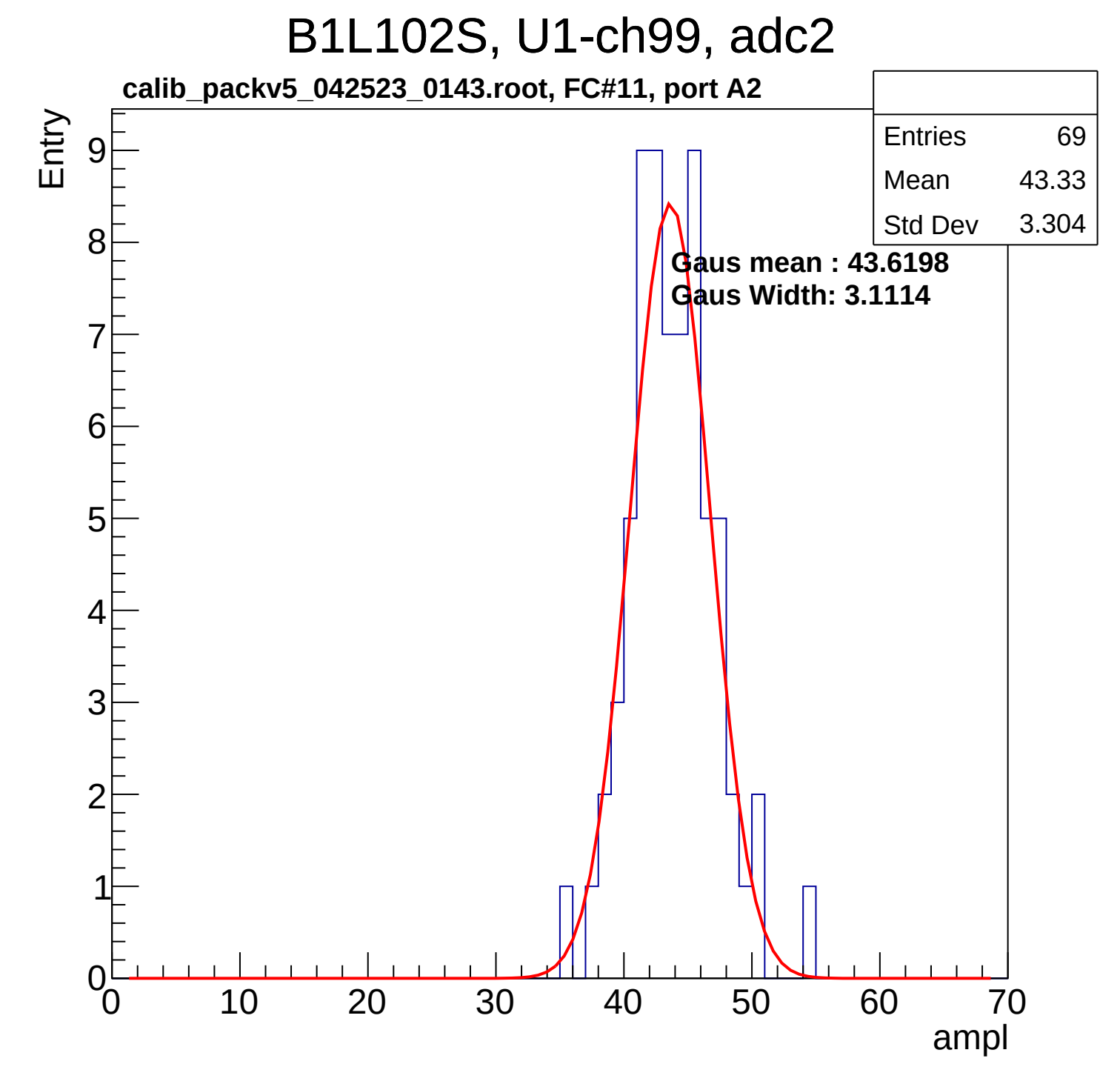

B1L102S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 58
Std Dev 2.867
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

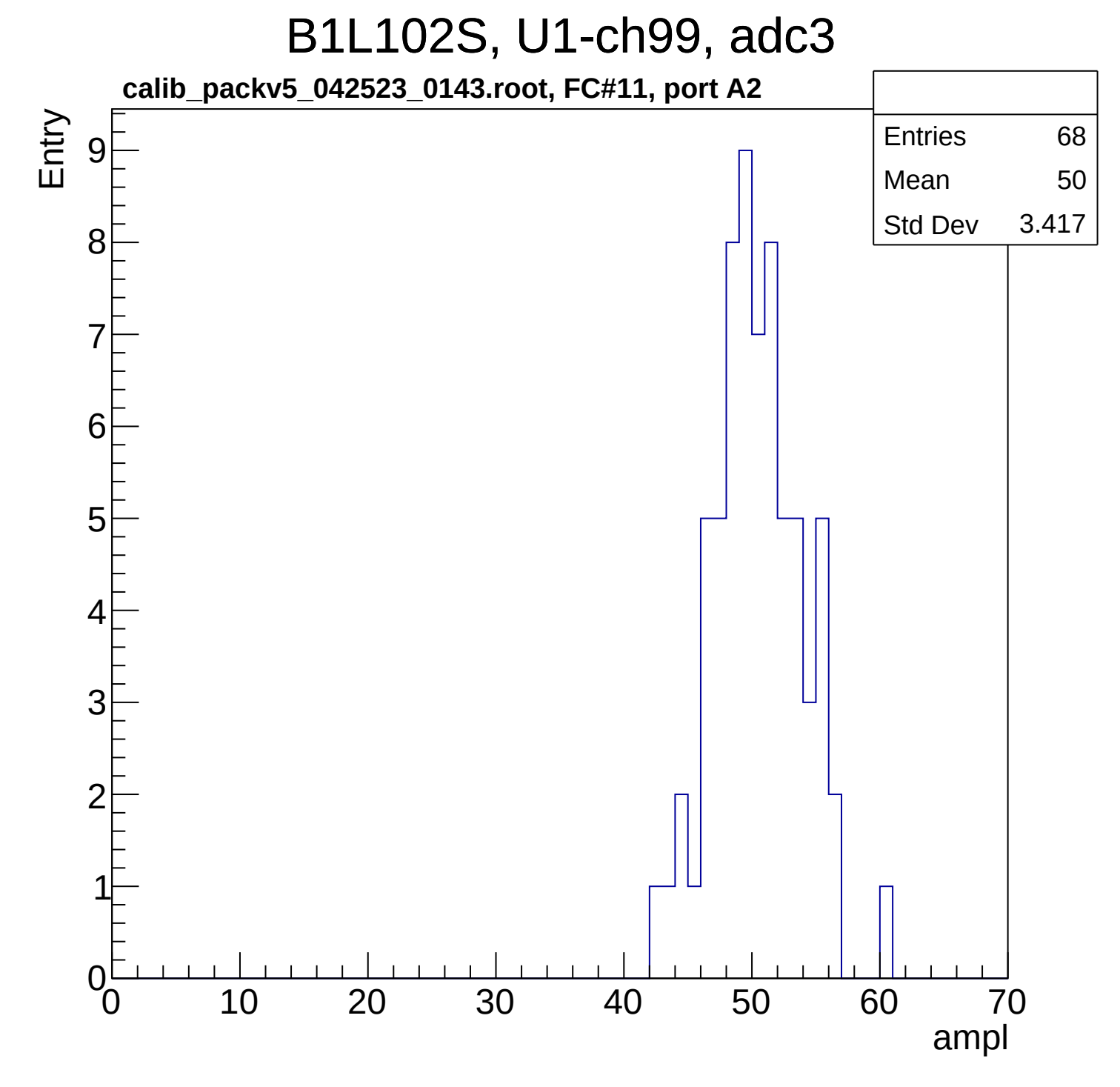

B1L102S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 25
Mean 58.72
Std Dev 12.08
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

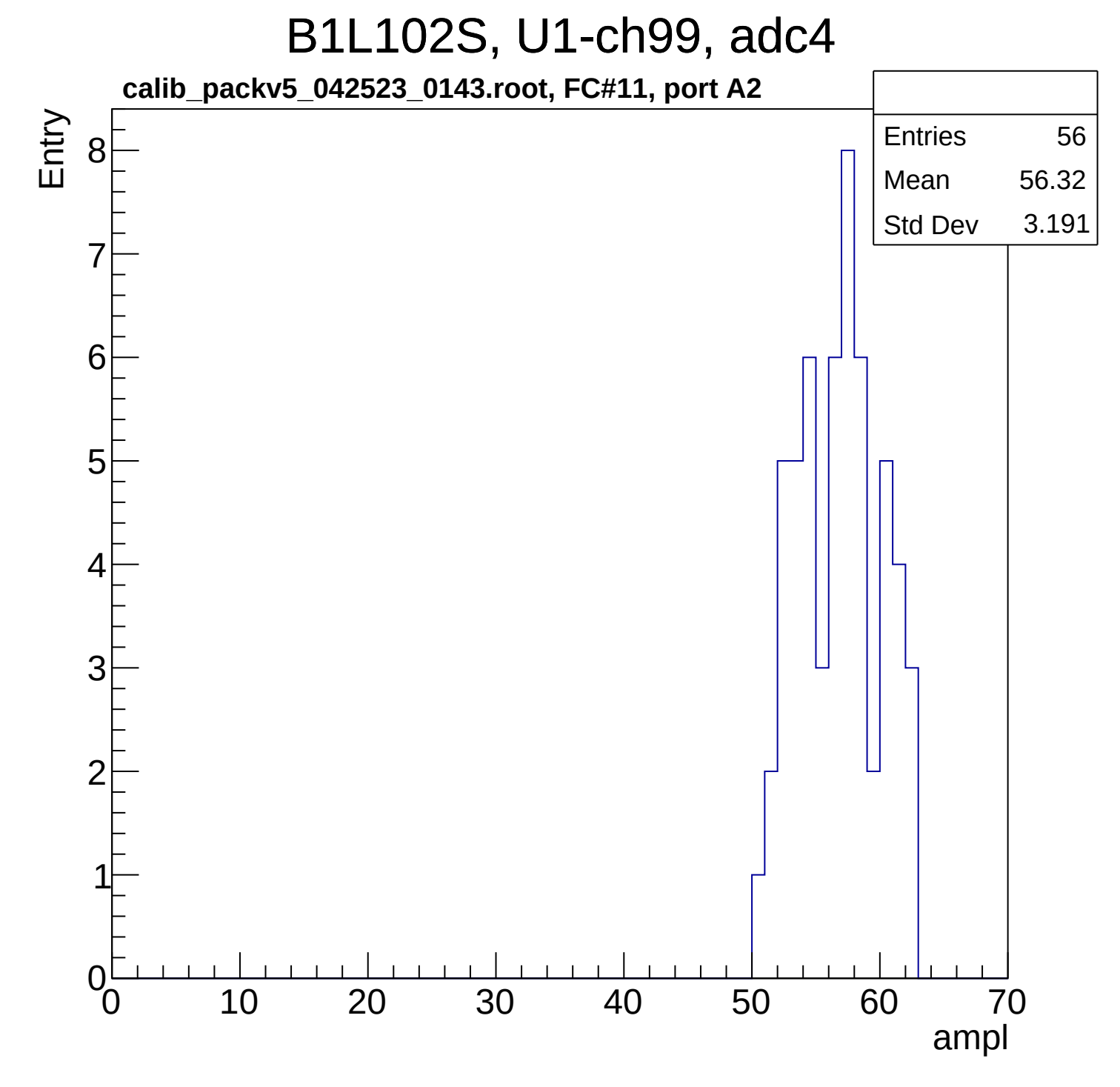

B1L102S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

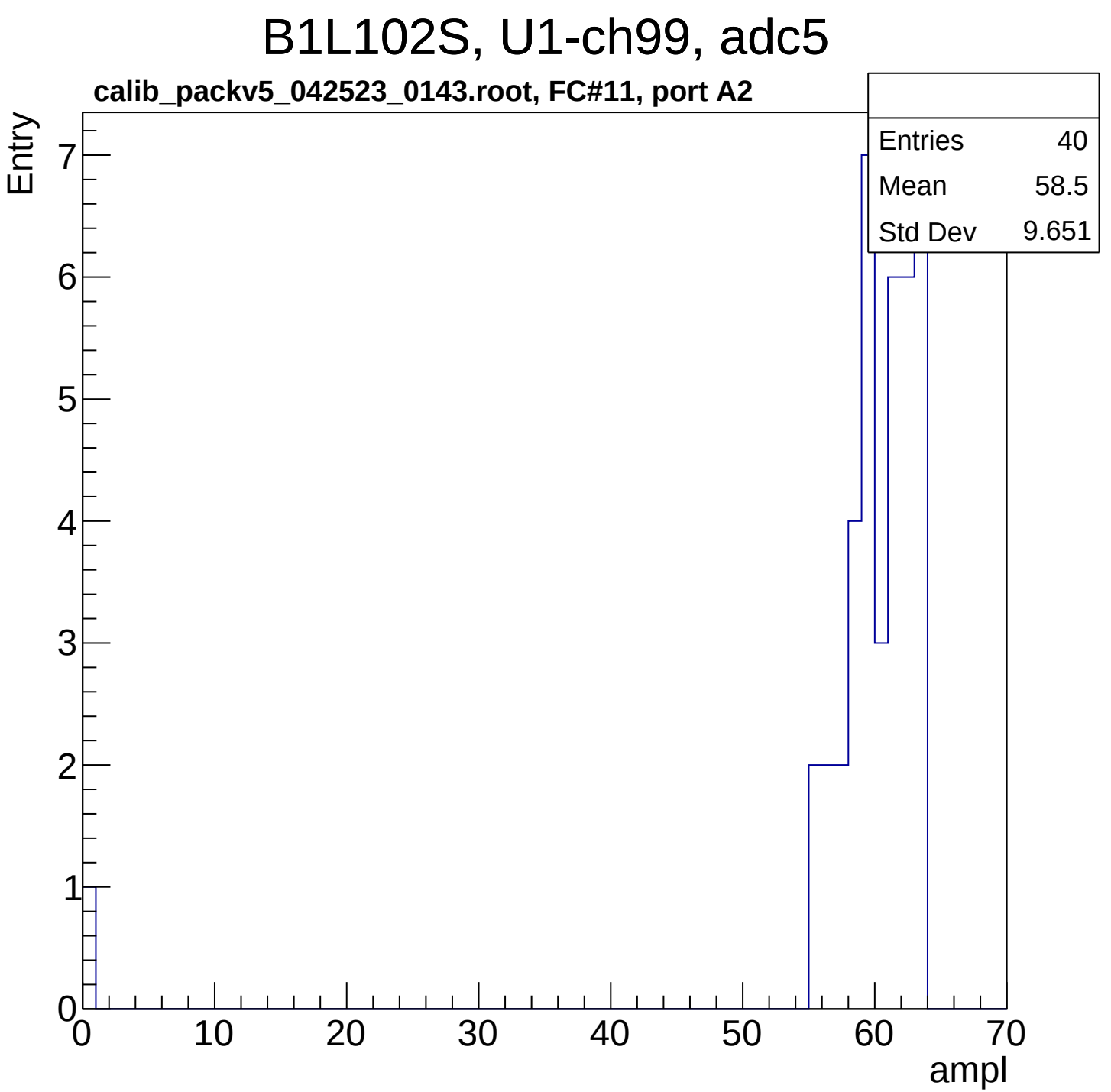

B1L102S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.16
Std Dev 3.184
Gaus mean : 29.7726
Gaus Width: 3.5005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

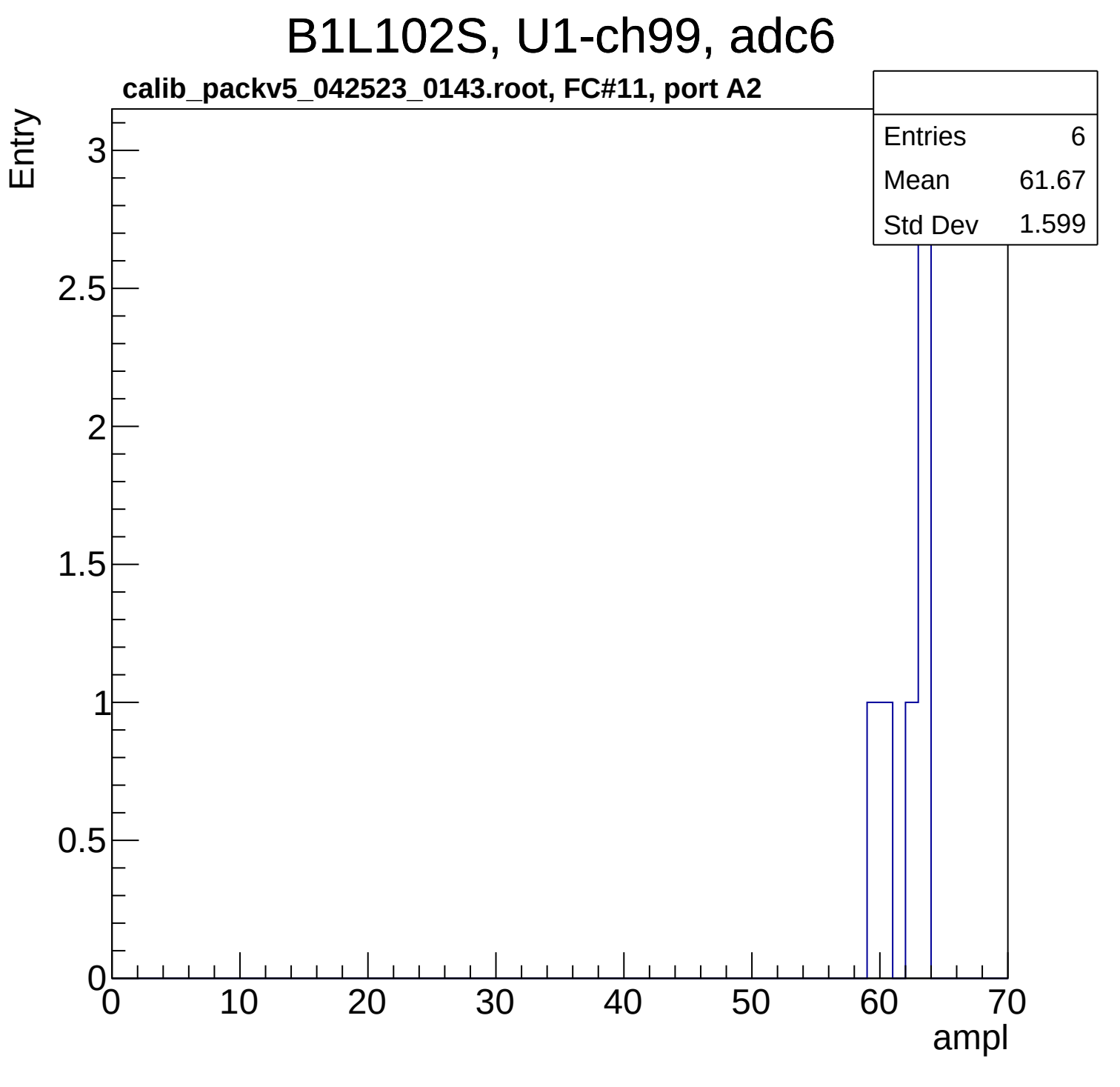

B1L102S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 35.53
Std Dev 3.68
Gaus mean : 36.3649
Gaus Width: 3.2728
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

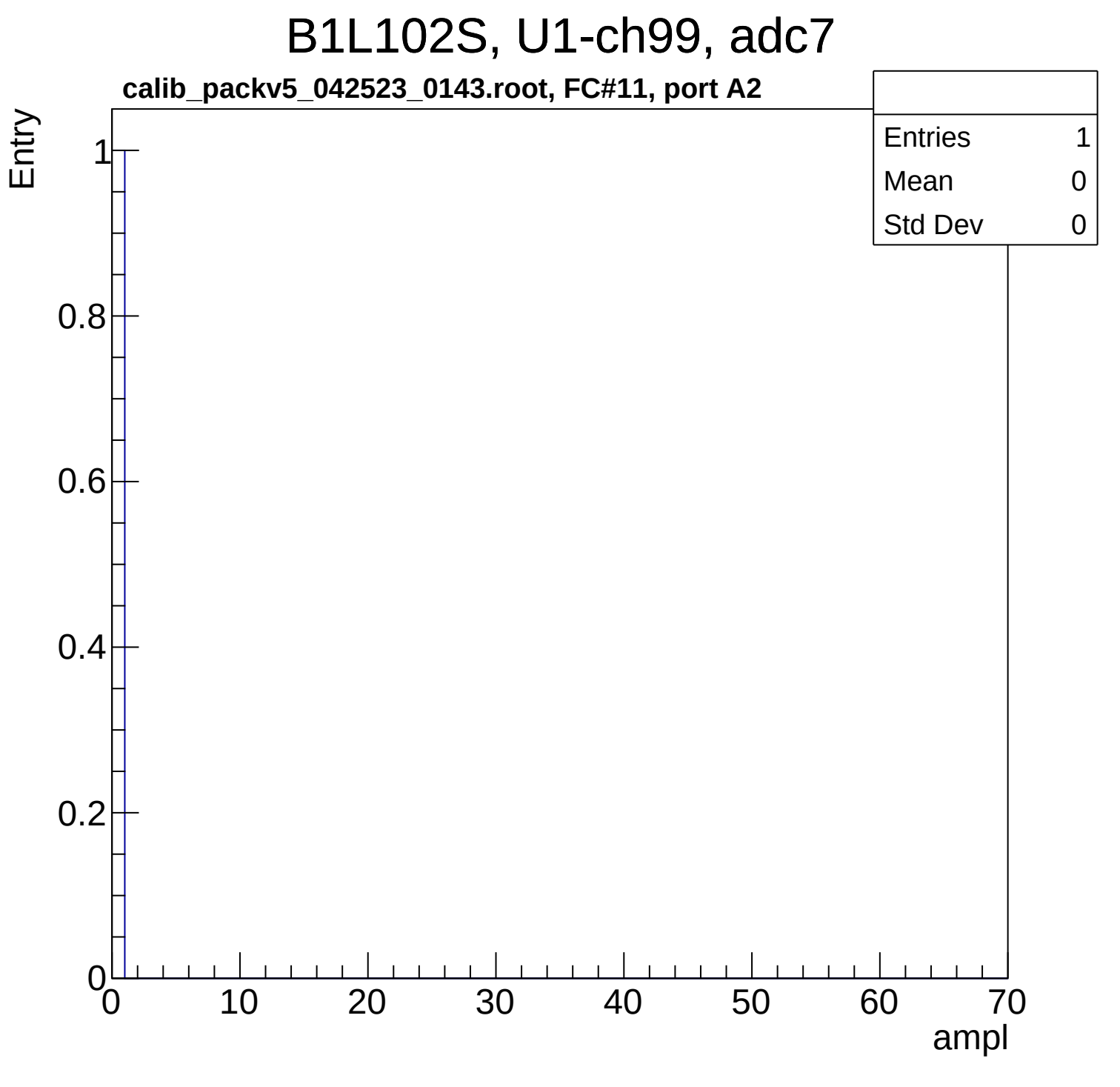

B1L102S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 43.33
Std Dev 3.304
Gaus mean : 43.6198
Gaus Width: 3.1114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

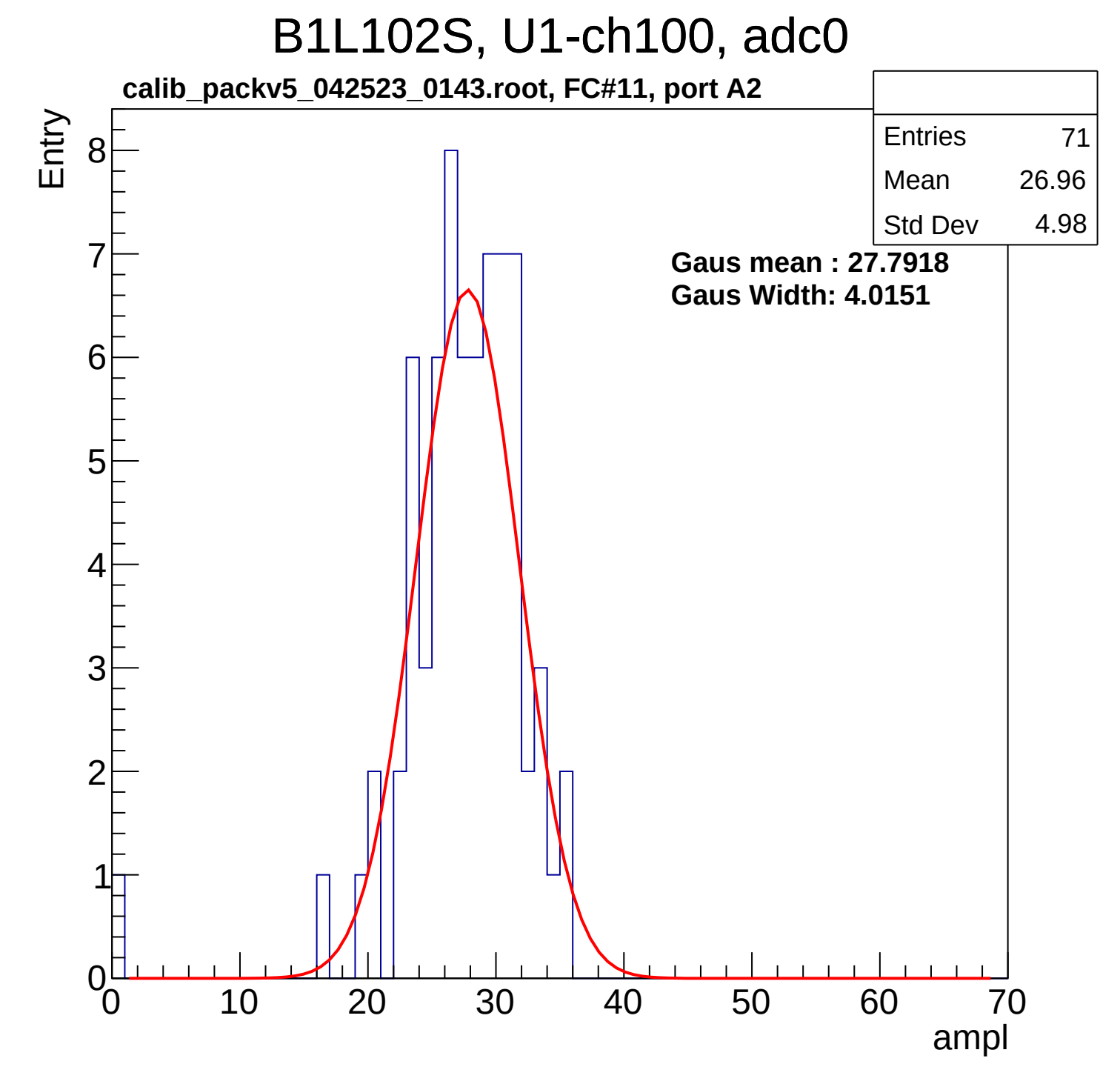

B1L102S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 50
Std Dev 3.417
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

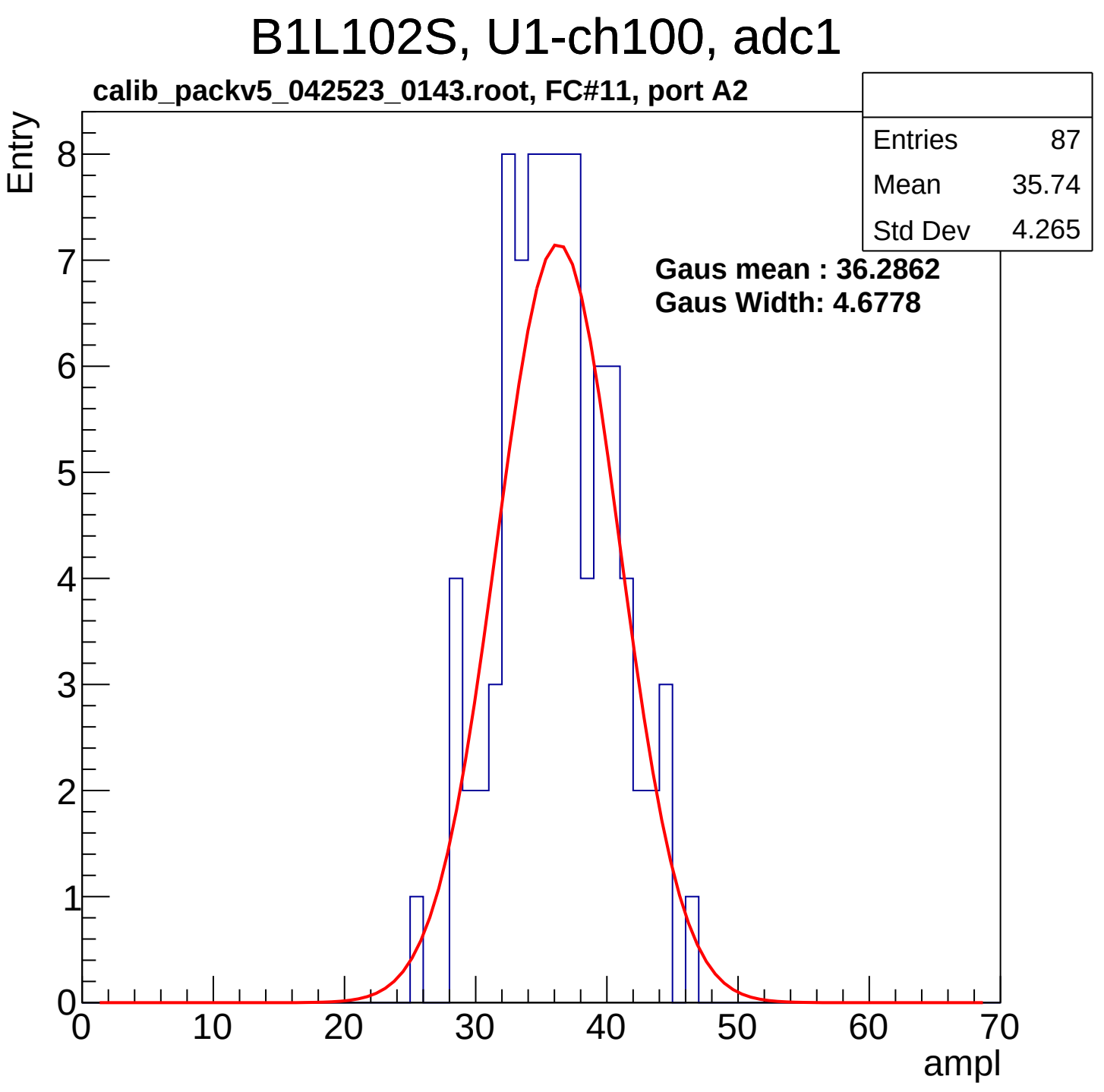

B1L102S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 56.32
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

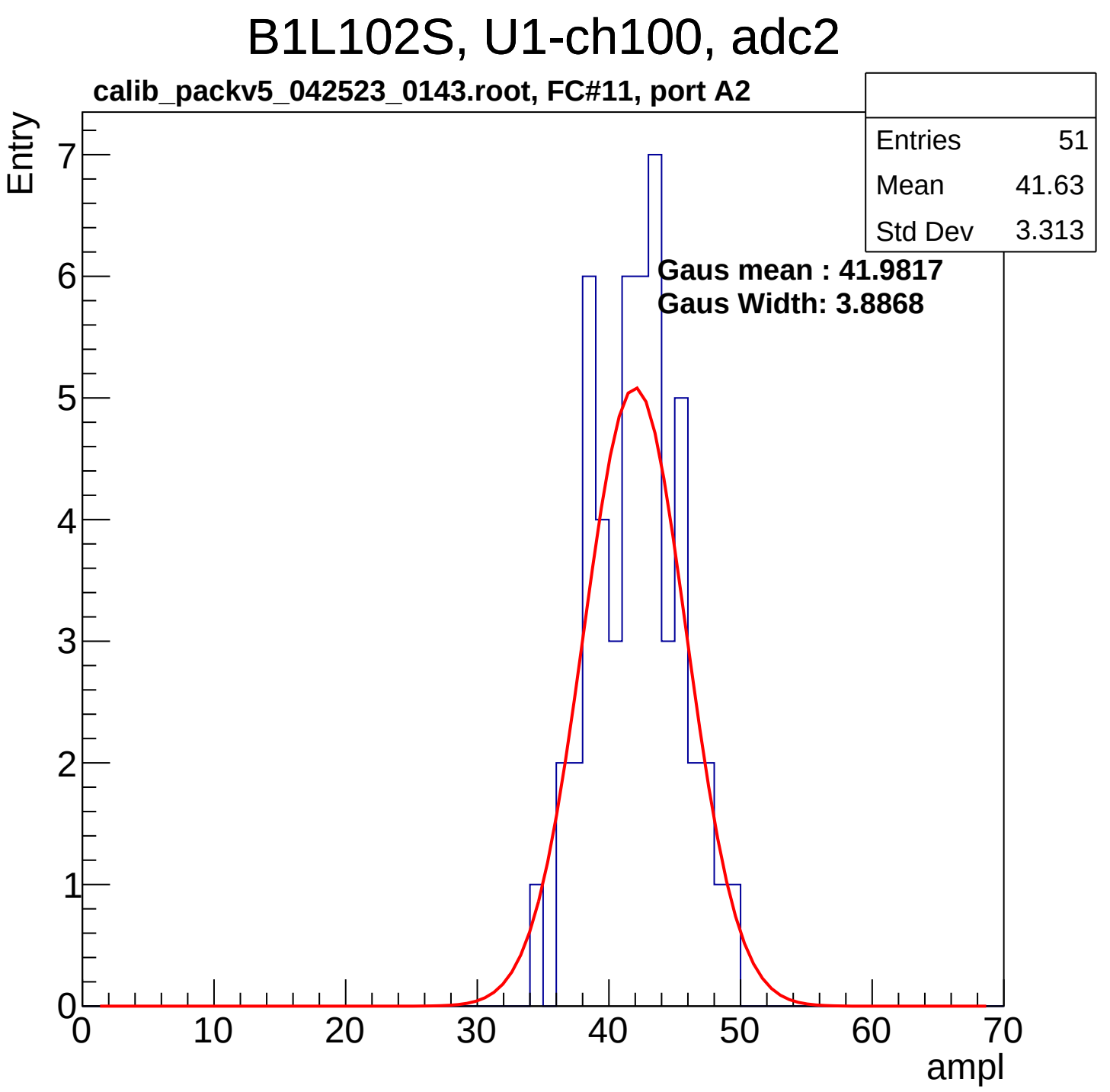

B1L102S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.5
Std Dev 9.651
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

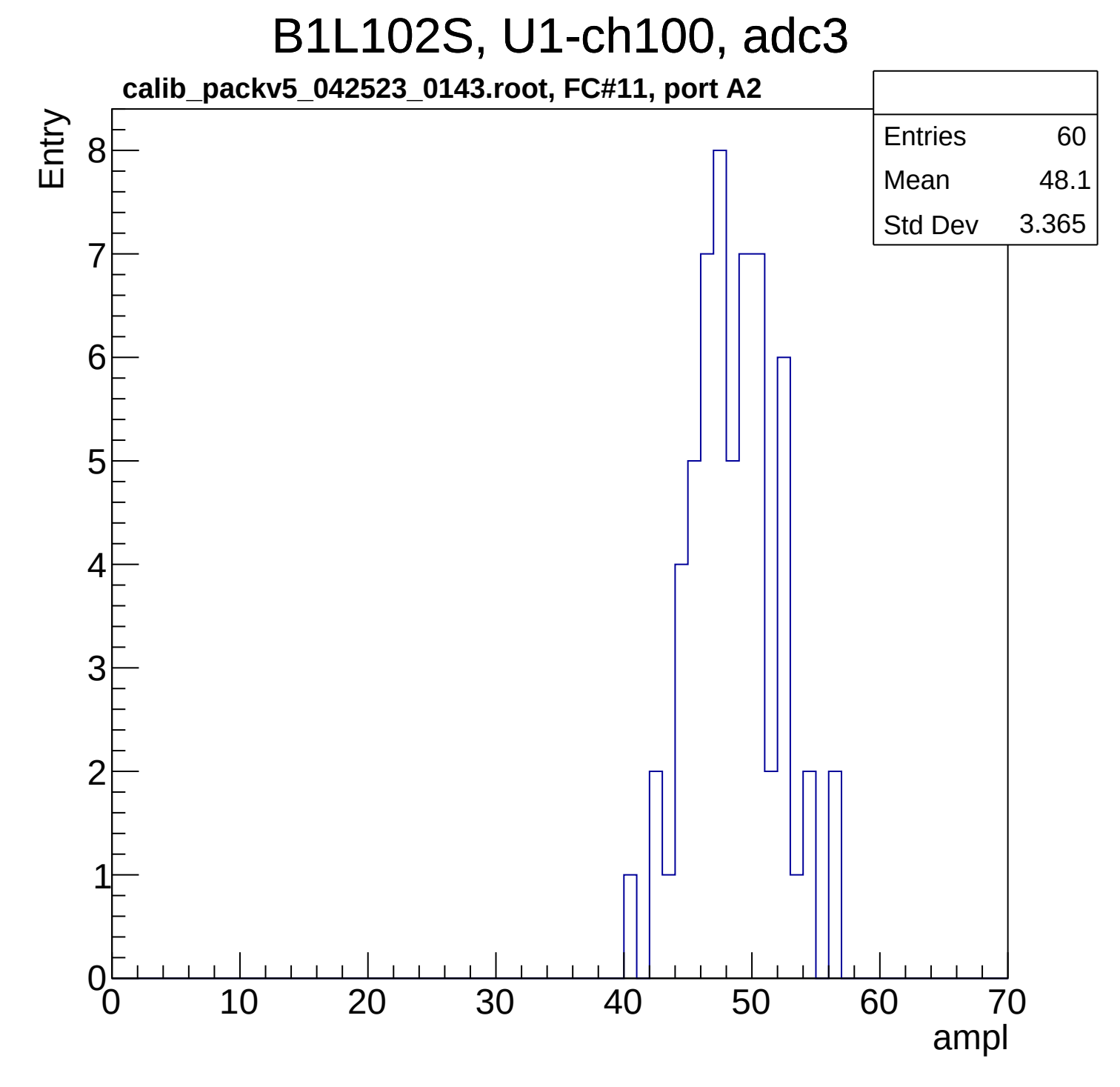

B1L102S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 61.67
Std Dev 1.599
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

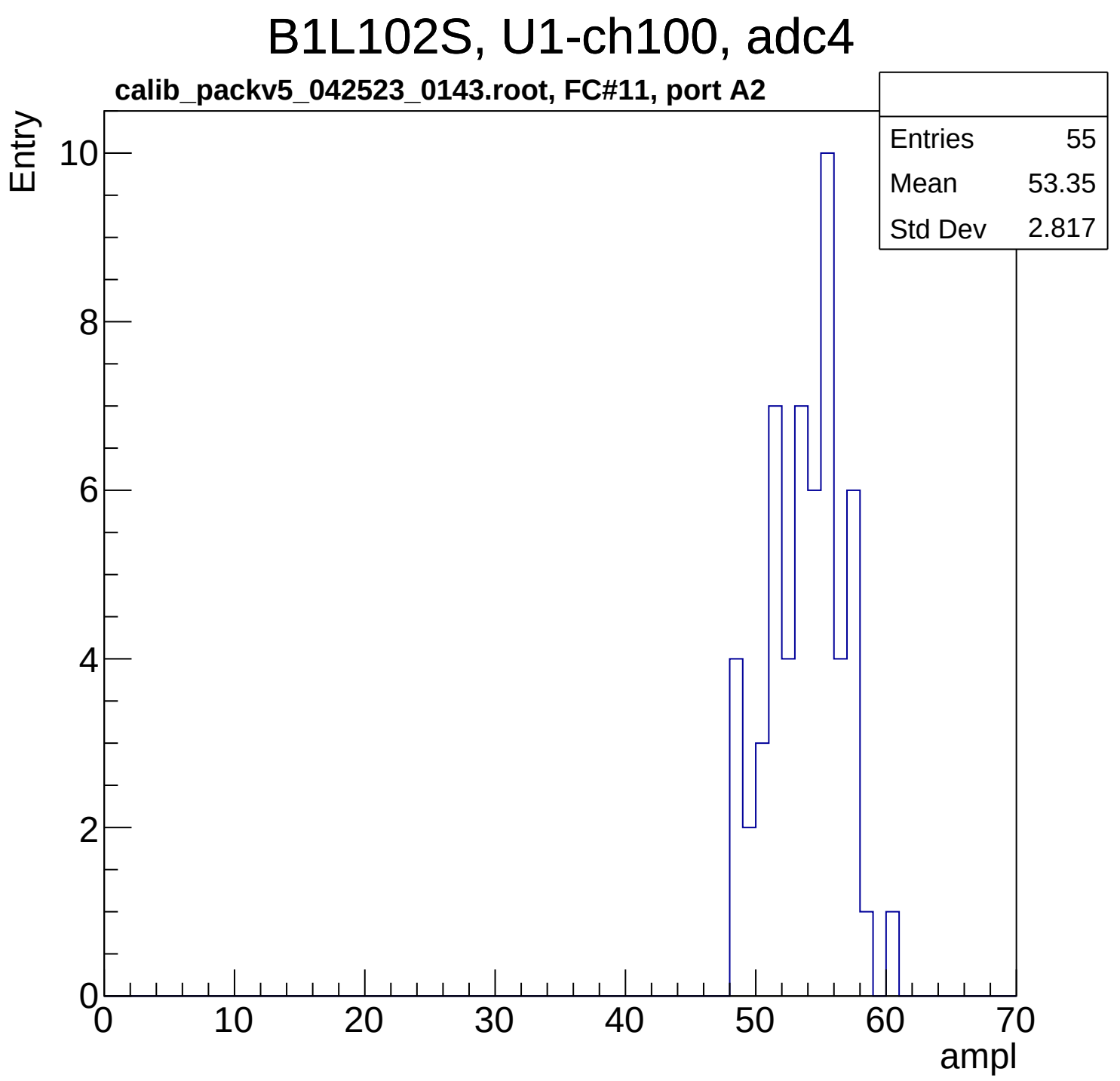

B1L102S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

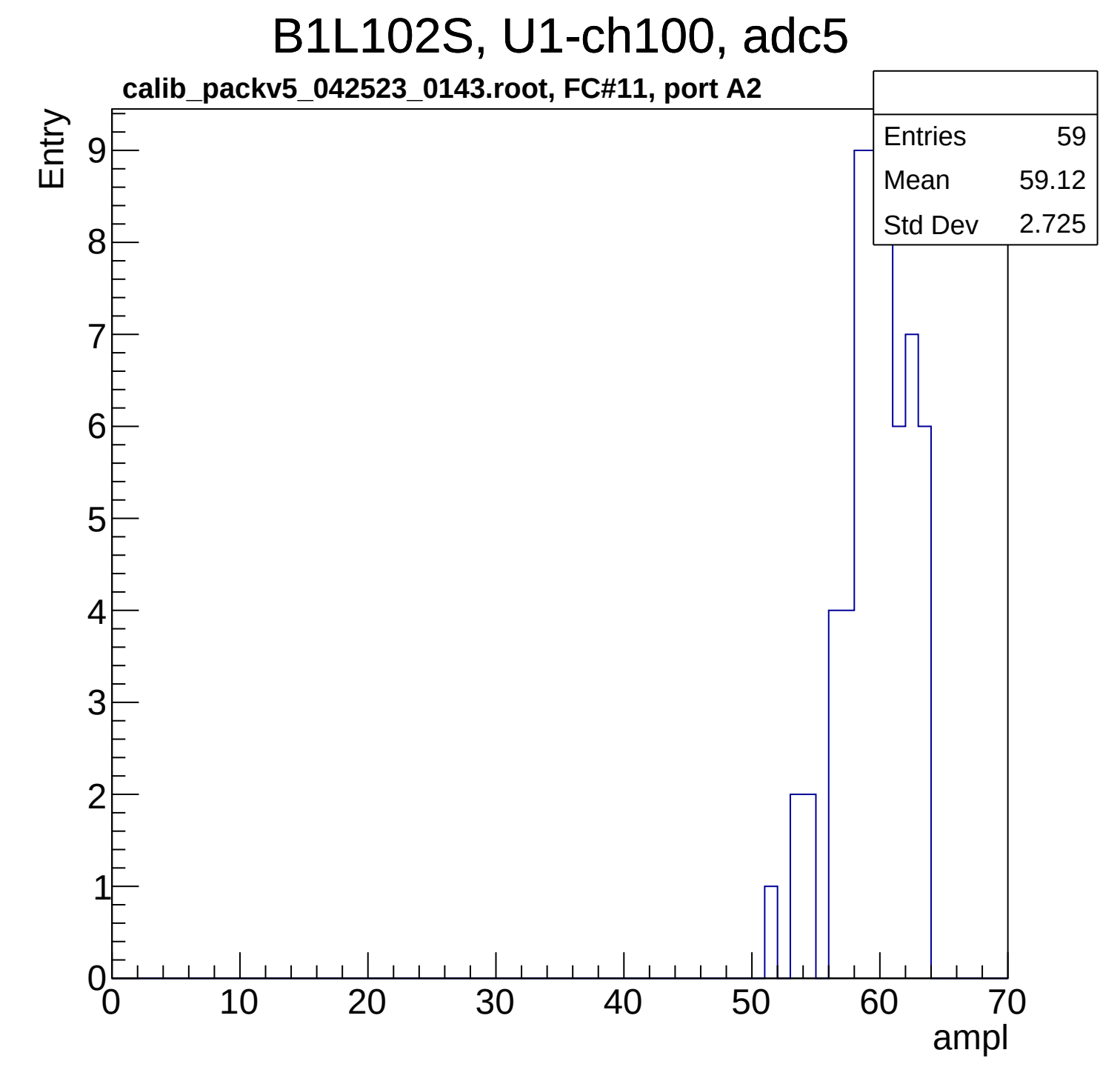

B1L102S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 26.96
Std Dev 4.98
Gaus mean : 27.7918
Gaus Width: 4.0151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 87
Mean 35.74
Std Dev 4.265
Gaus mean : 36.2862
Gaus Width: 4.6778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 41.63
Std Dev 3.313
Gaus mean : 41.9817
Gaus Width: 3.8868
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 48.1
Std Dev 3.365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.35
Std Dev 2.817
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 59.12
Std Dev 2.725
Entry
ampl
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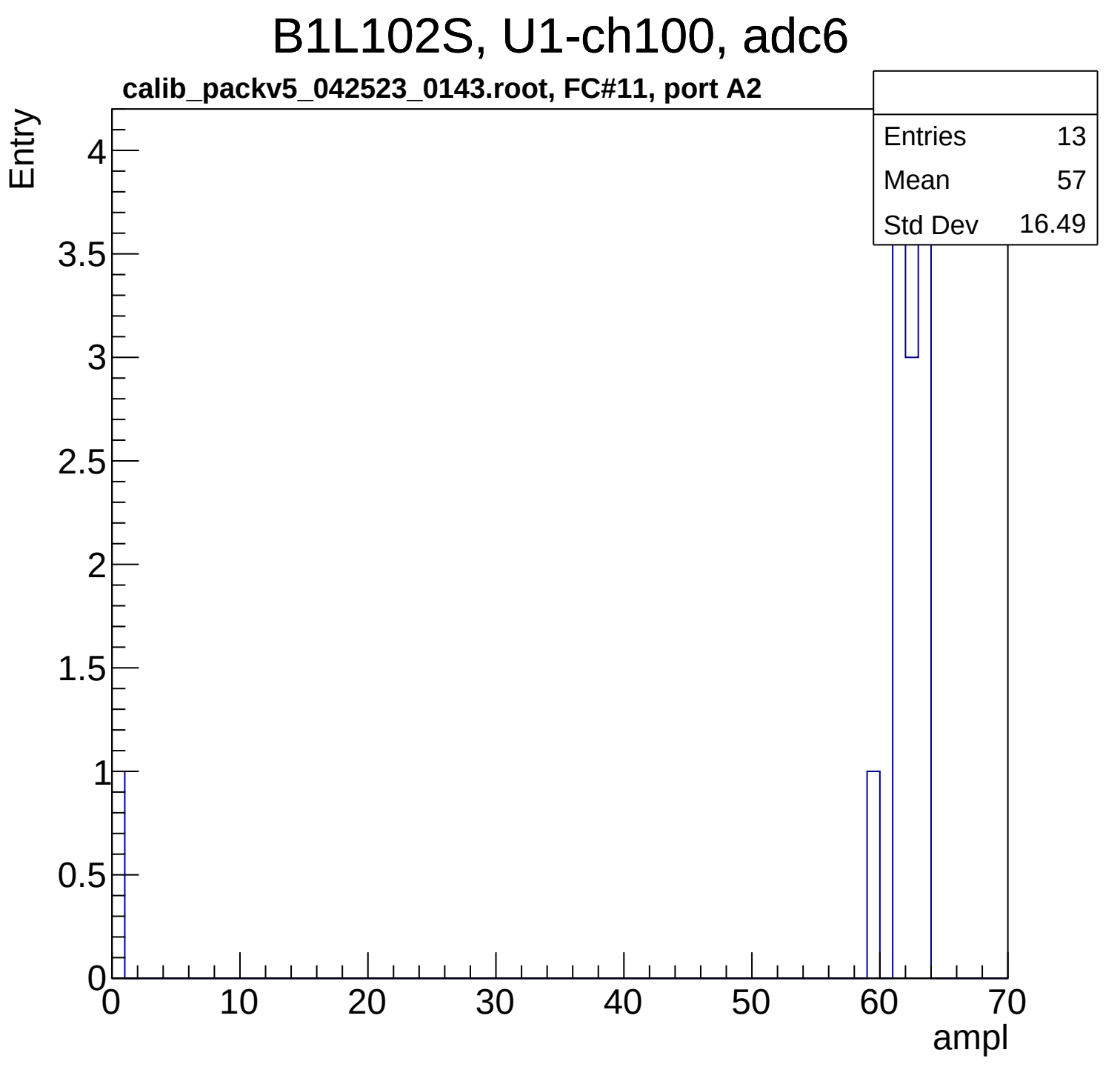
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 57
Std Dev 16.49
Entry
ampl
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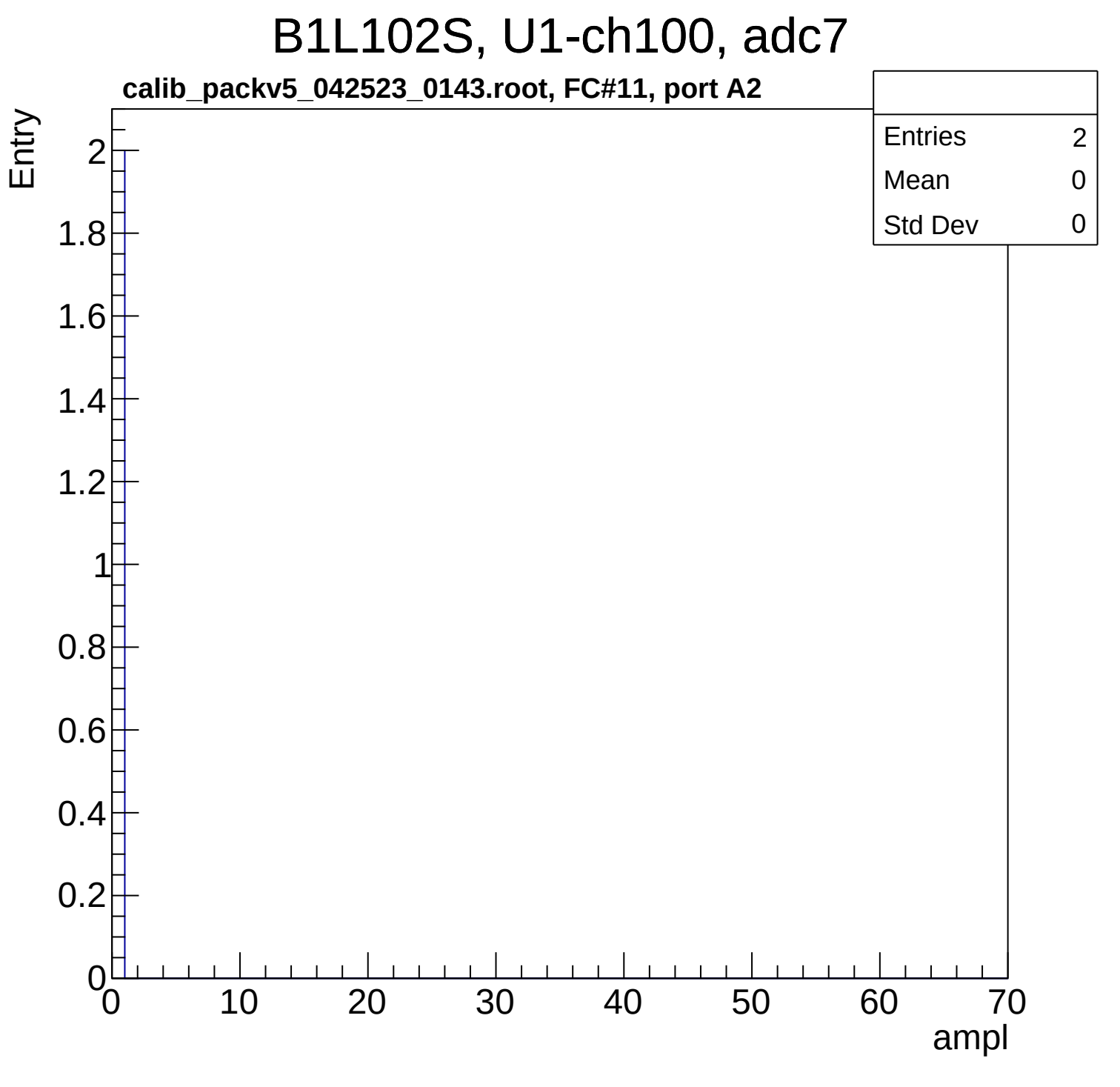
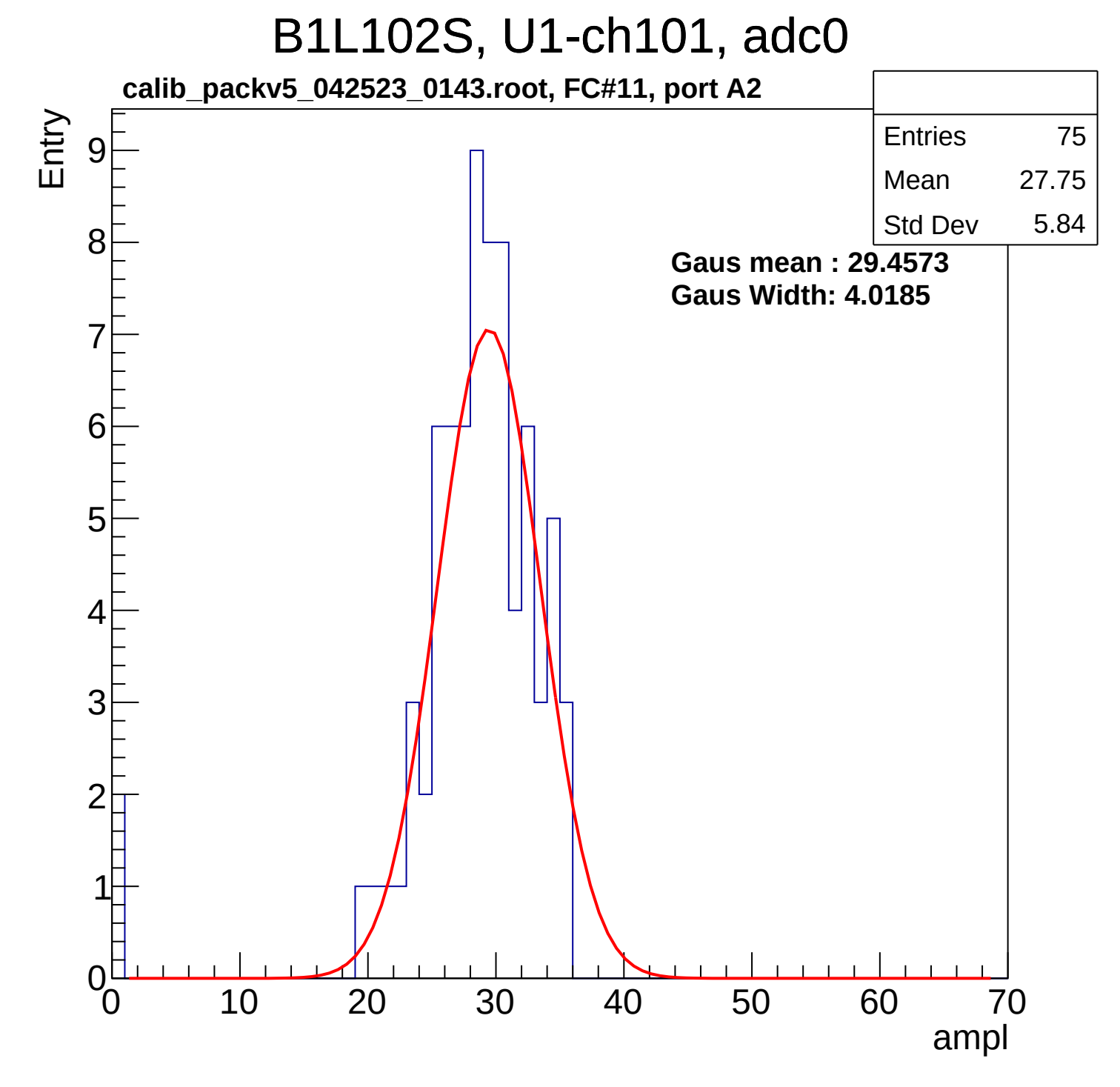
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 27.75
Std Dev 5.84
Gaus mean : 29.4573
Gaus Width: 4.0185
Entry
ampl
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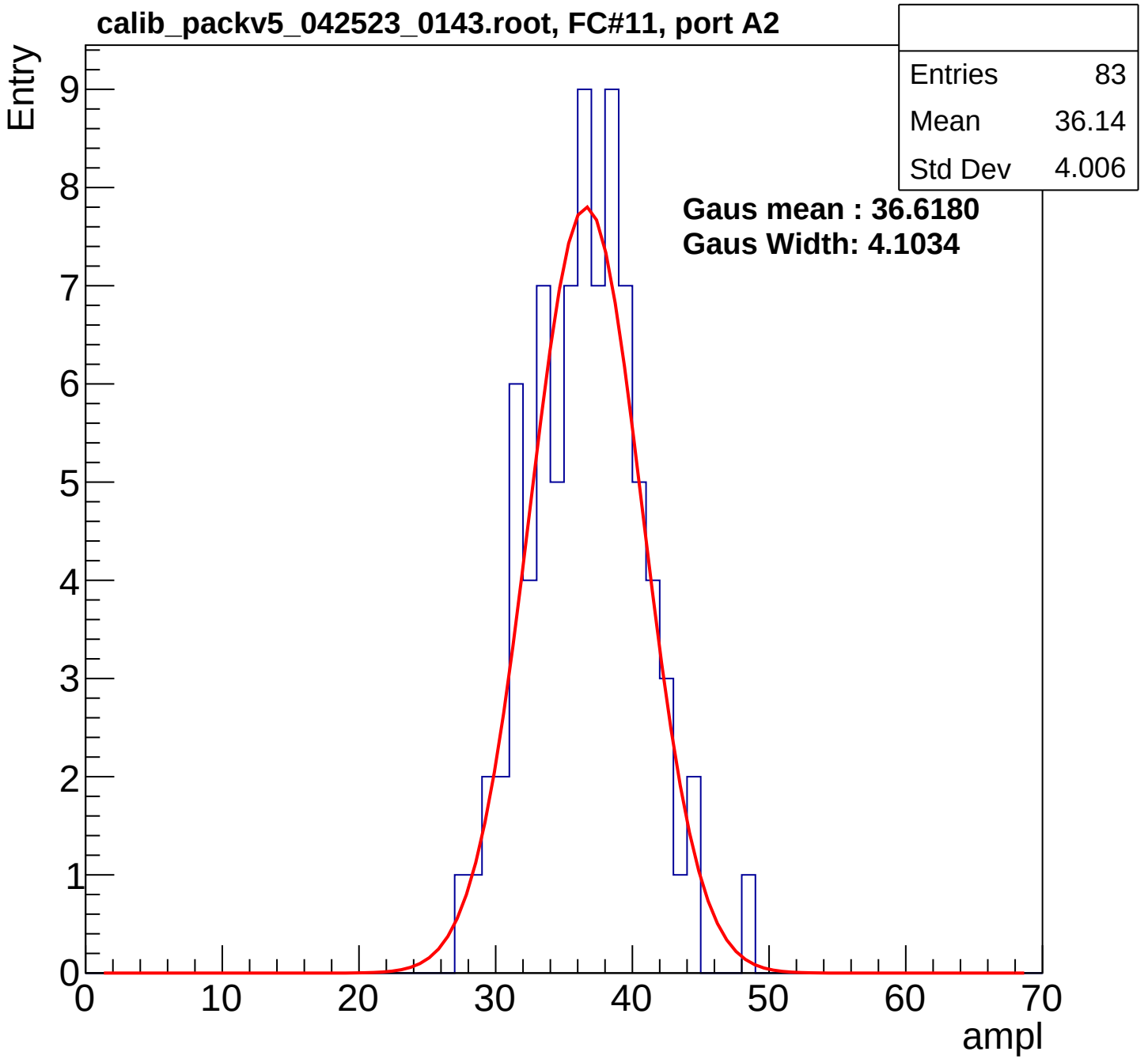
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 36.14
Std Dev 4.006
Gaus mean : 36.6180
Gaus Width: 4.1034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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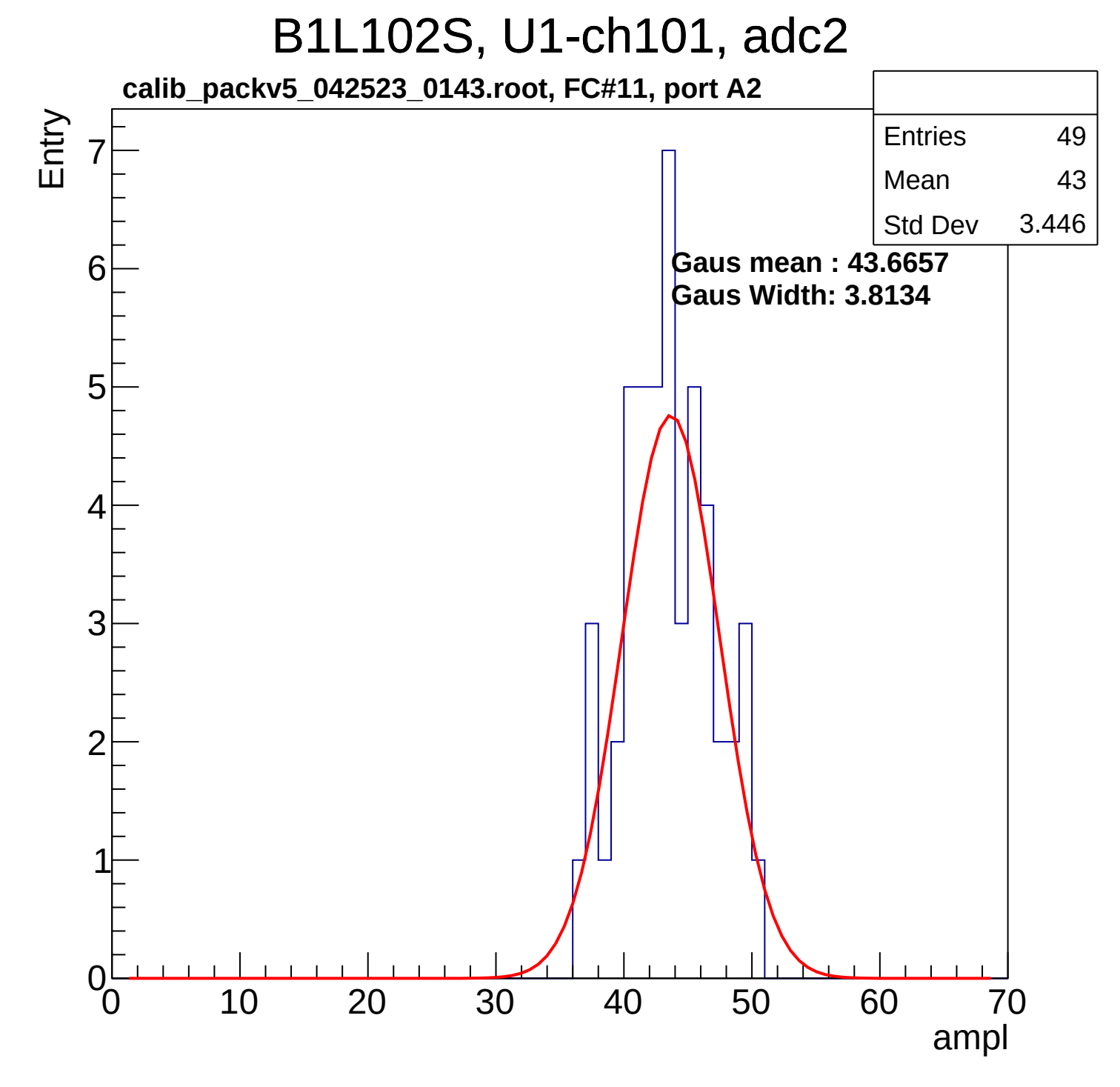
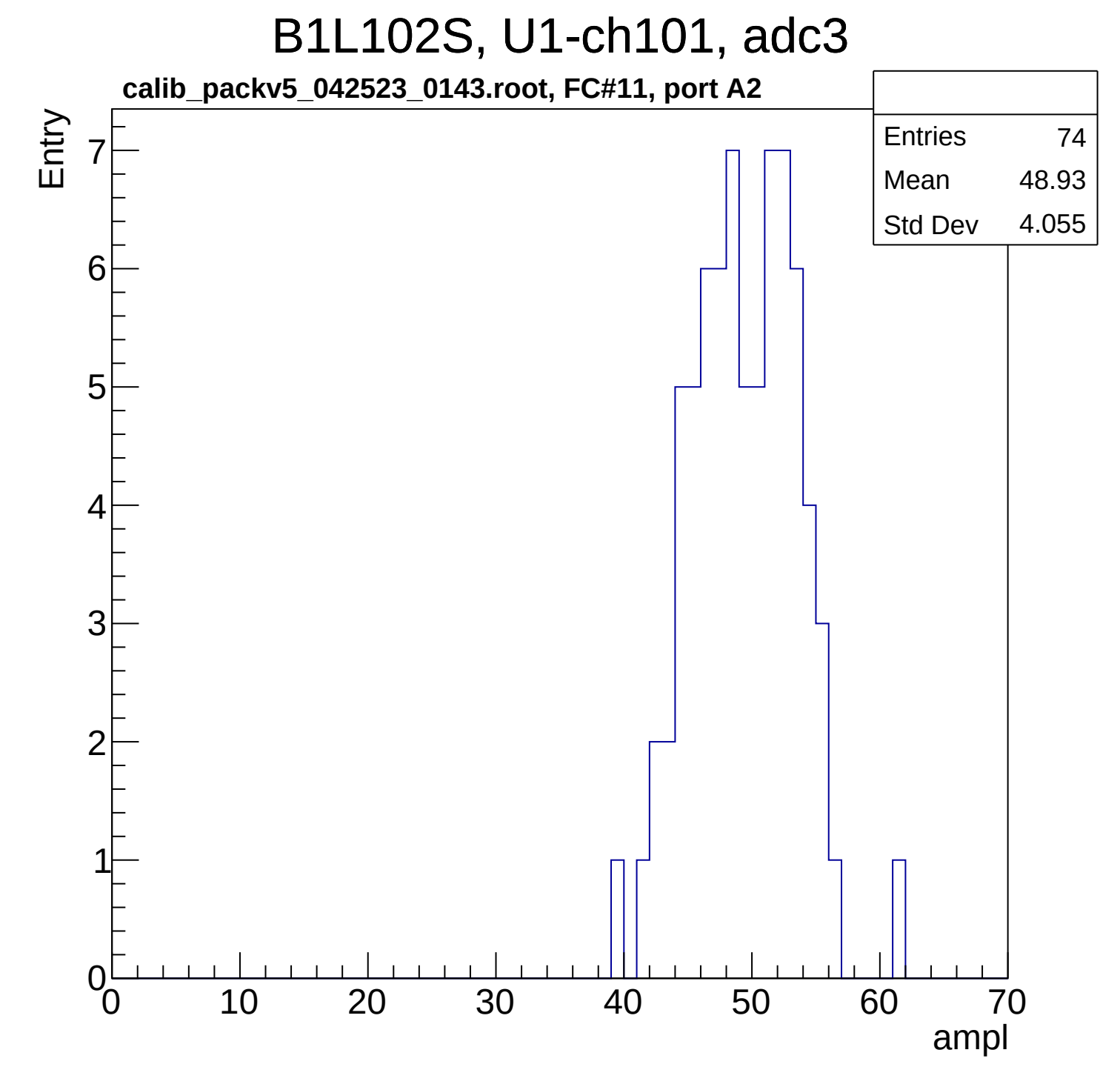
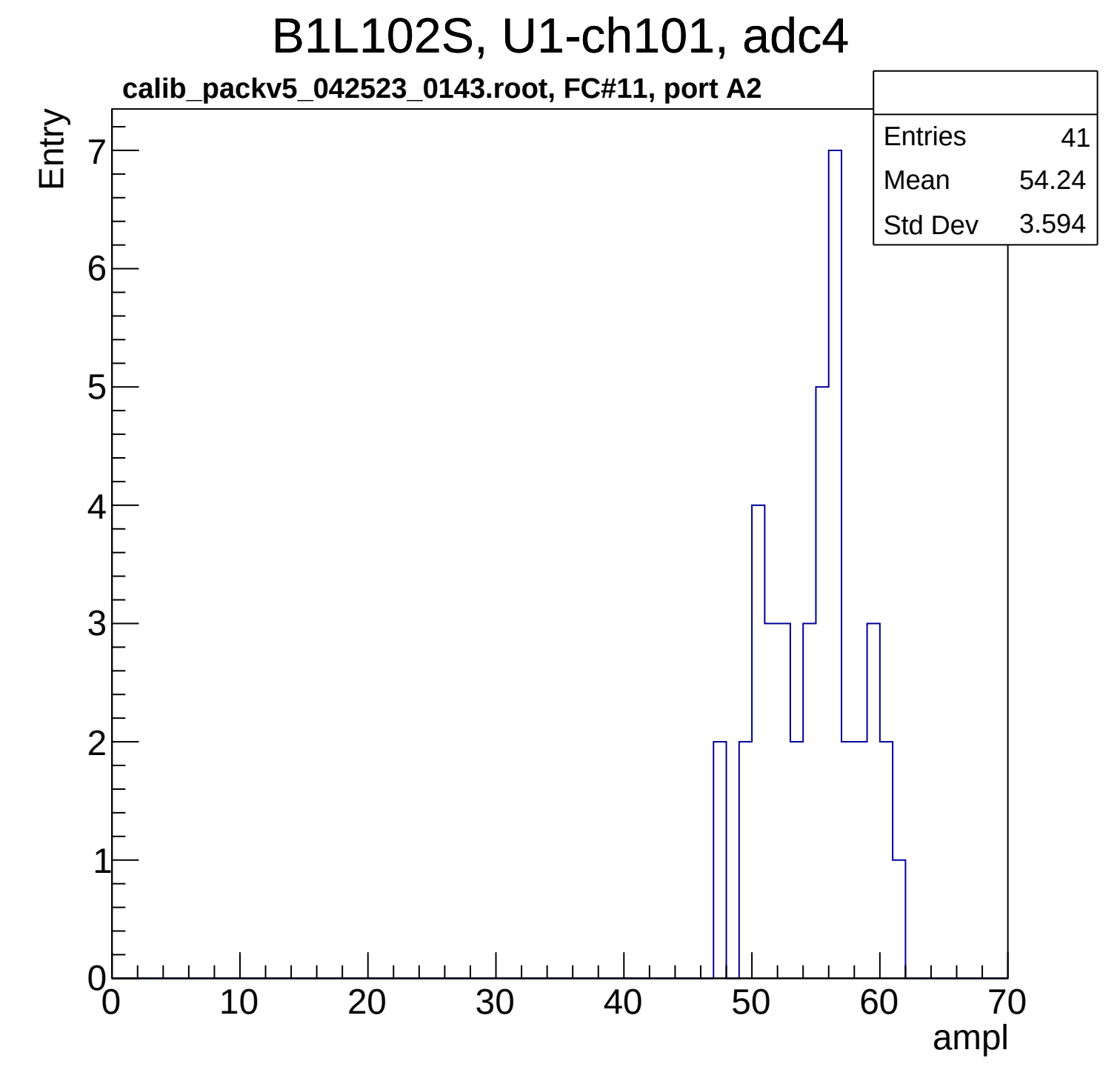

B1L102S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 43
Std Dev 3.446
Gaus mean : 43.6657
Gaus Width: 3.8134
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 48.93
Std Dev 4.055
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 41
Mean 54.24
Std Dev 3.594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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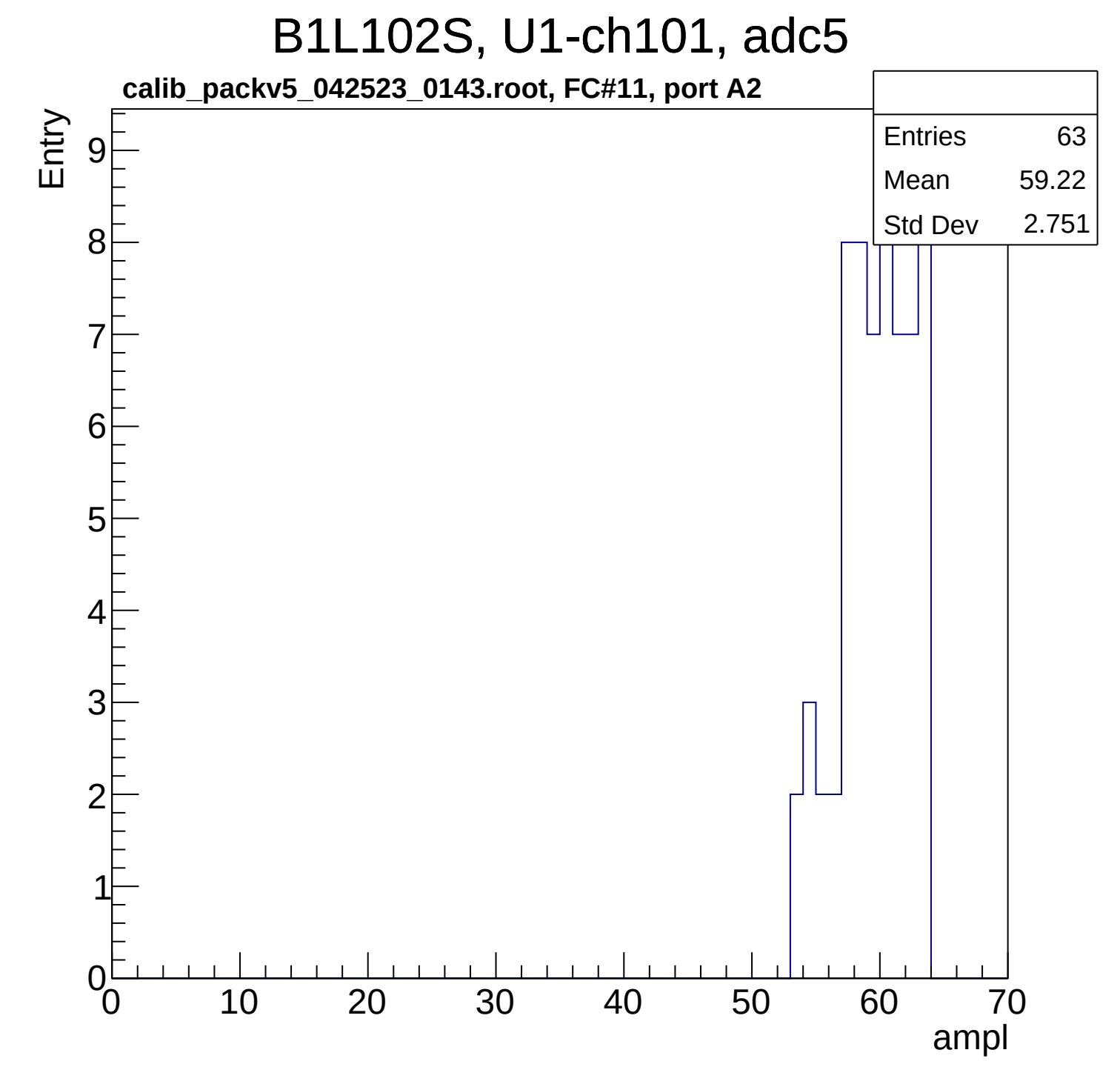
B1L102S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 59.22
Std Dev 2.751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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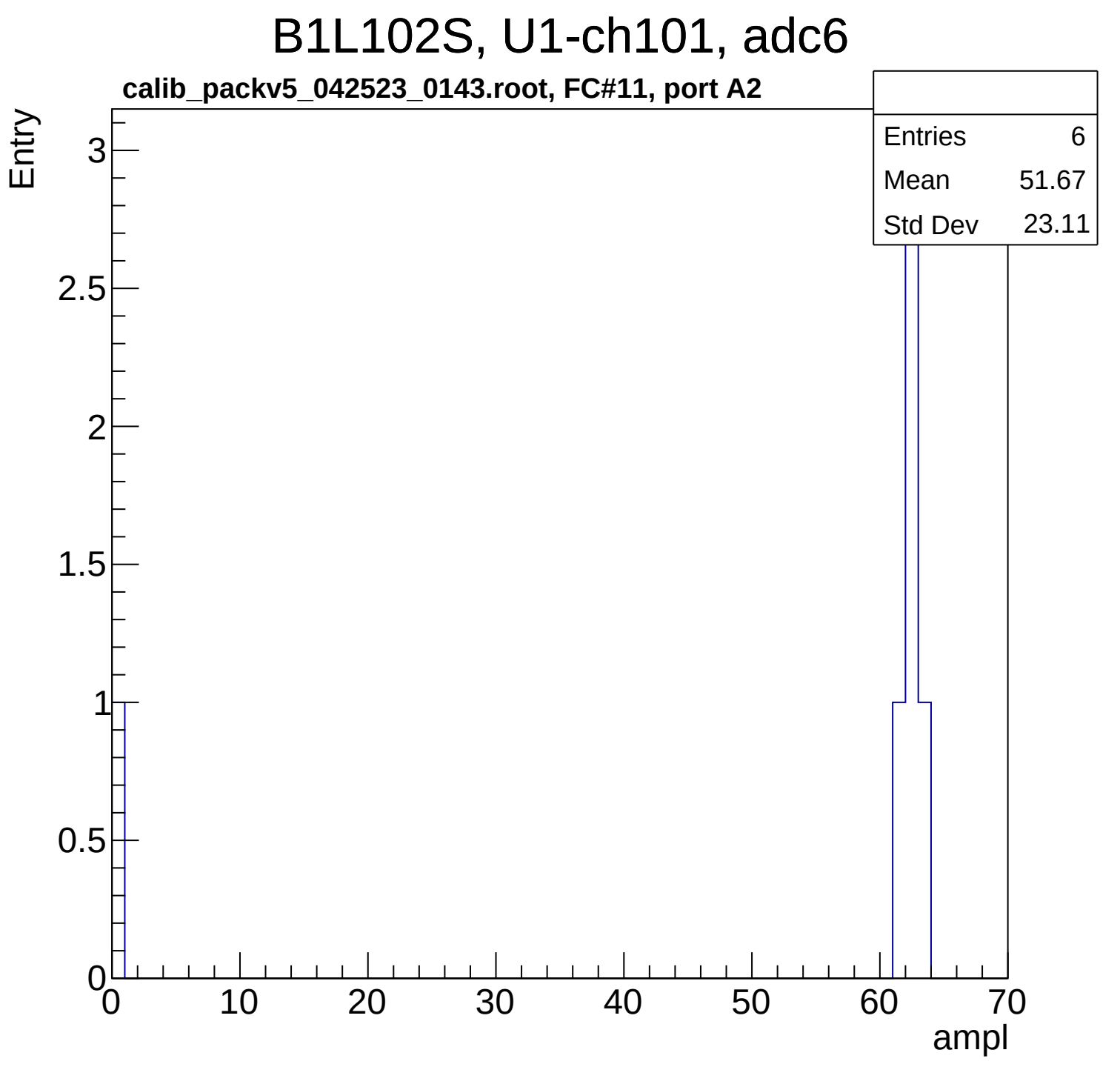
40
50
60
70


B1L102S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 6
Mean 51.67
Std Dev 23.11
Entry
ampl
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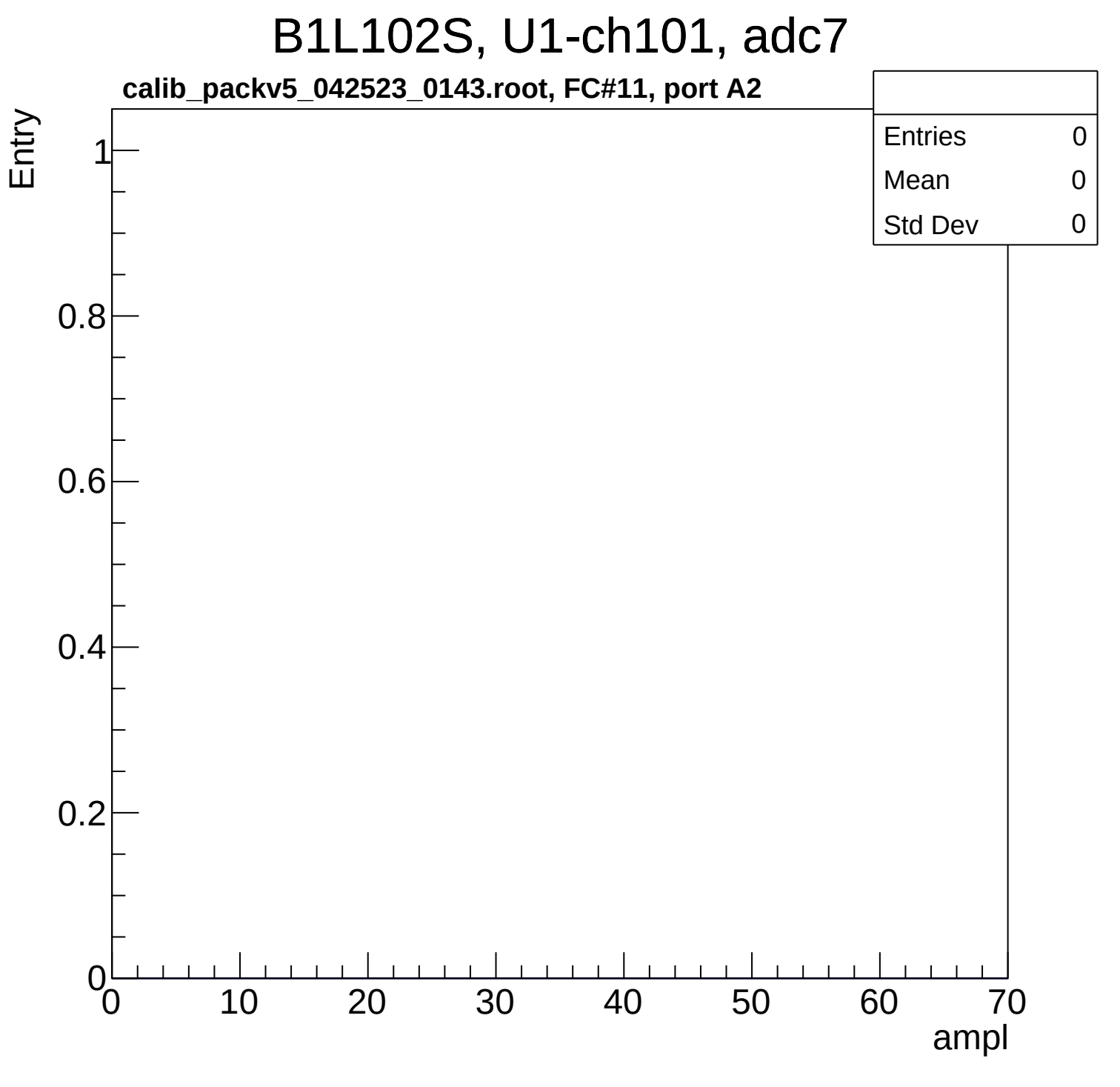
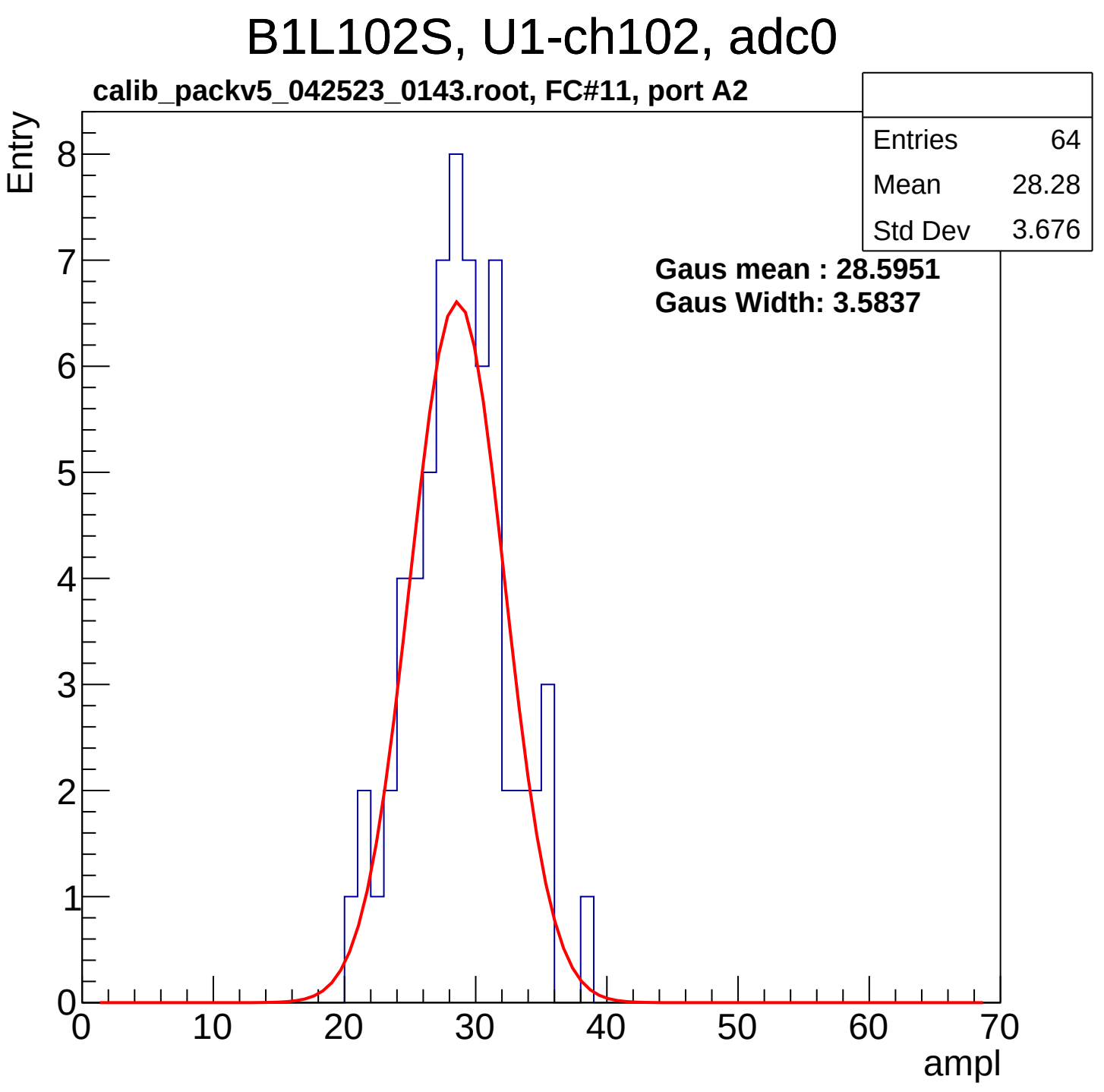
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 28.28
Std Dev 3.676
Gaus mean : 28.5951
Gaus Width: 3.5837
Entry
ampl
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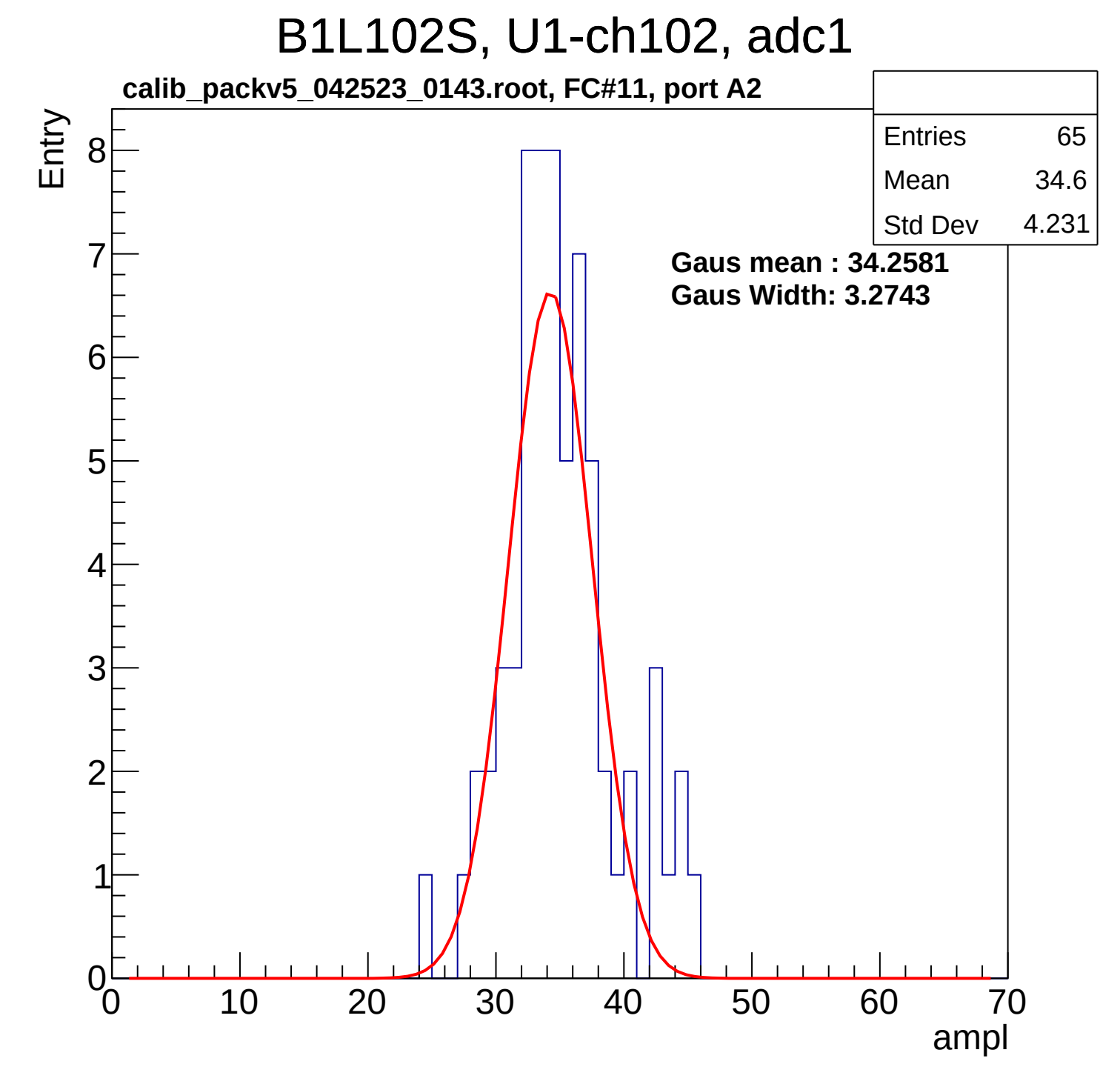
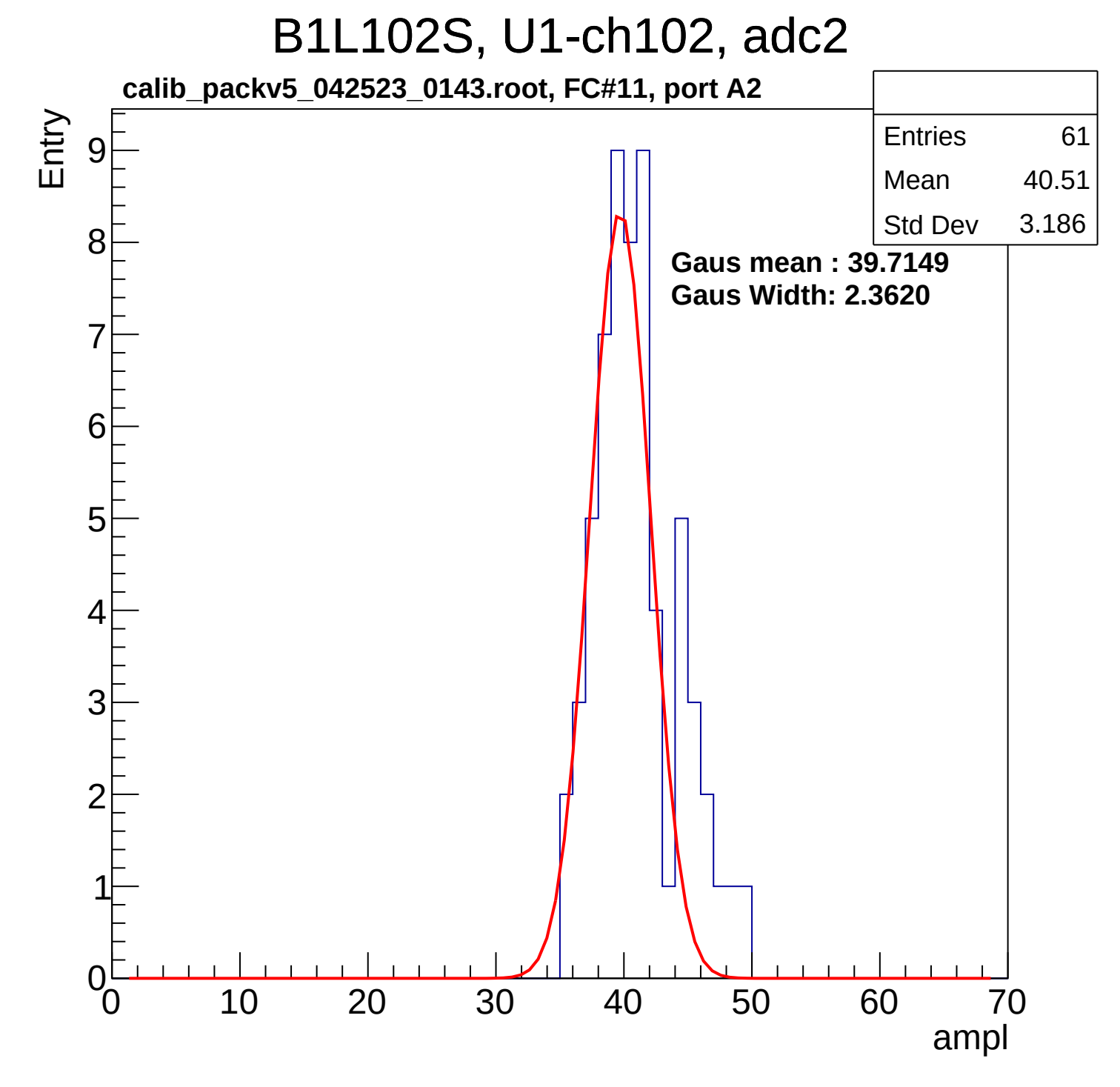
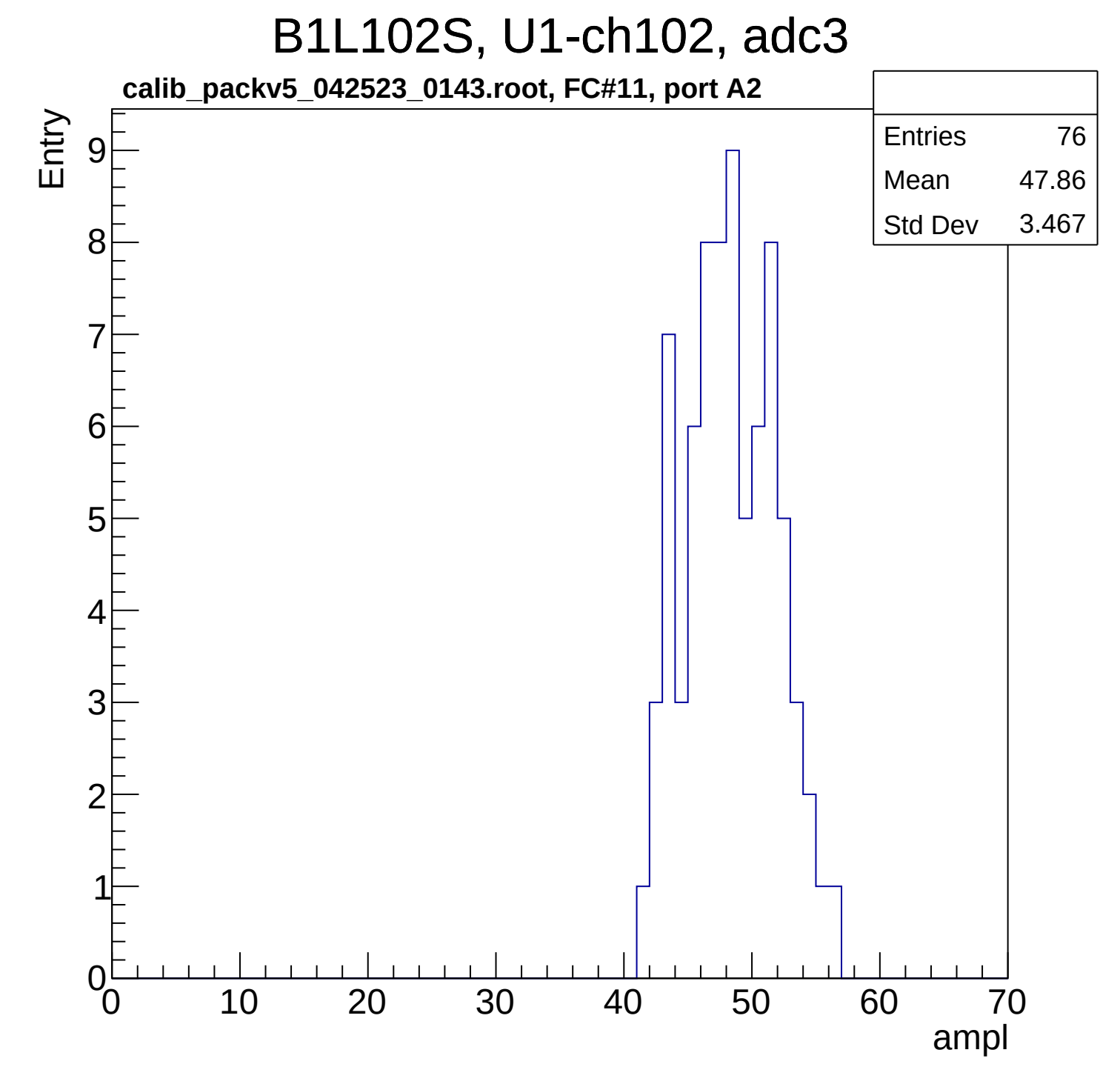
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 34.6
Std Dev 4.231
Gaus mean : 34.2581
Gaus Width: 3.2743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 40.51
Std Dev 3.186
Gaus mean : 39.7149
Gaus Width: 2.3620
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 47.86
Std Dev 3.467
Entry
ampl
0
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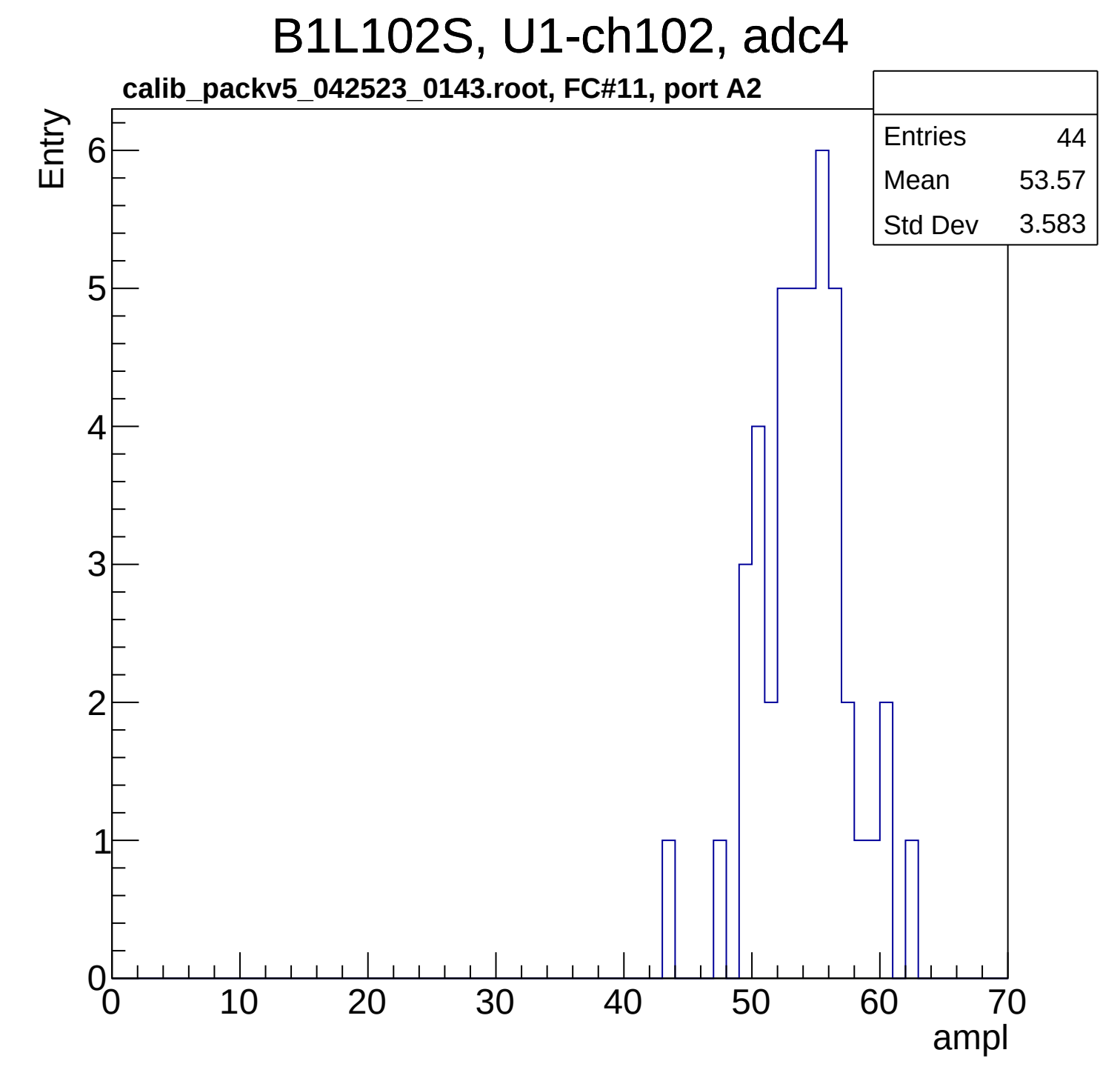
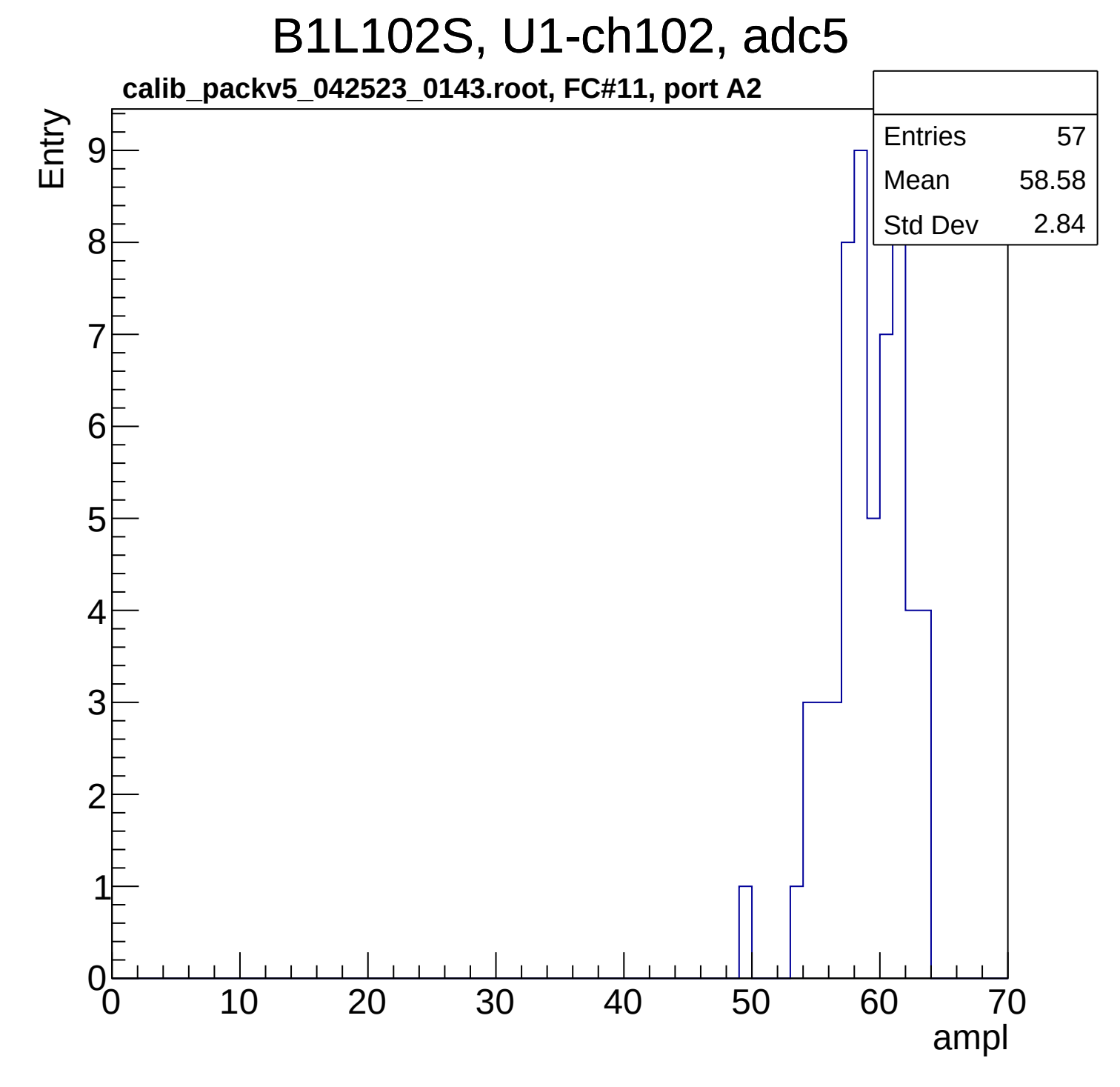
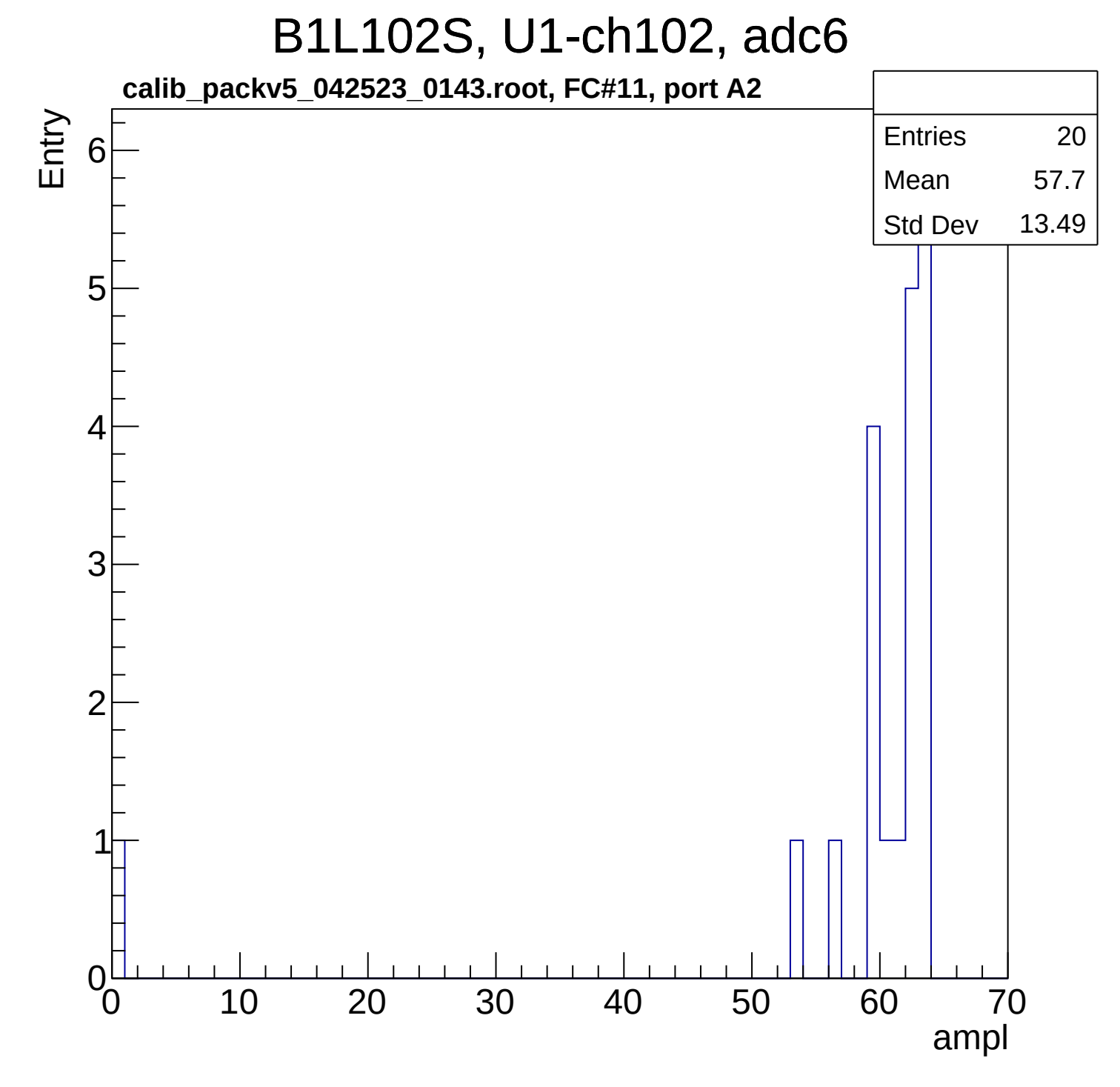
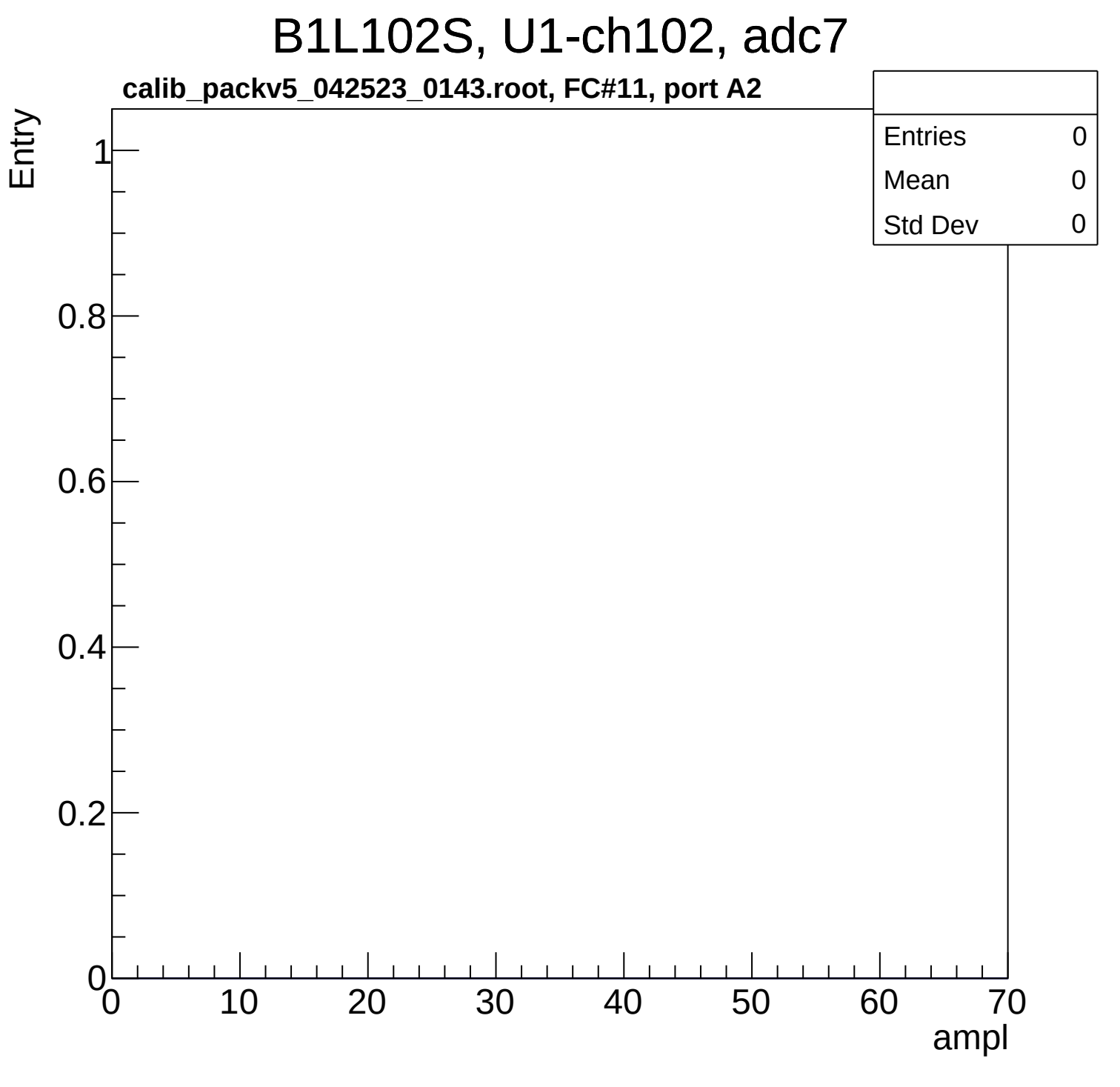
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 53.57
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 58.58
Std Dev 2.84
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 20
Mean 57.7
Std Dev 13.49
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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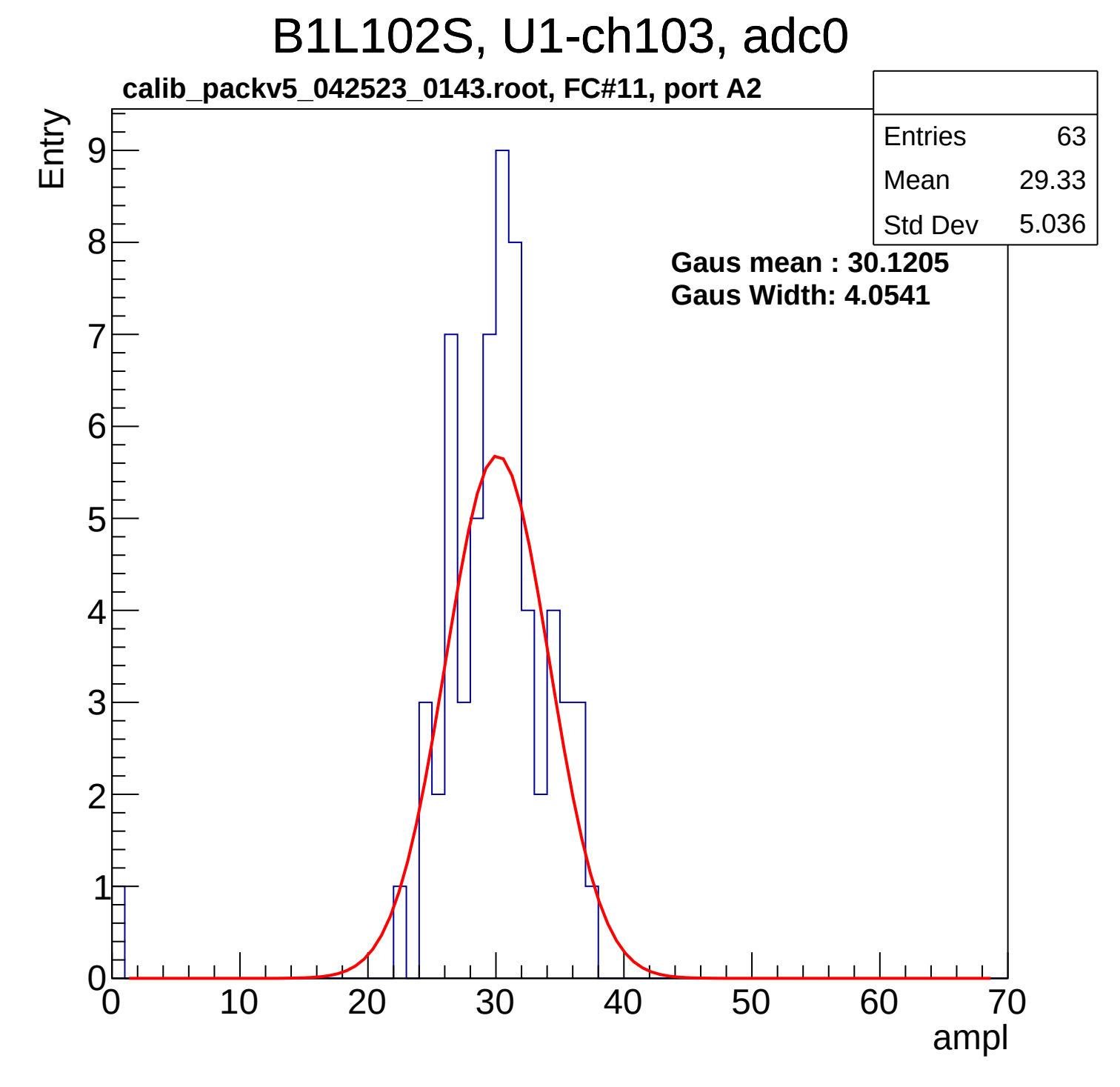
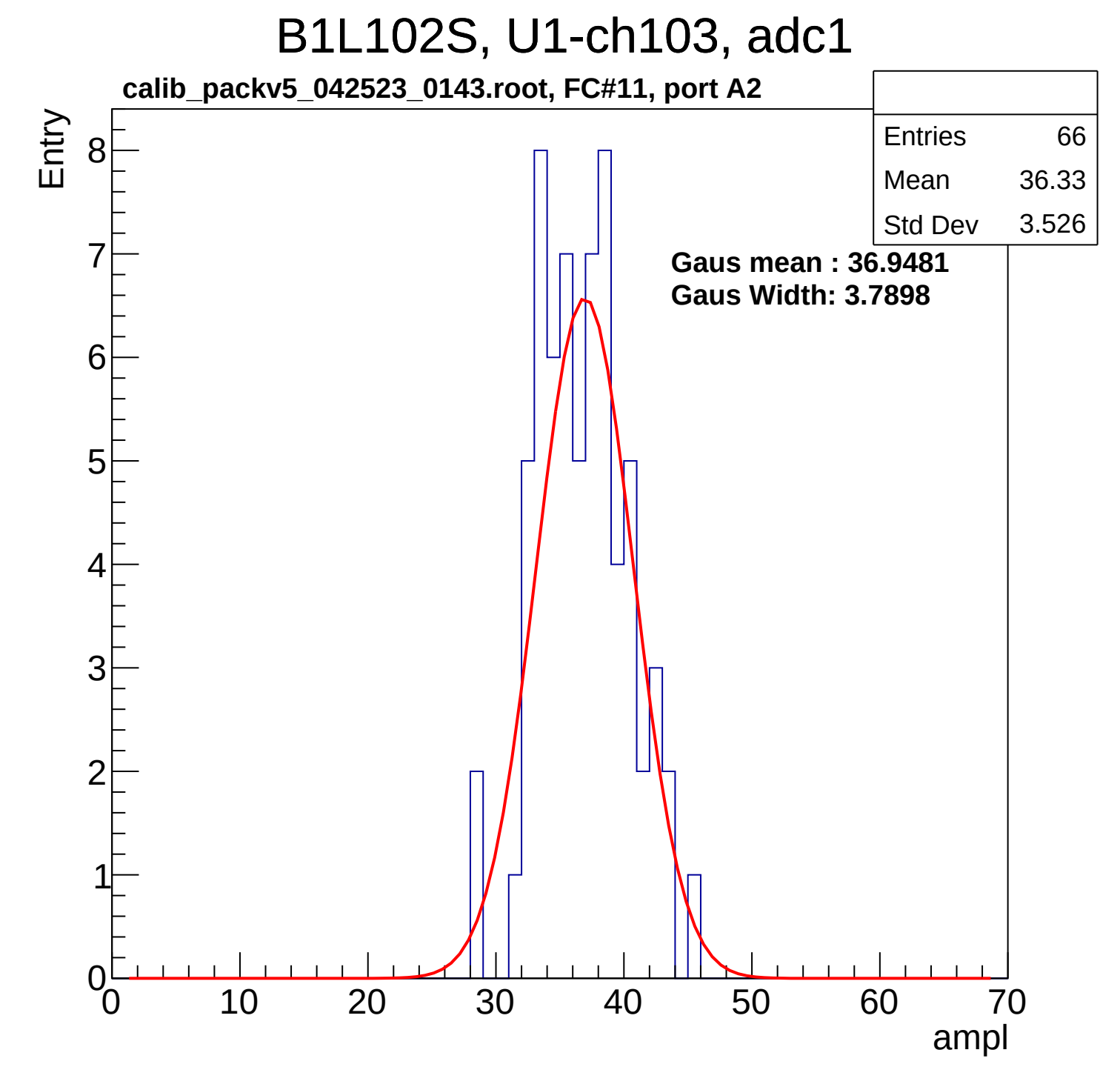
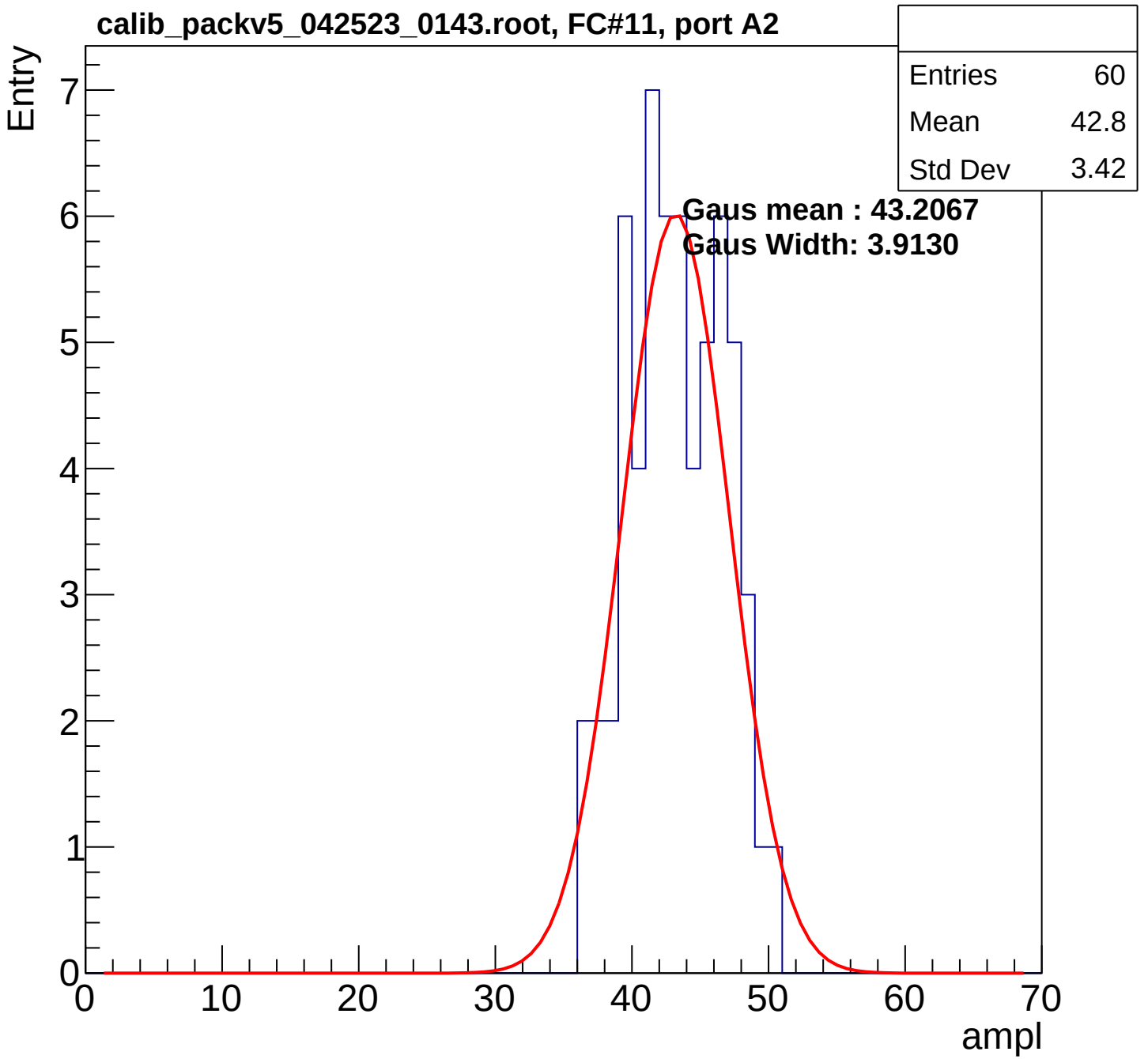
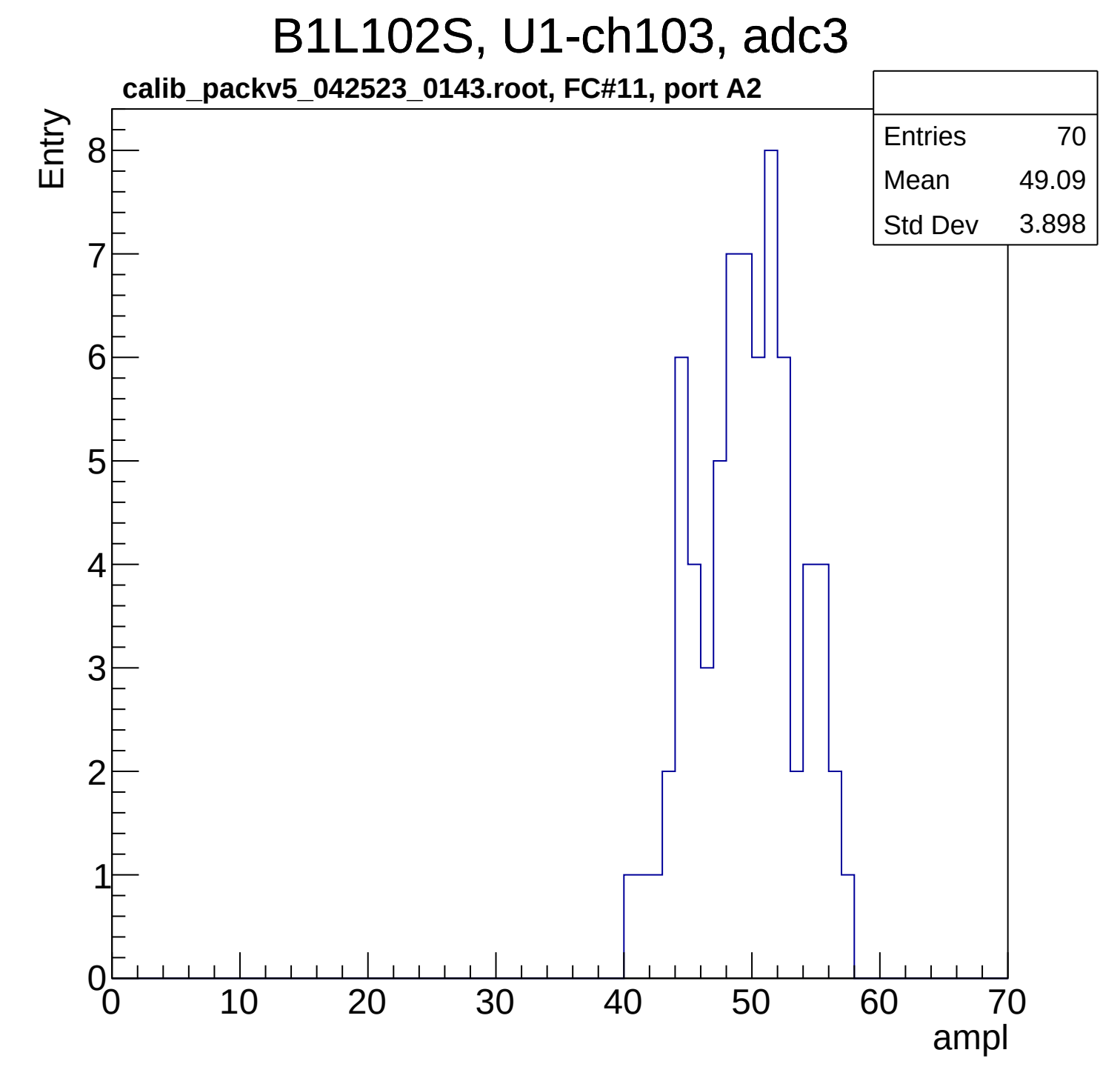
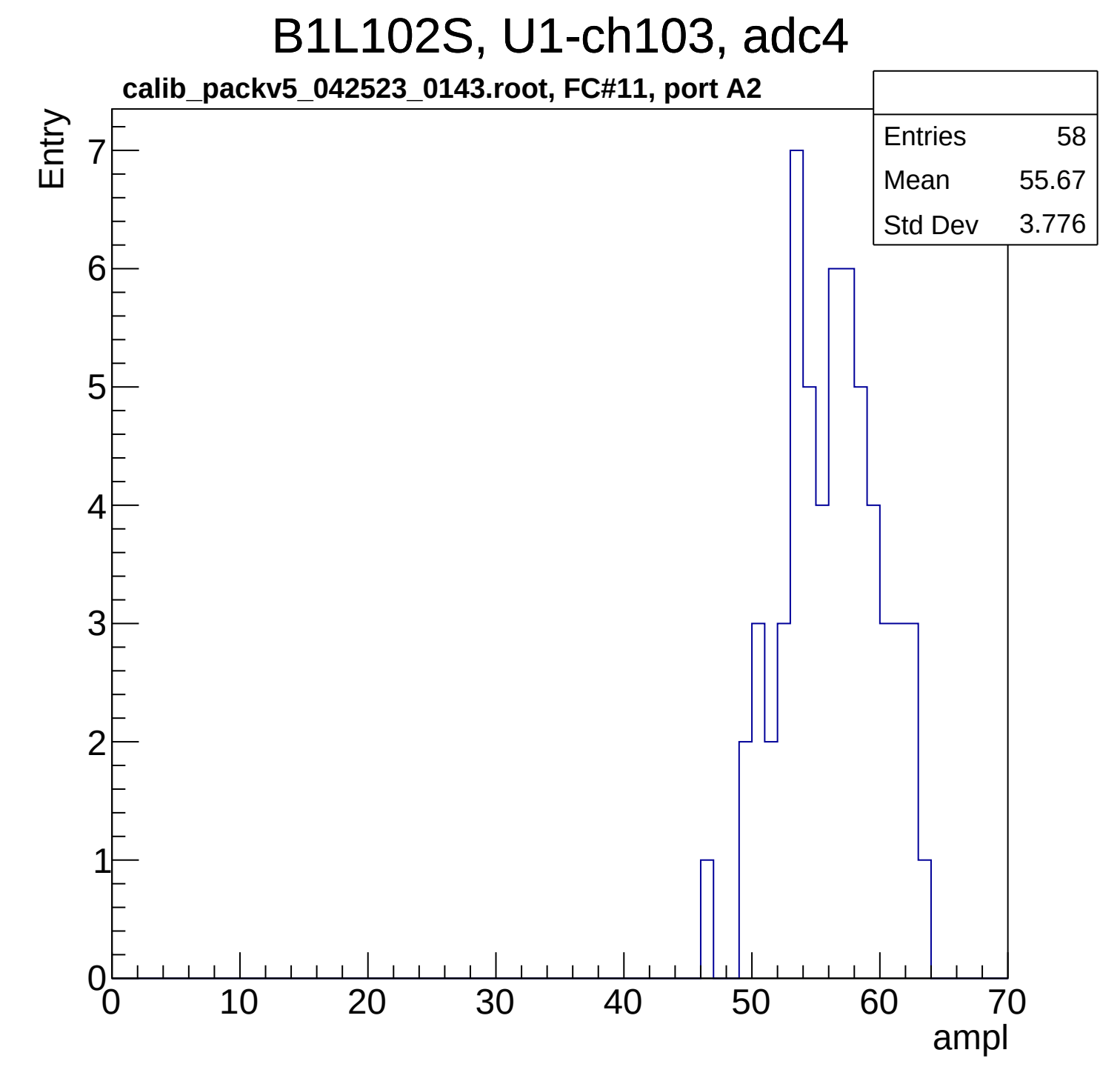
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

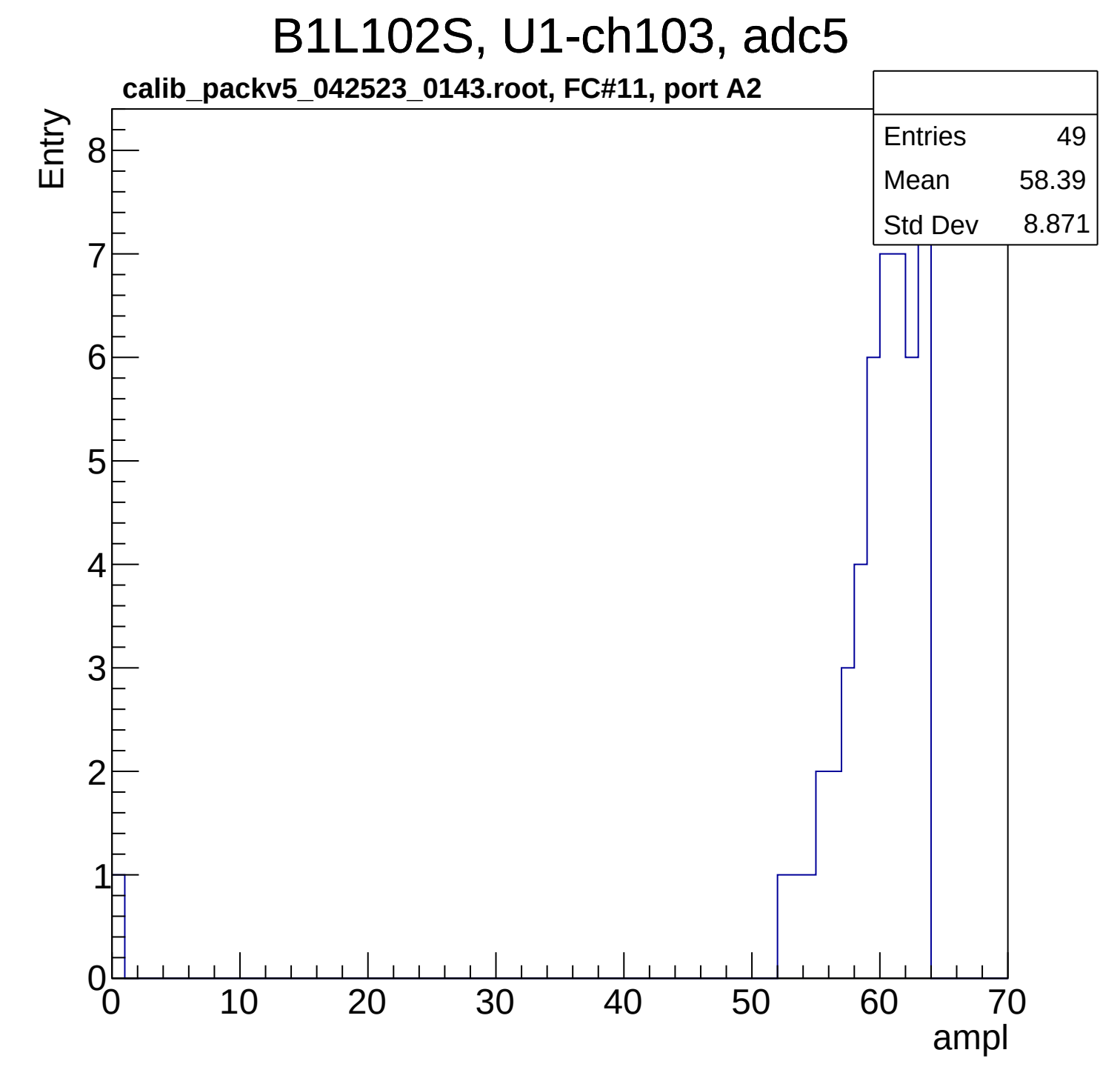

B1L102S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 29.33
Std Dev 5.036
Gaus mean : 30.1205
Gaus Width: 4.0541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 36.33
Std Dev 3.526
Gaus mean : 36.9481
Gaus Width: 3.7898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 42.8
Std Dev 3.42
Gaus mean : 43.2067
Gaus Width: 3.9130
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 49.09
Std Dev 3.898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 55.67
Std Dev 3.776
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 58.39
Std Dev 8.871
Entry
ampl
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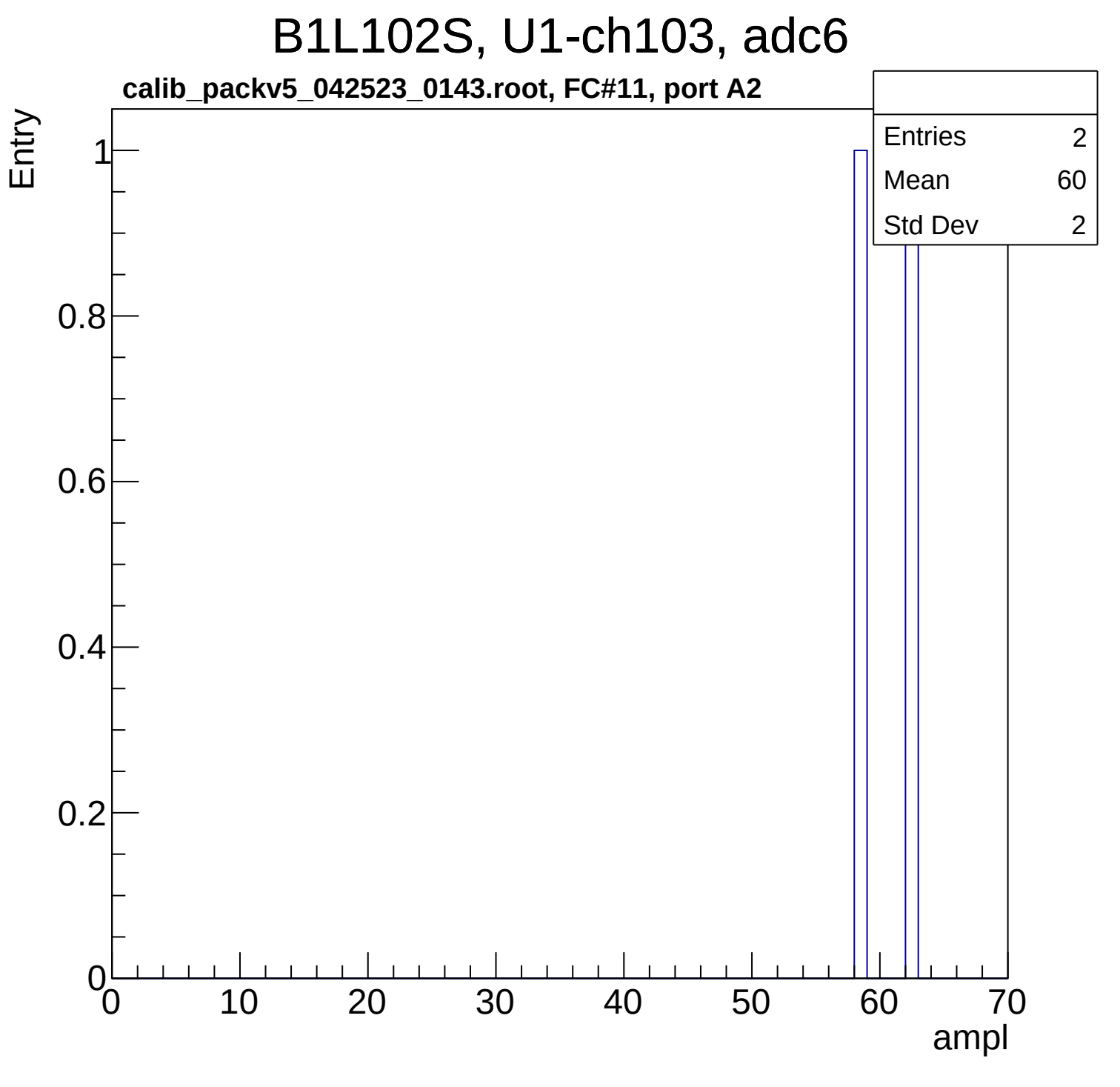
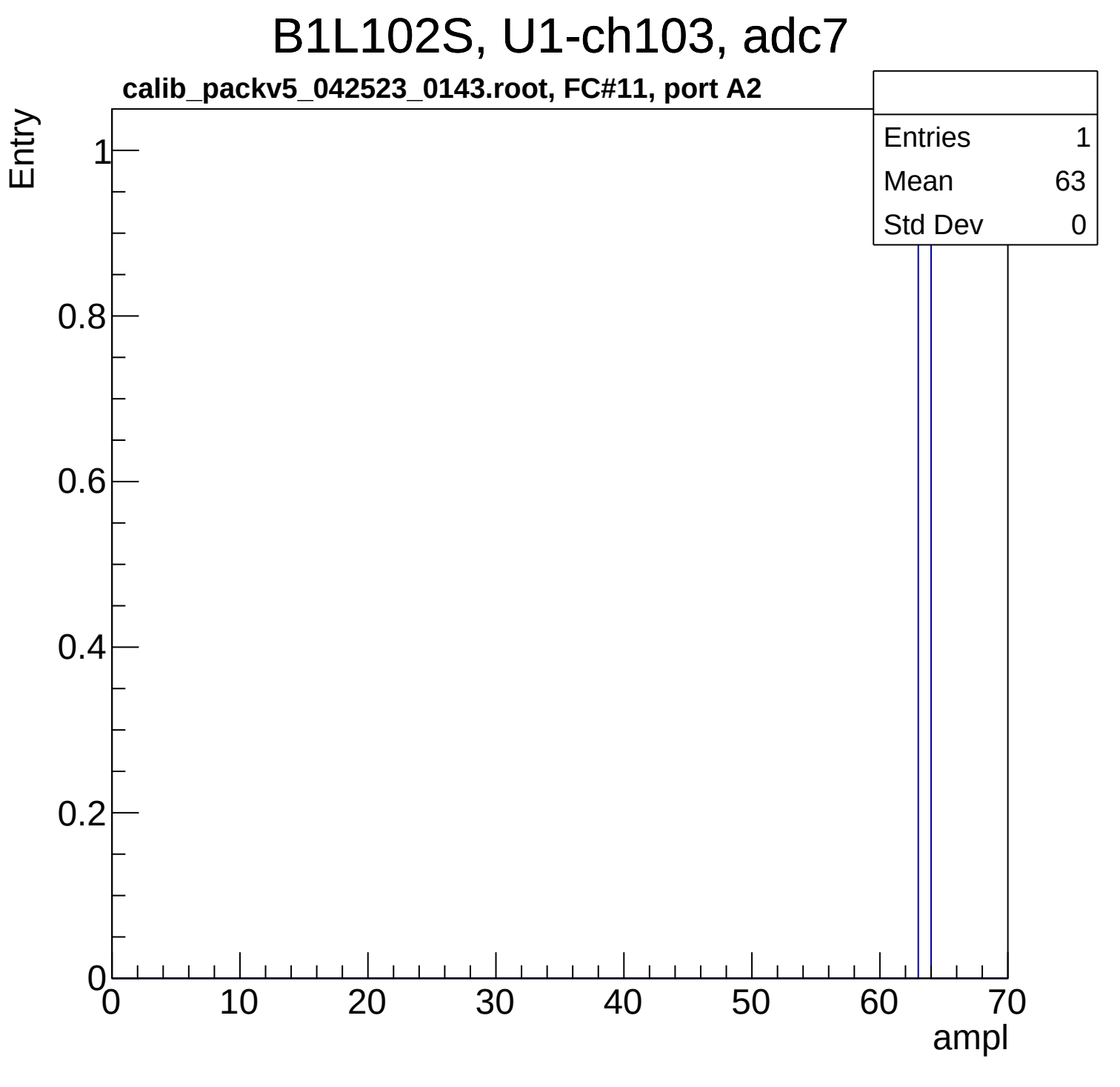
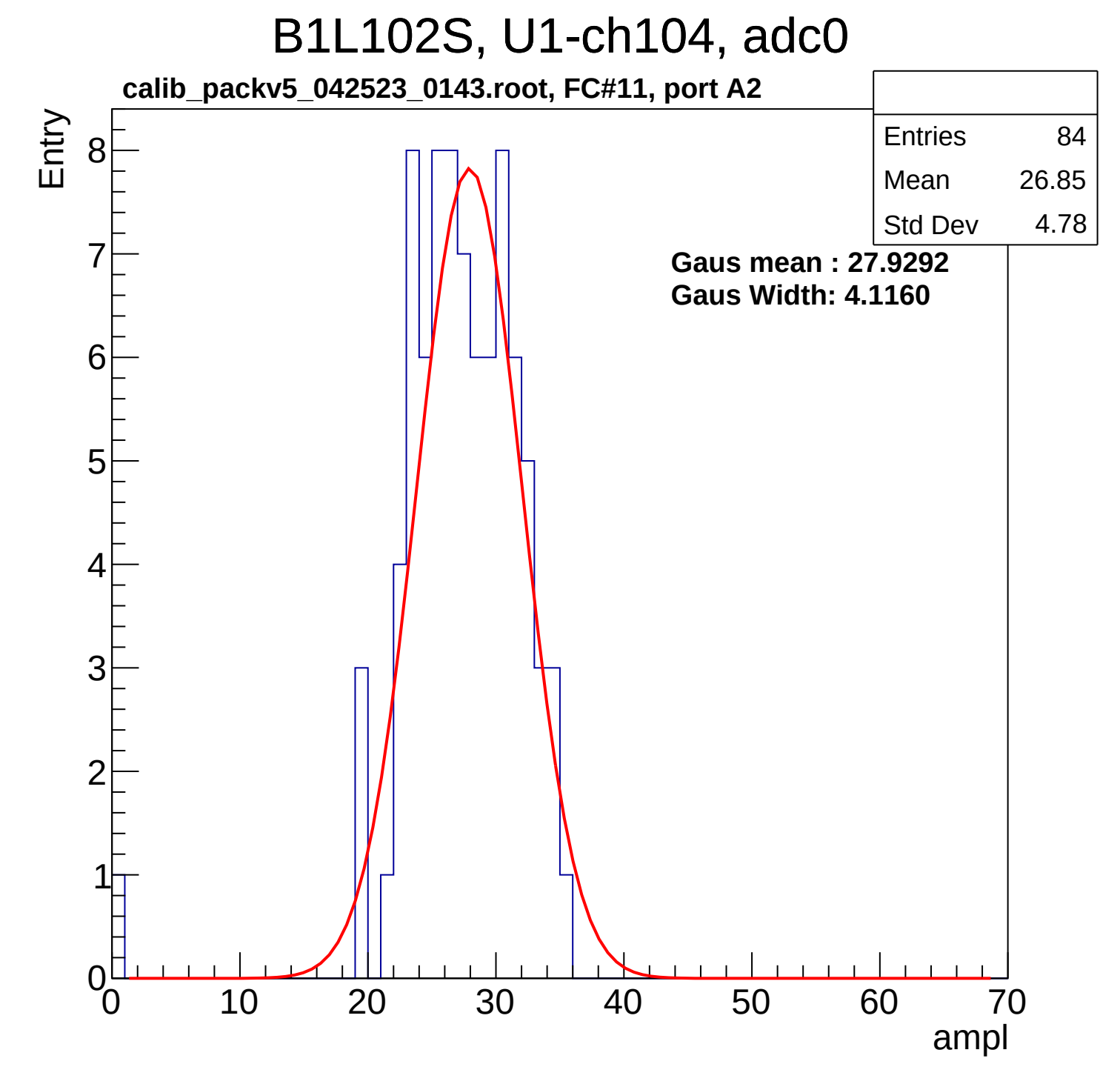
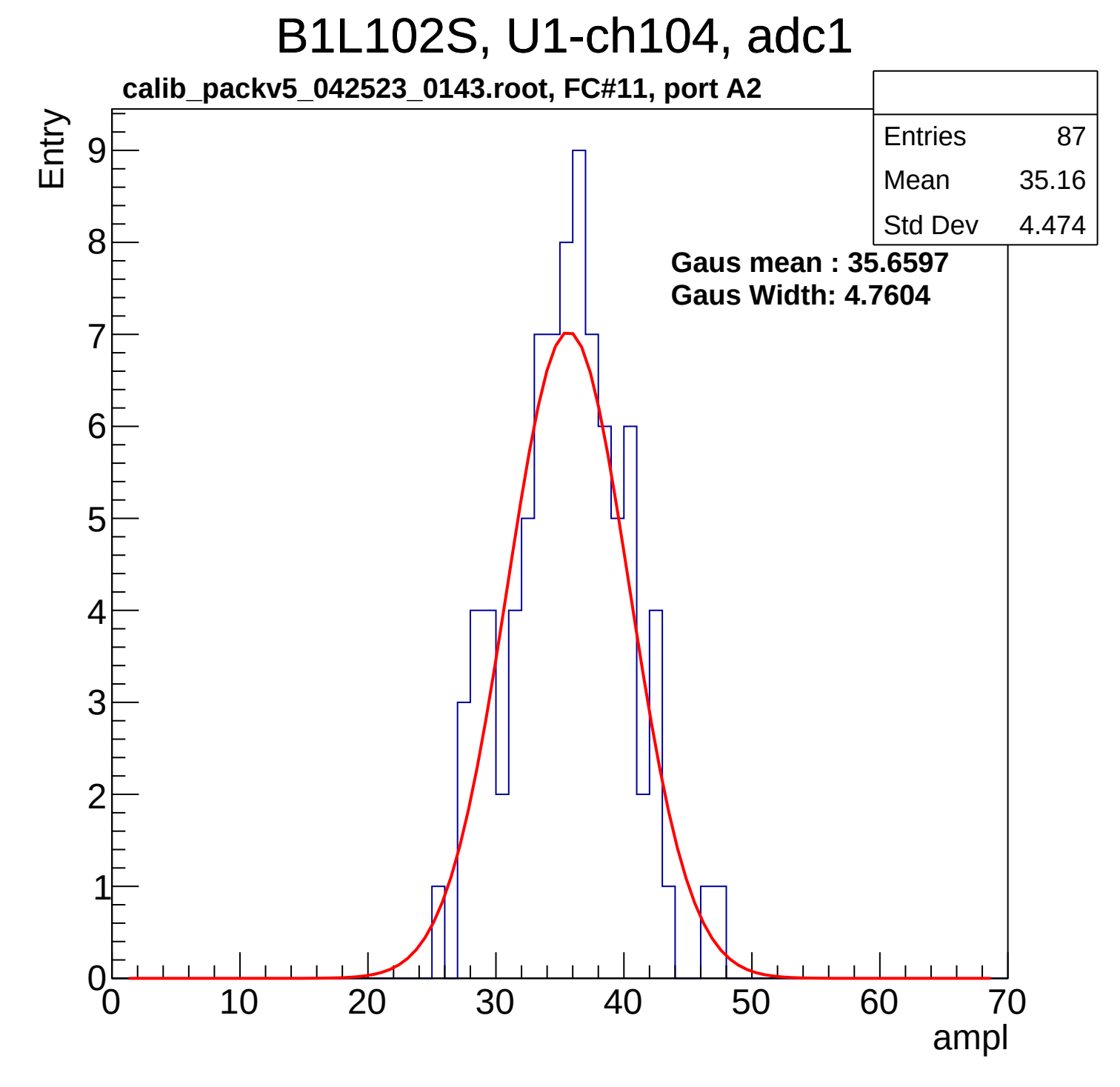
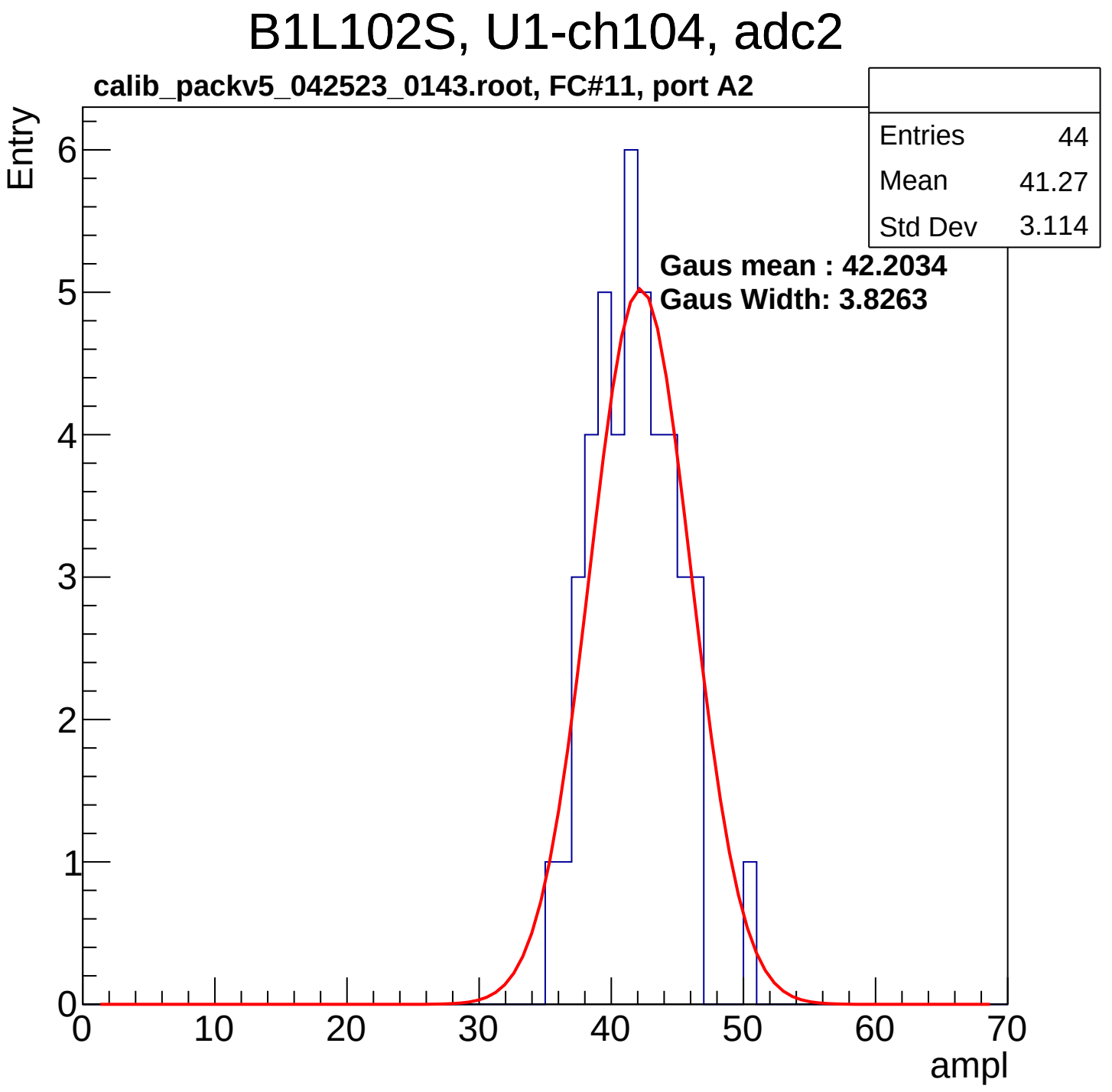
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

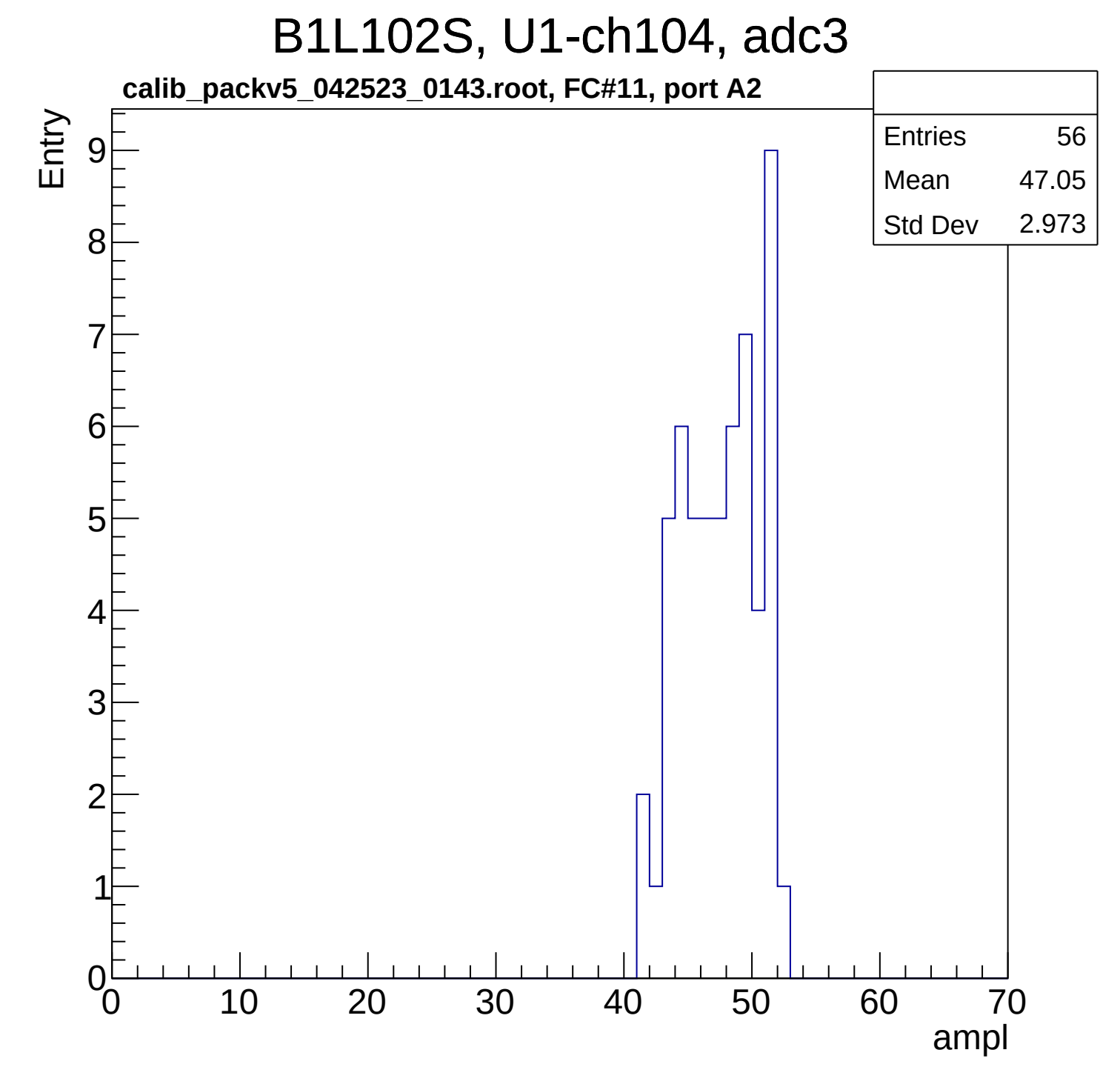

B1L102S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 60
Std Dev 2
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 84
Mean 26.85
Std Dev 4.78
Gaus mean : 27.9292
Gaus Width: 4.1160
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 87
Mean 35.16
Std Dev 4.474
Gaus mean : 35.6597
Gaus Width: 4.7604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 44
Mean 41.27
Std Dev 3.114
Gaus mean : 42.2034
Gaus Width: 3.8263
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 47.05
Std Dev 2.973
Entry
ampl
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3
4
5
6
7
8
9
0
10
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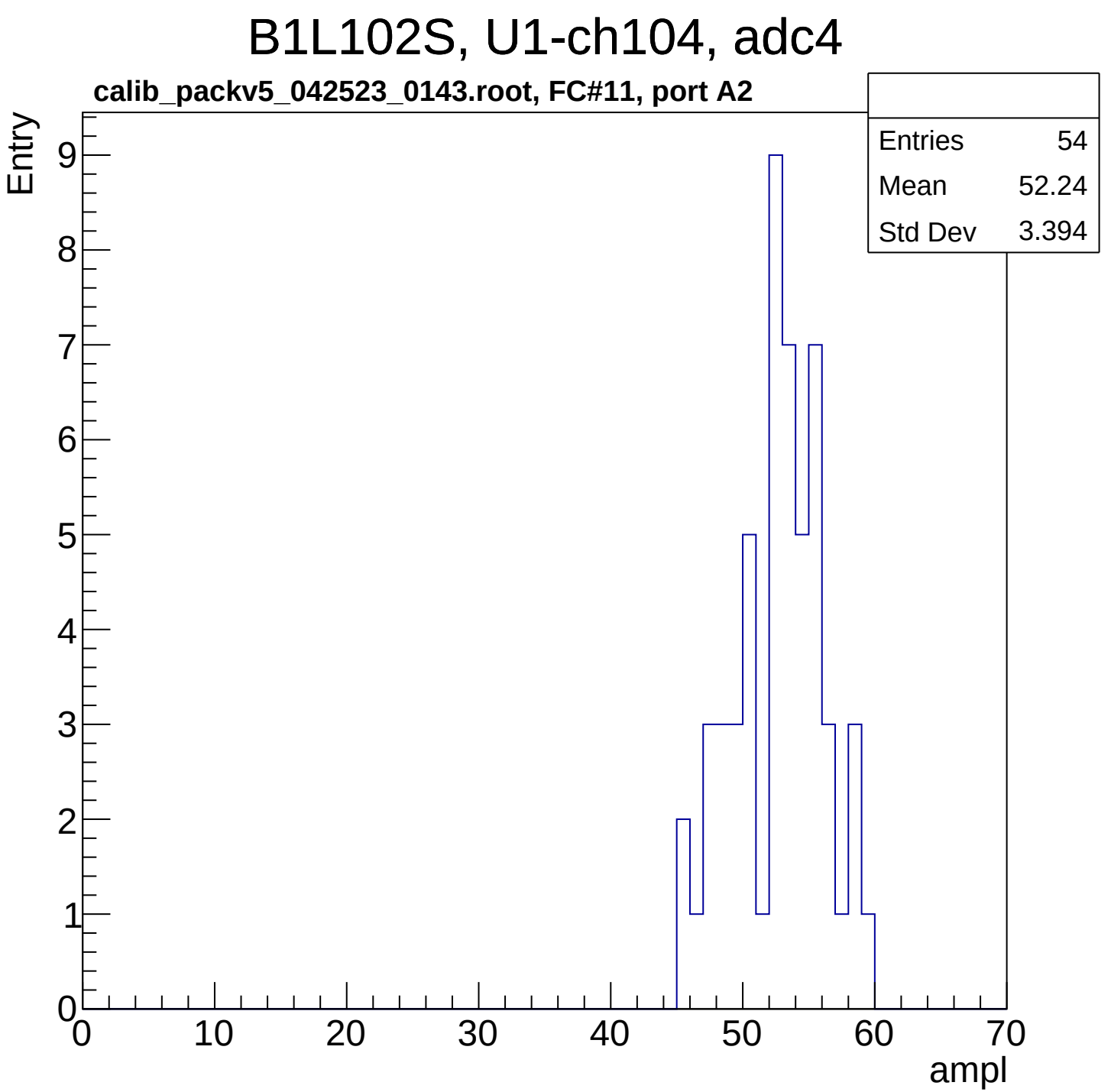
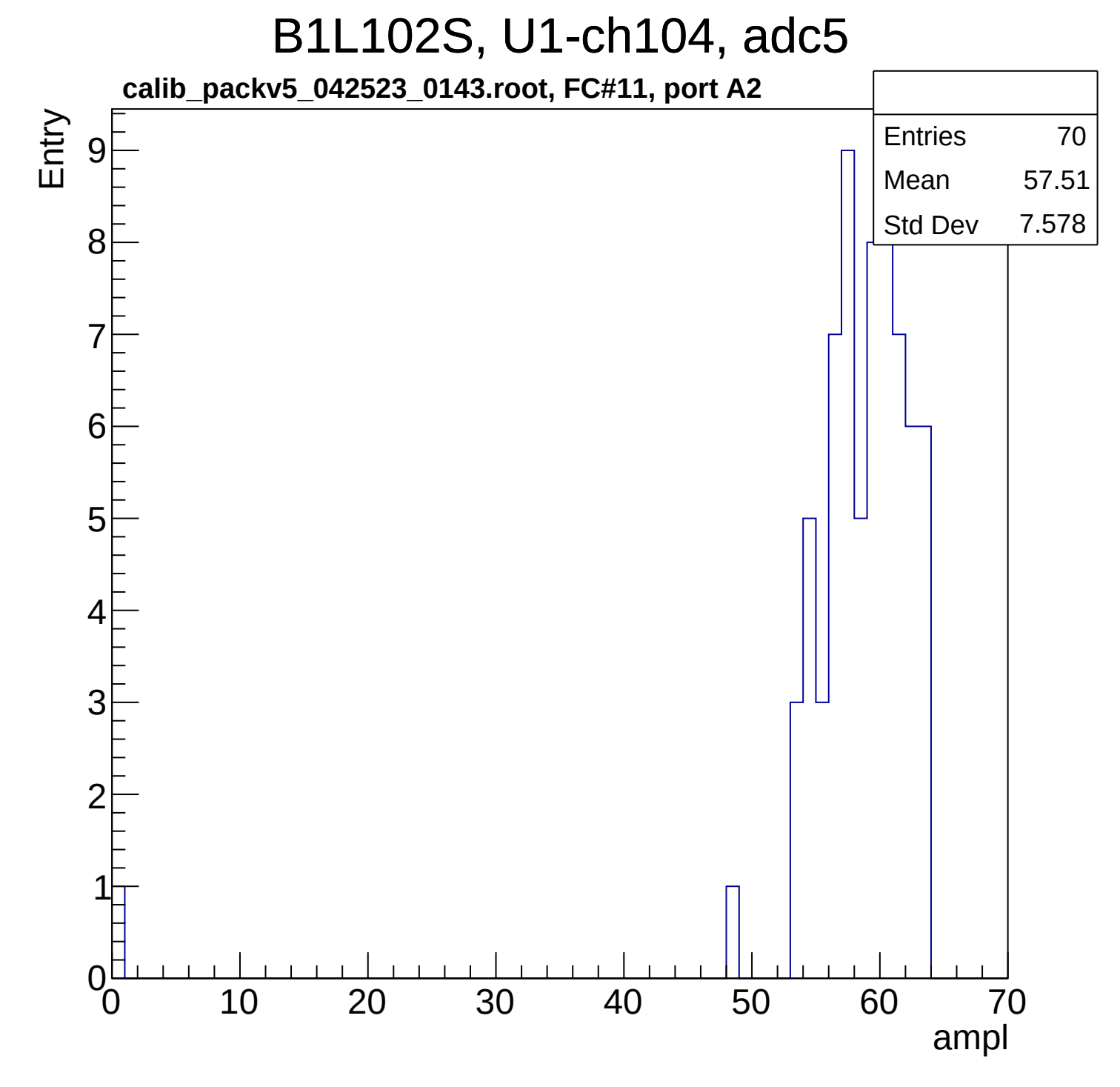
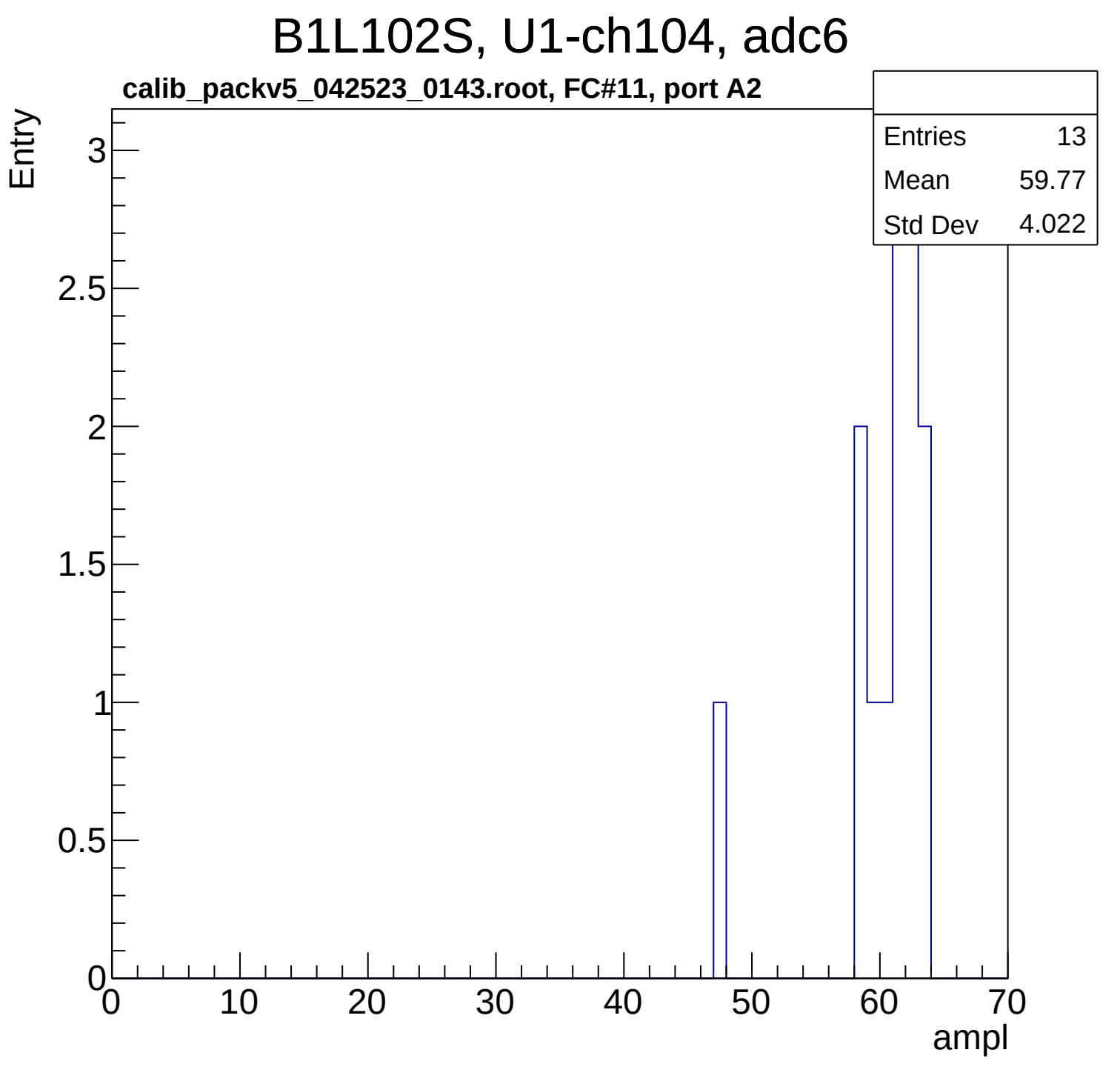
60
70


B1L102S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 52.24
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 57.51
Std Dev 7.578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 59.77
Std Dev 4.022
Entry
ampl
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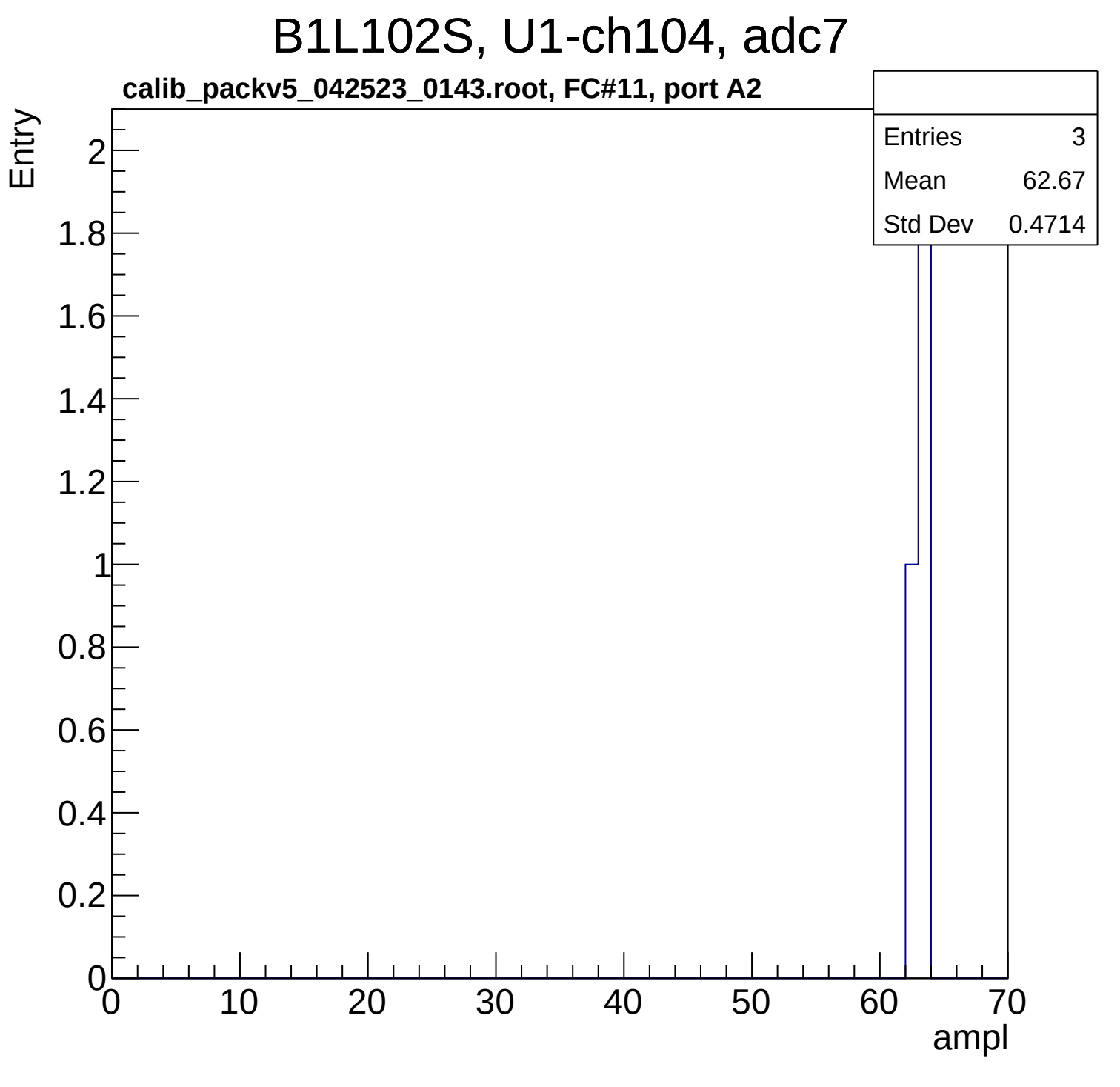
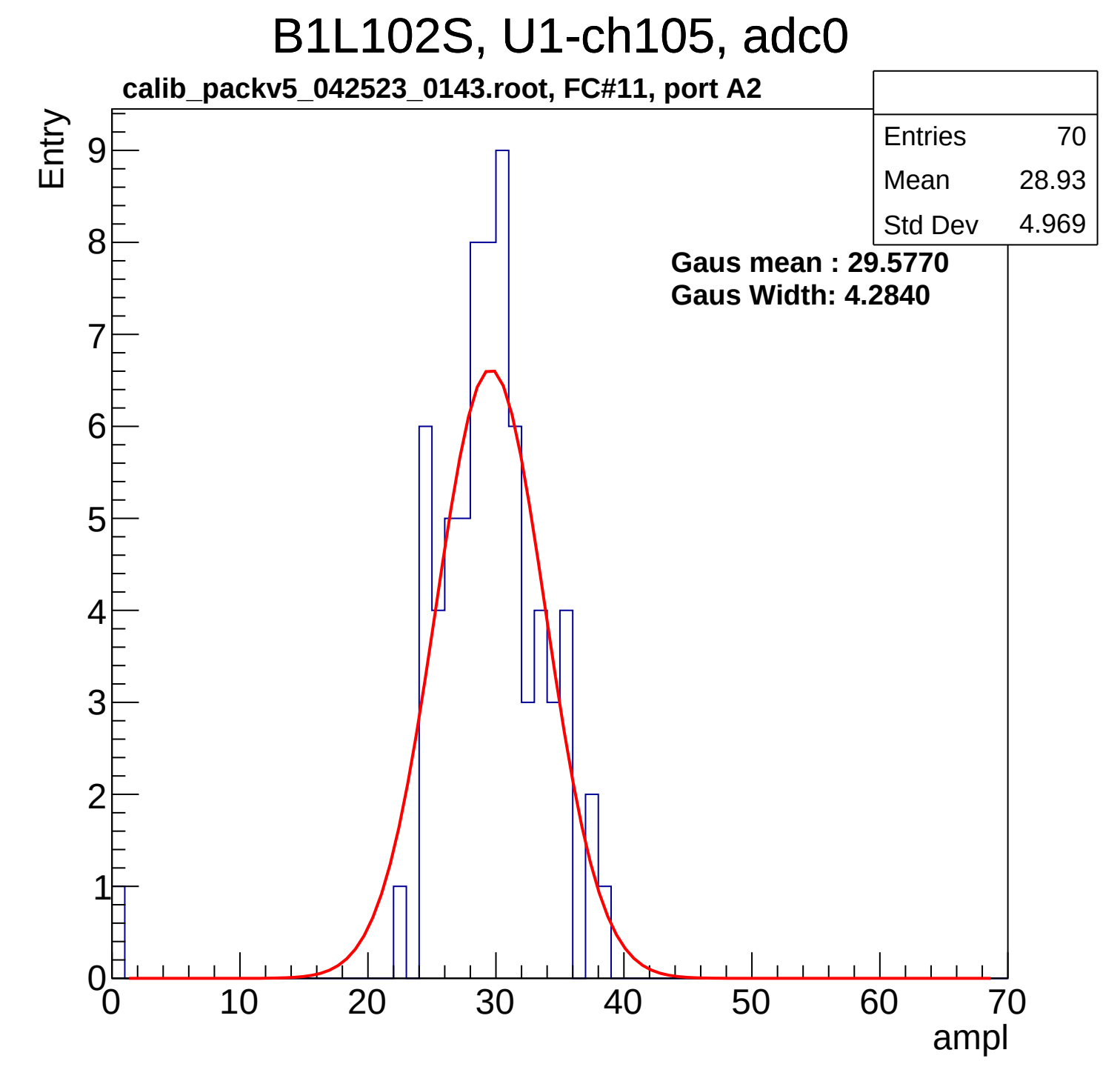
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 28.93
Std Dev 4.969
Gaus mean : 29.5770
Gaus Width: 4.2840
Entry
ampl
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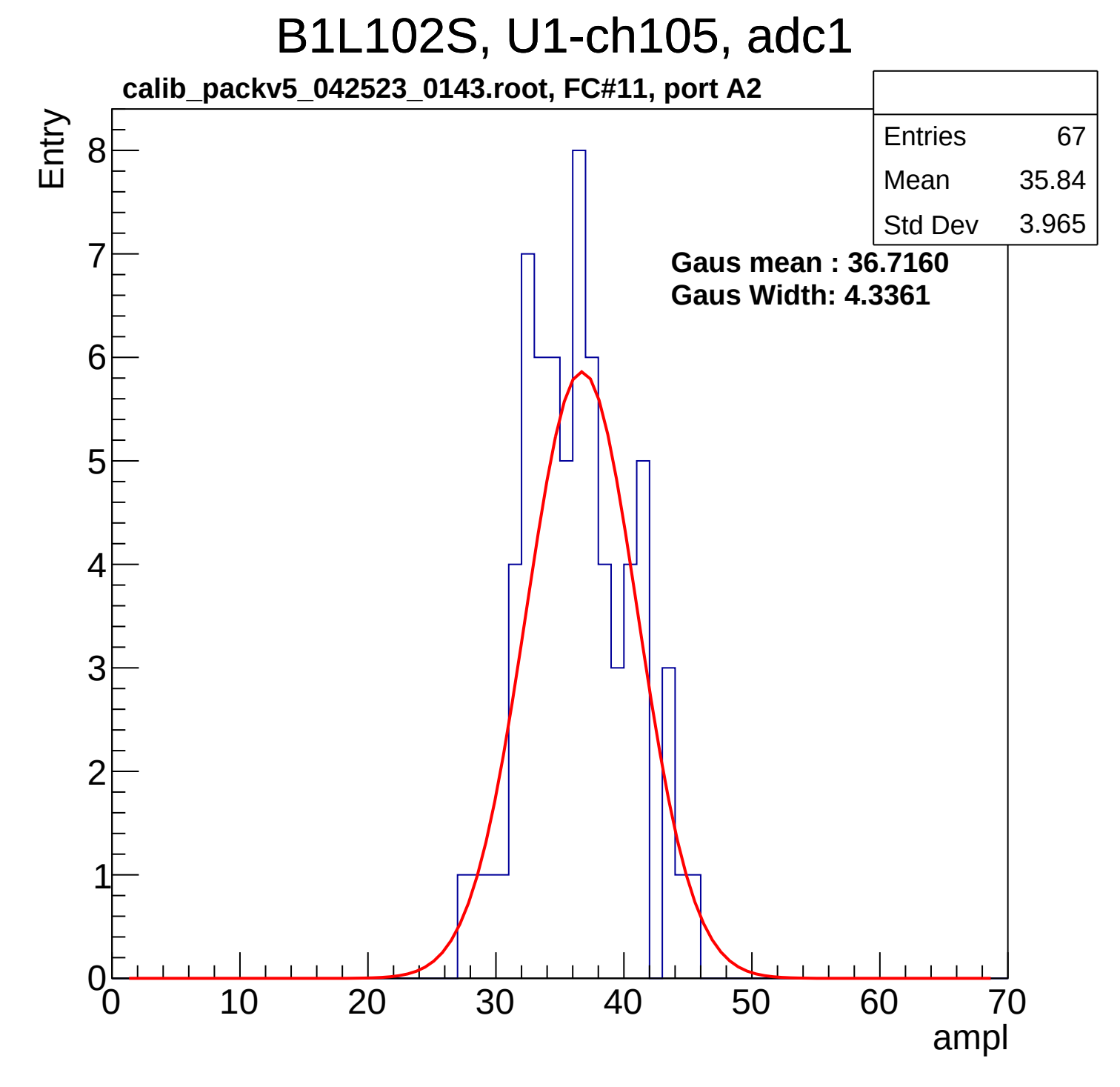
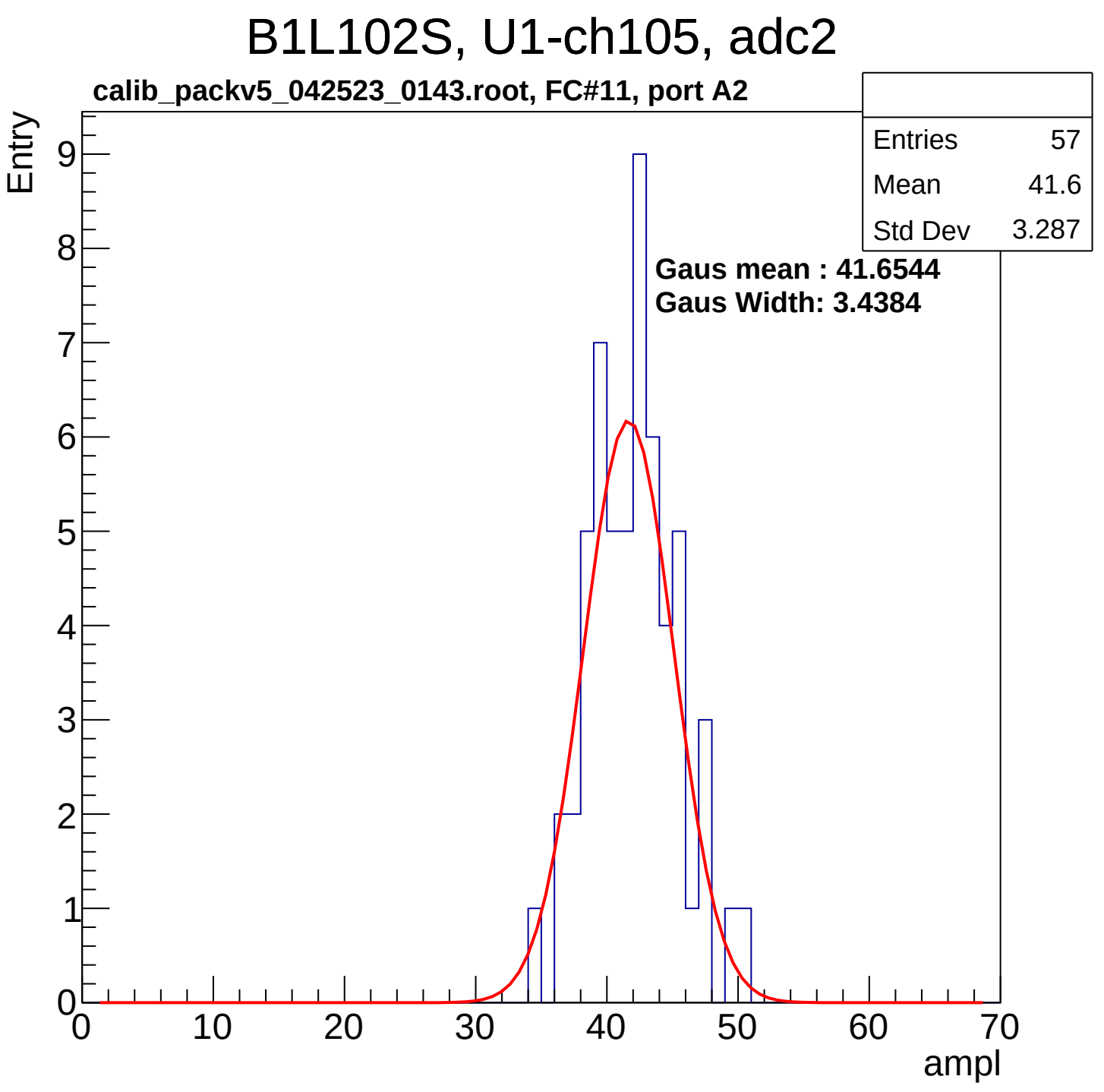
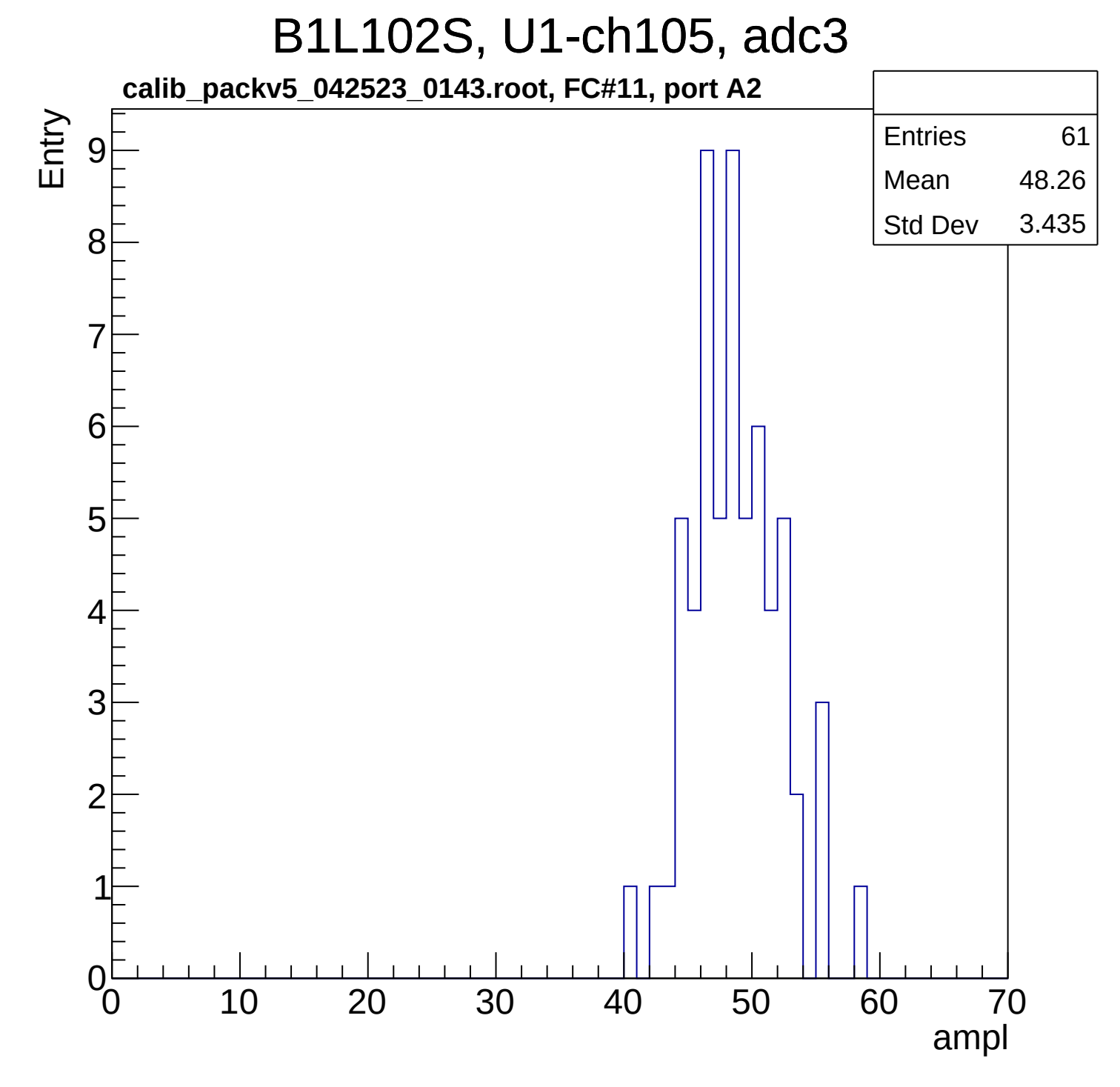
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 35.84
Std Dev 3.965
Gaus mean : 36.7160
Gaus Width: 4.3361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 41.6
Std Dev 3.287
Gaus mean : 41.6544
Gaus Width: 3.4384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 48.26
Std Dev 3.435
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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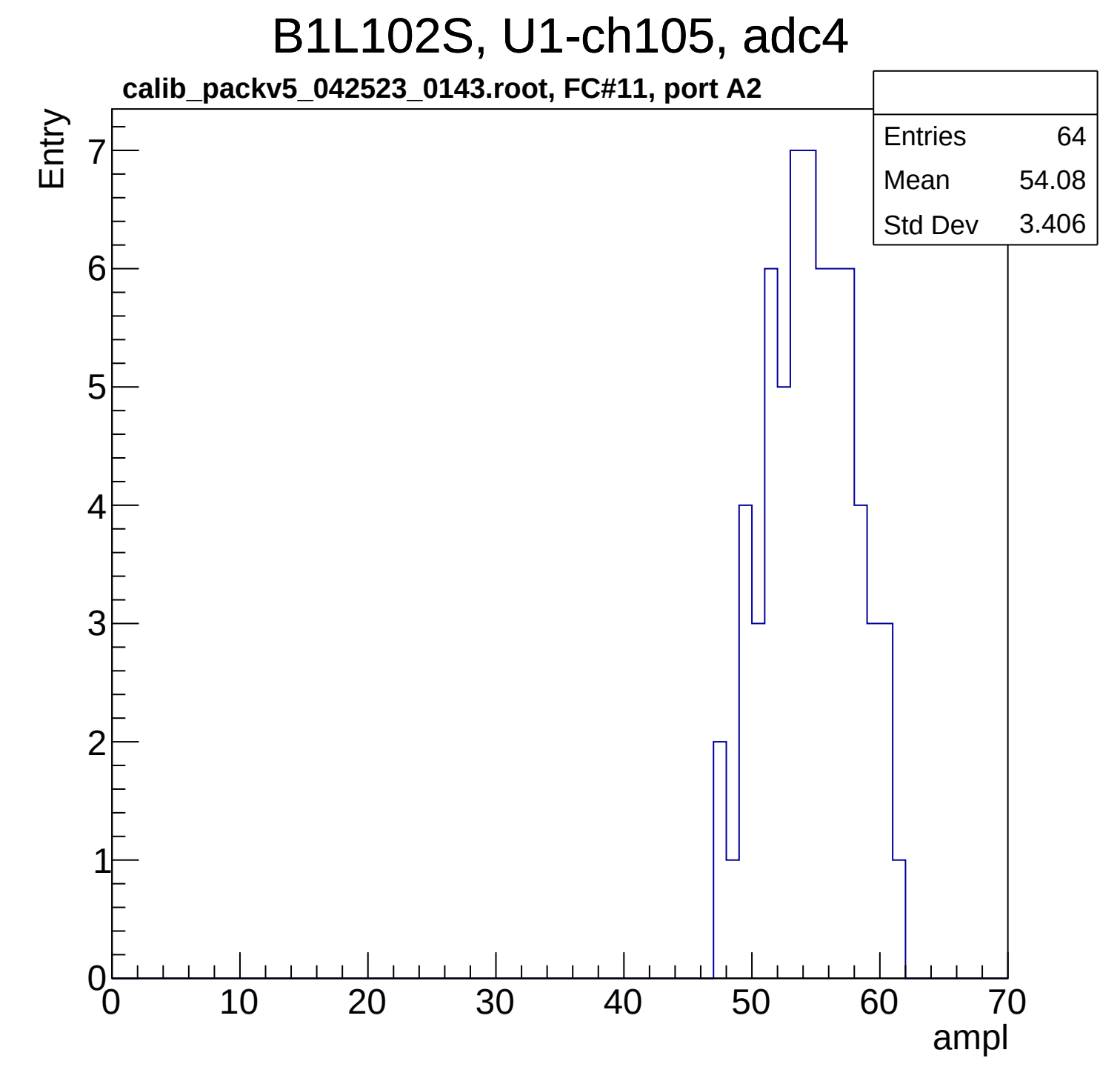
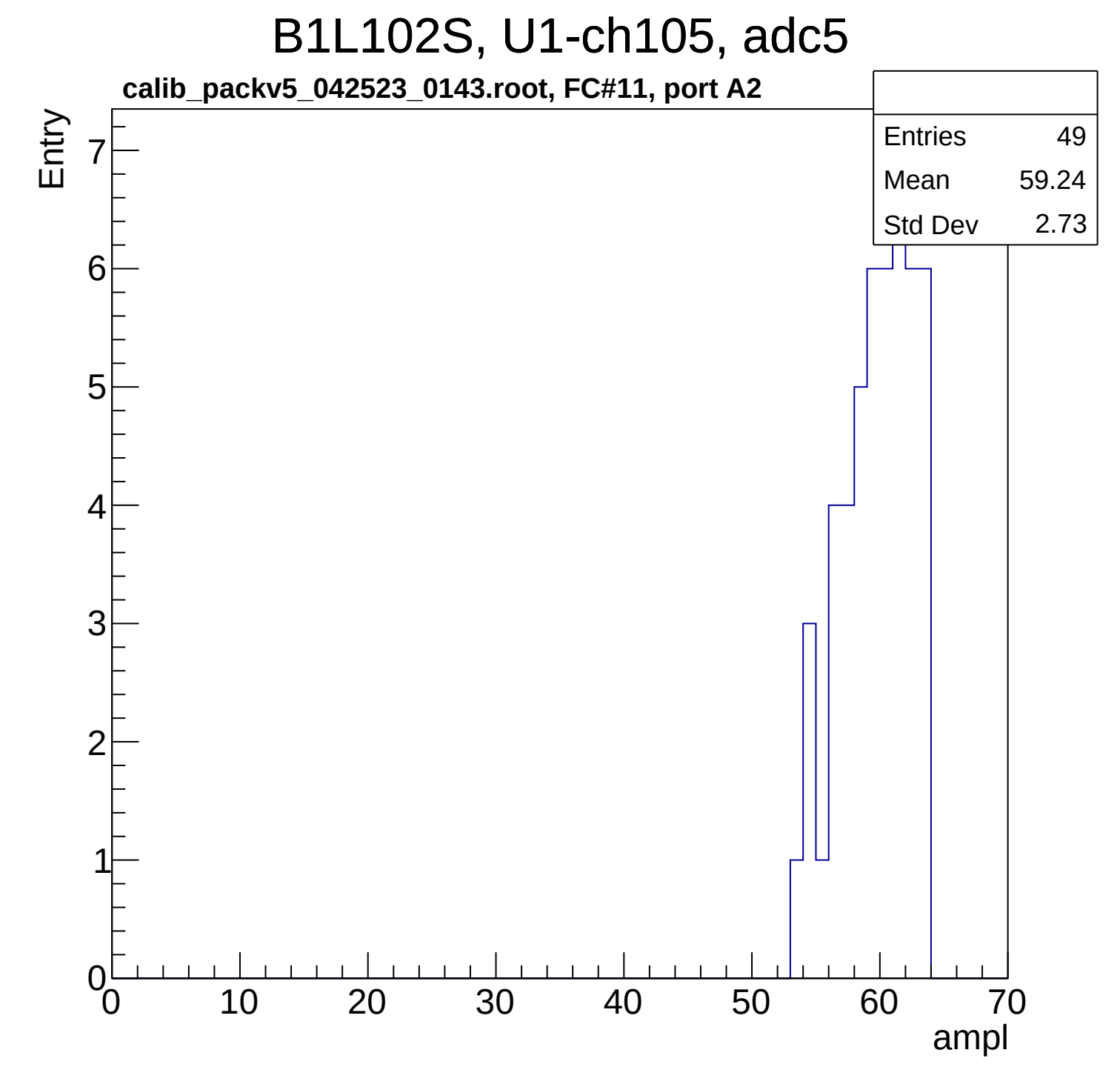
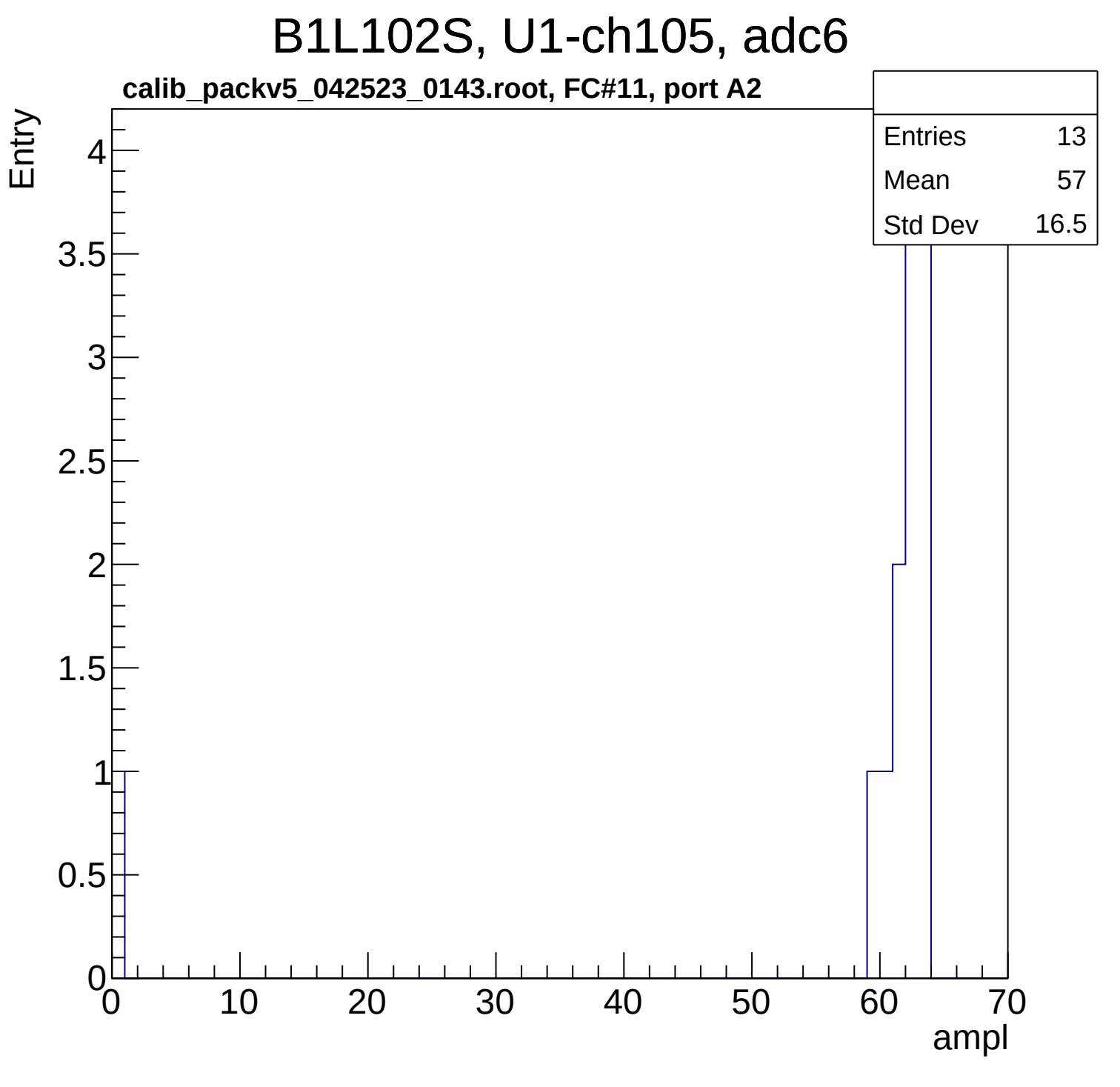
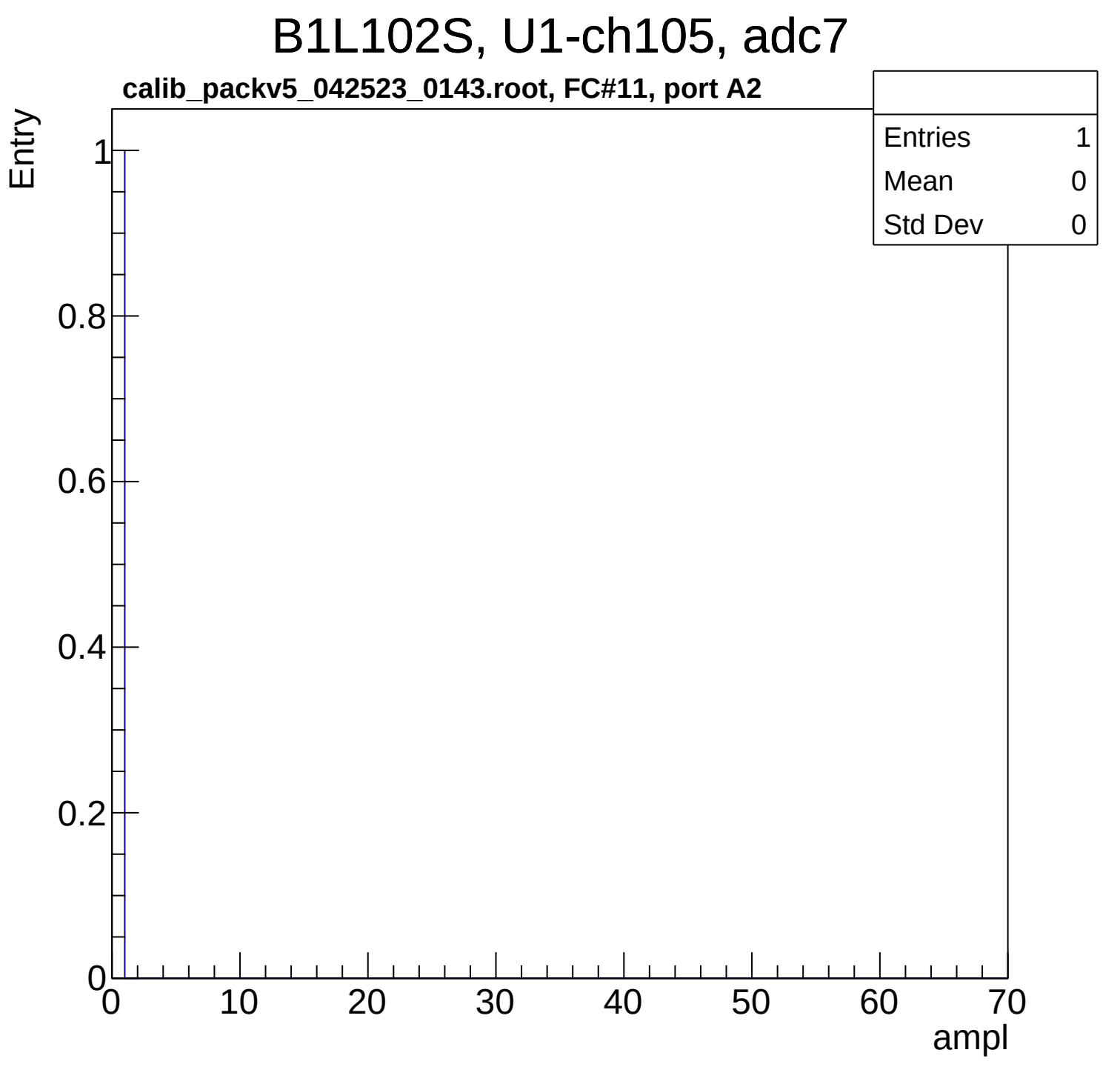
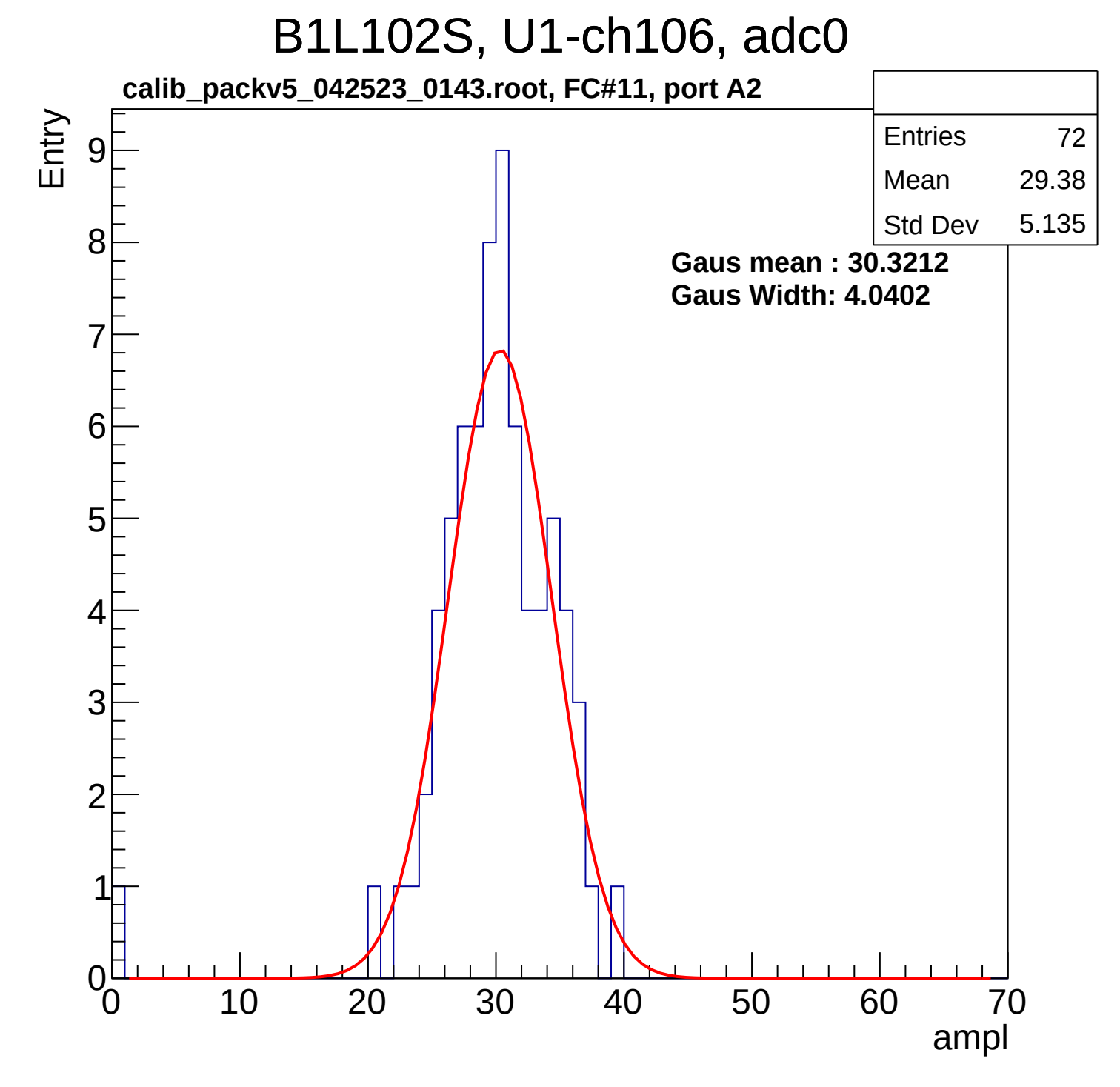
40
50
60
70

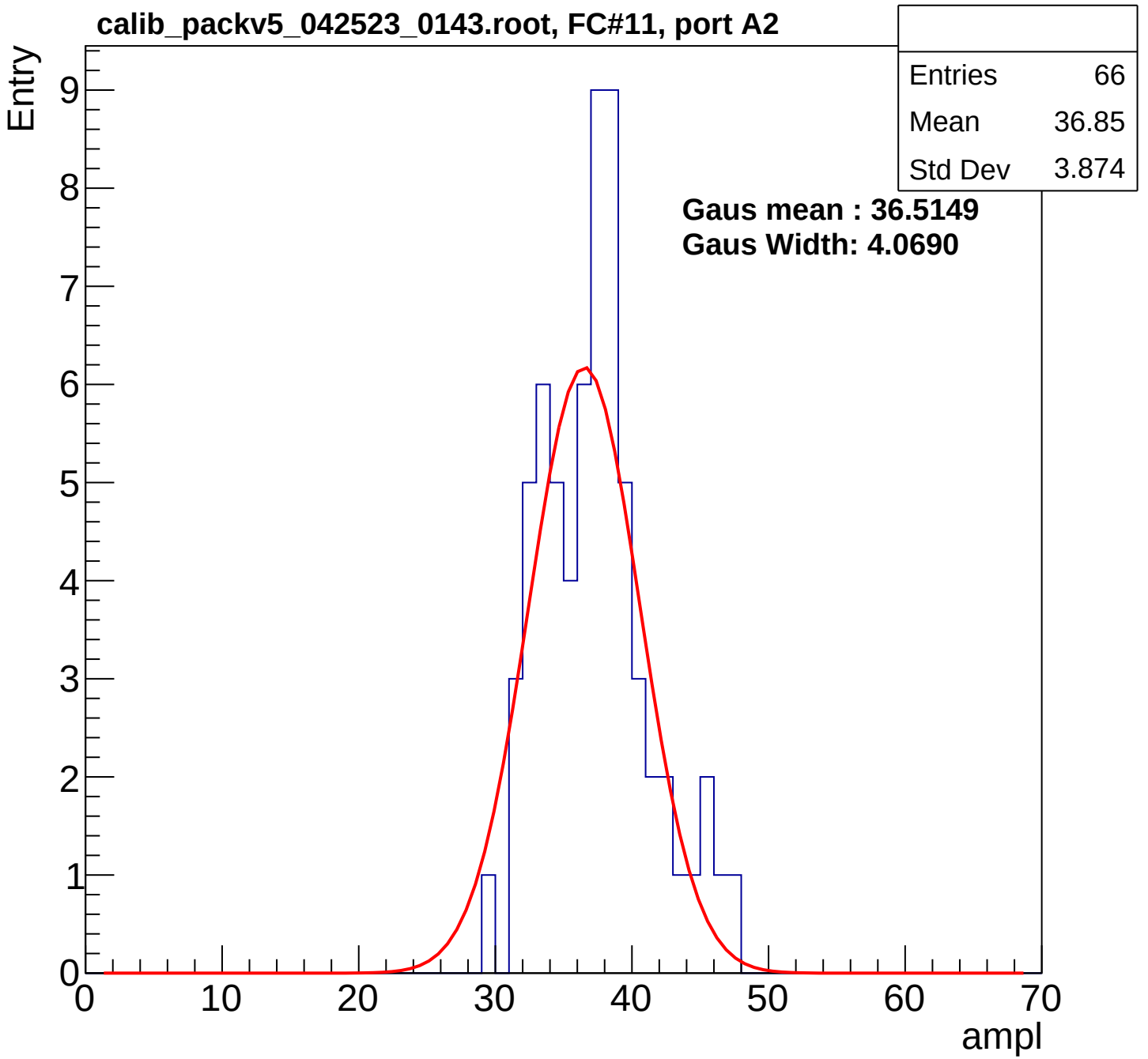

B1L102S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 54.08
Std Dev 3.406
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

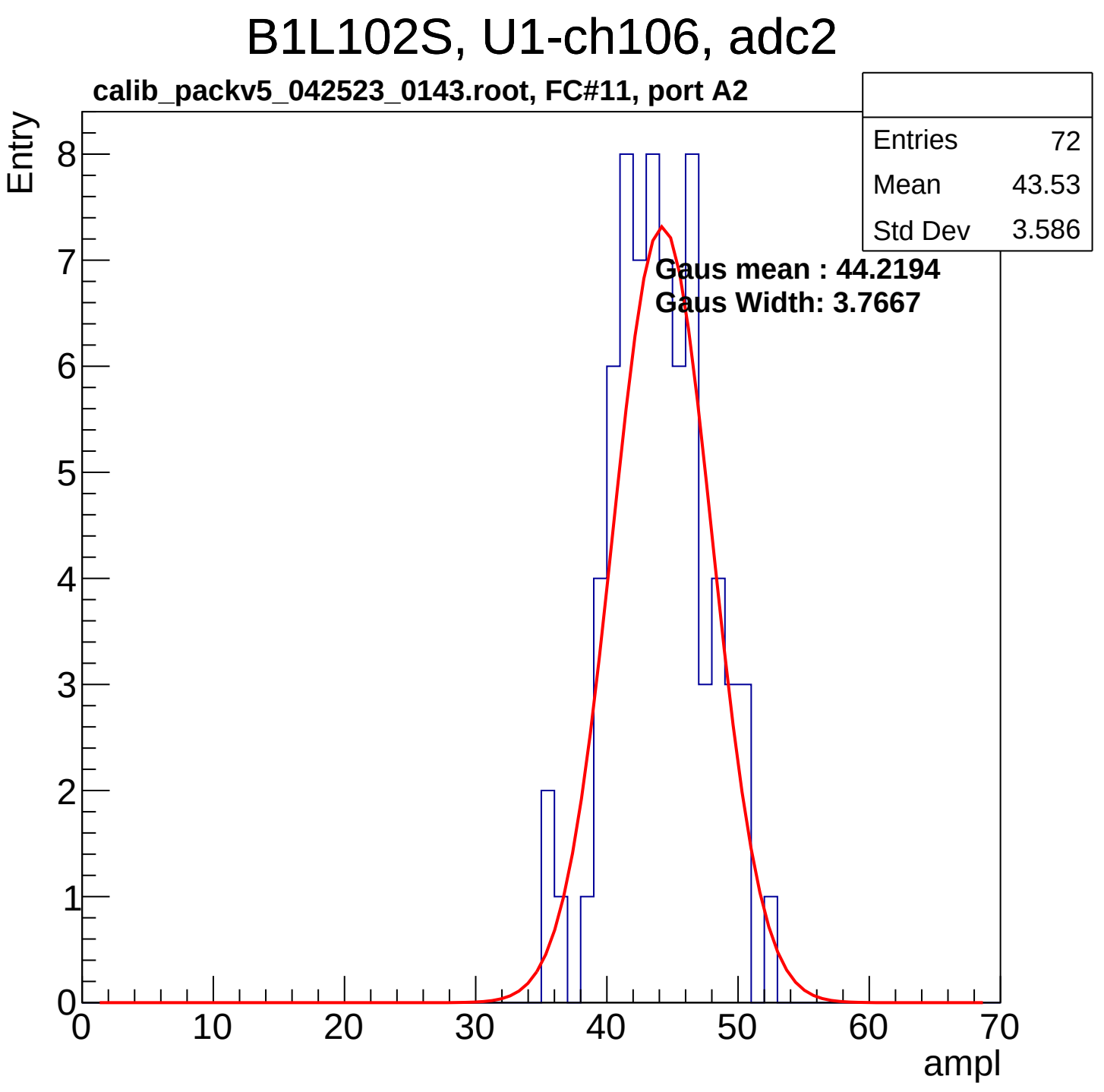

B1L102S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.24
Std Dev 2.73
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

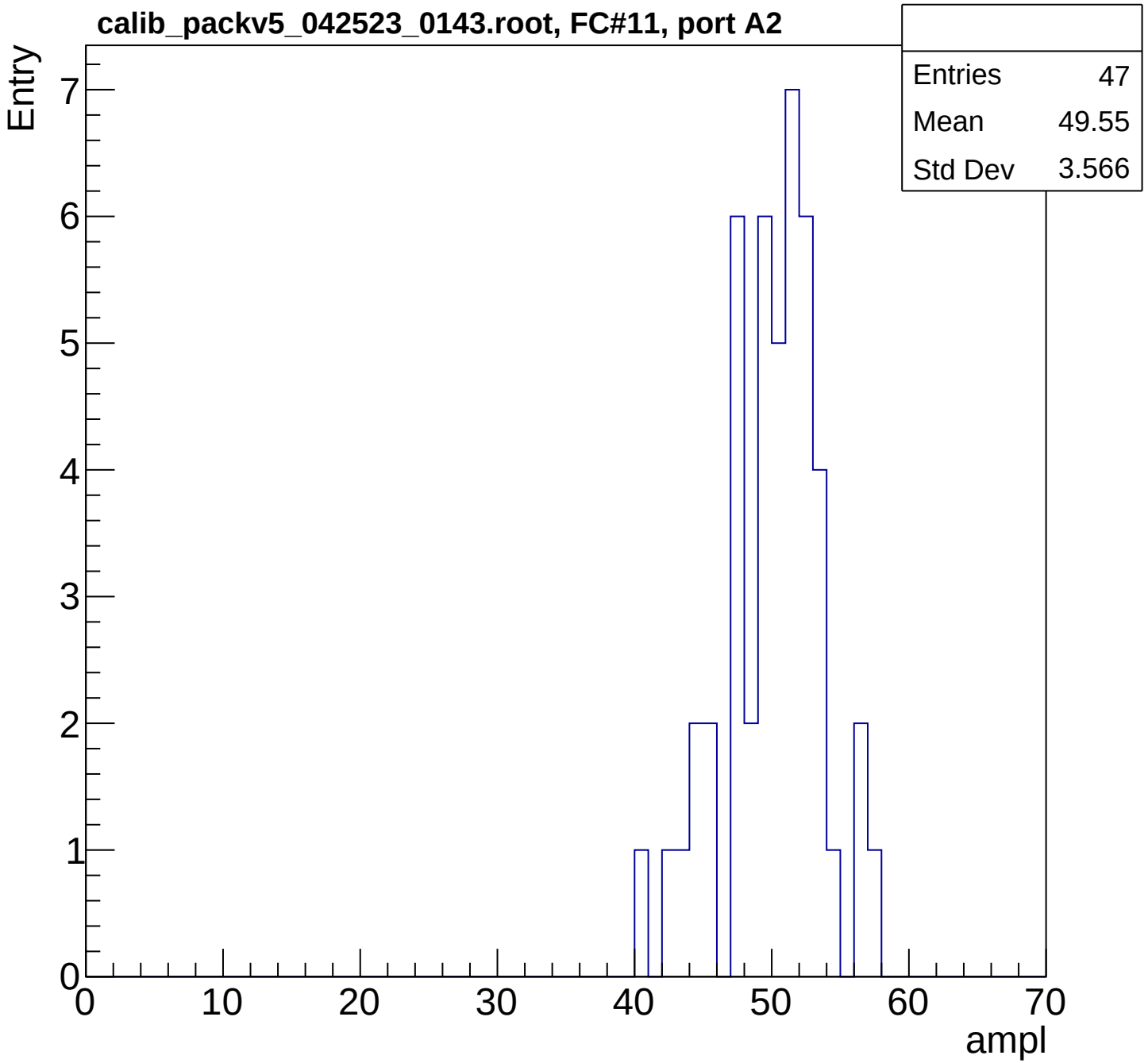

B1L102S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 57
Std Dev 16.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

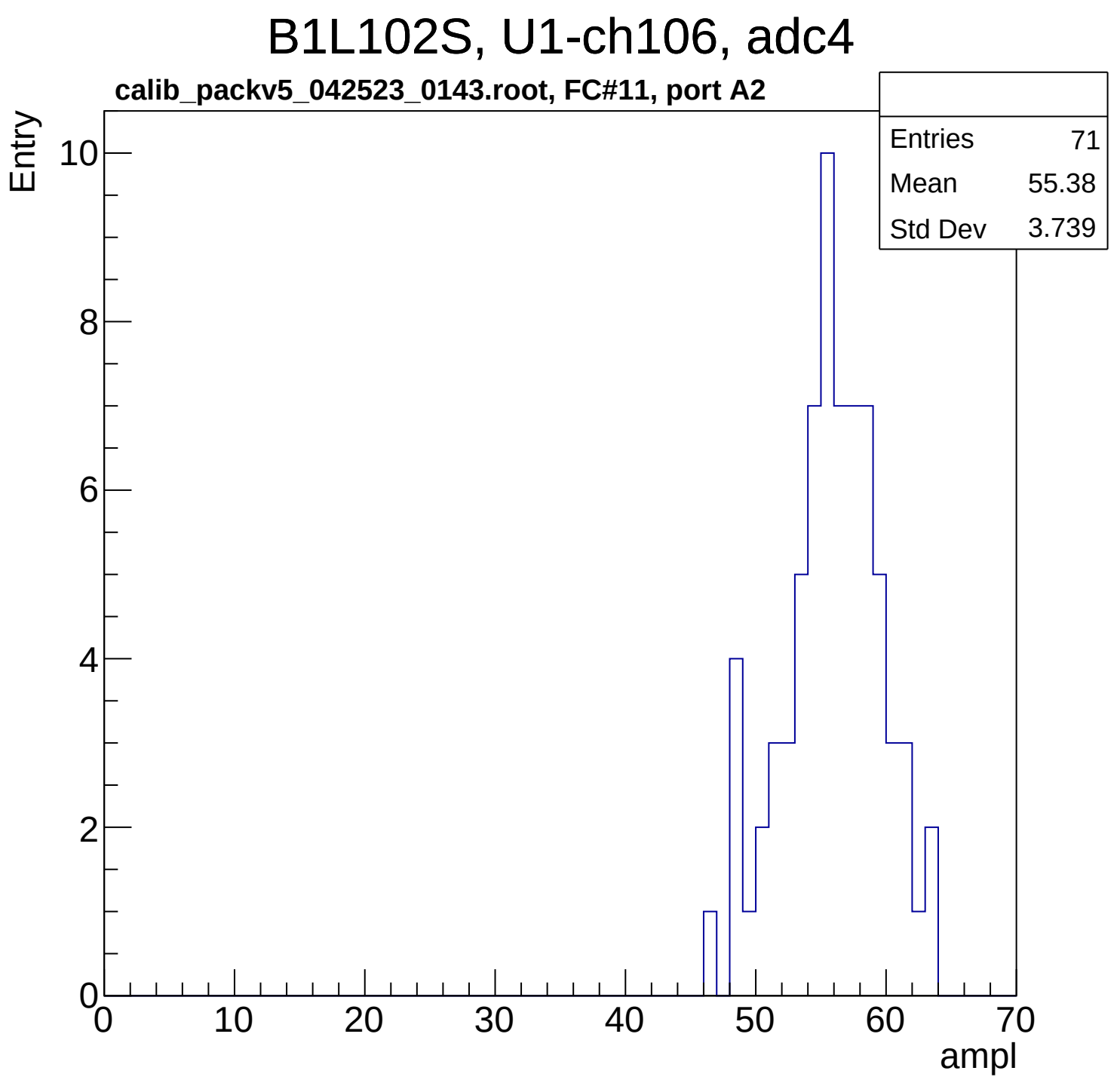

B1L102S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

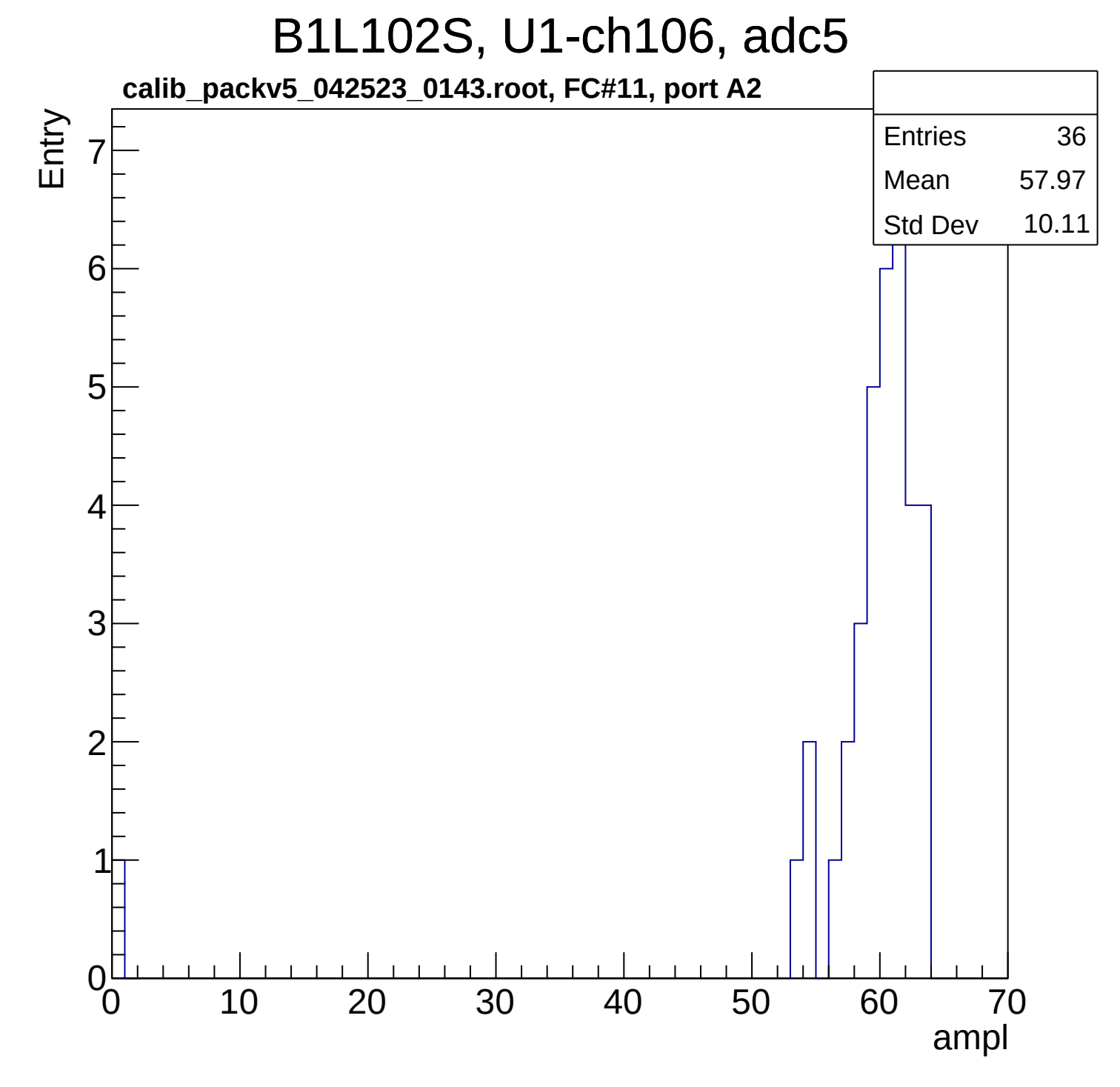

B1L102S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 29.38
Std Dev 5.135
Gaus mean : 30.3212
Gaus Width: 4.0402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

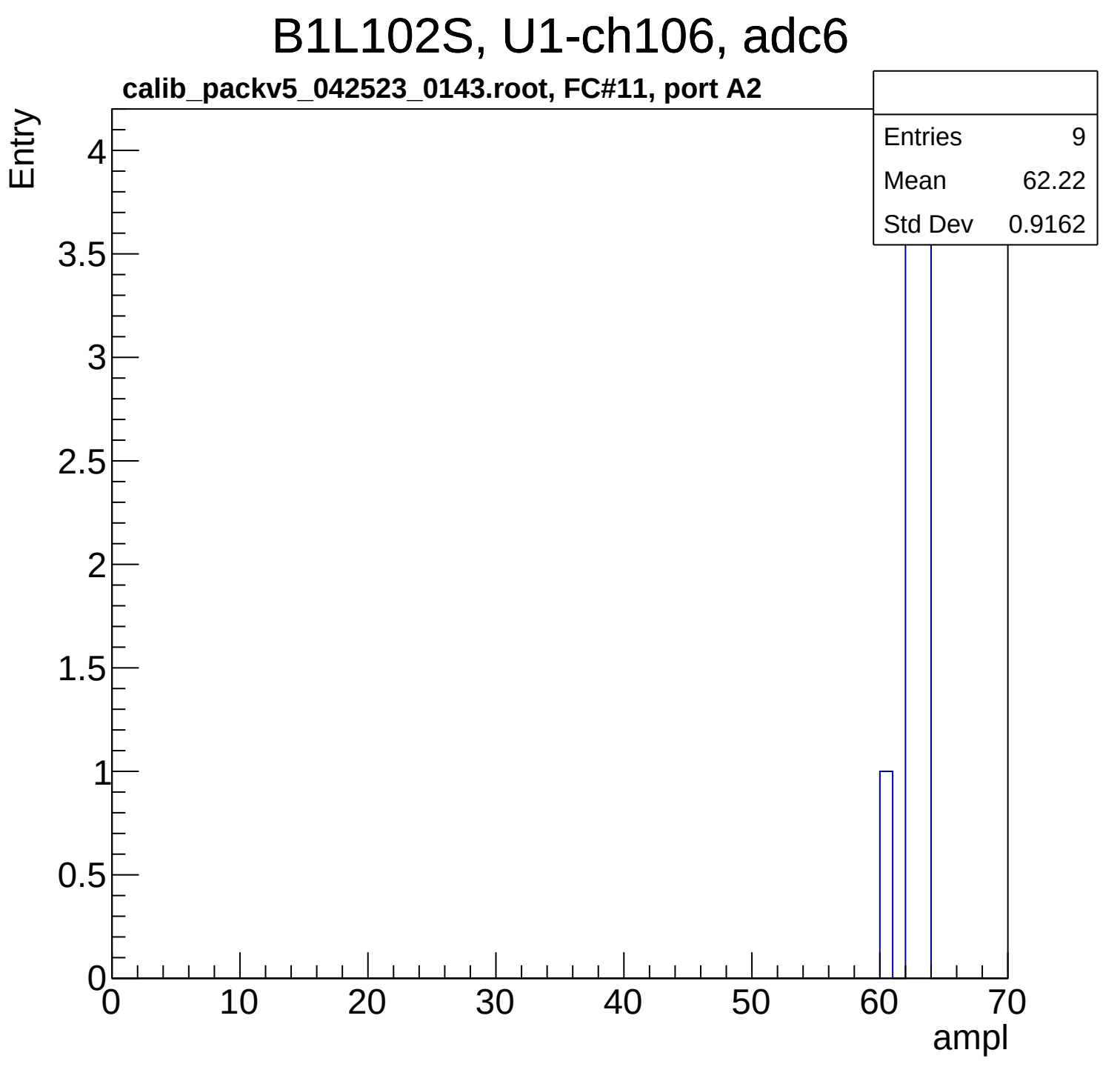

B1L102S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 36.85
Std Dev 3.874
Gaus mean : 36.5149
Gaus Width: 4.0690
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

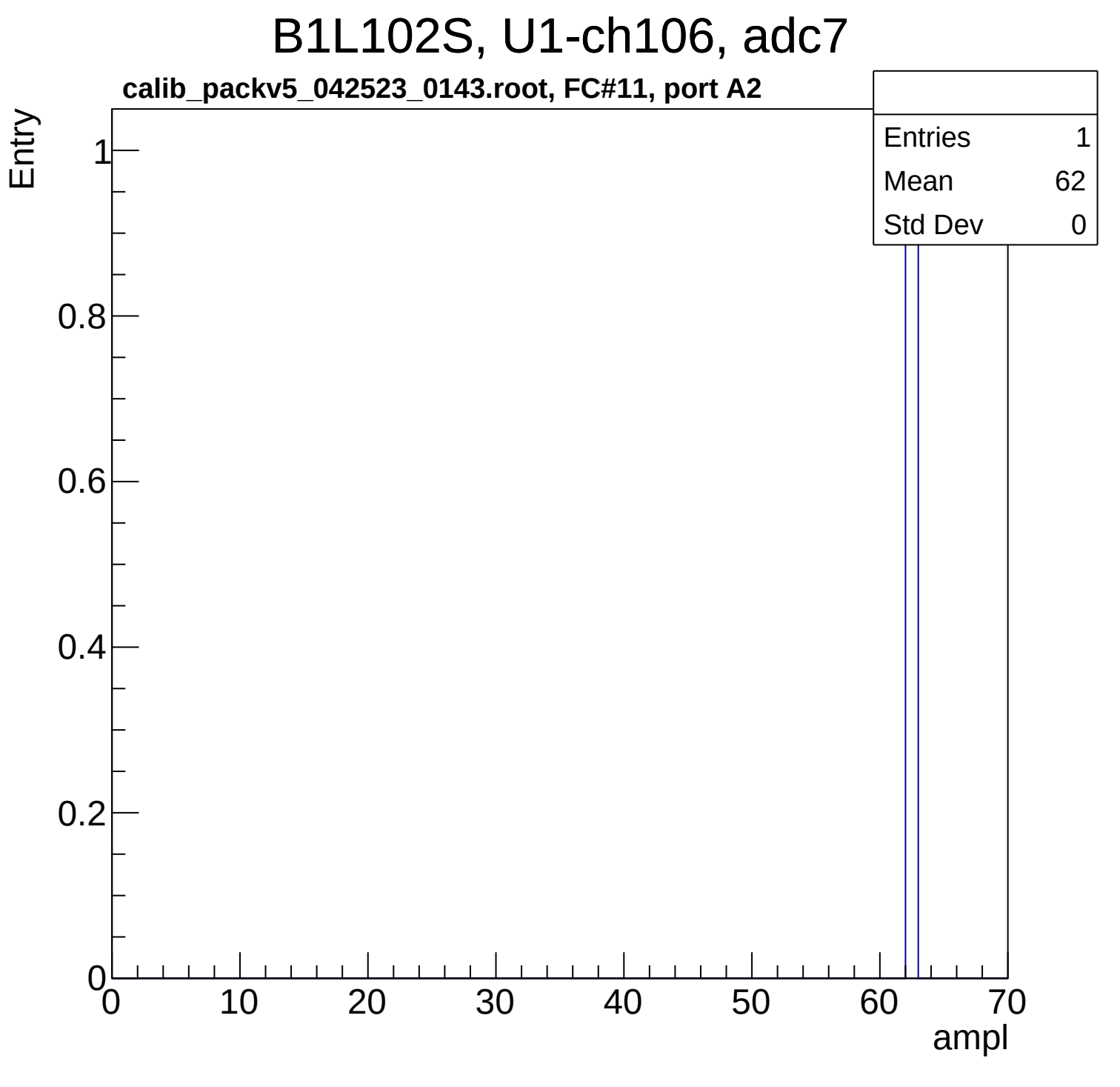

B1L102S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 43.53
Std Dev 3.586
Gaus mean : 44.2194
Gaus Width: 3.7667
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

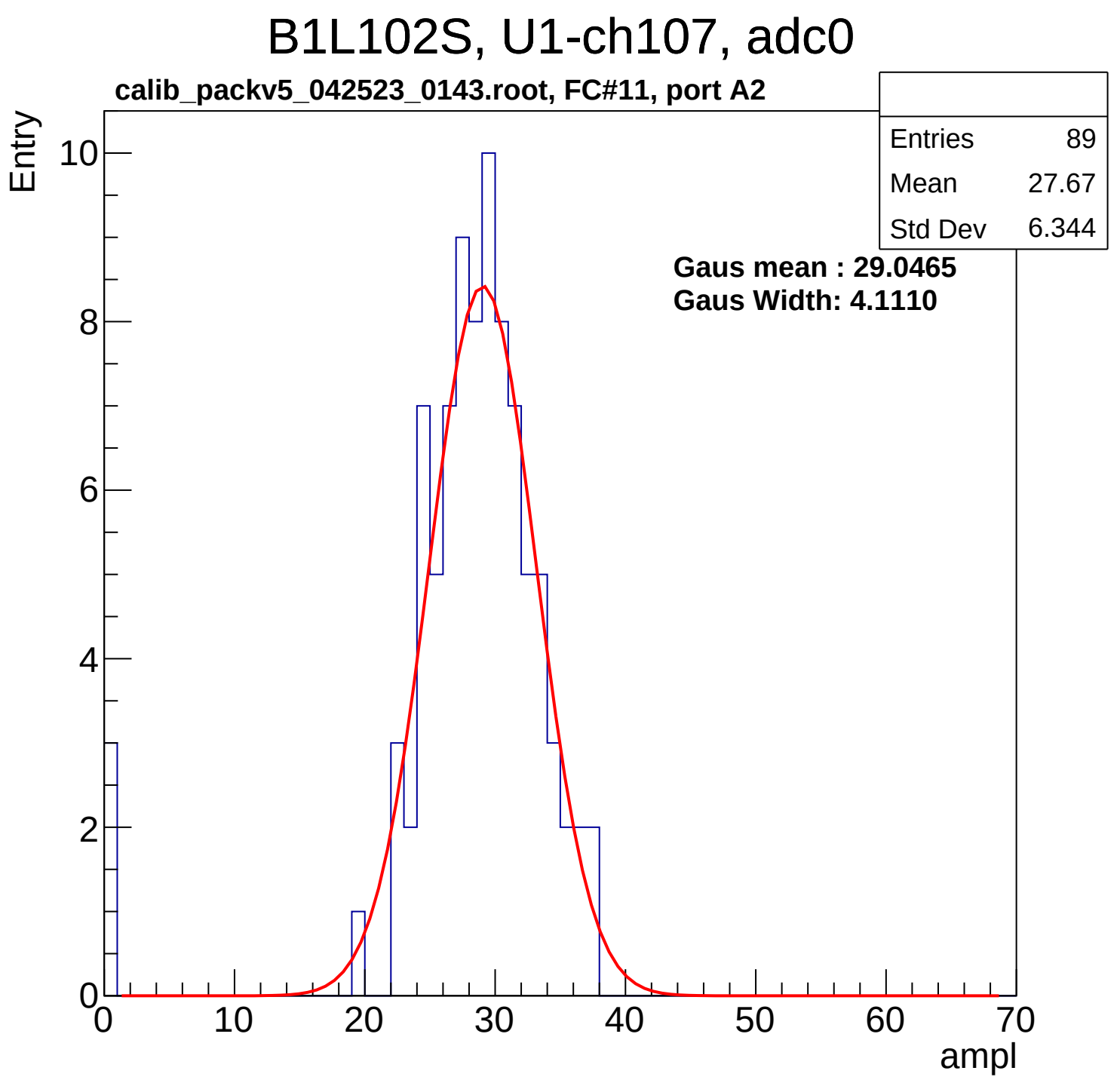

B1L102S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 49.55
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

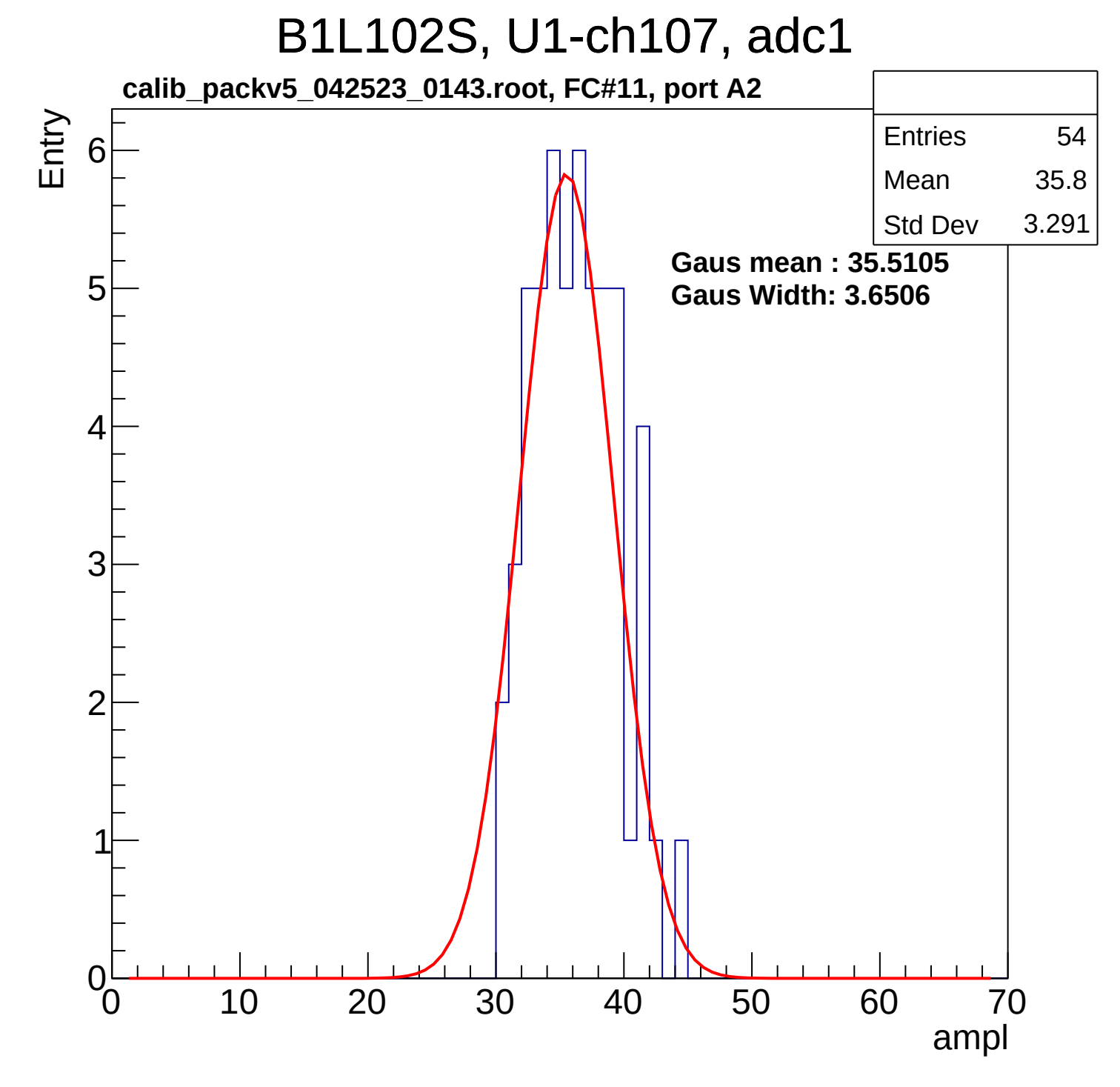

B1L102S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 55.38
Std Dev 3.739
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

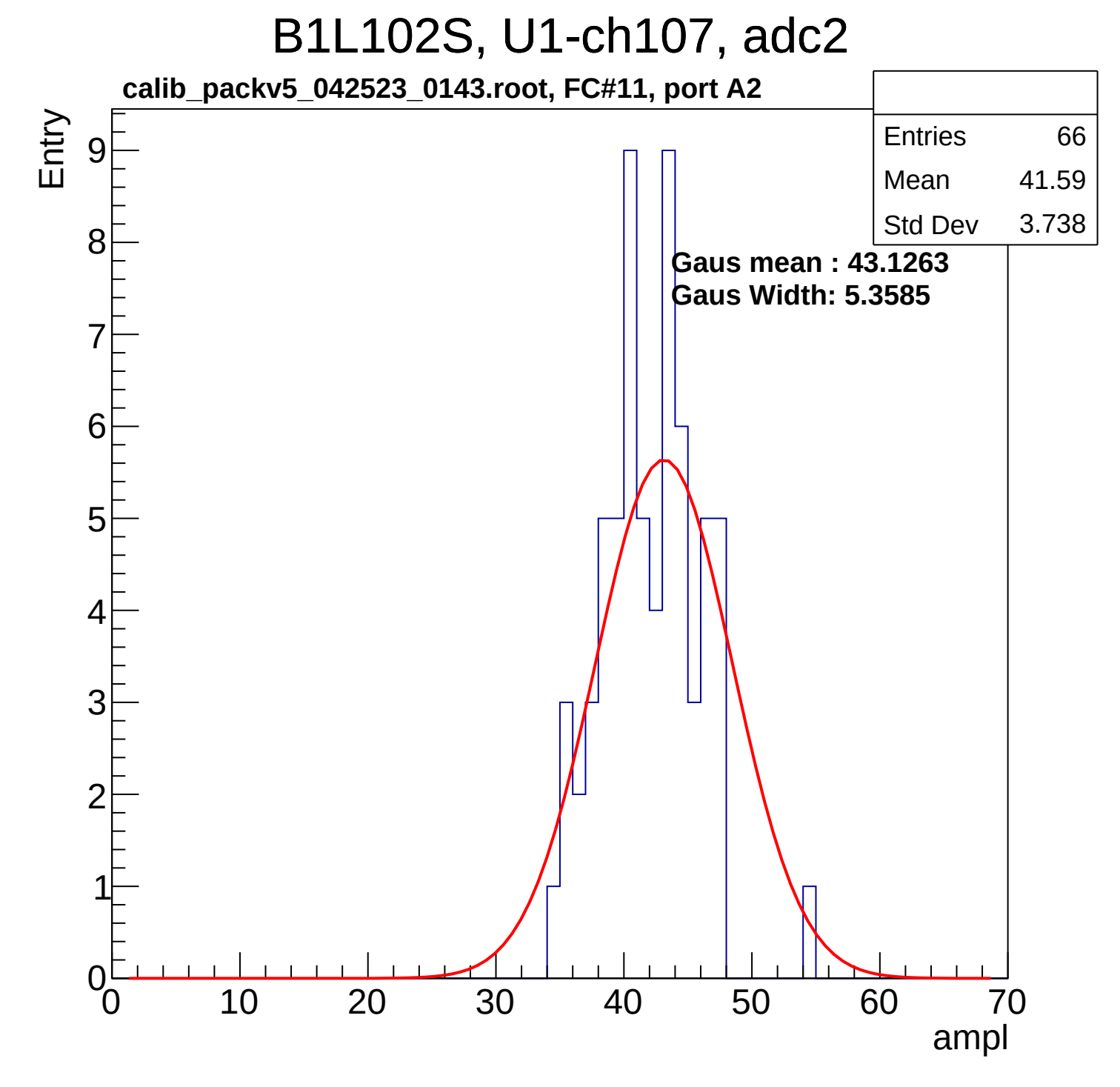

B1L102S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 36
Mean 57.97
Std Dev 10.11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

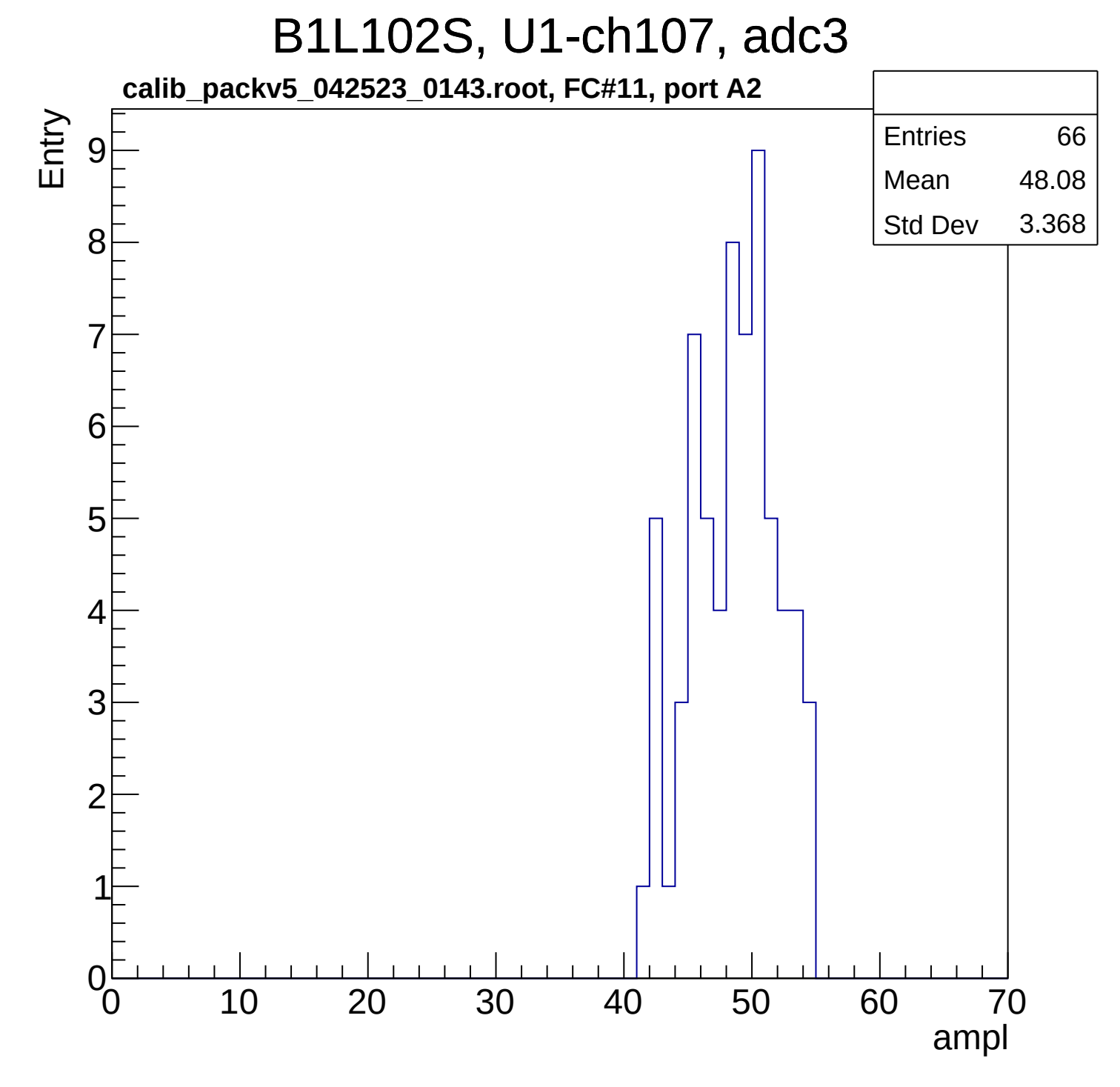

B1L102S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 62.22
Std Dev 0.9162
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 27.67
Std Dev 6.344
Gaus mean : 29.0465
Gaus Width: 4.1110
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 35.8
Std Dev 3.291
Gaus mean : 35.5105
Gaus Width: 3.6506
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 41.59
Std Dev 3.738
Gaus mean : 43.1263
Gaus Width: 5.3585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 48.08
Std Dev 3.368
Entry
ampl
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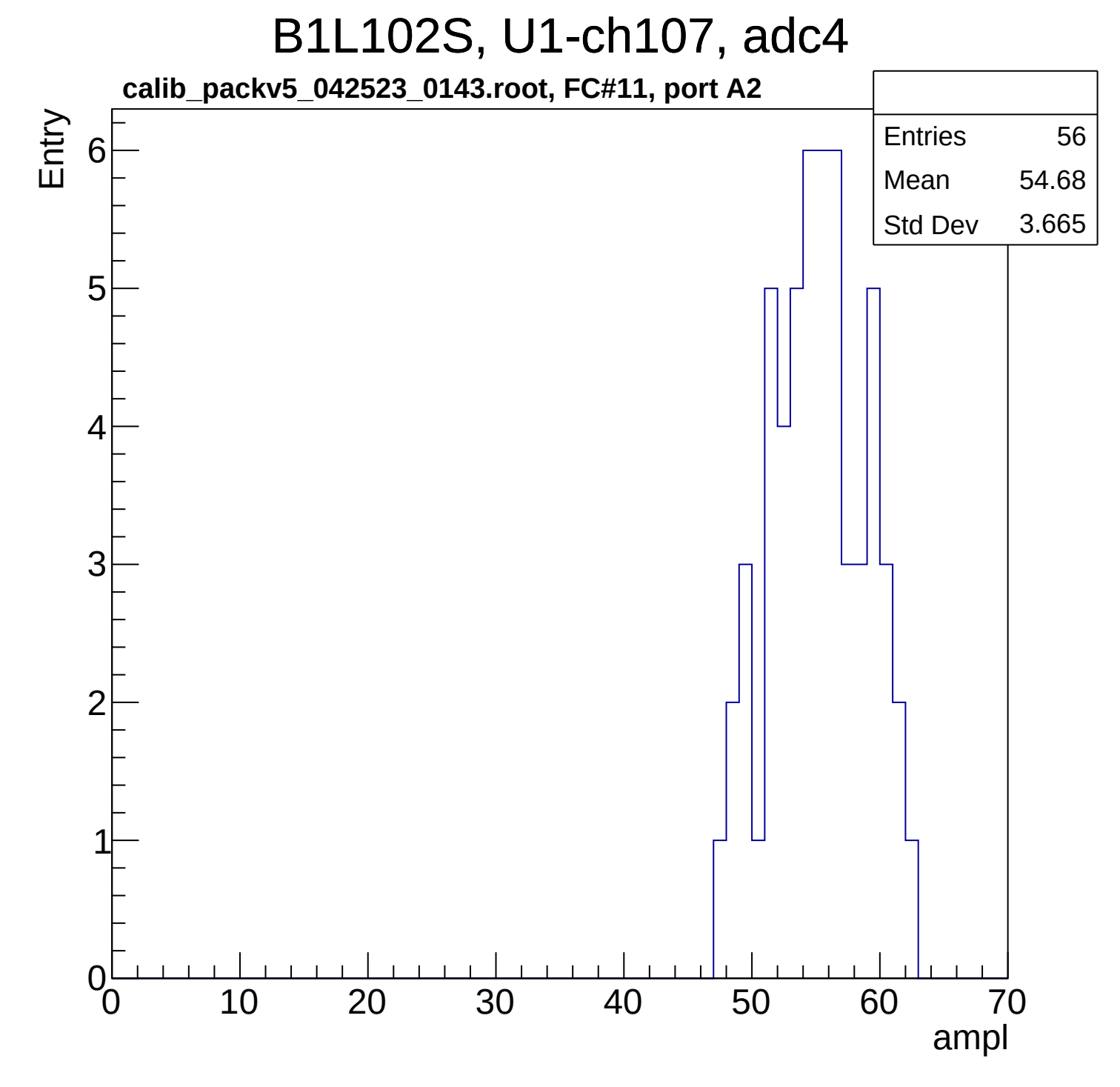
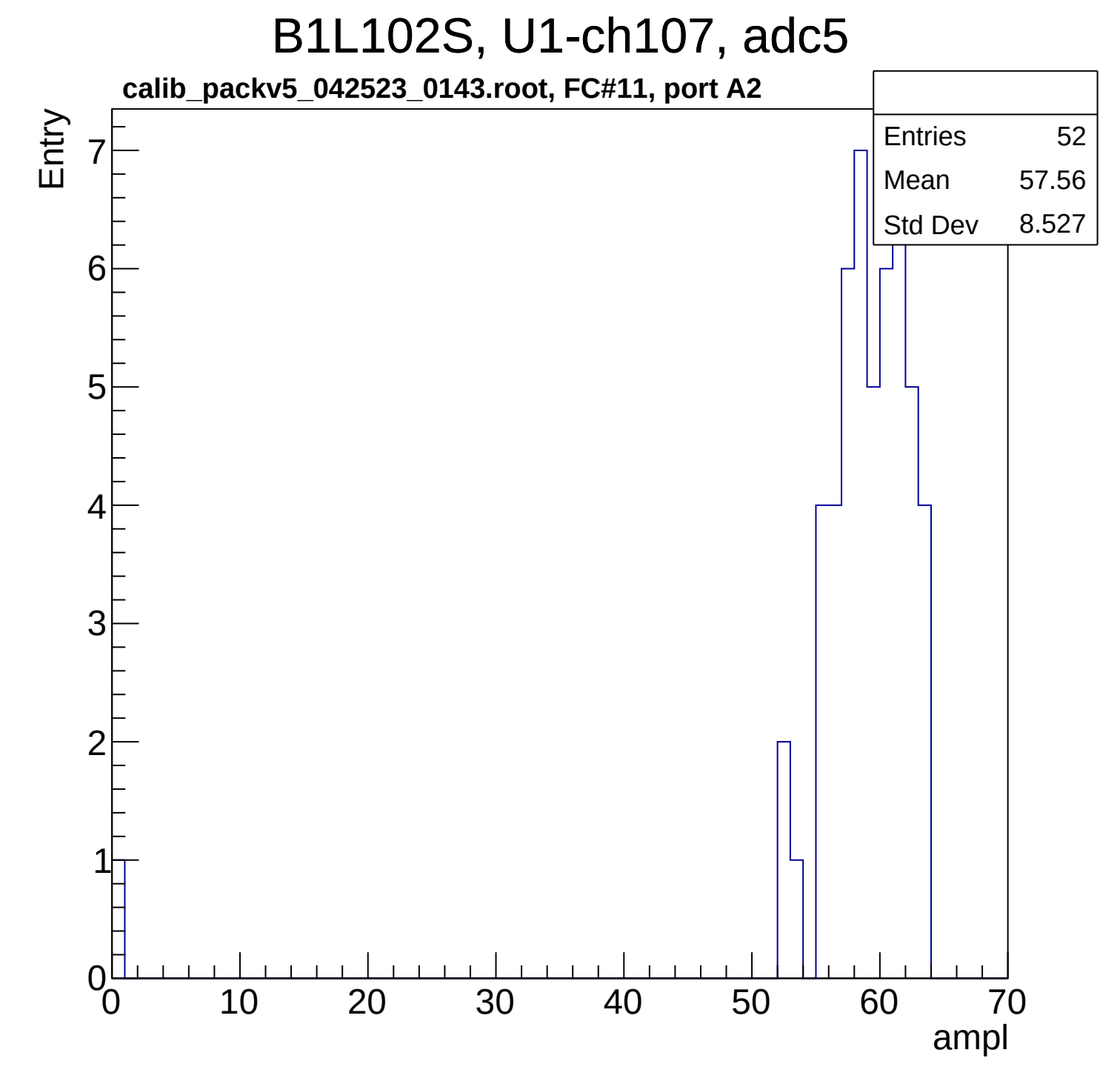
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 54.68
Std Dev 3.665
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 57.56
Std Dev 8.527
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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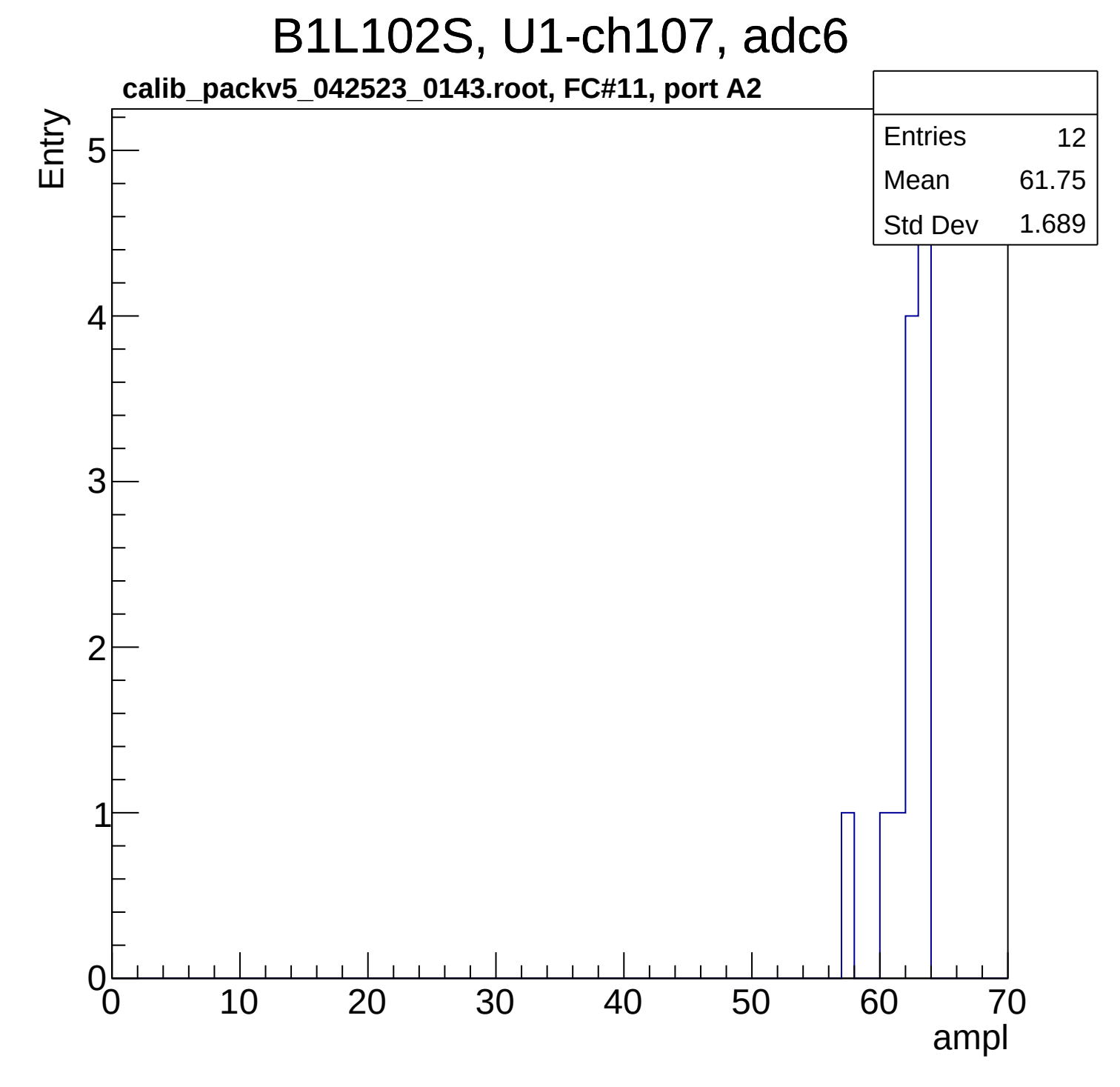
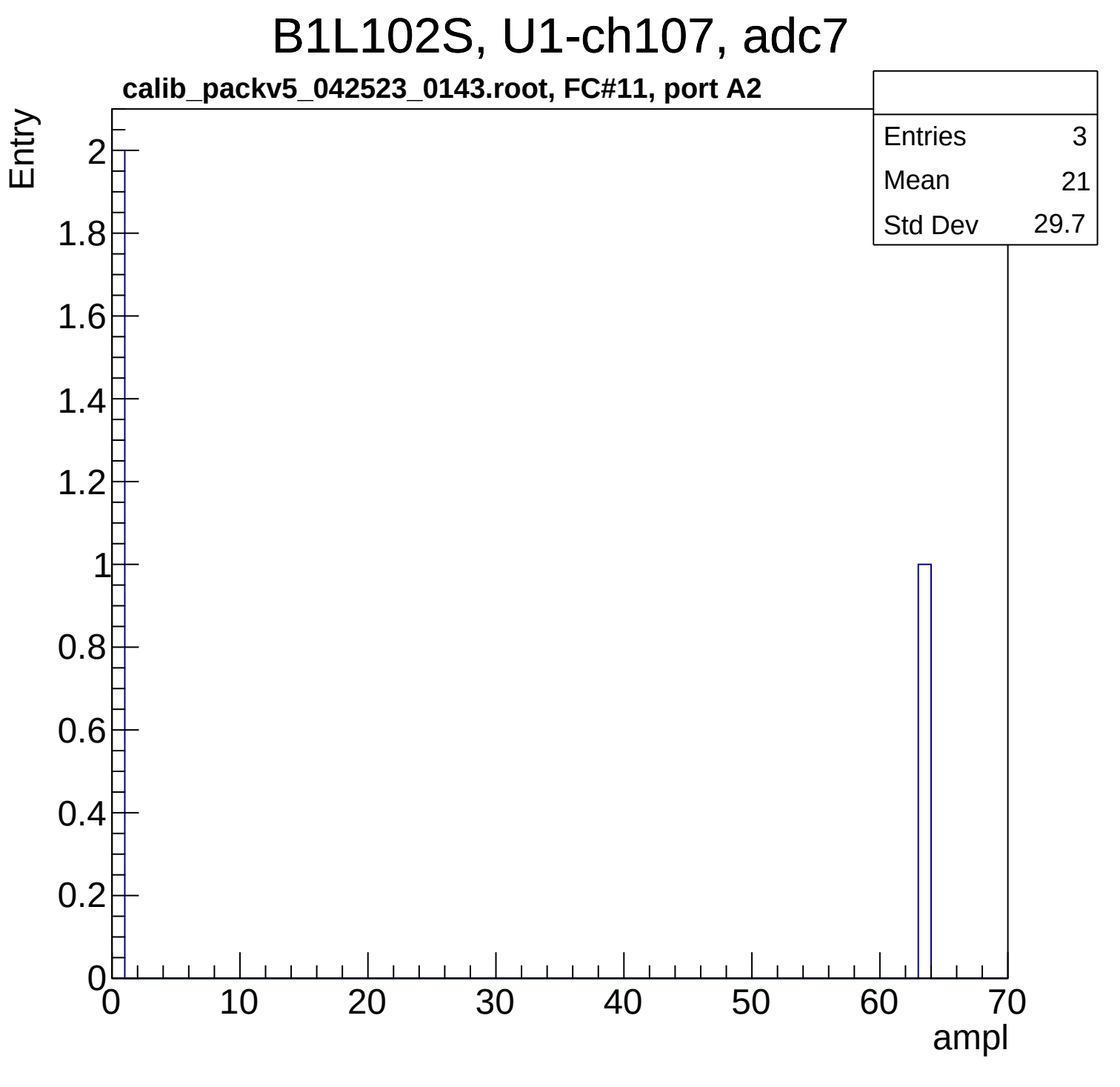
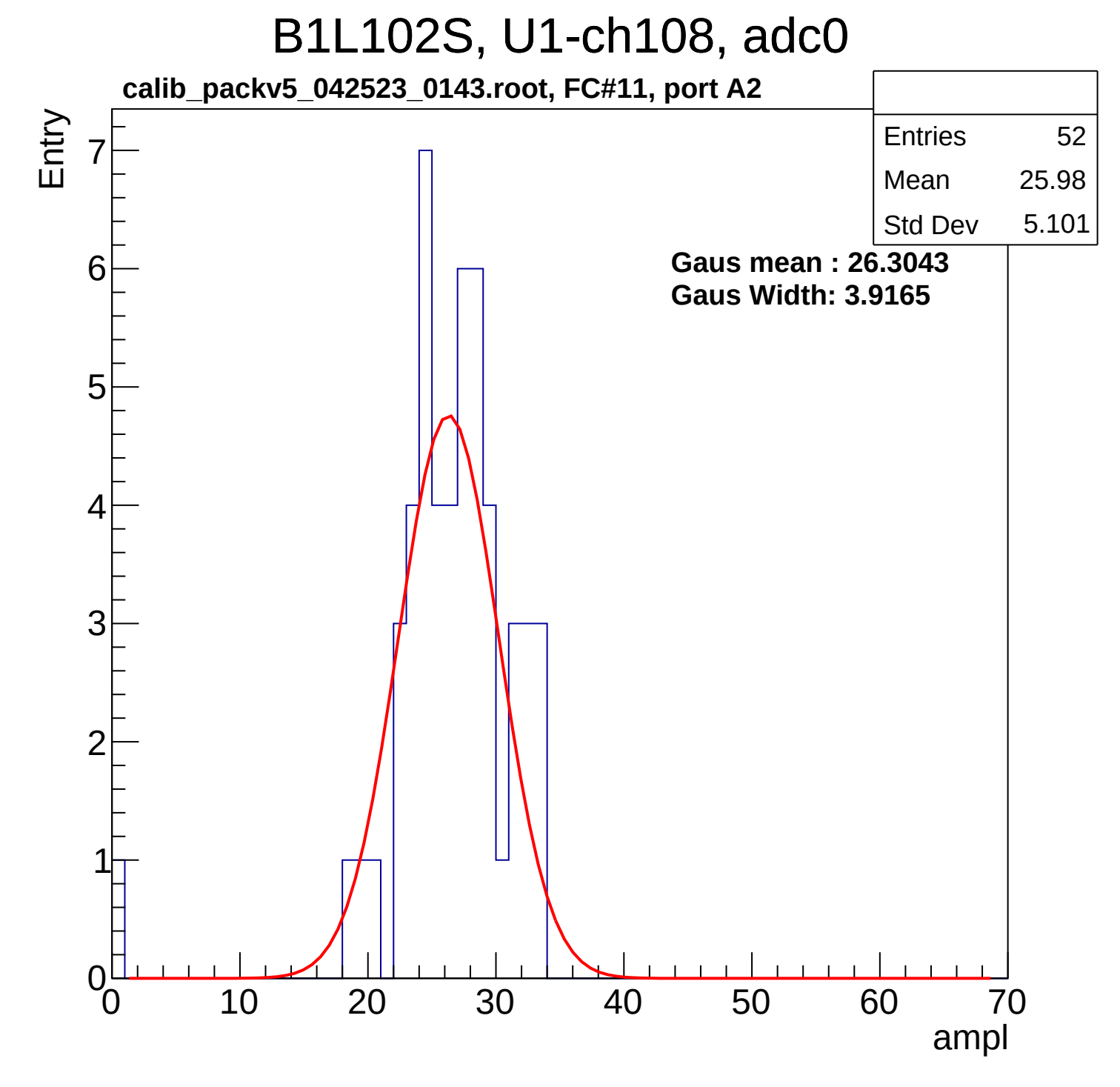
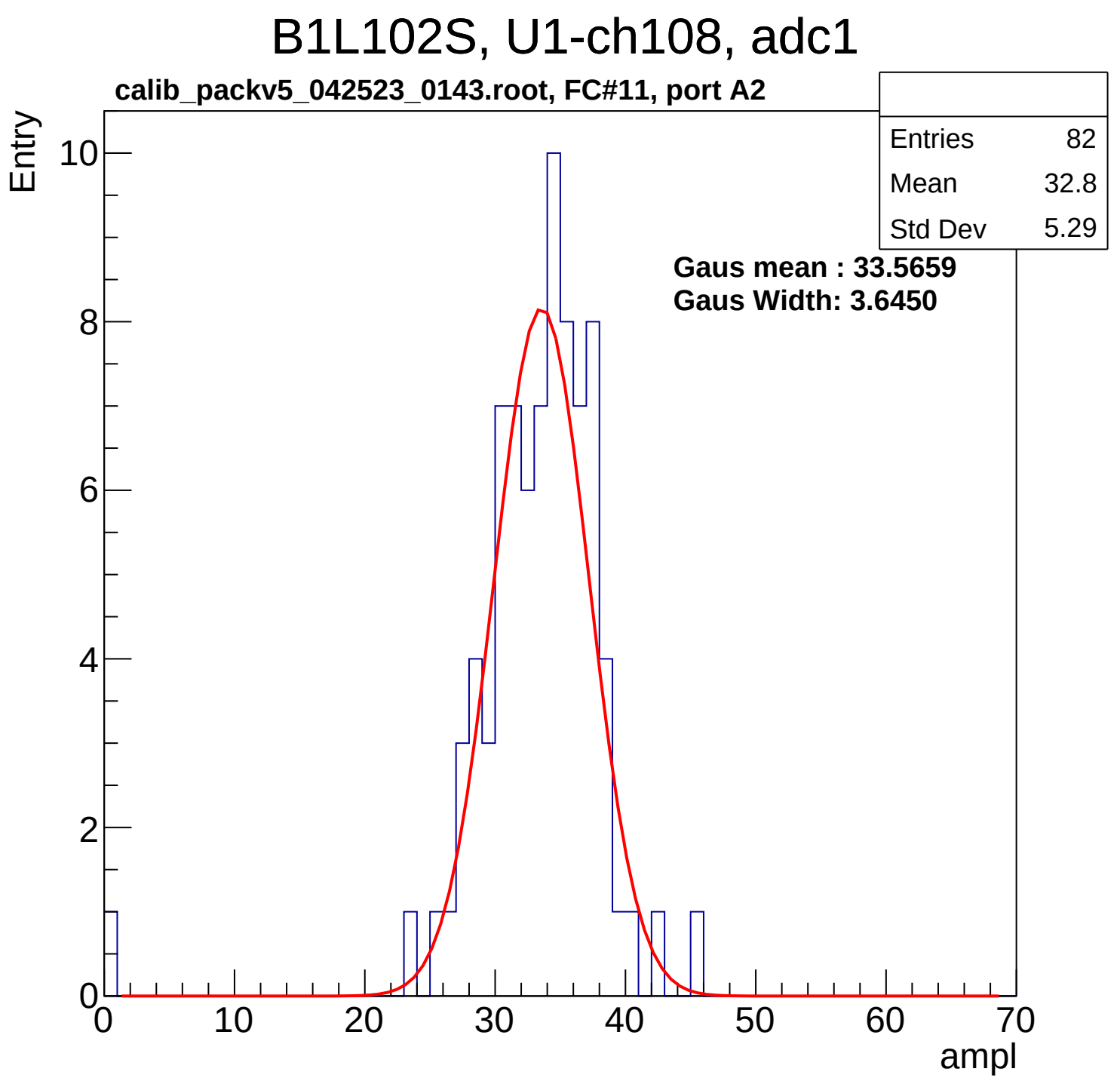
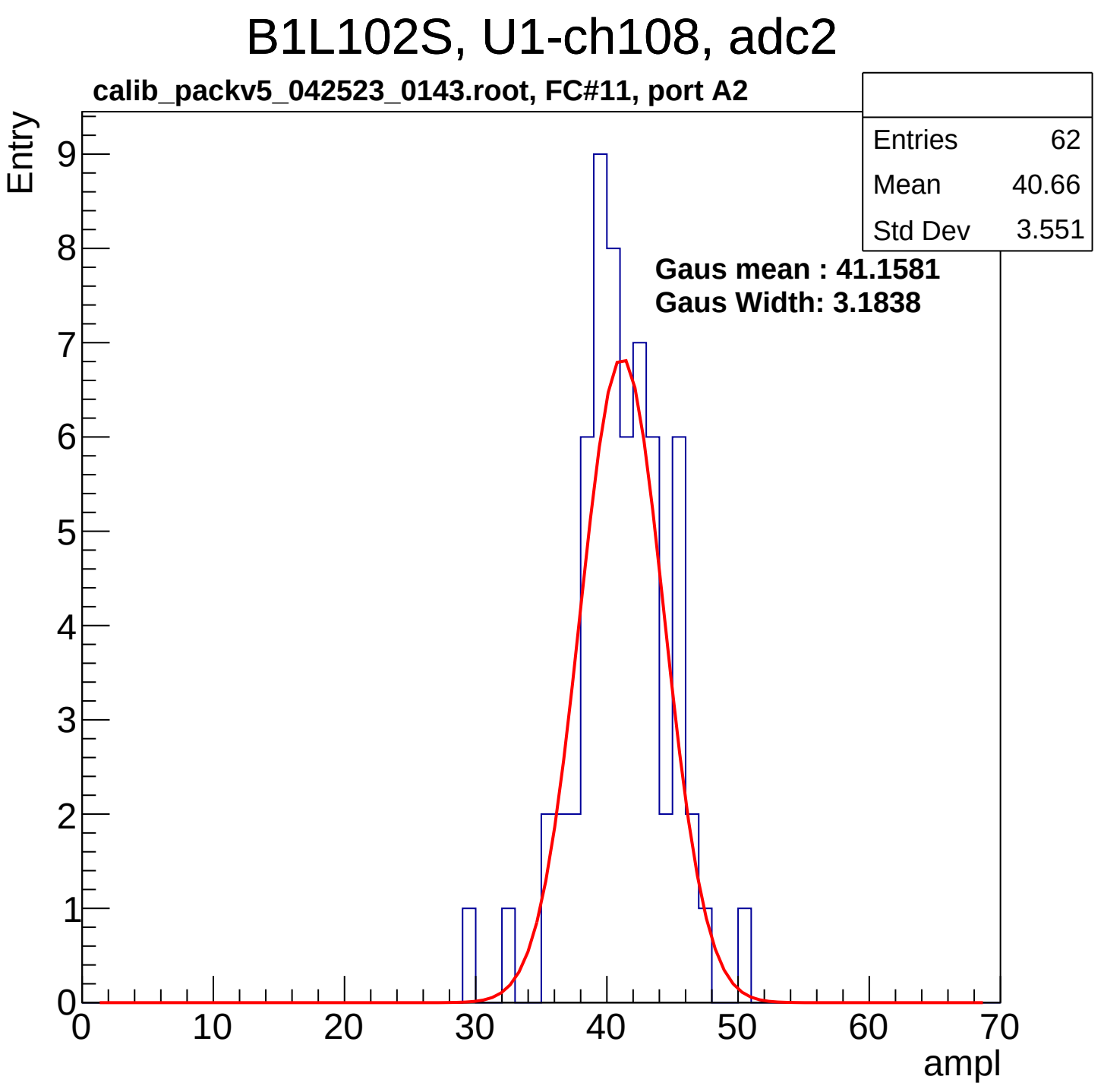
60
70


B1L102S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.75
Std Dev 1.689
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 25.98
Std Dev 5.101
Gaus mean : 26.3043
Gaus Width: 3.9165
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 82
Mean 32.8
Std Dev 5.29
Gaus mean : 33.5659
Gaus Width: 3.6450
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 40.66
Std Dev 3.551
Gaus mean : 41.1581
Gaus Width: 3.1838
Entry
ampl
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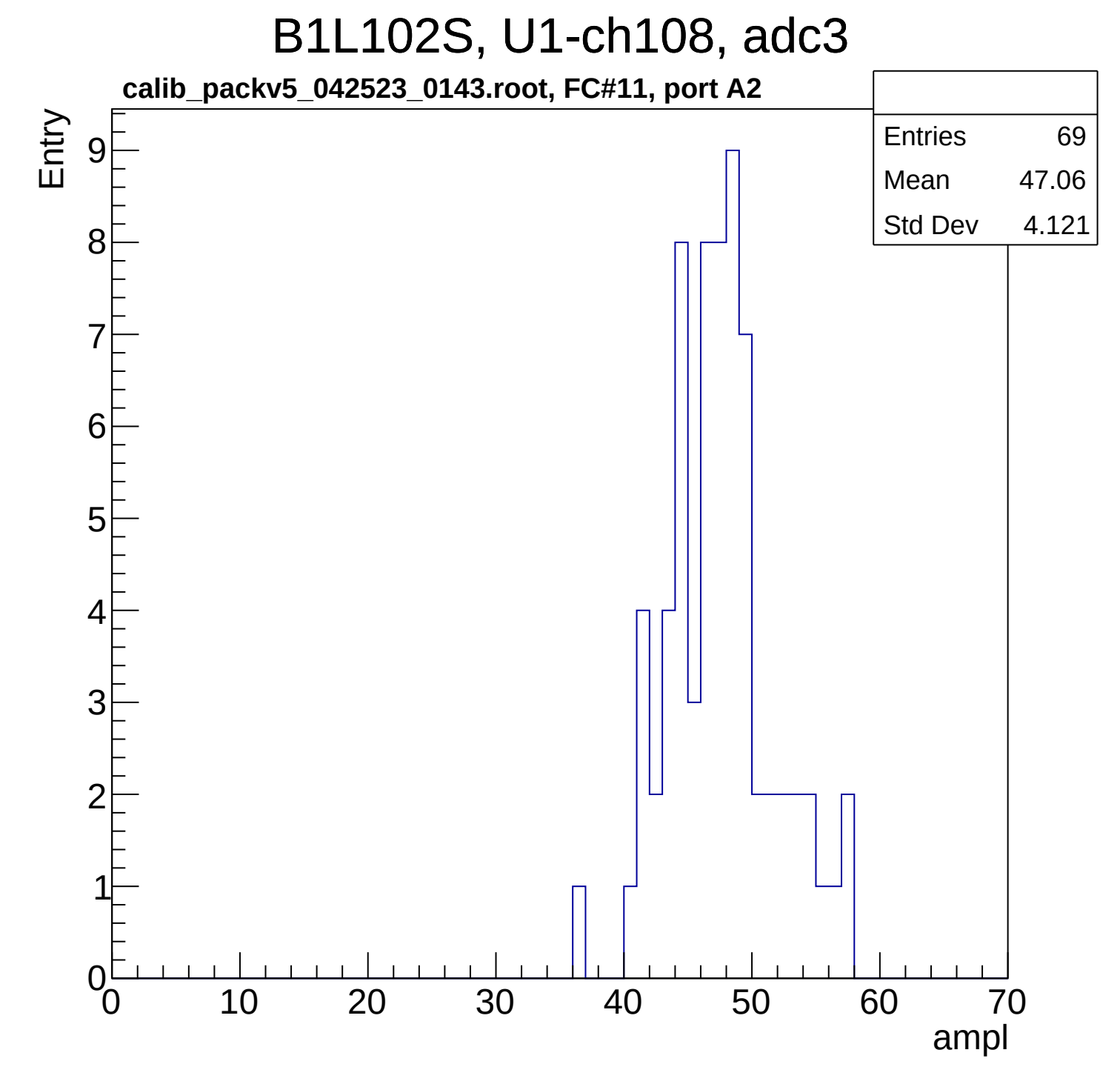
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 47.06
Std Dev 4.121
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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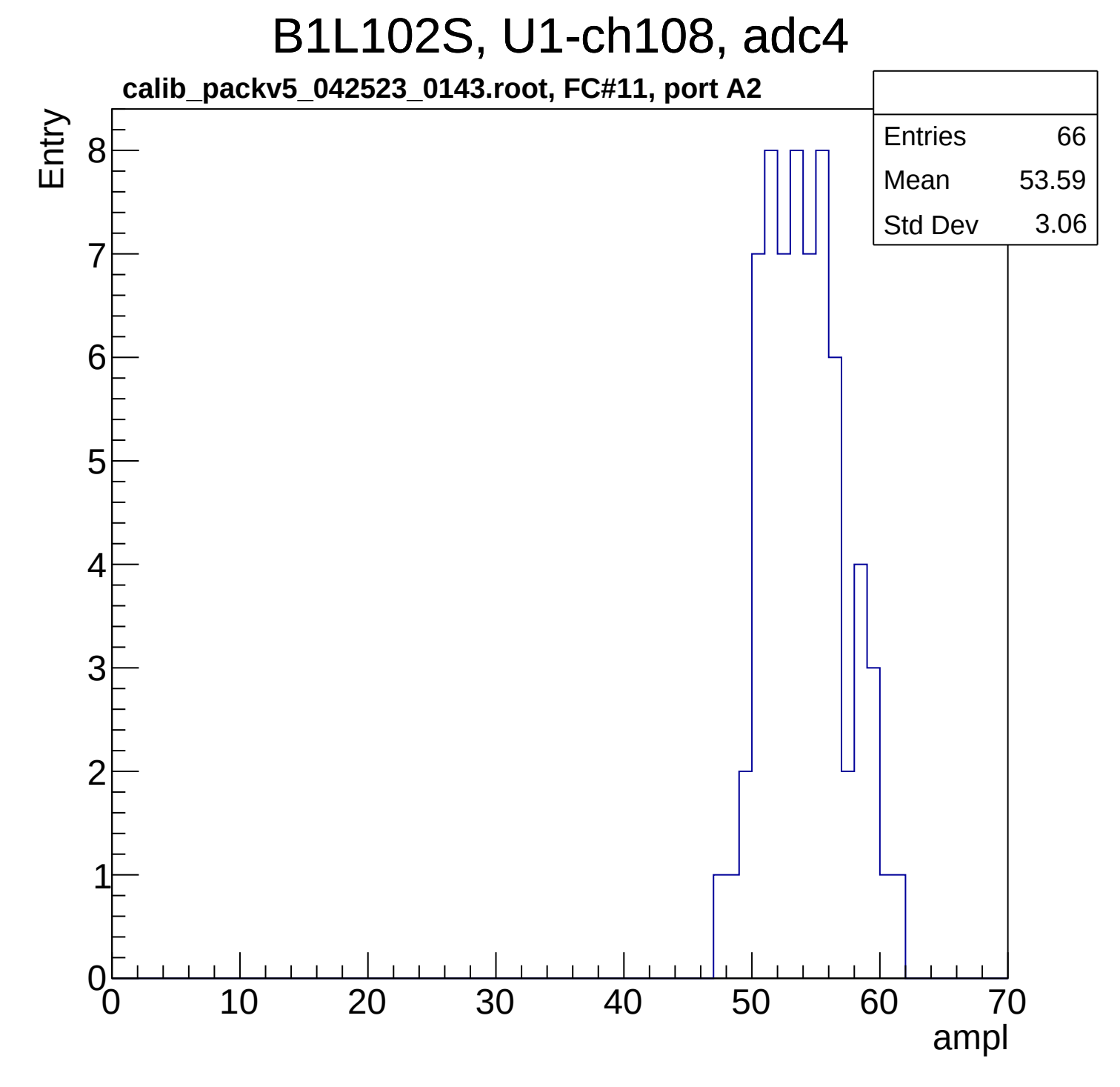
B1L102S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 53.59
Std Dev 3.06
Entry
ampl
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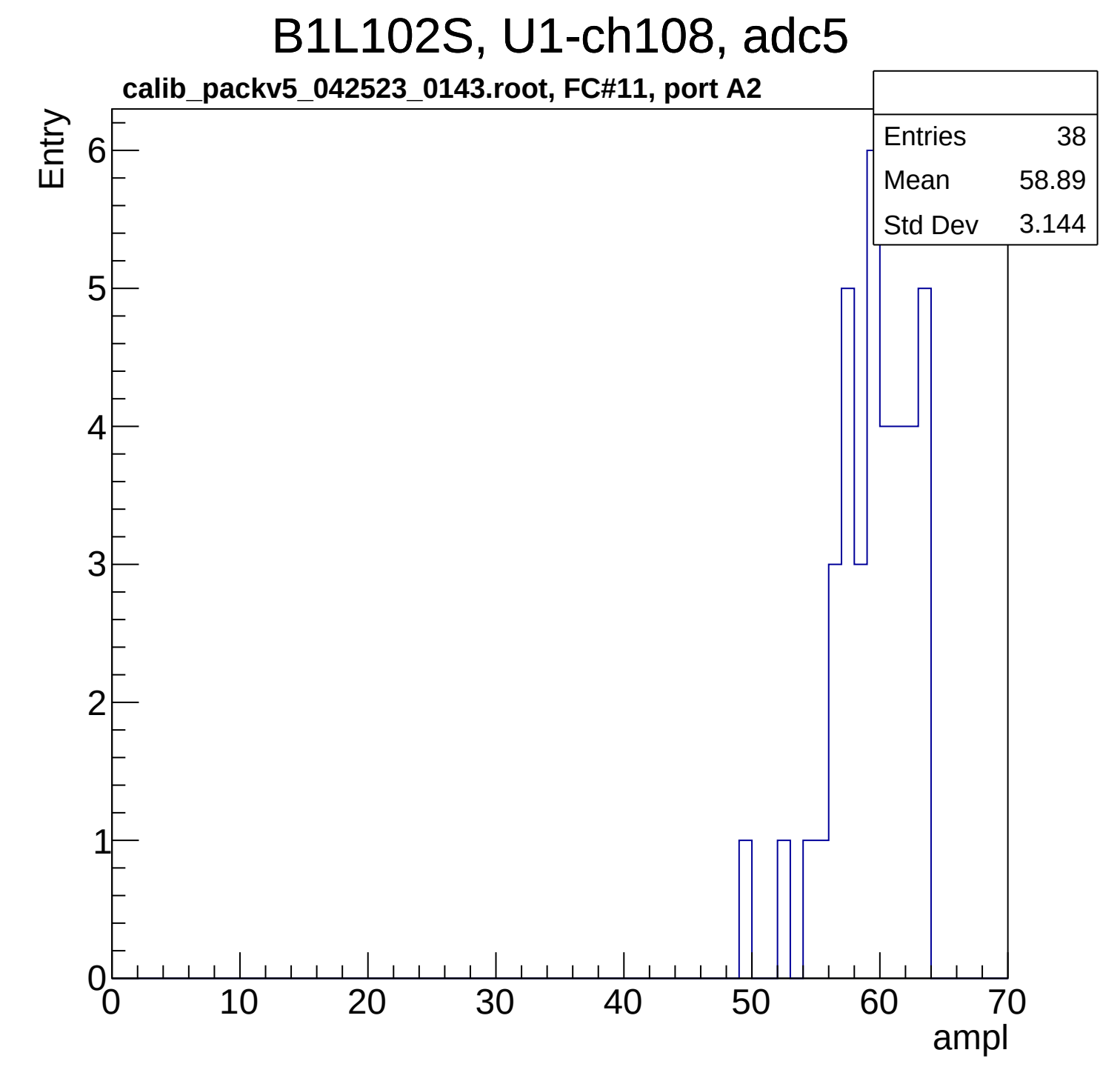
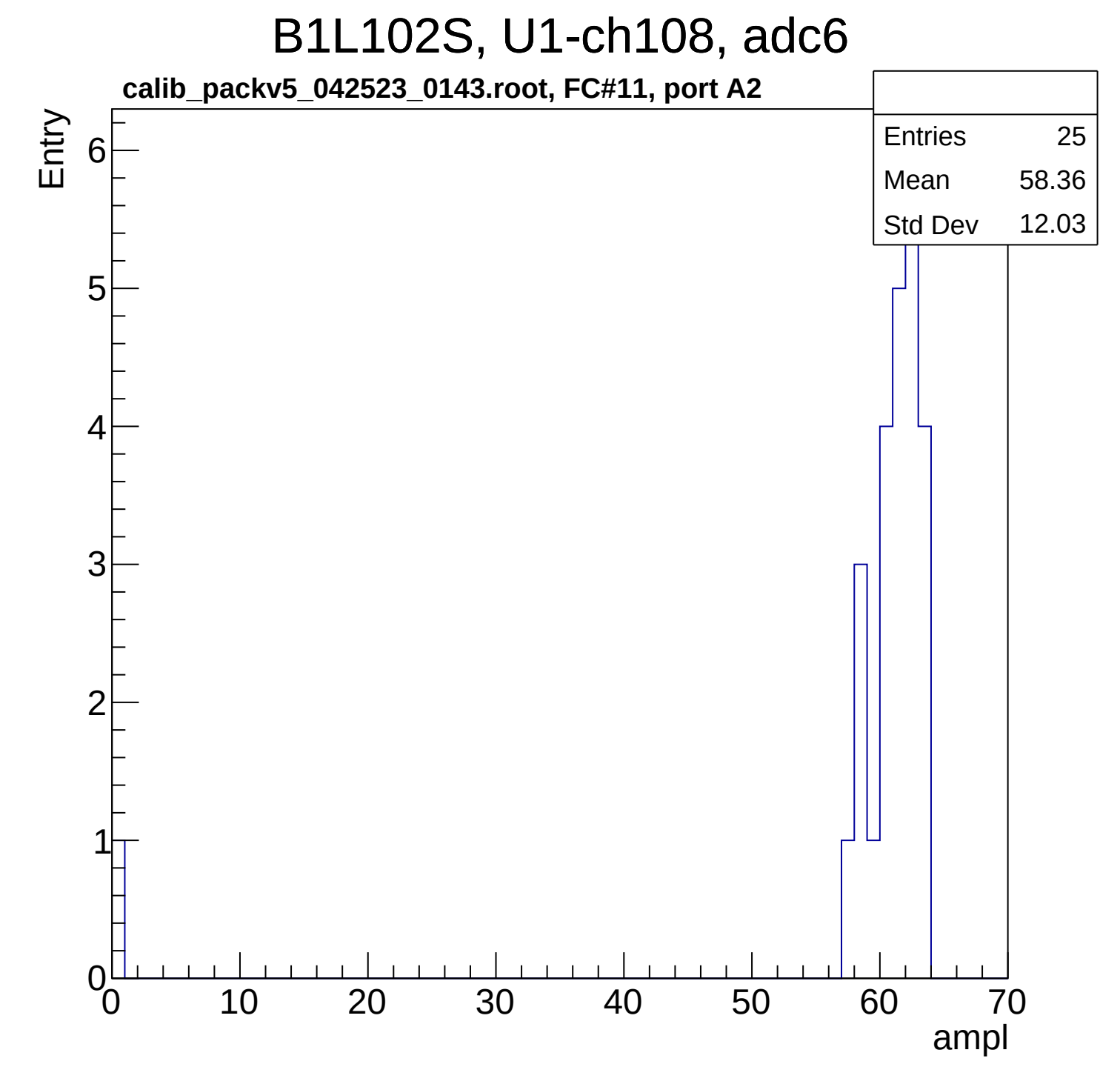
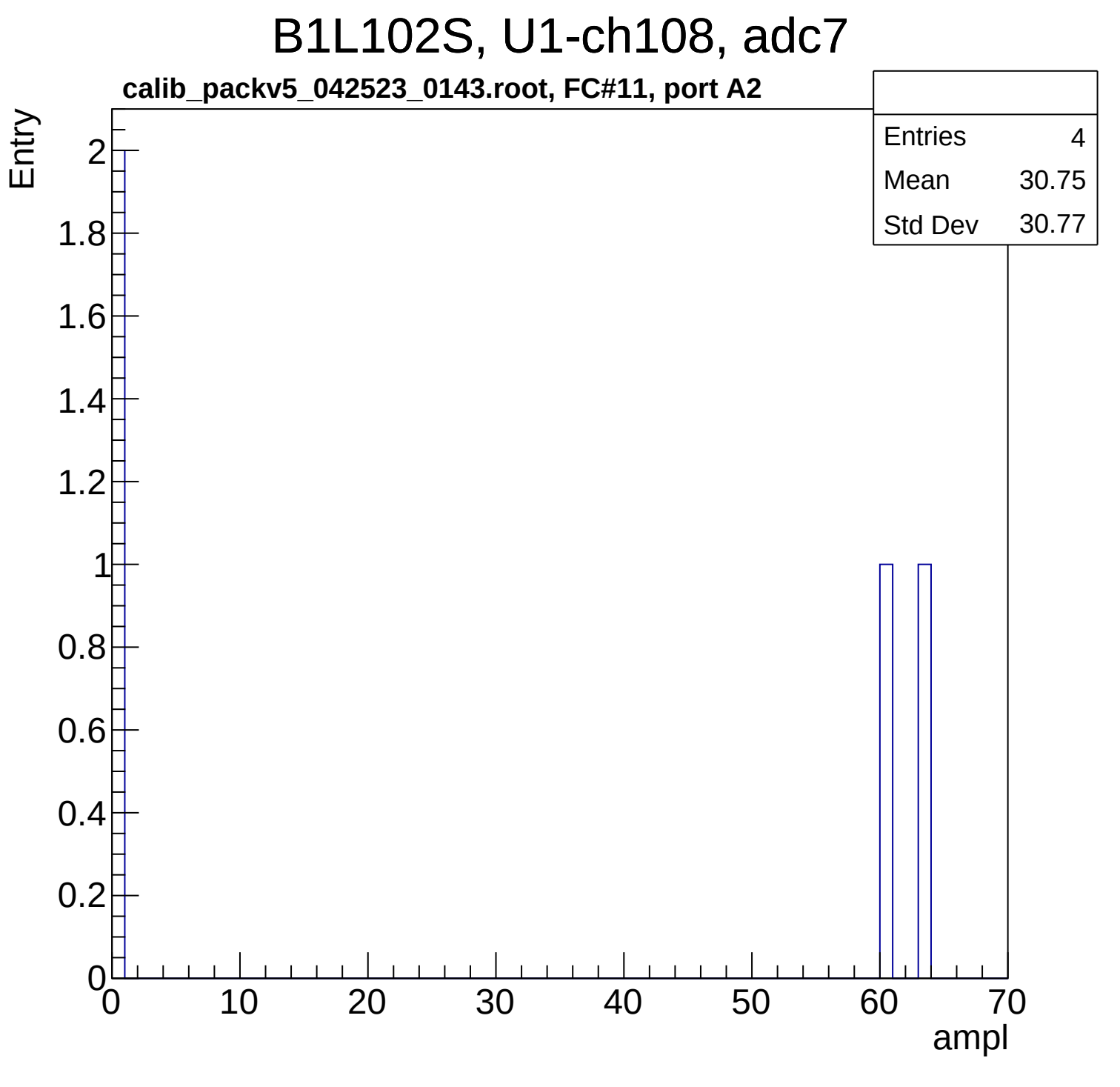
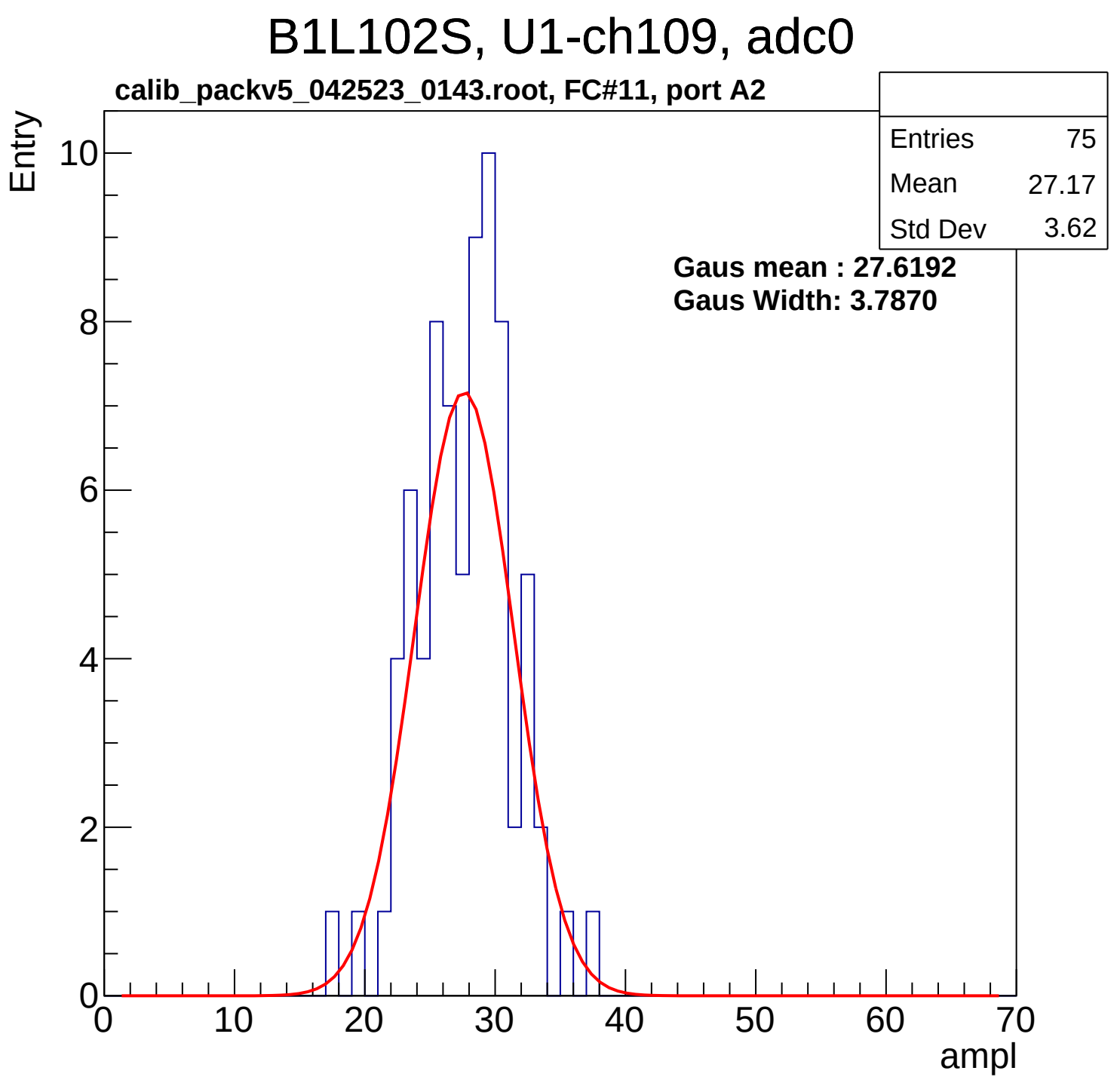
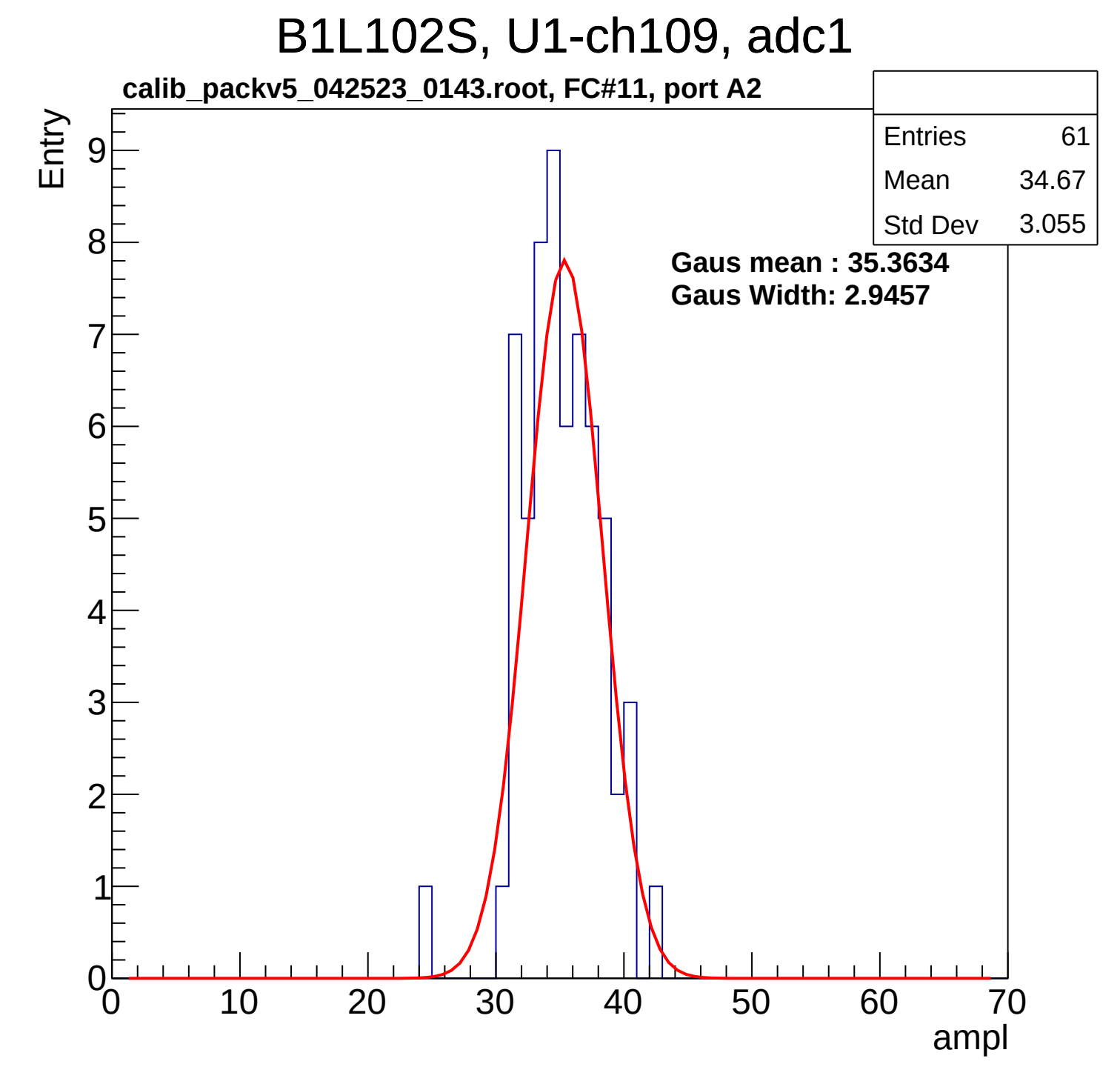
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

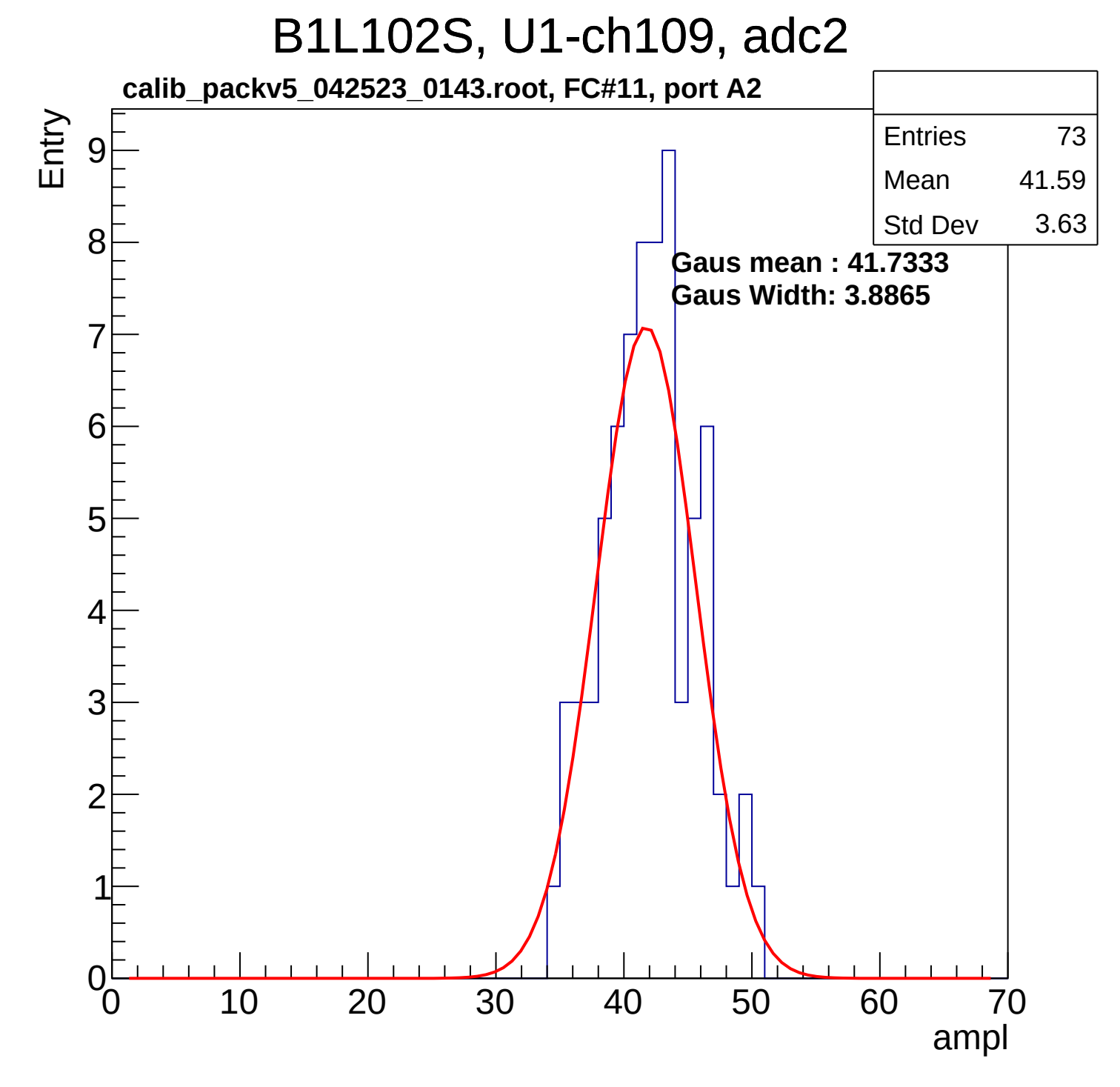

B1L102S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.89
Std Dev 3.144
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

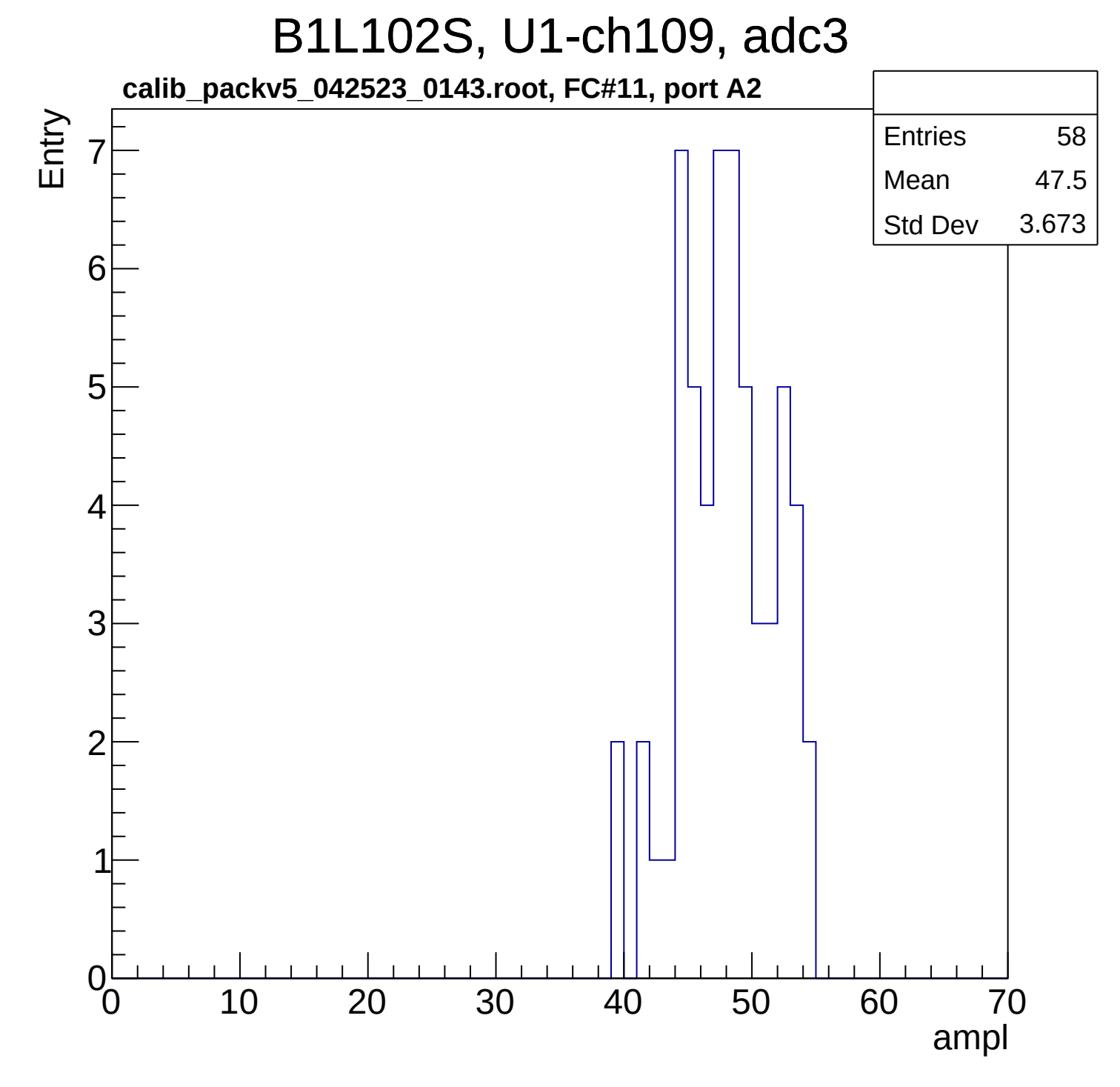

B1L102S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 25
Mean 58.36
Std Dev 12.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

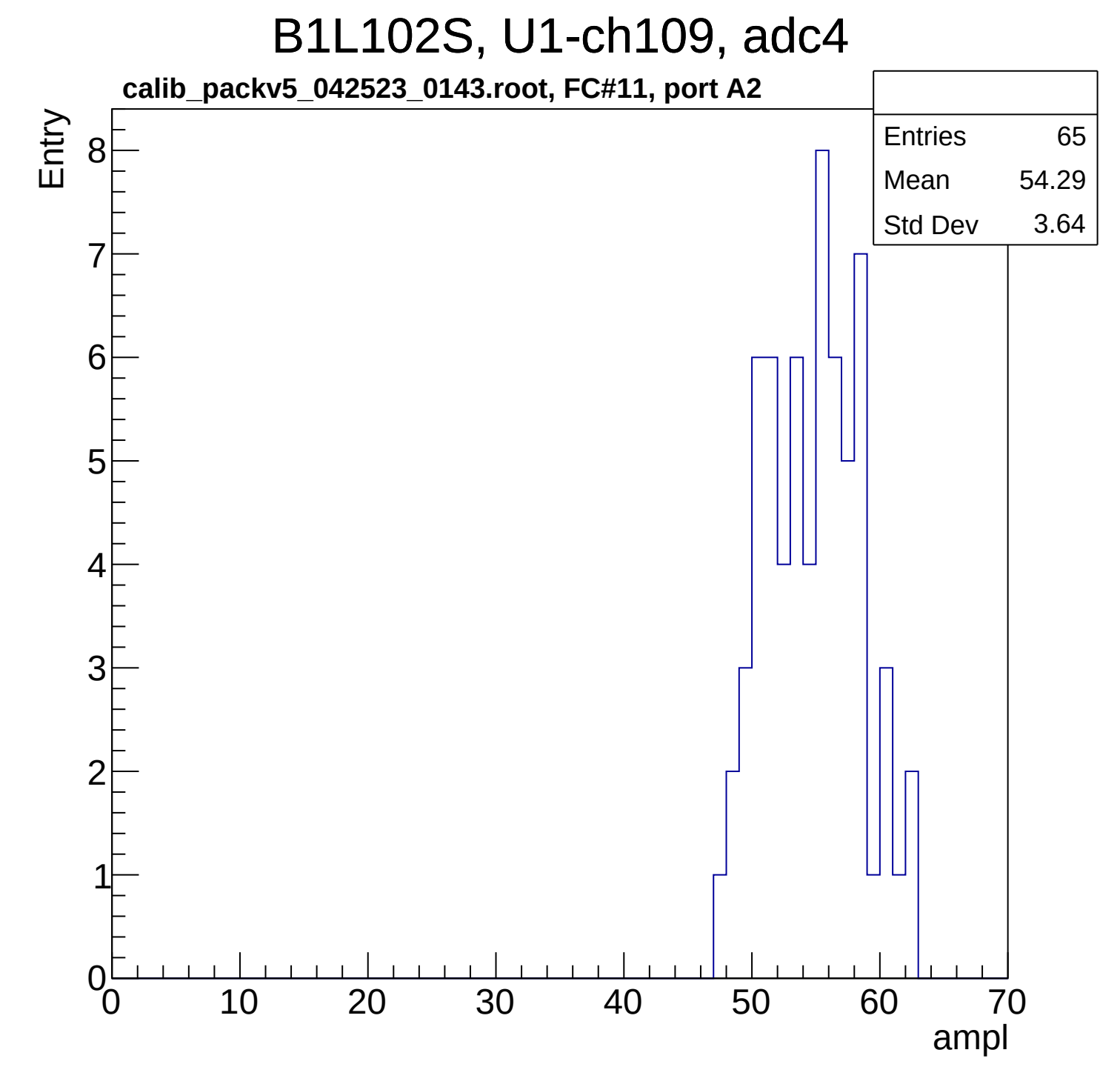

B1L102S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 30.75
Std Dev 30.77
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

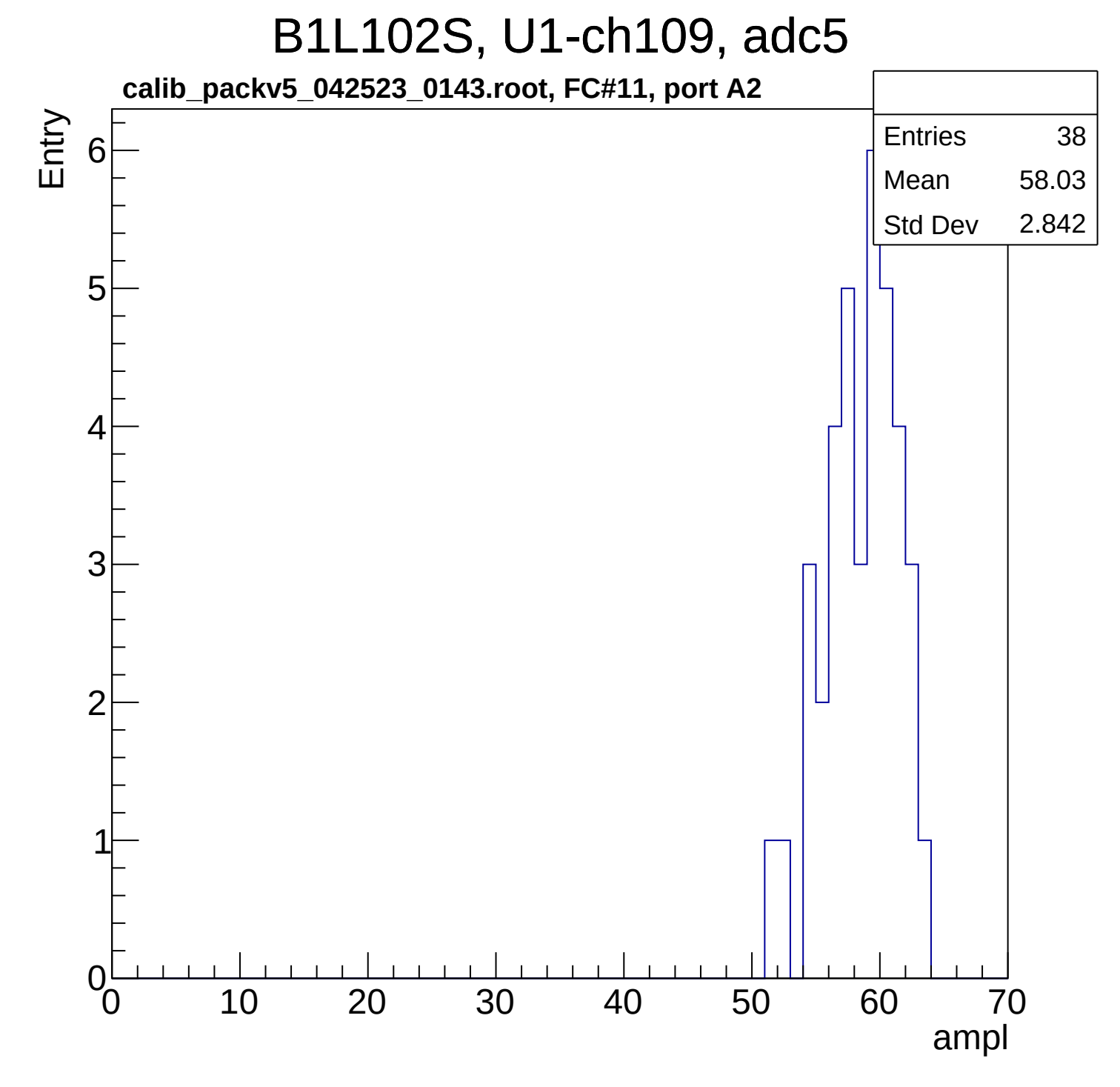

B1L102S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 27.17
Std Dev 3.62
Gaus mean : 27.6192
Gaus Width: 3.7870
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

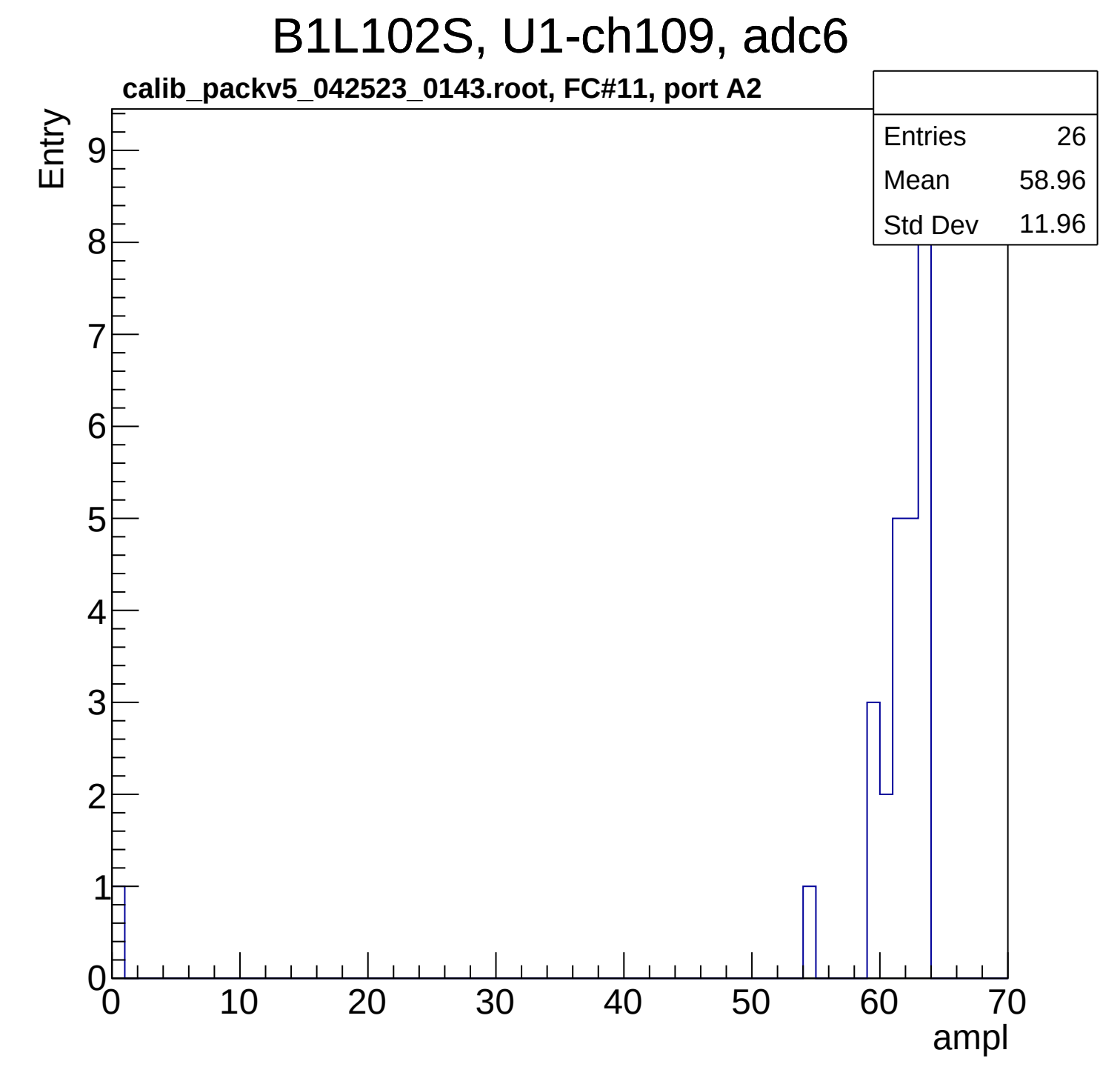

B1L102S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 34.67
Std Dev 3.055
Gaus mean : 35.3634
Gaus Width: 2.9457
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

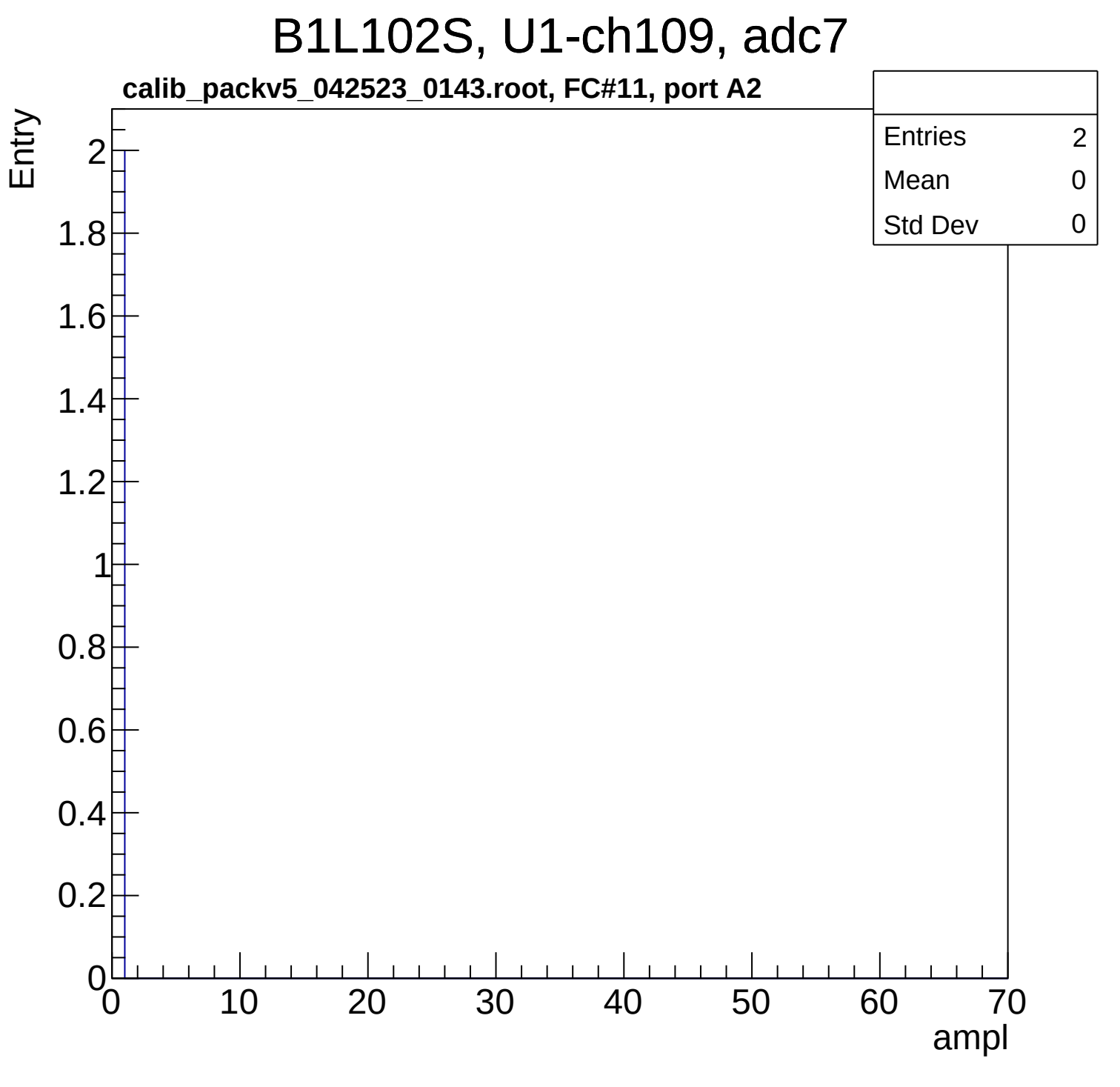

B1L102S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 41.59
Std Dev 3.63
Gaus mean : 41.7333
Gaus Width: 3.8865
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

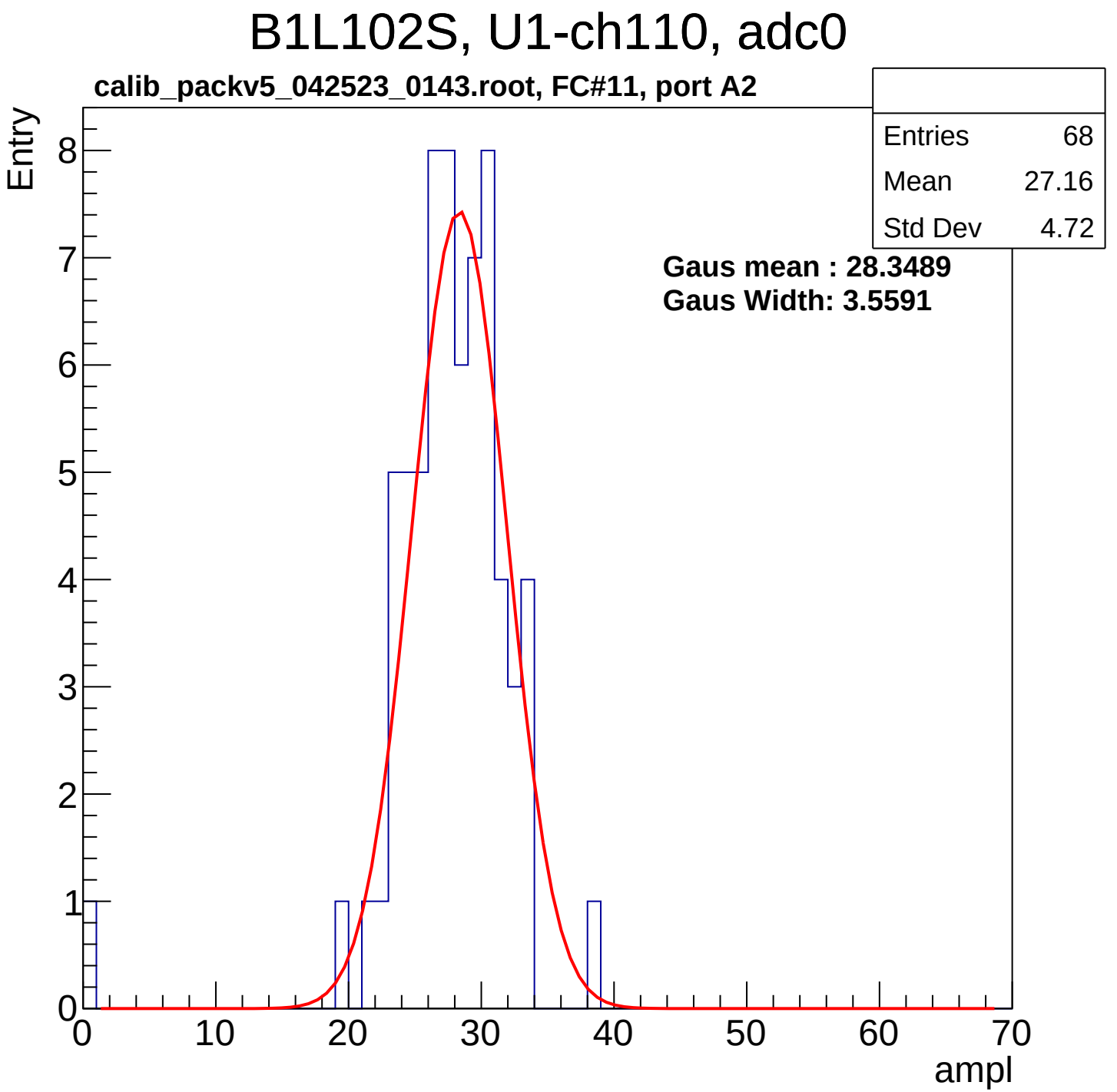

B1L102S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 47.5
Std Dev 3.673
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

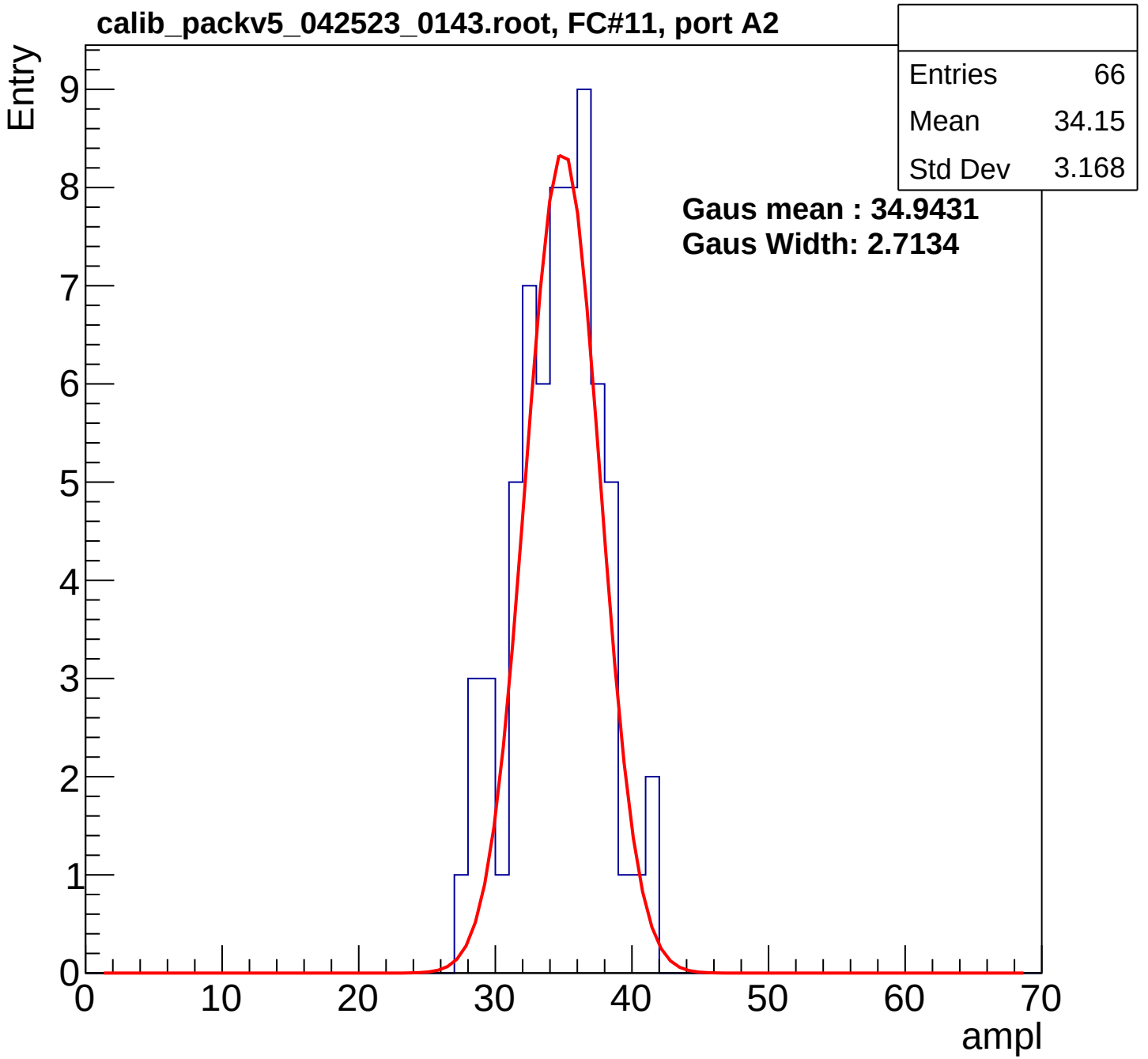

B1L102S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 54.29
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

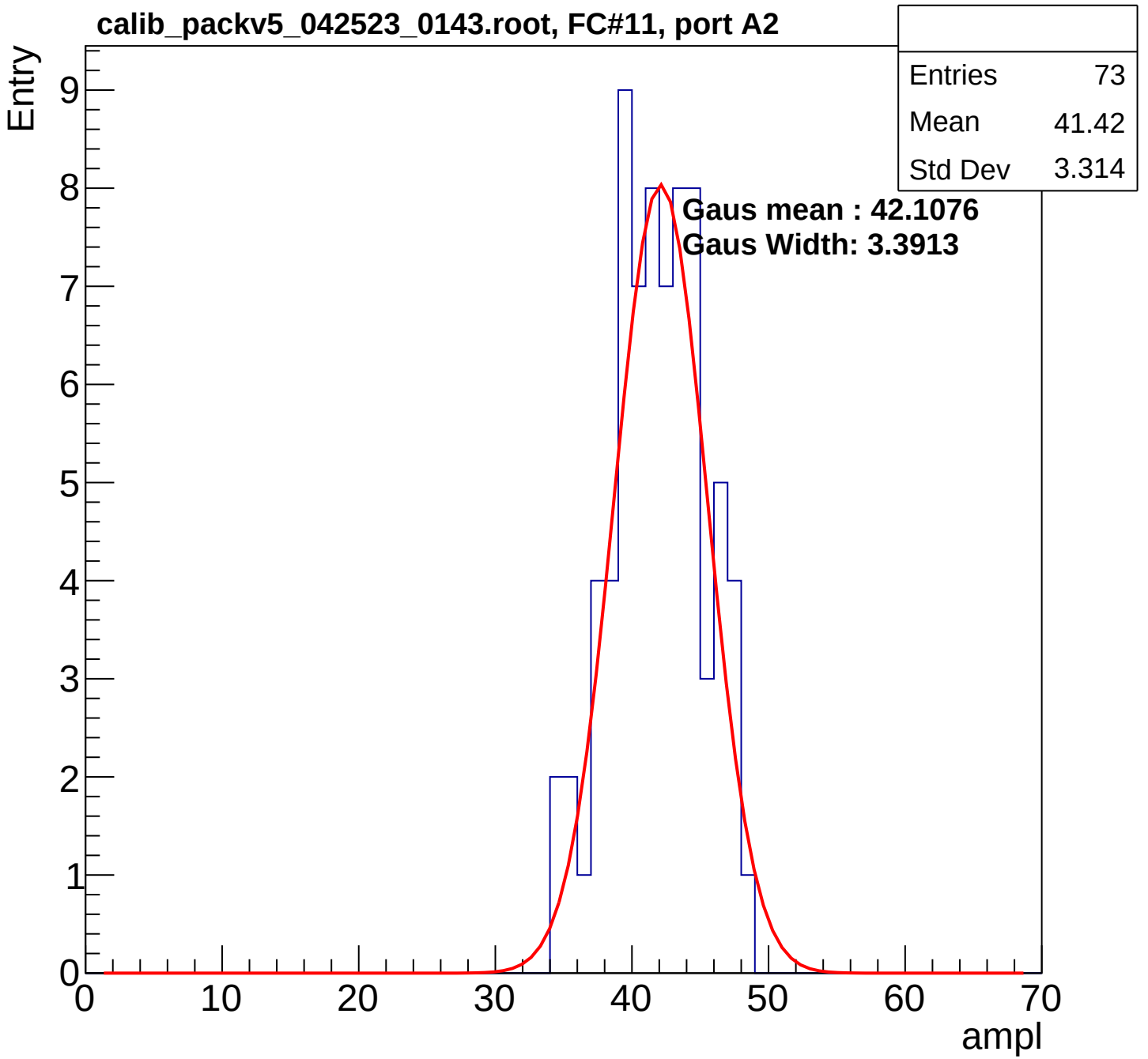

B1L102S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 38
Mean 58.03
Std Dev 2.842
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

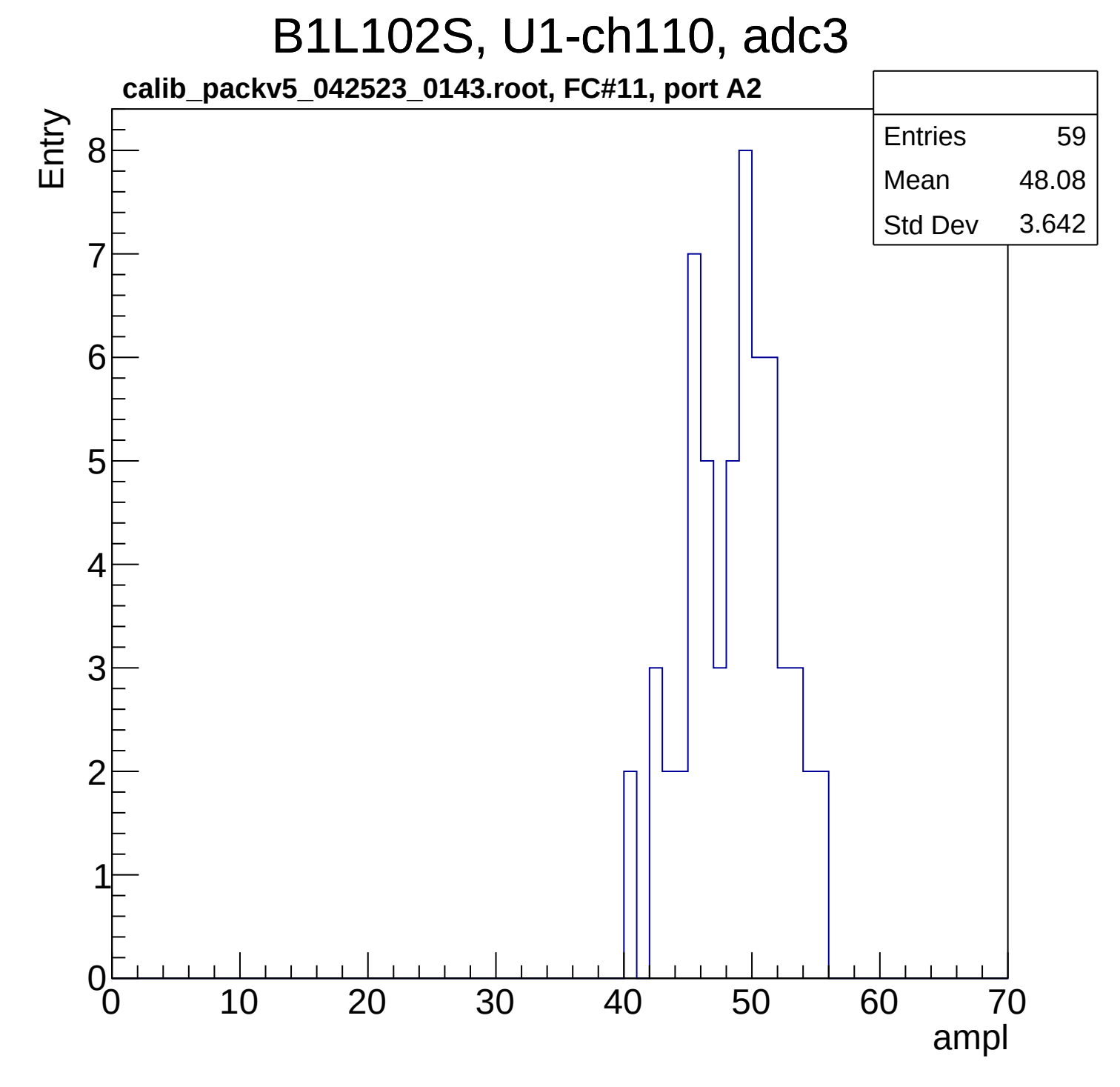

B1L102S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 26
Mean 58.96
Std Dev 11.96
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

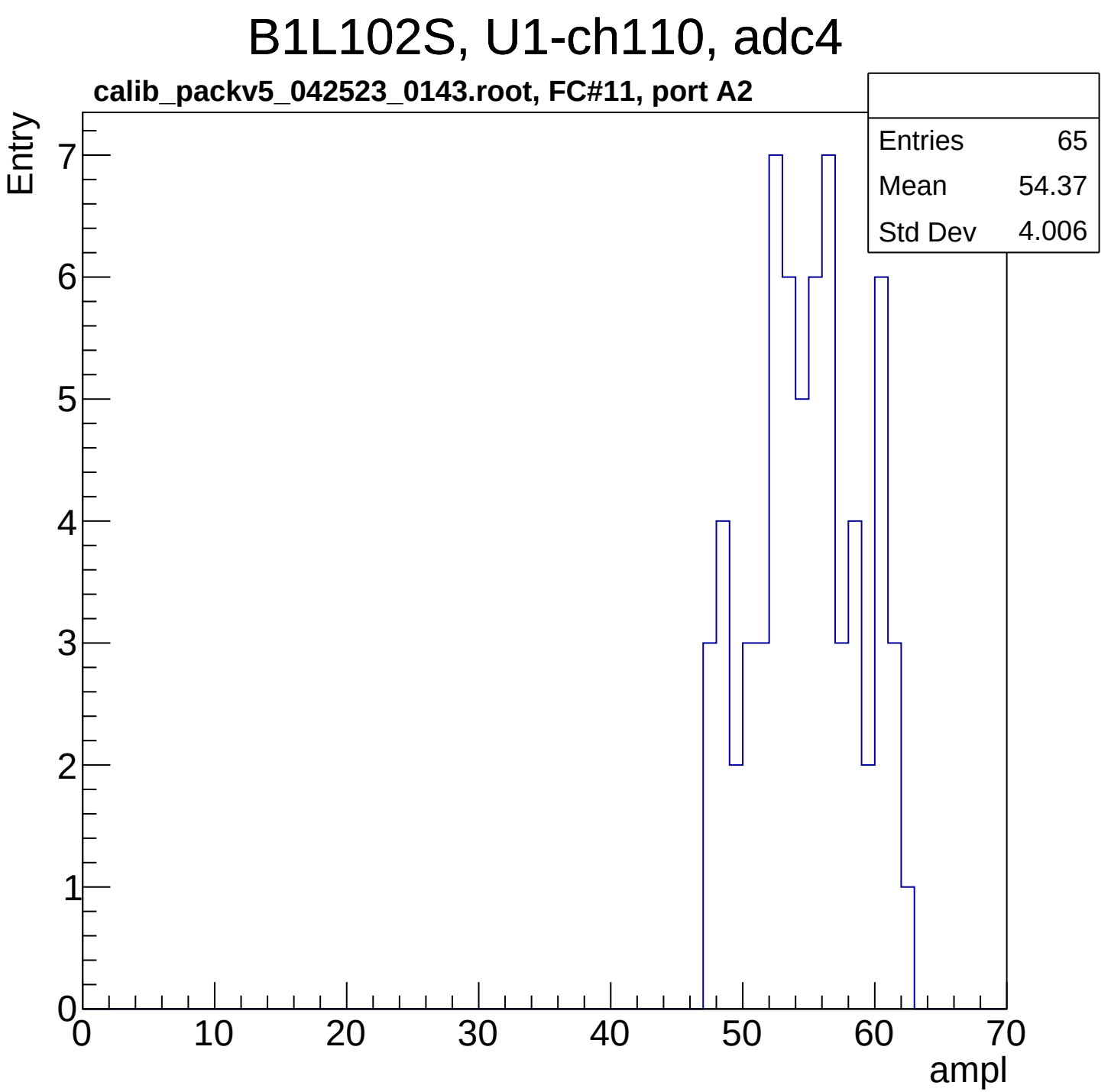

B1L102S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

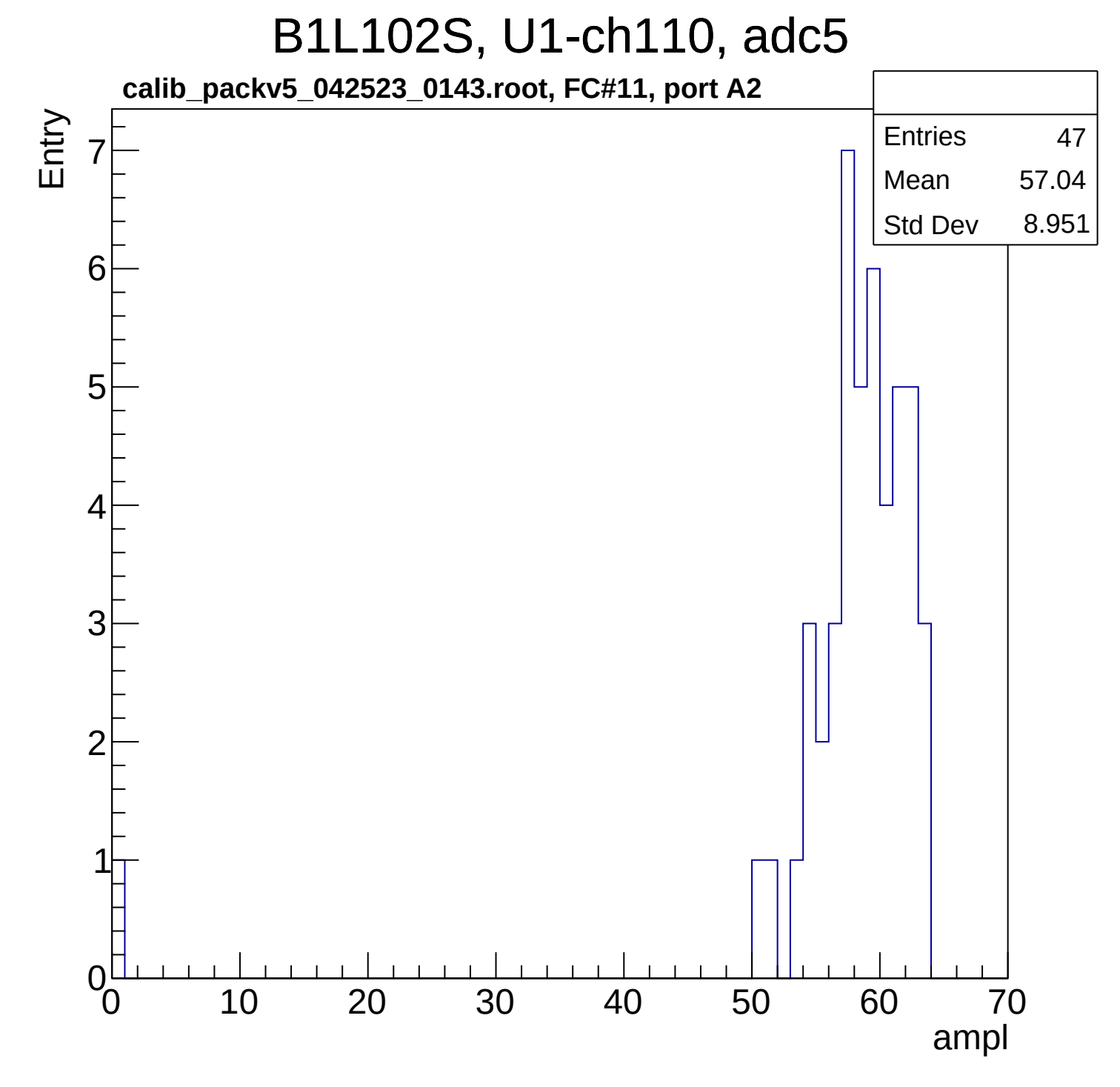

B1L102S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 27.16
Std Dev 4.72
Gaus mean : 28.3489
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

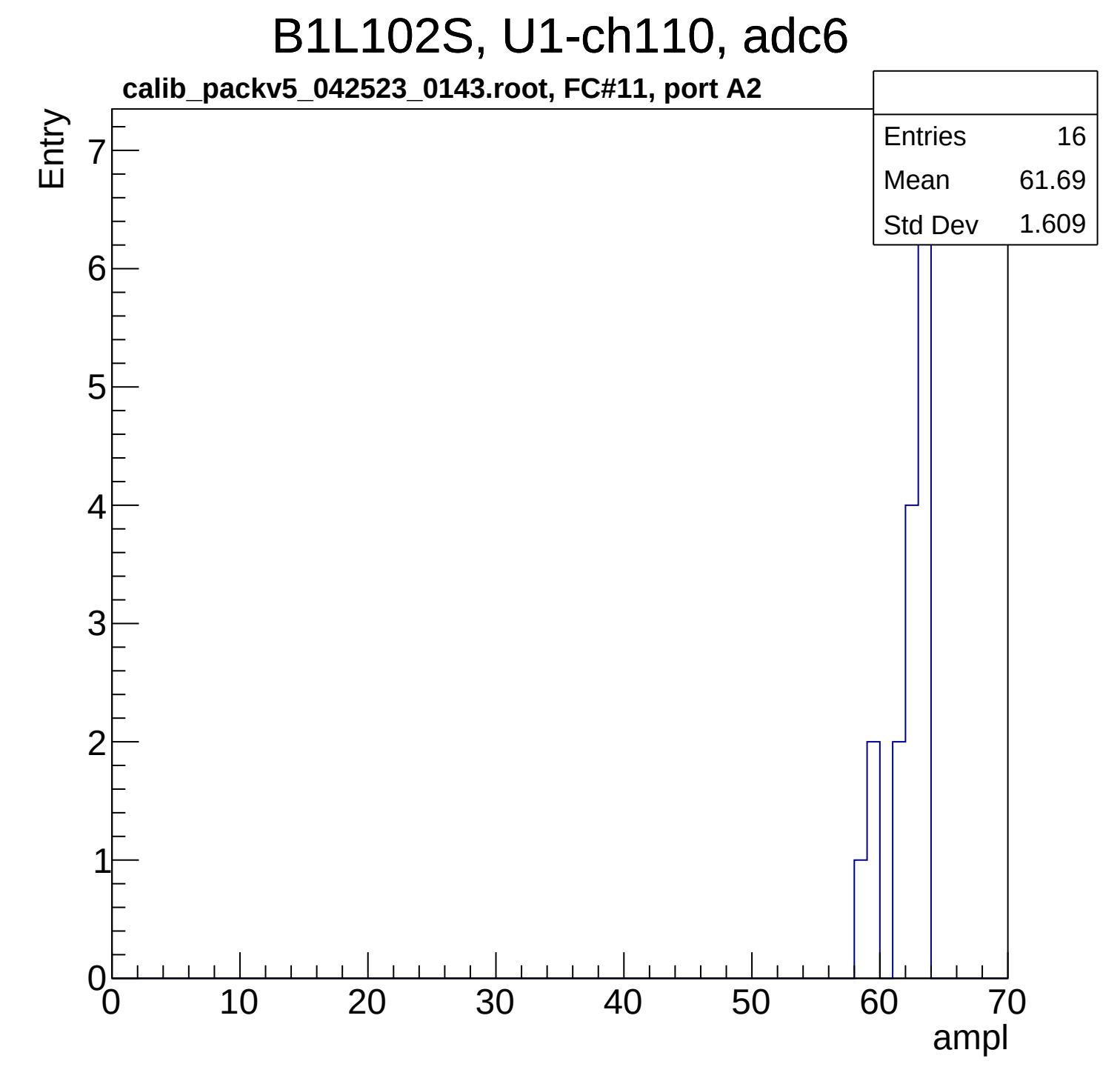

B1L102S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 34.15
Std Dev 3.168
Gaus mean : 34.9431
Gaus Width: 2.7134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

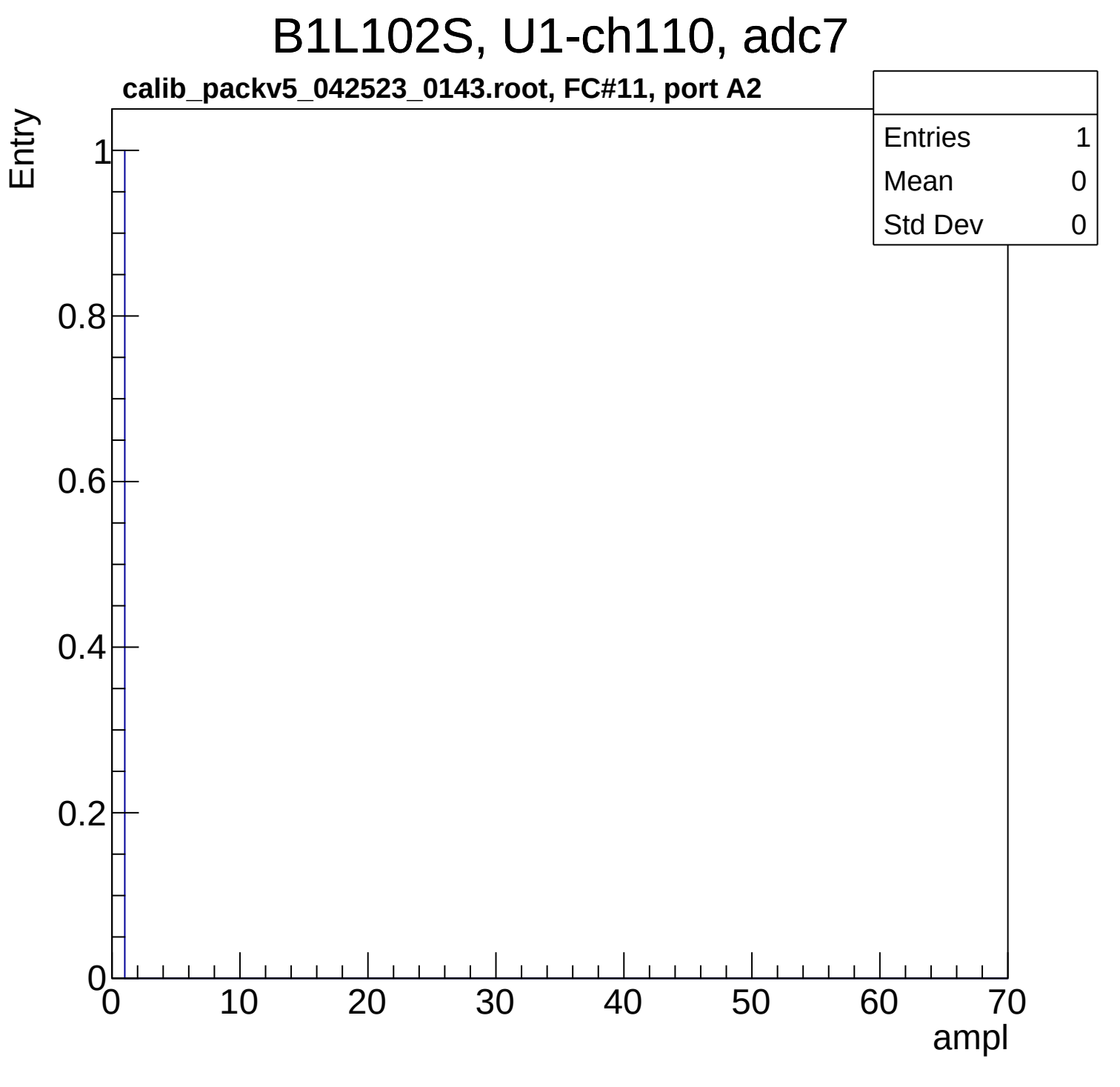

B1L102S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 41.42
Std Dev 3.314
Gaus mean : 42.1076
Gaus Width: 3.3913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

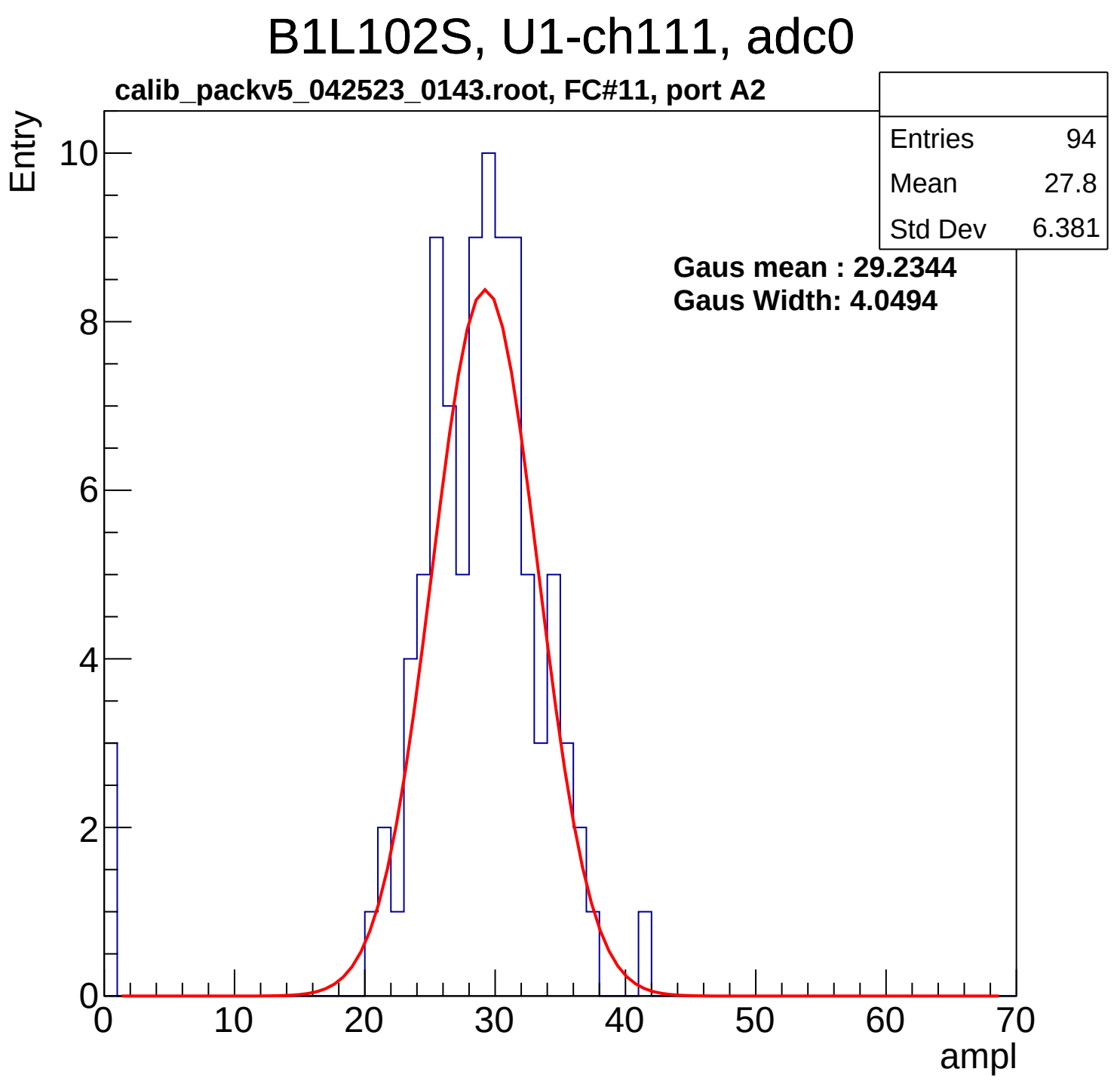

B1L102S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 48.08
Std Dev 3.642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

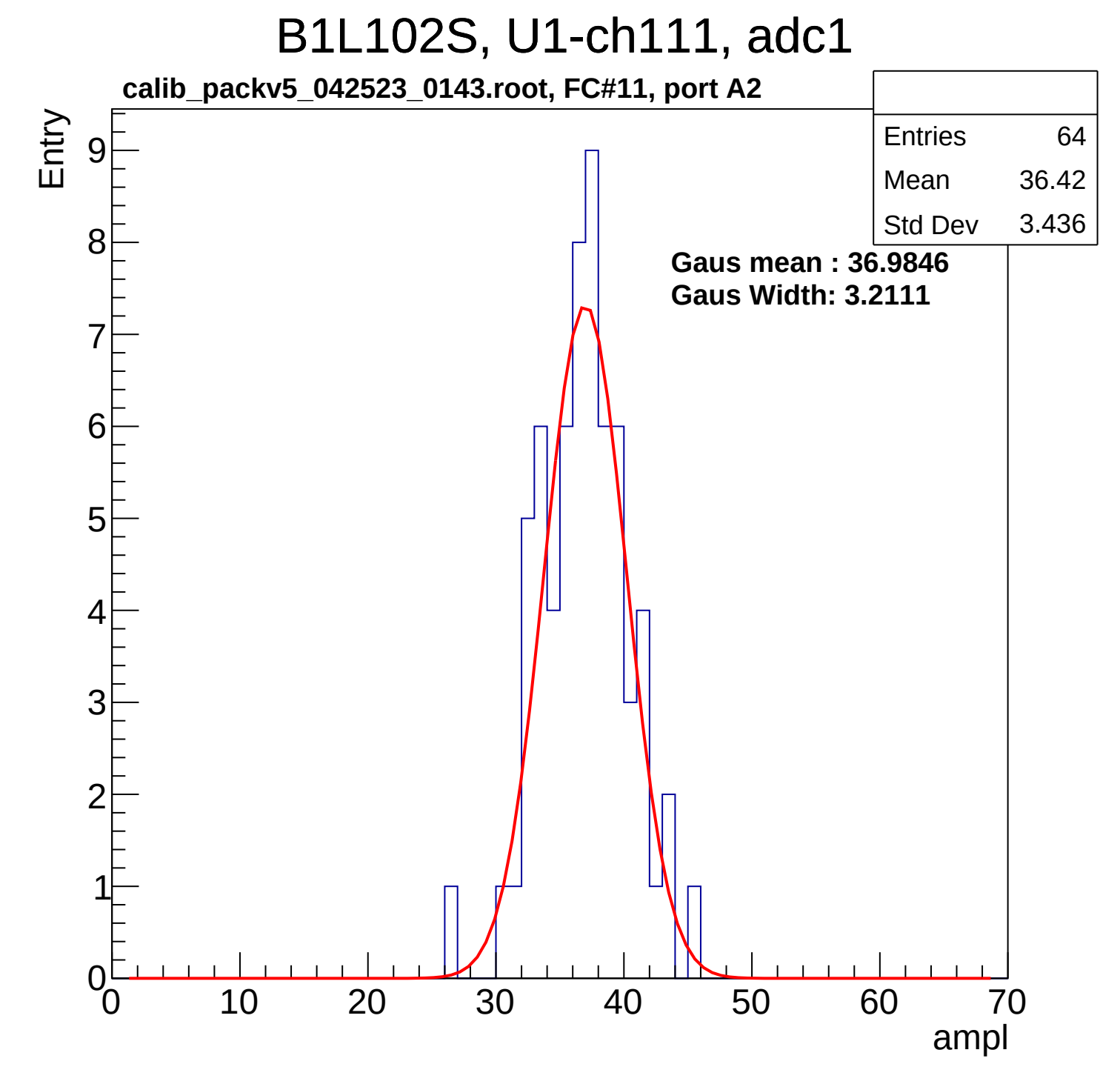

B1L102S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 54.37
Std Dev 4.006
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

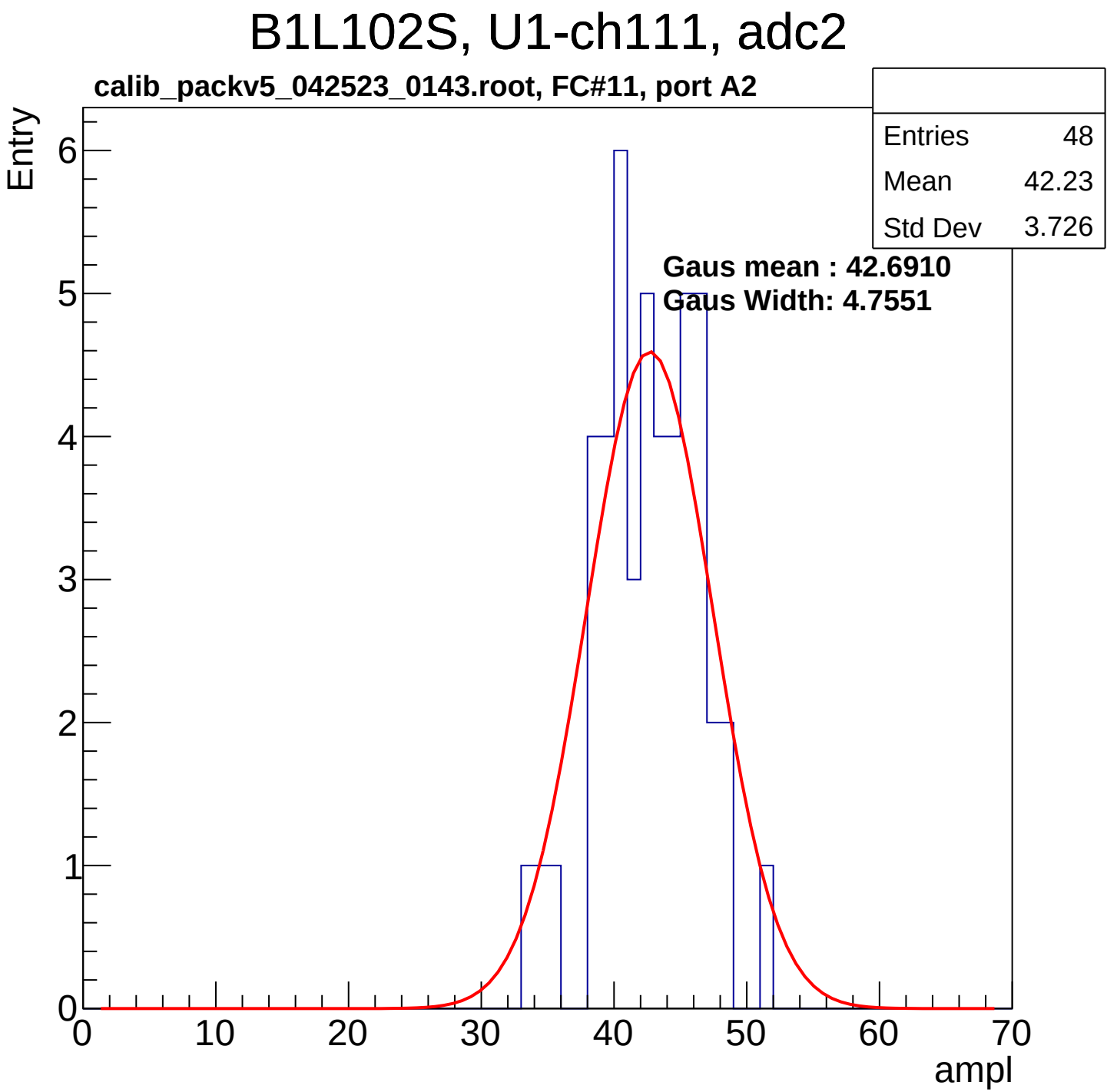

B1L102S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 57.04
Std Dev 8.951
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

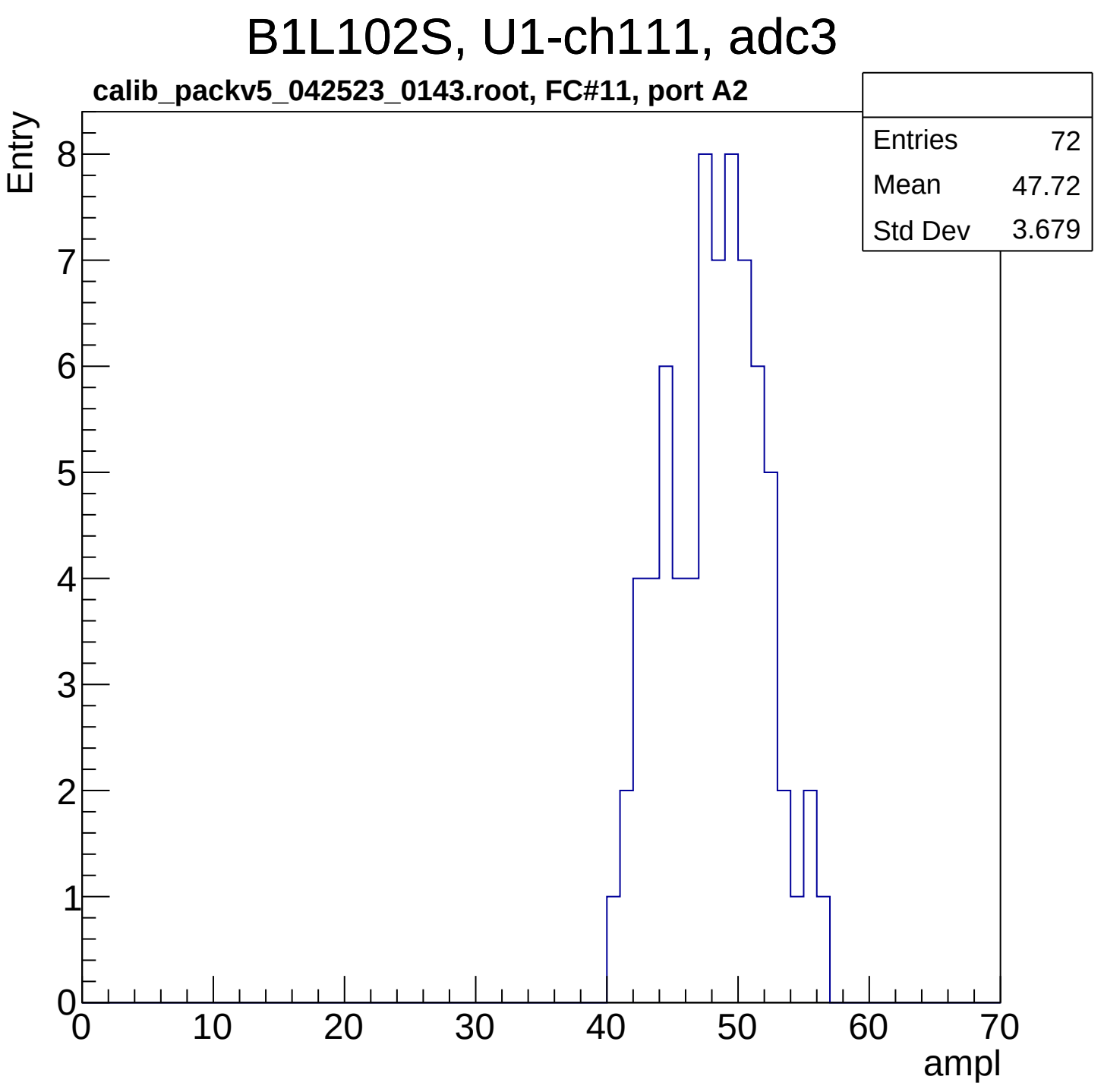

B1L102S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 16
Mean 61.69
Std Dev 1.609
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

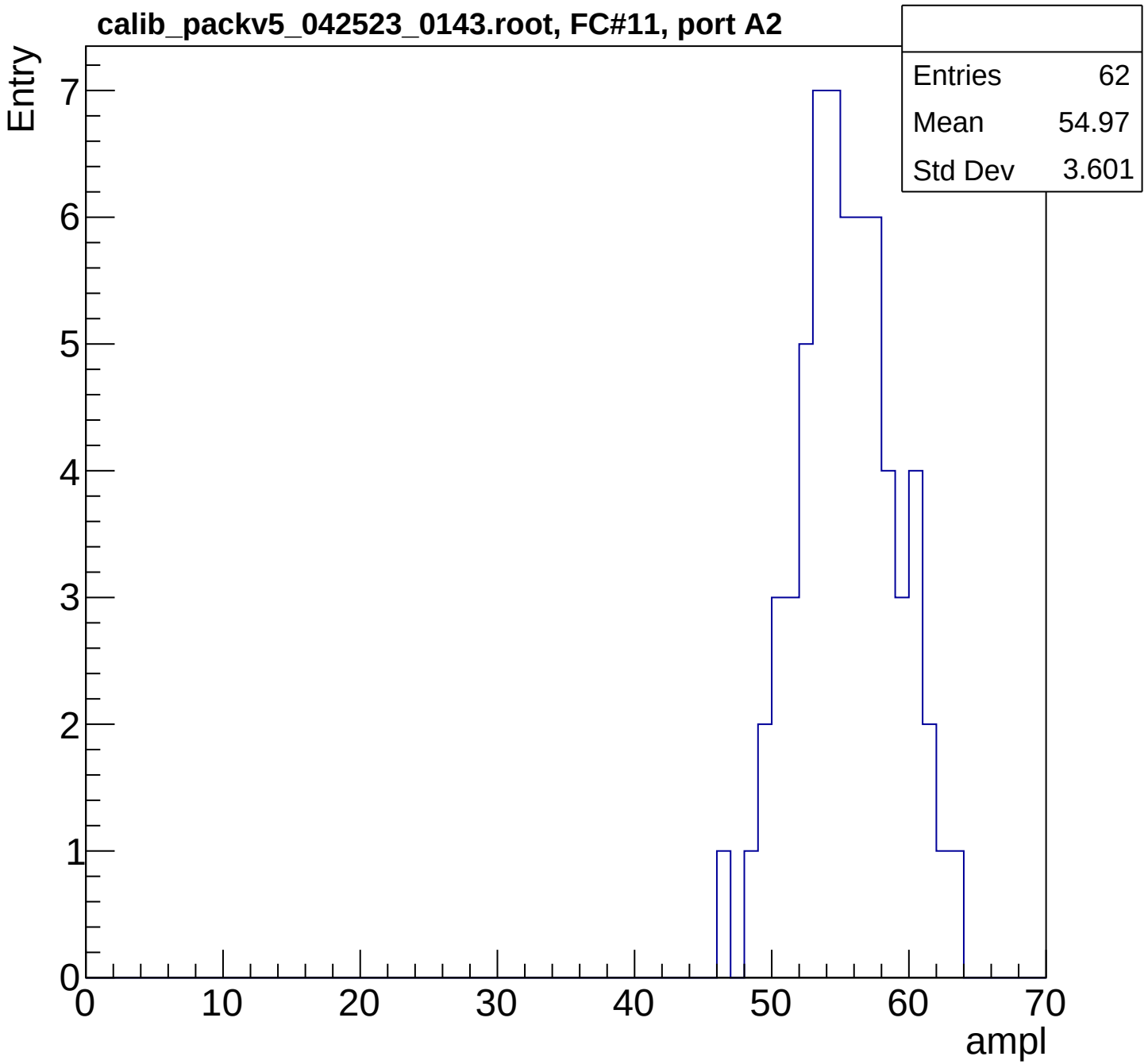

B1L102S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

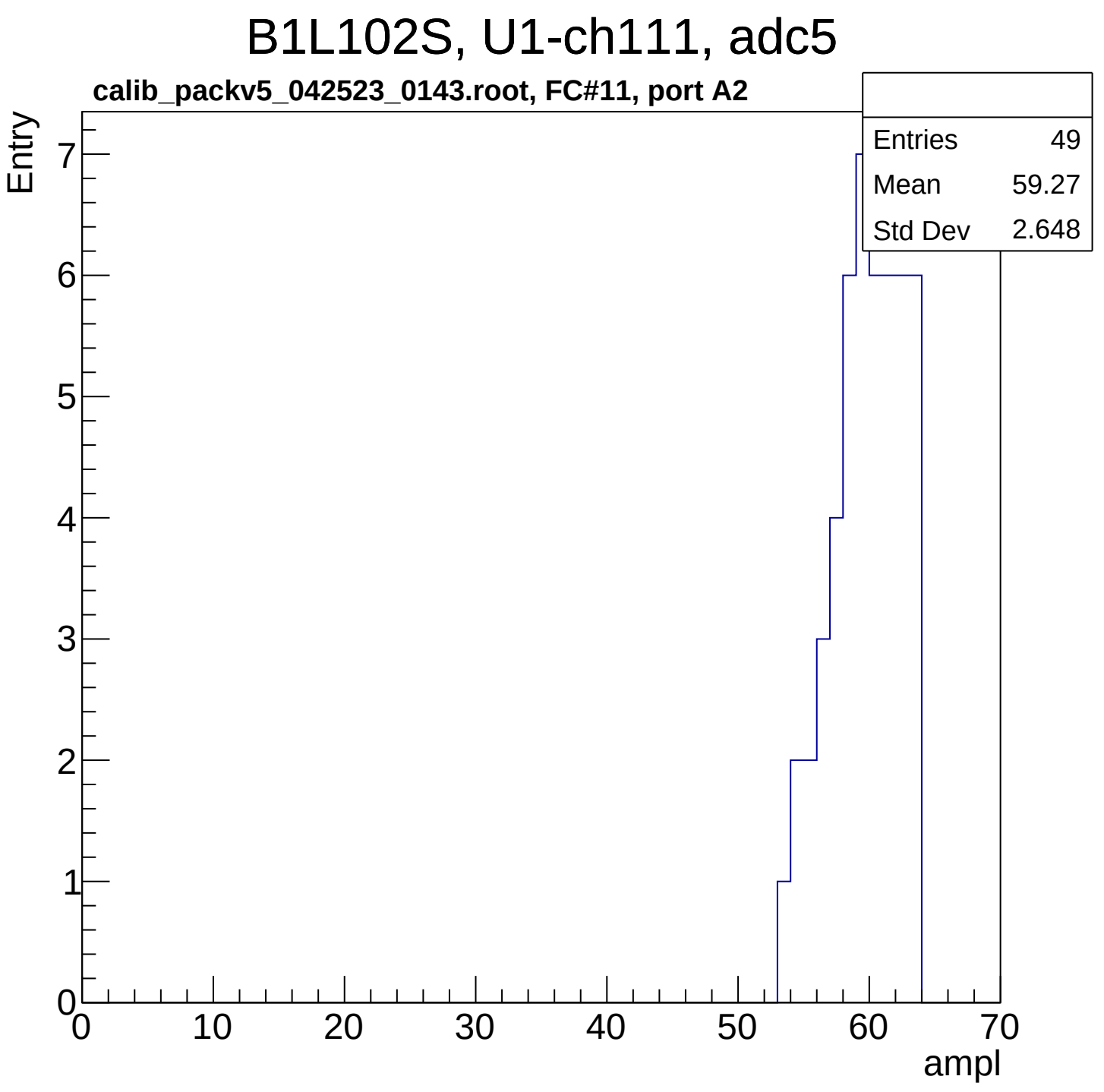

B1L102S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 94
Mean 27.8
Std Dev 6.381
Gaus mean : 29.2344
Gaus Width: 4.0494
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

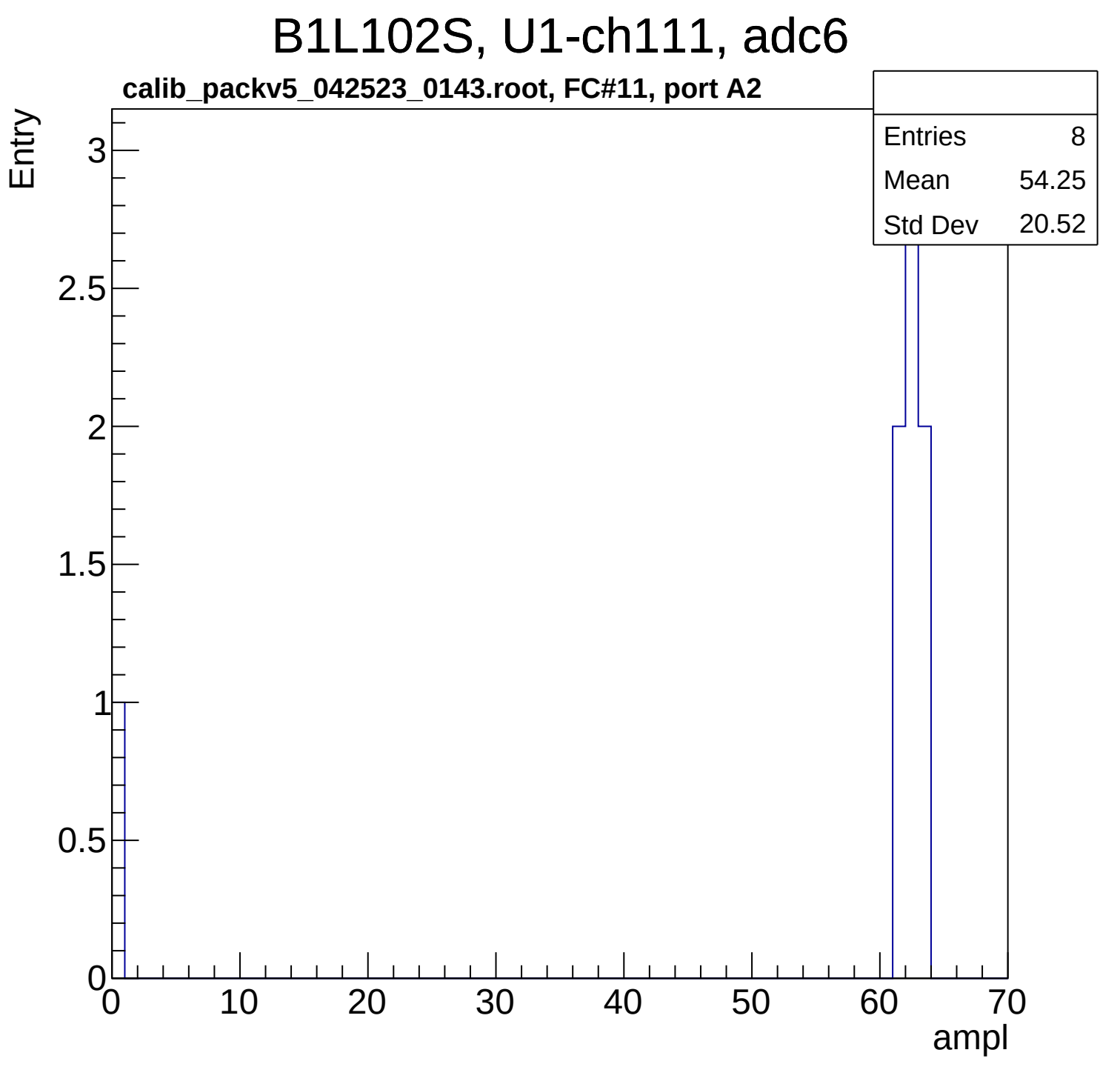

B1L102S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 36.42
Std Dev 3.436
Gaus mean : 36.9846
Gaus Width: 3.2111
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 48
Mean 42.23
Std Dev 3.726
Gaus mean : 42.6910
Gaus Width: 4.7551
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 47.72
Std Dev 3.679
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 54.97
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 49
Mean 59.27
Std Dev 2.648
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 54.25
Std Dev 20.52
Entry
ampl
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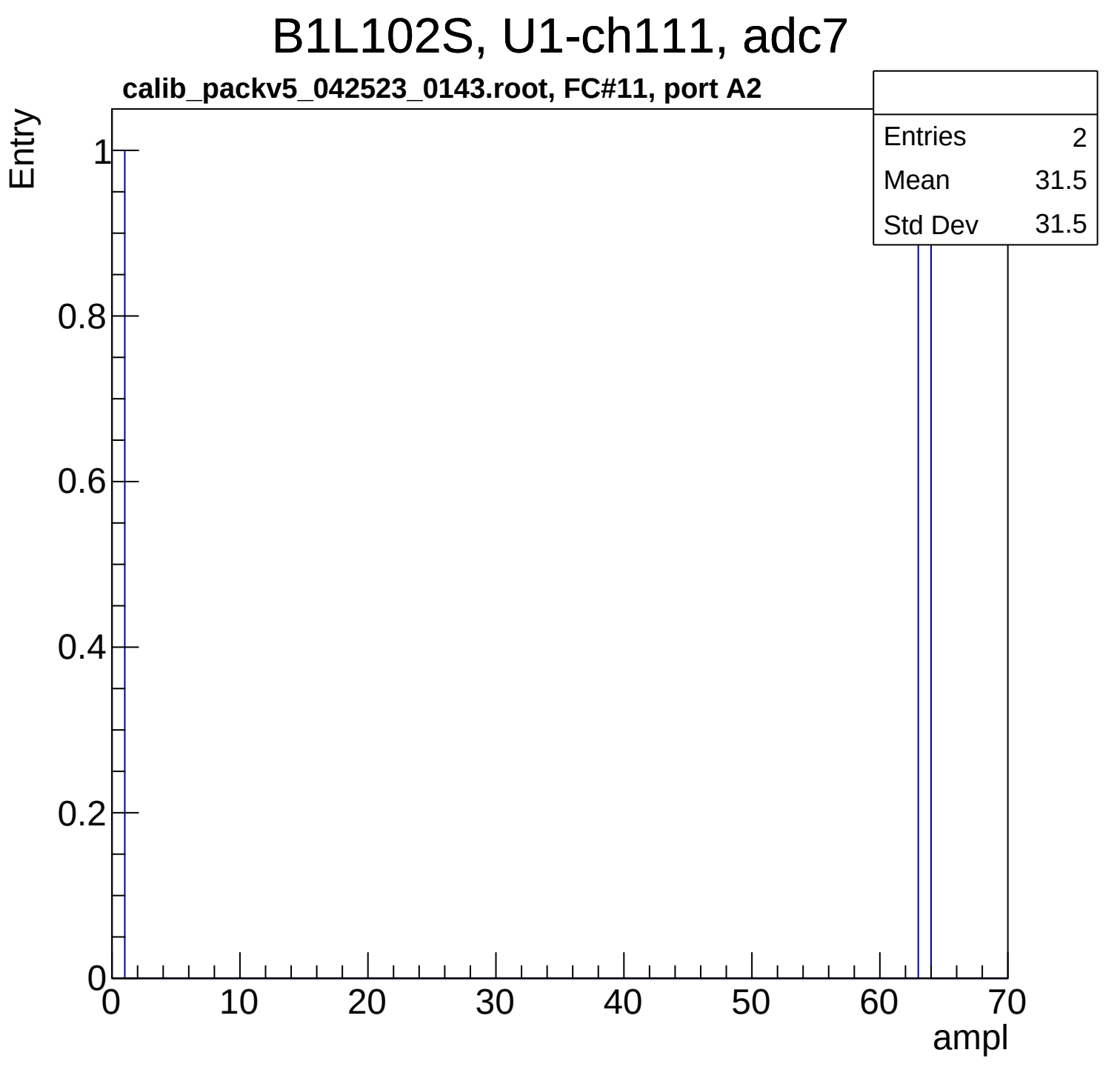
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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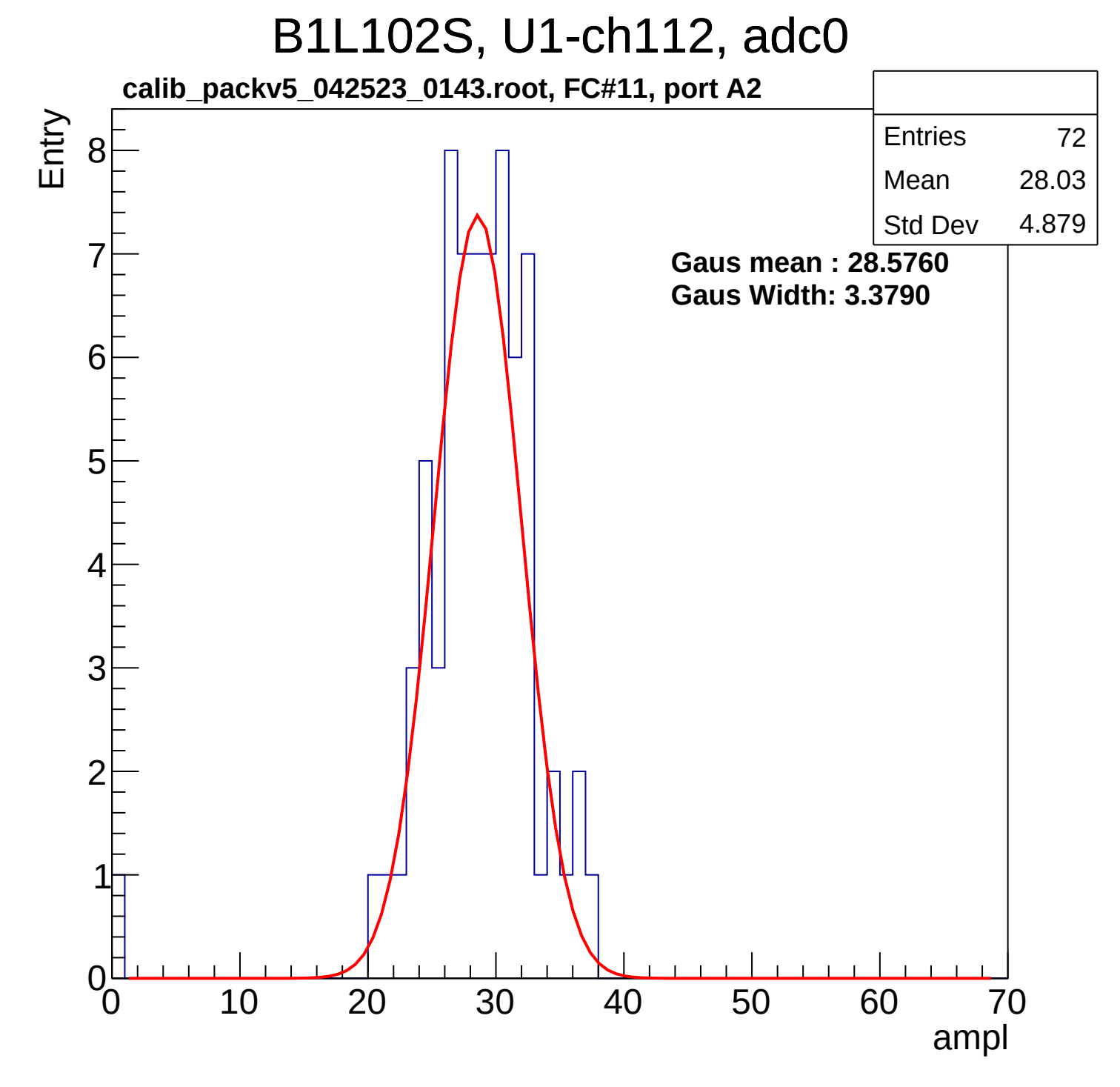
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 72
Mean 28.03
Std Dev 4.879
Gaus mean : 28.5760
Gaus Width: 3.3790
Entry
ampl
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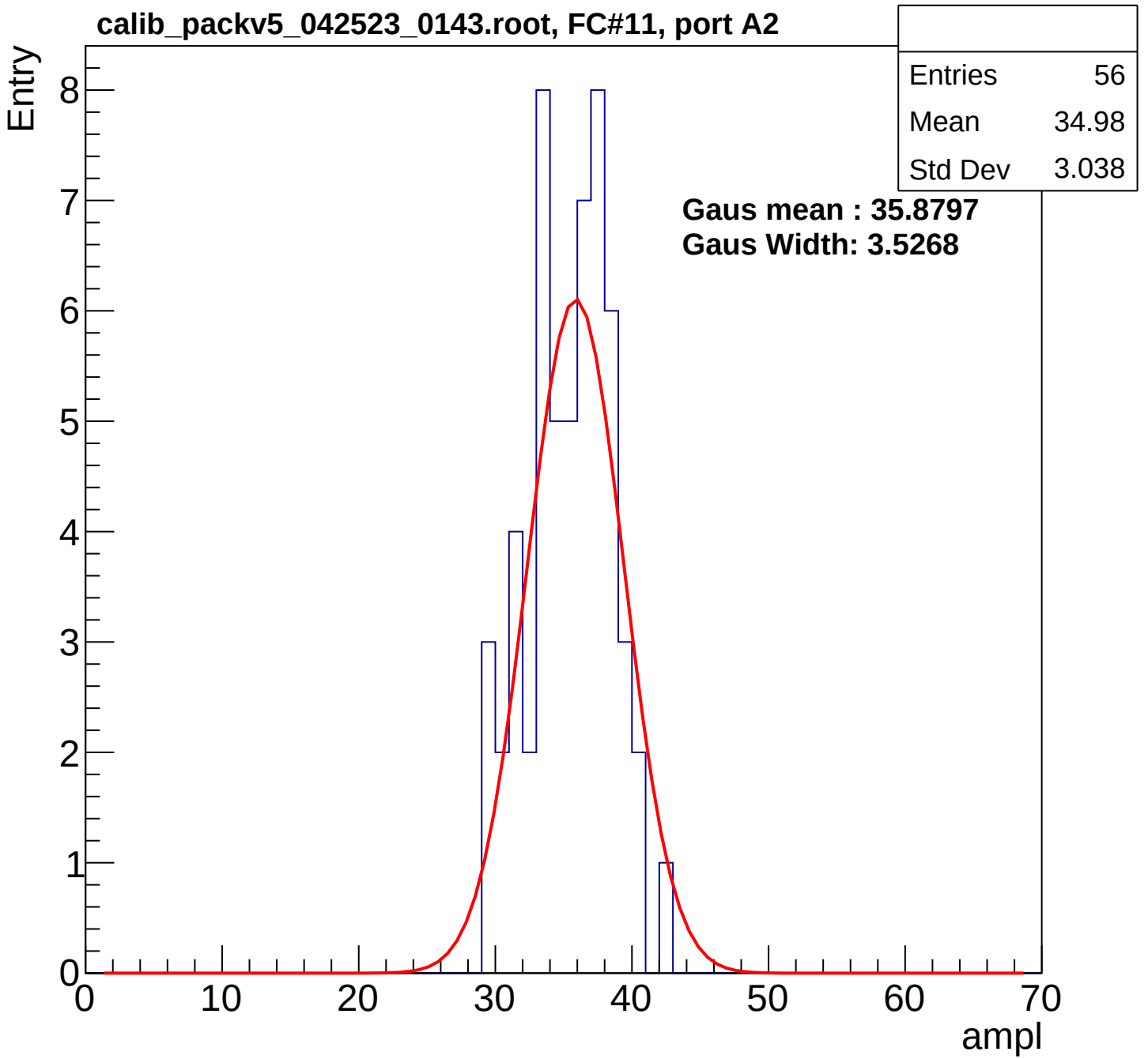
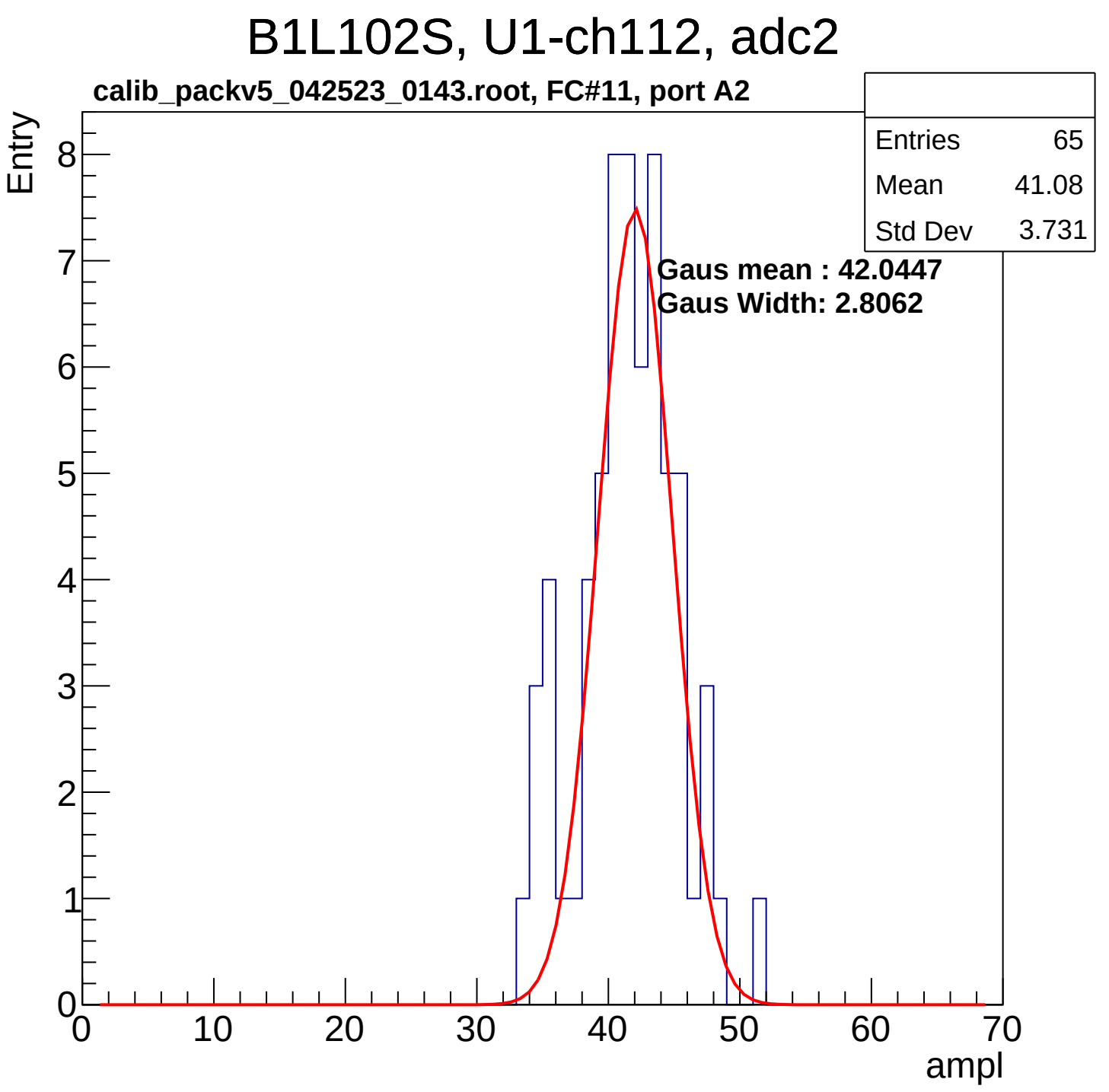
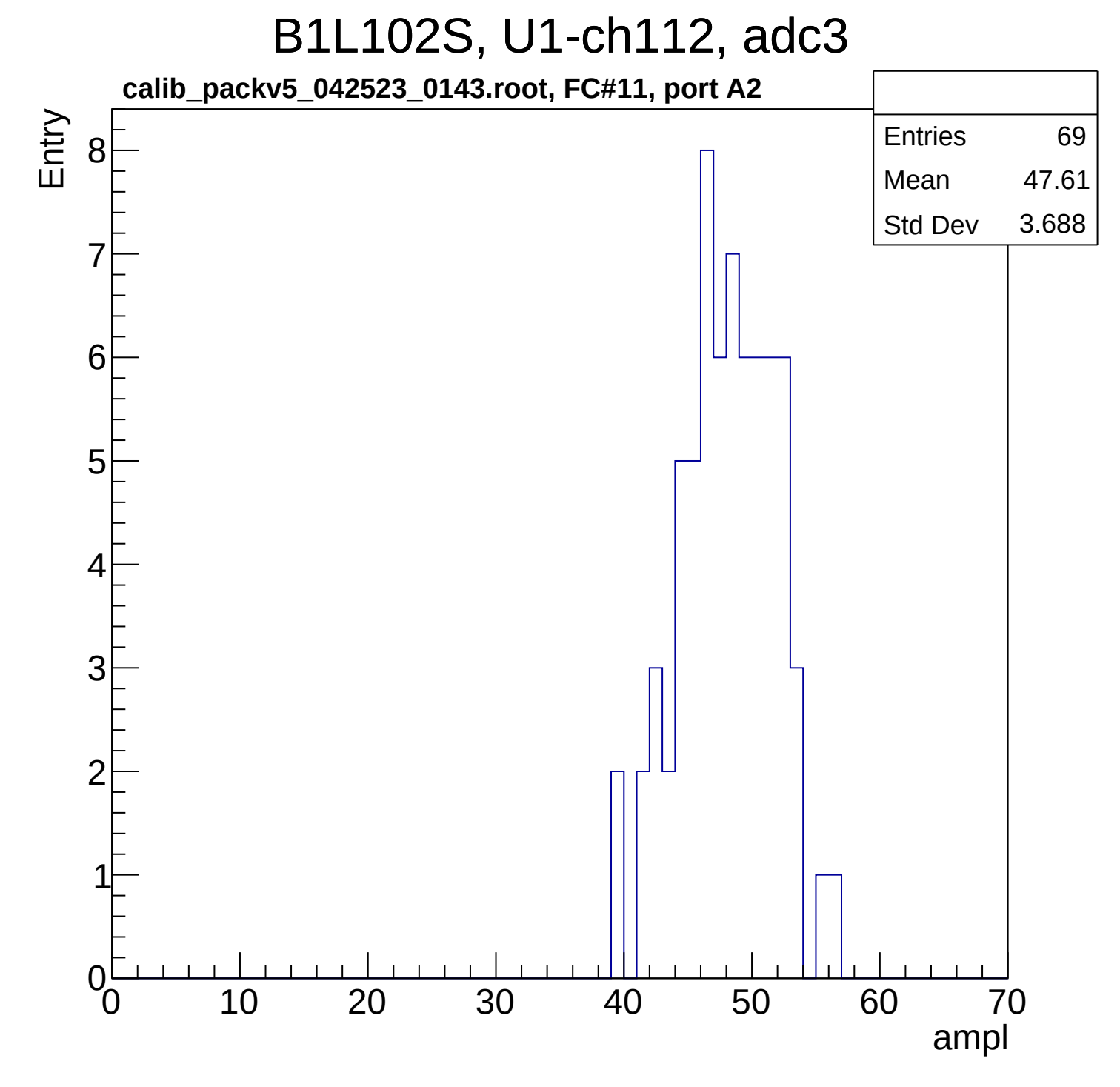
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 34.98
Std Dev 3.038
Gaus mean : 35.8797
Gaus Width: 3.5268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 41.08
Std Dev 3.731
Gaus mean : 42.0447
Gaus Width: 2.8062
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 47.61
Std Dev 3.688
Entry
ampl
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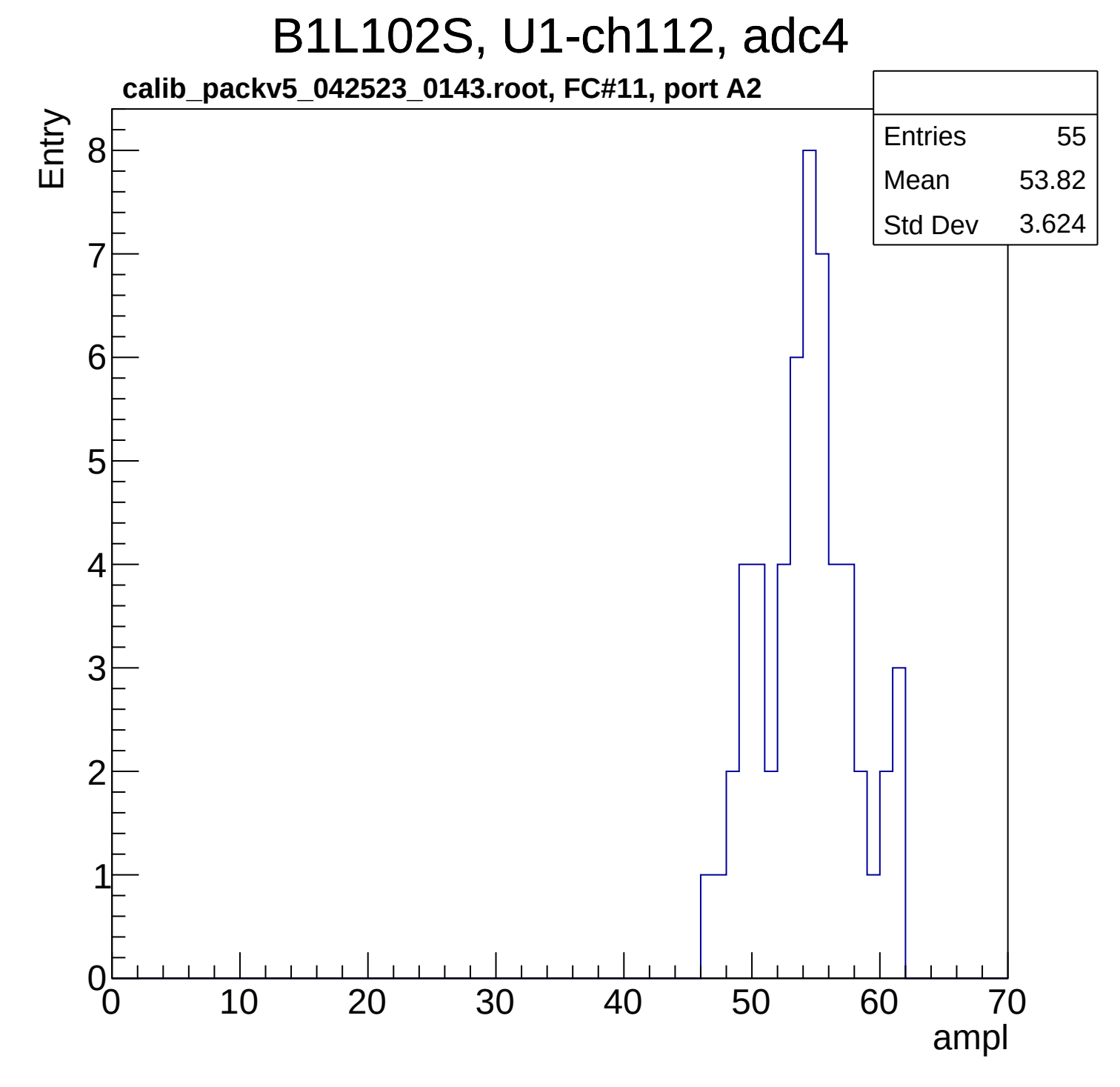
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 53.82
Std Dev 3.624
Entry
ampl
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4
5
6
7
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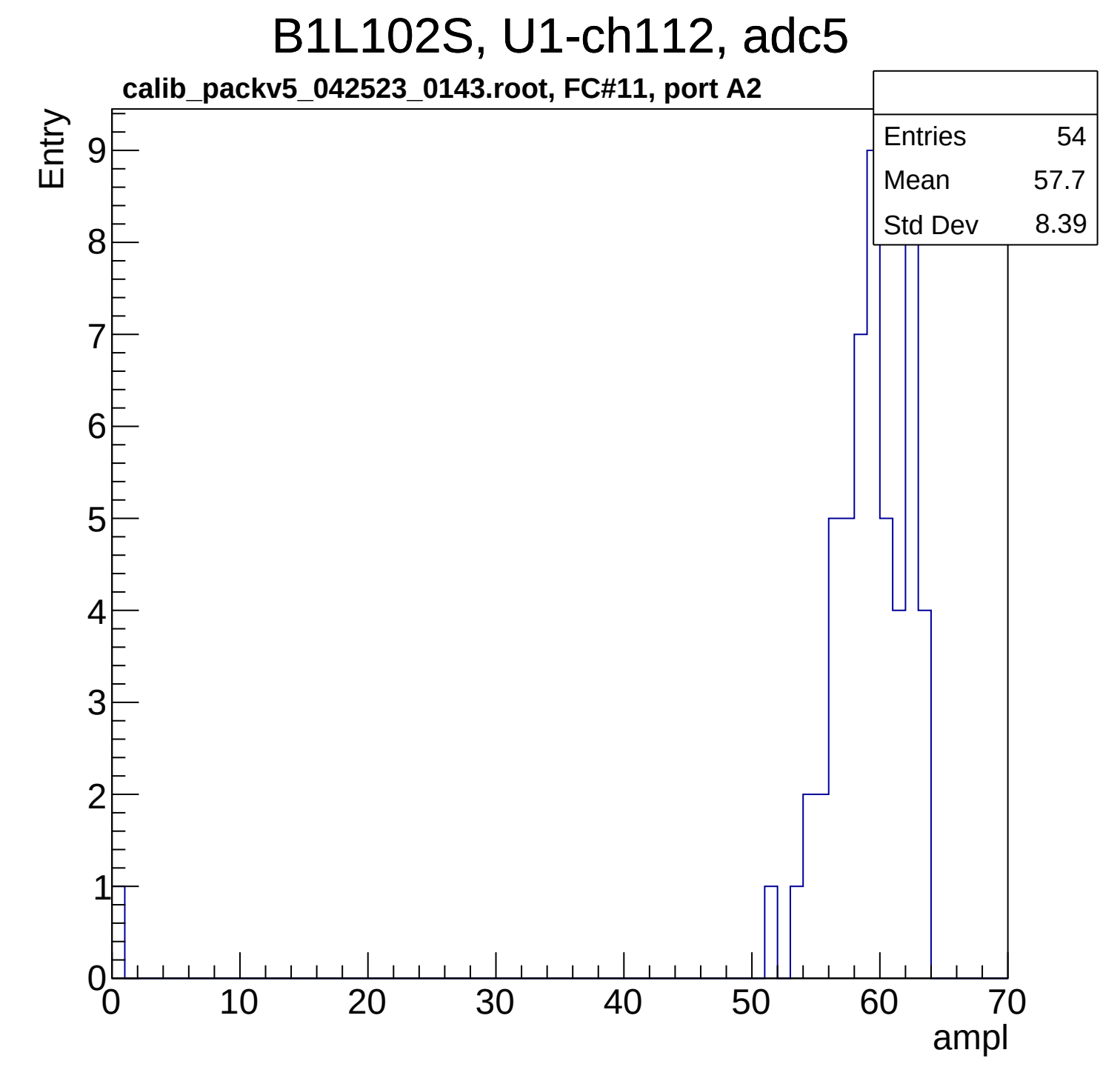
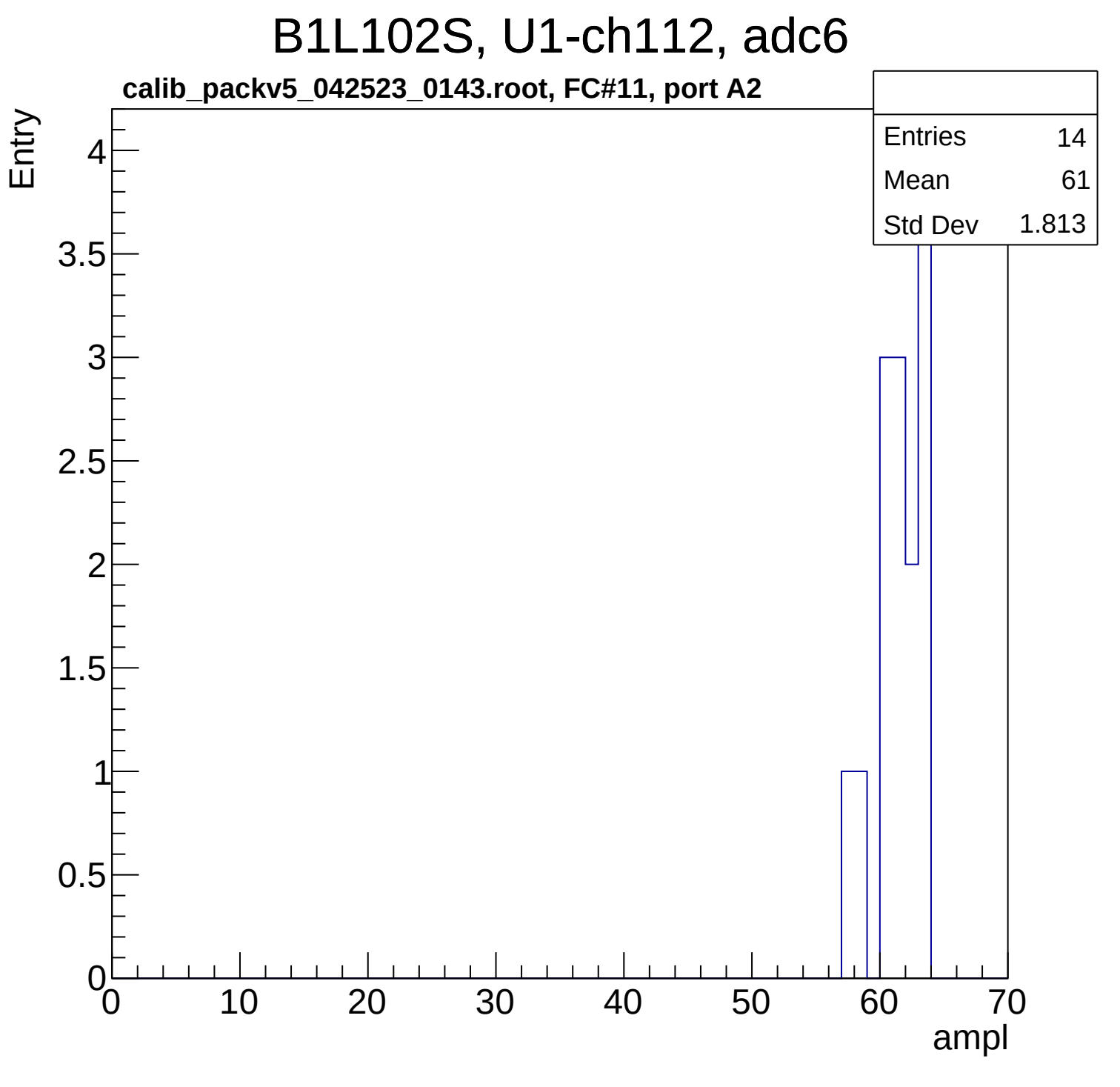
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 57.7
Std Dev 8.39
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61
Std Dev 1.813
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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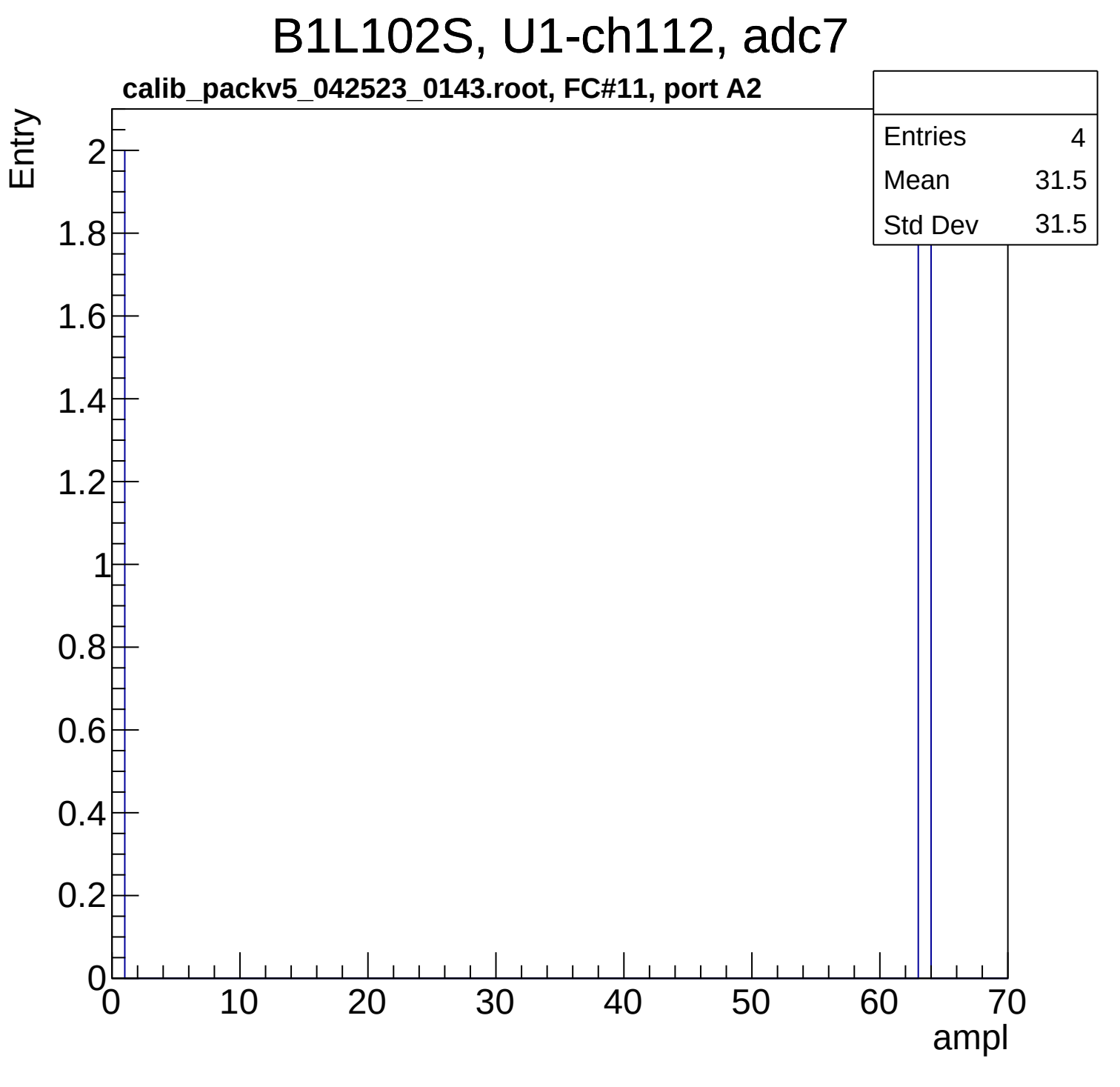
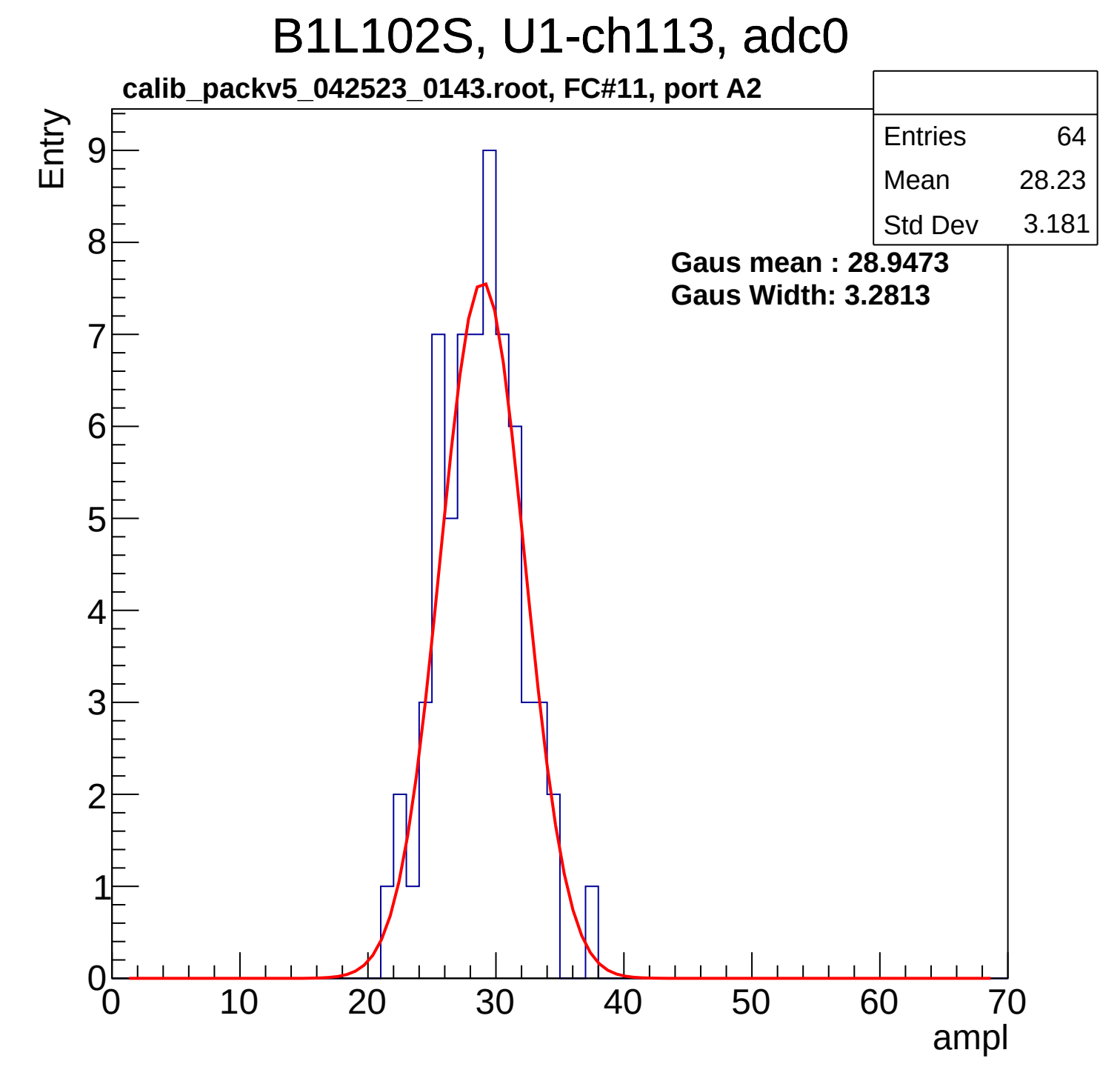
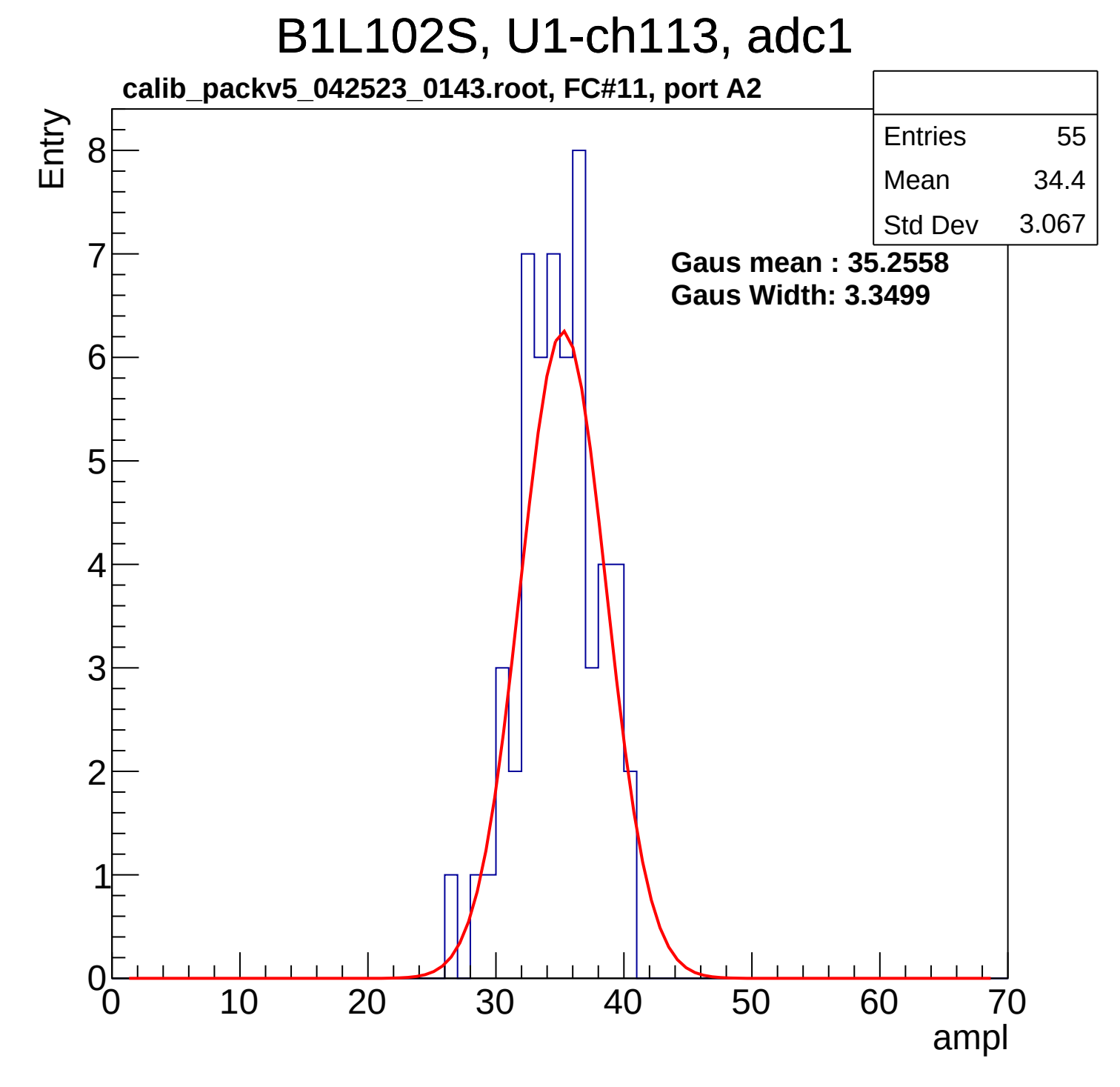
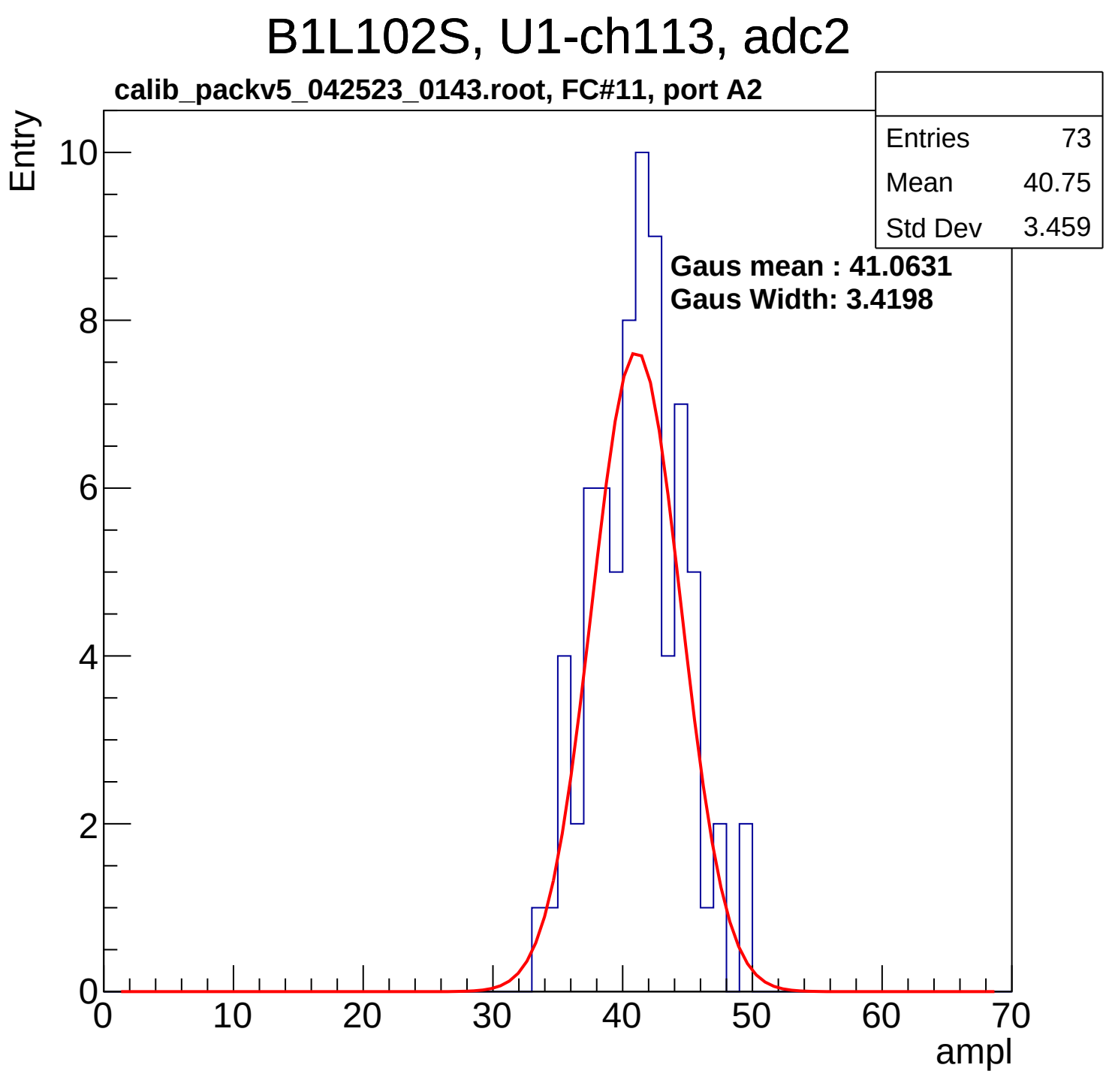
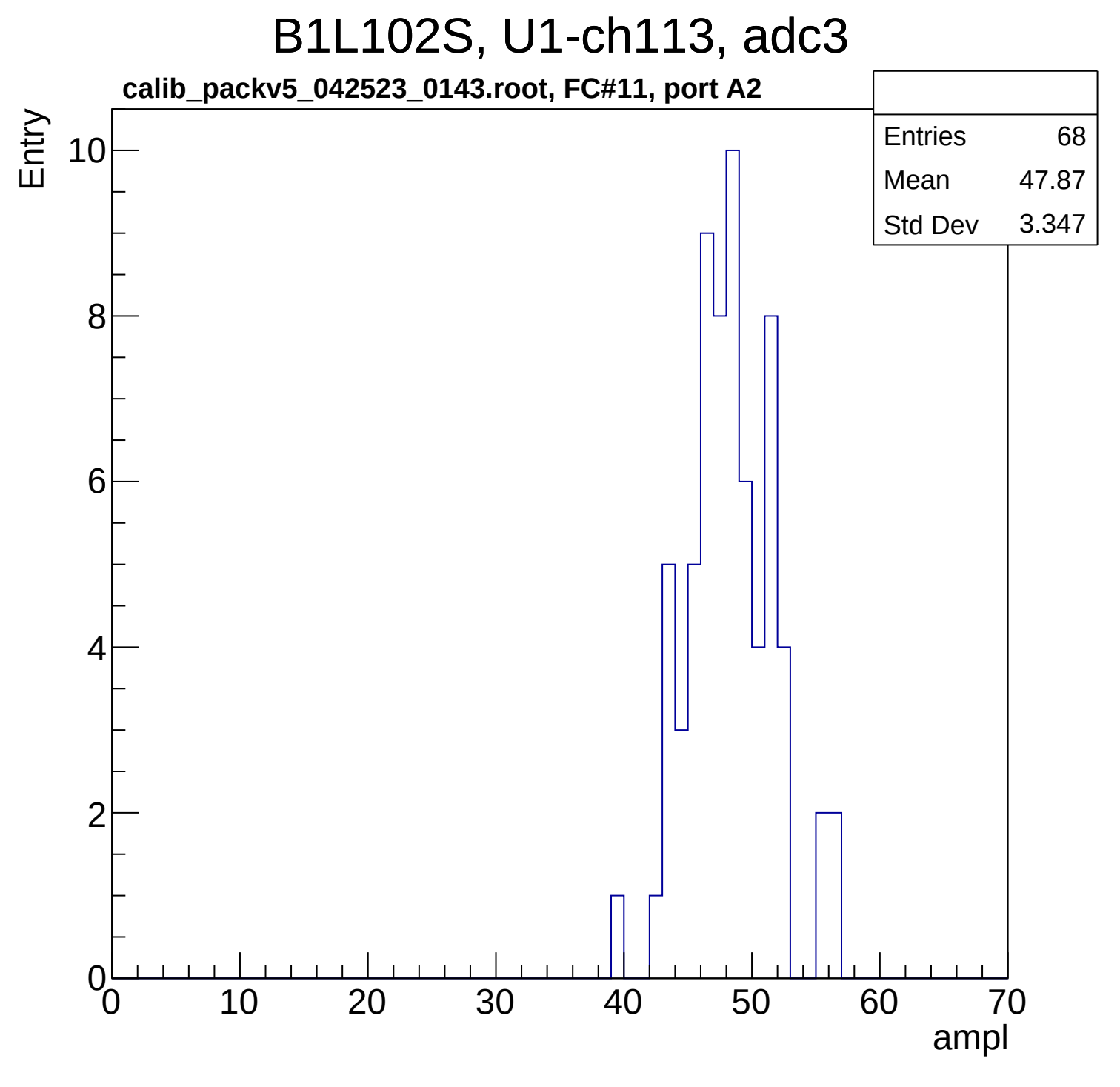
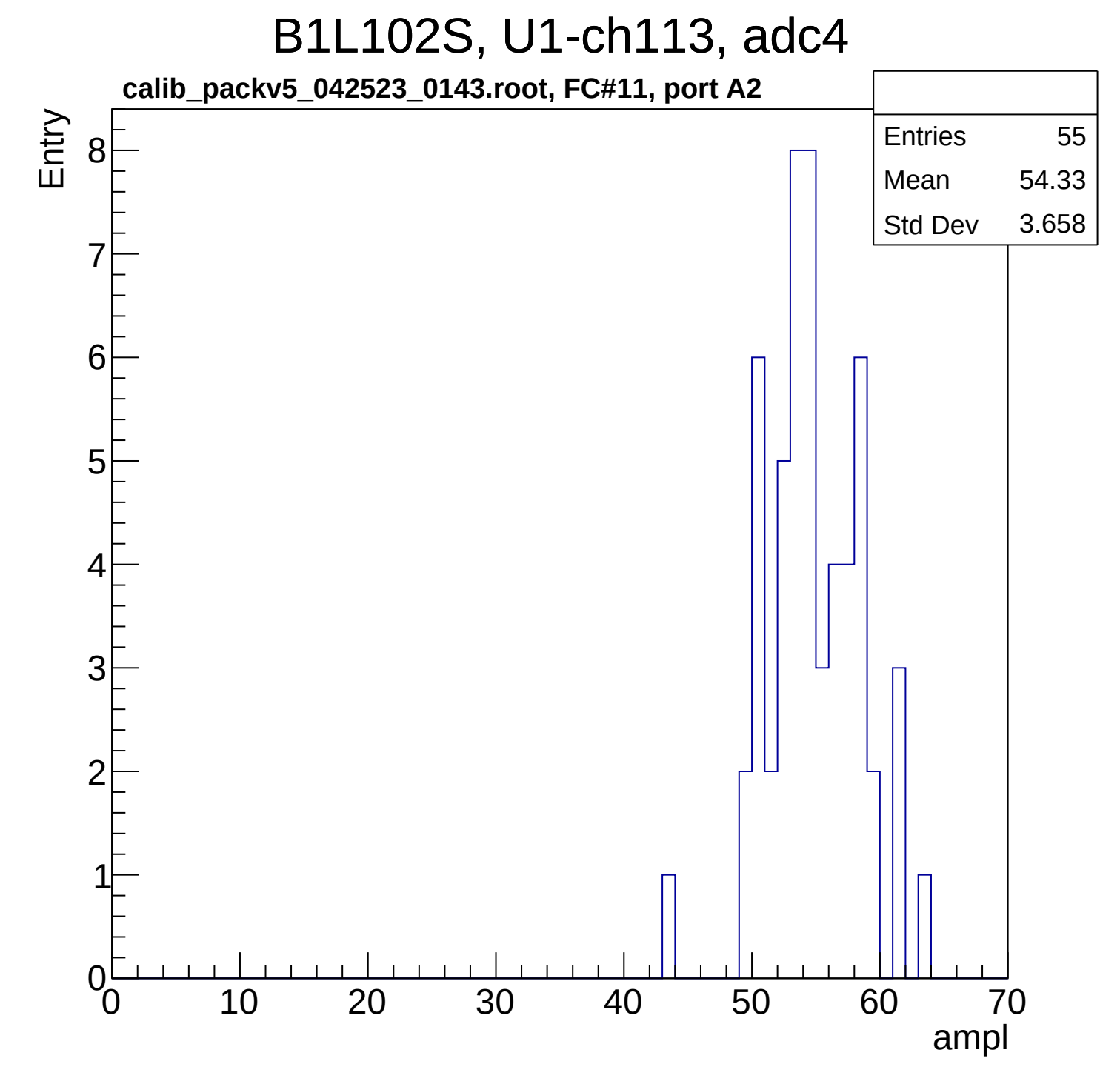

B1L102S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 64
Mean 28.23
Std Dev 3.181
Gaus mean : 28.9473
Gaus Width: 3.2813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 34.4
Std Dev 3.067
Gaus mean : 35.2558
Gaus Width: 3.3499
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 40.75
Std Dev 3.459
Gaus mean : 41.0631
Gaus Width: 3.4198
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 47.87
Std Dev 3.347
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 54.33
Std Dev 3.658
Entry
ampl
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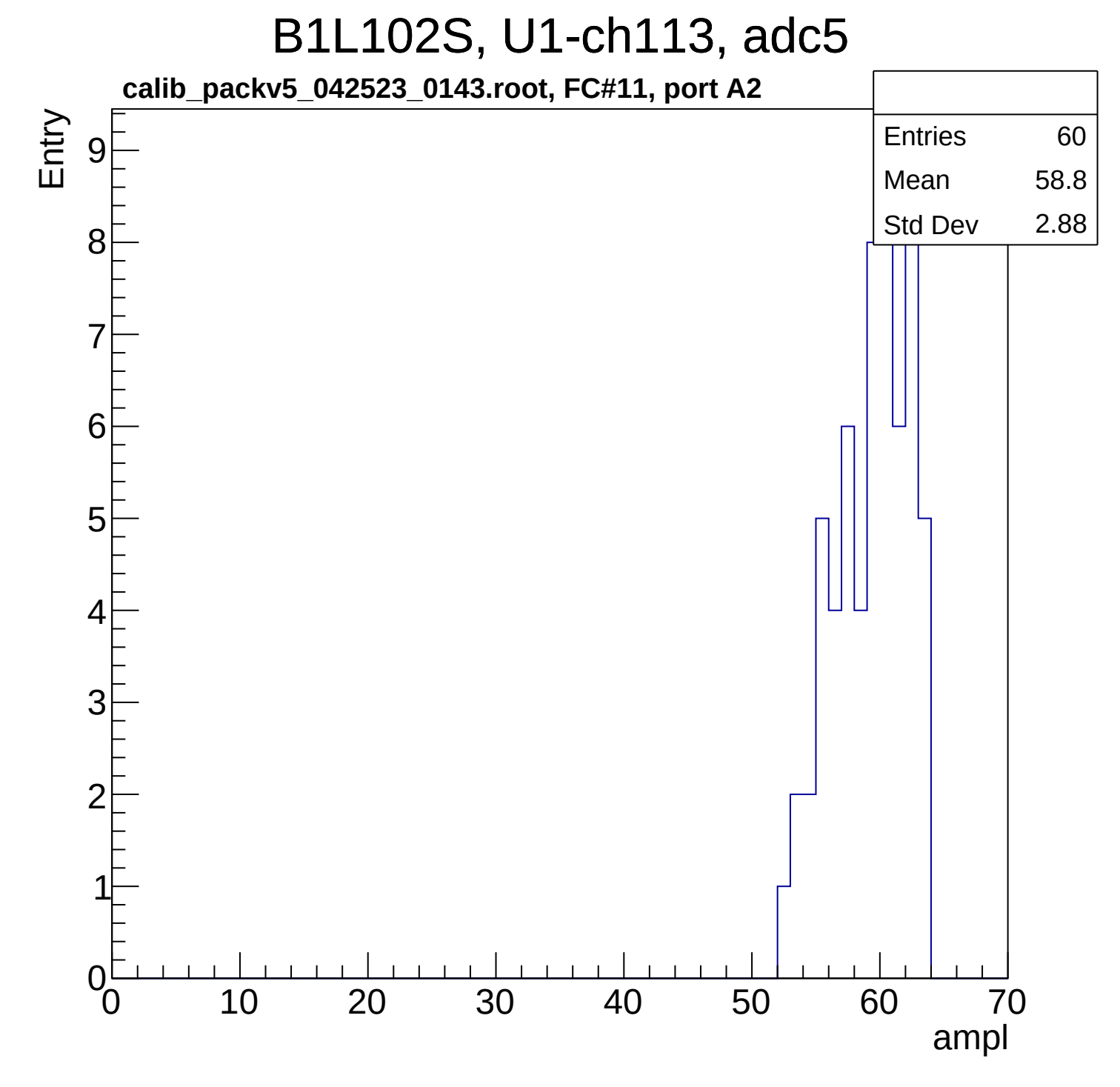
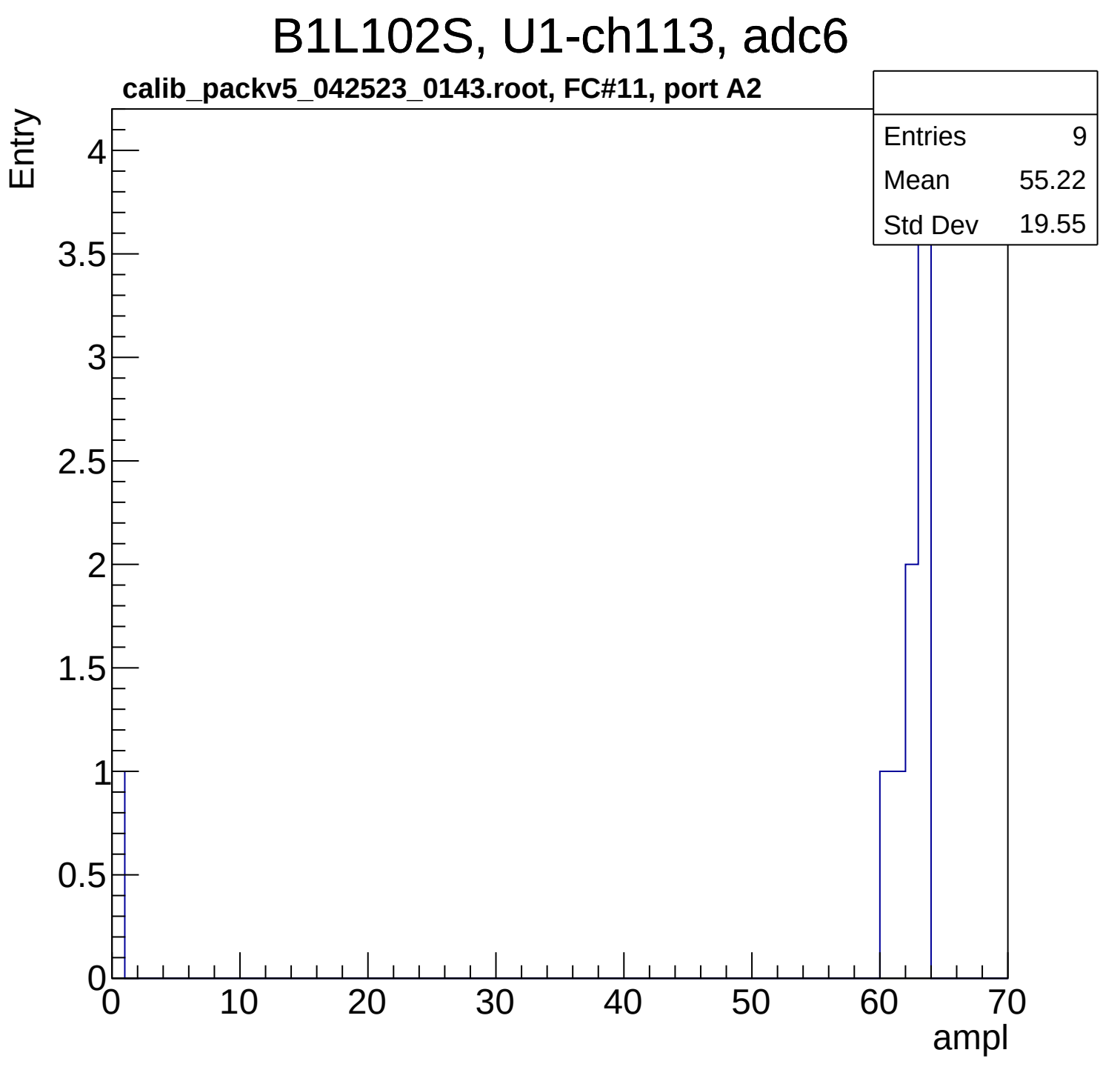
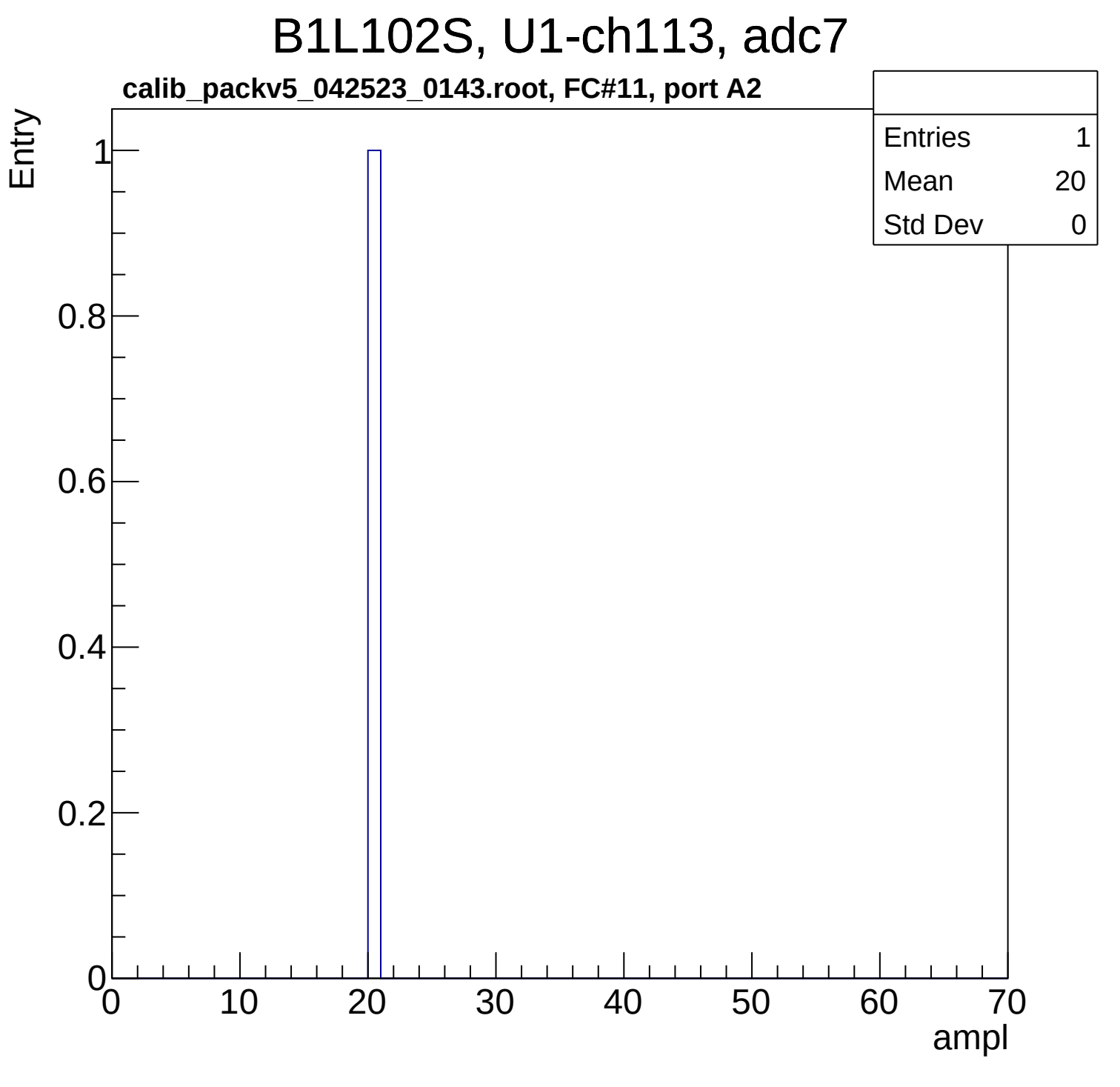
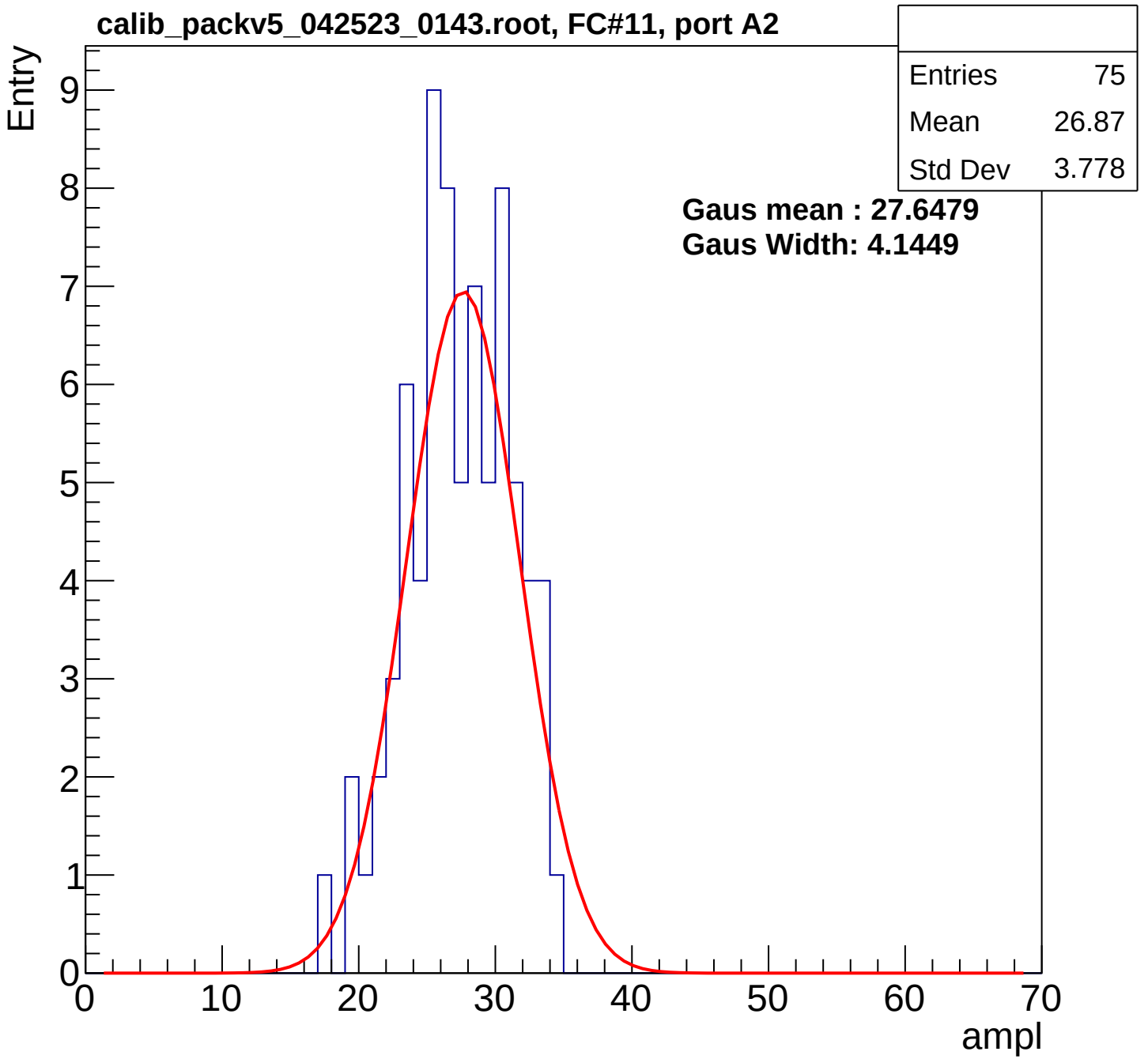
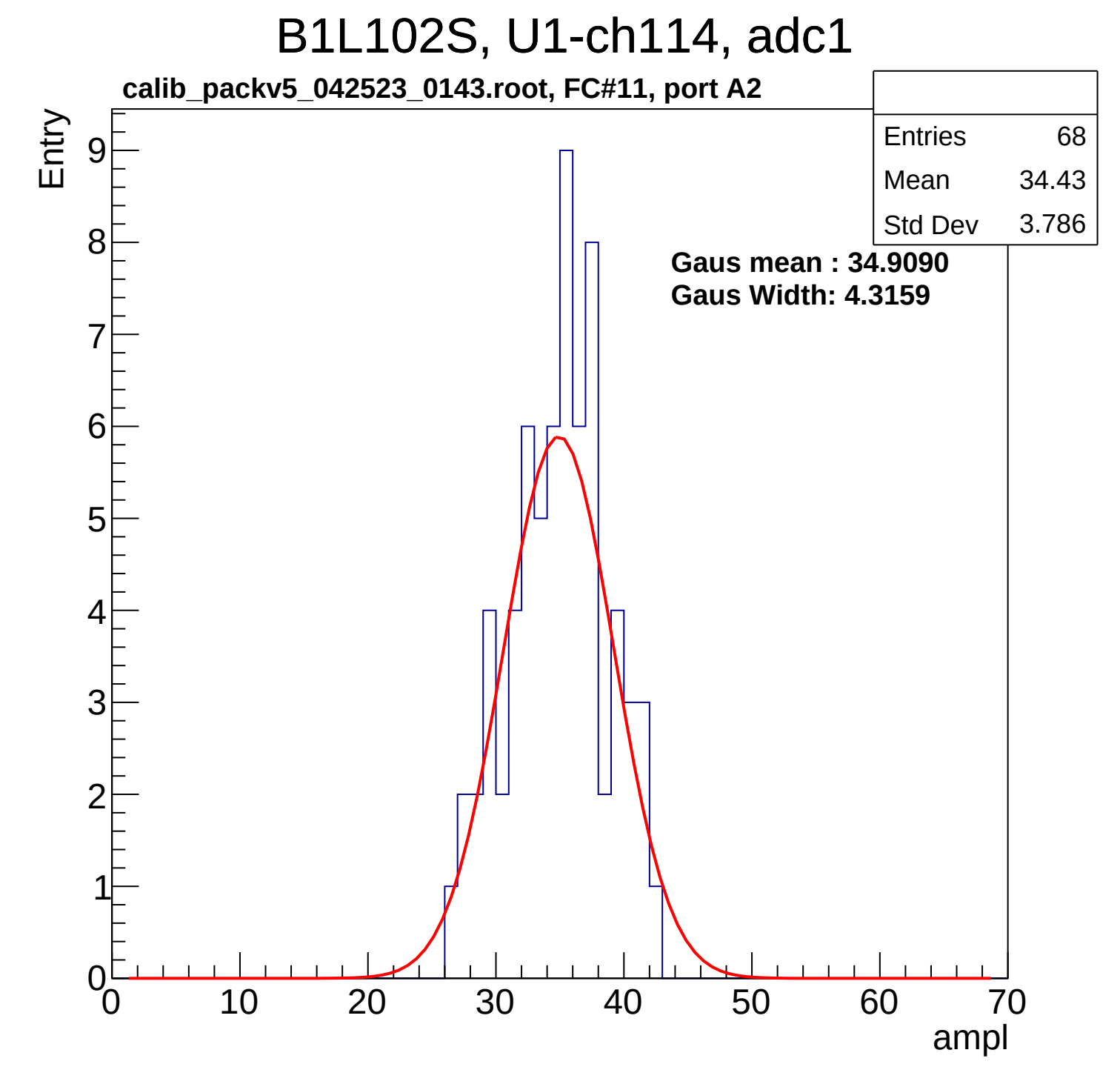
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

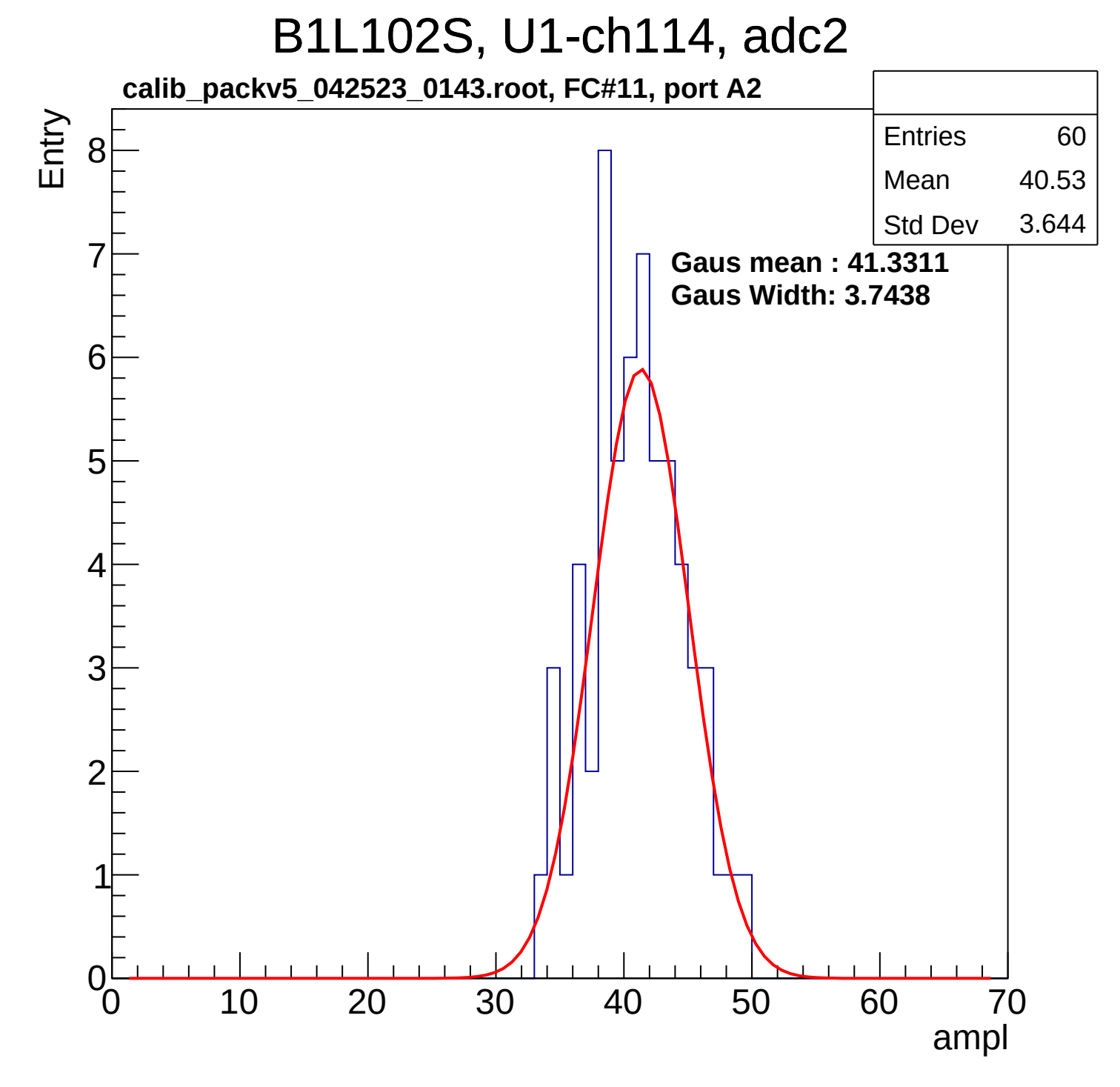

B1L102S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 58.8
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

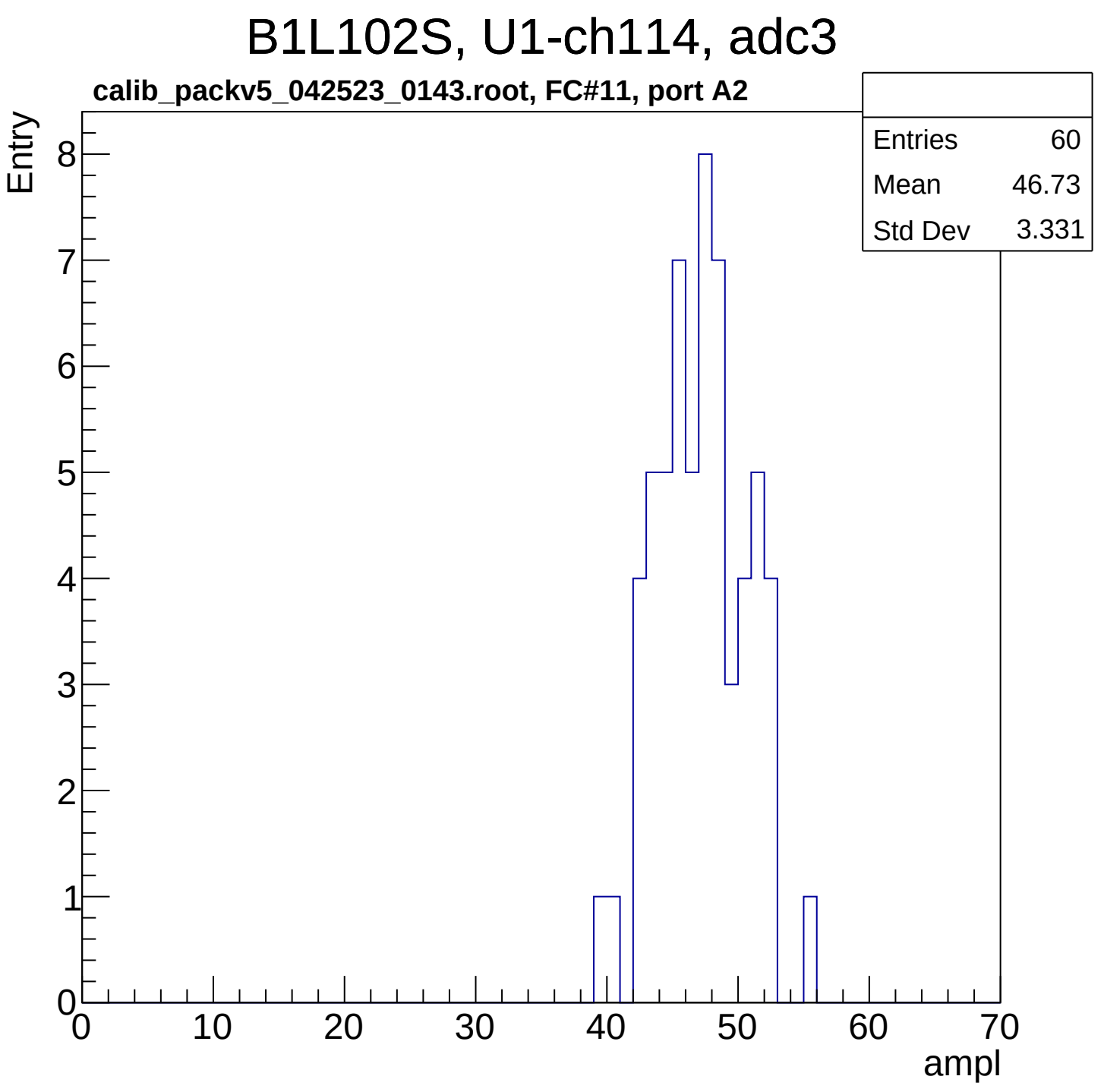

B1L102S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 55.22
Std Dev 19.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

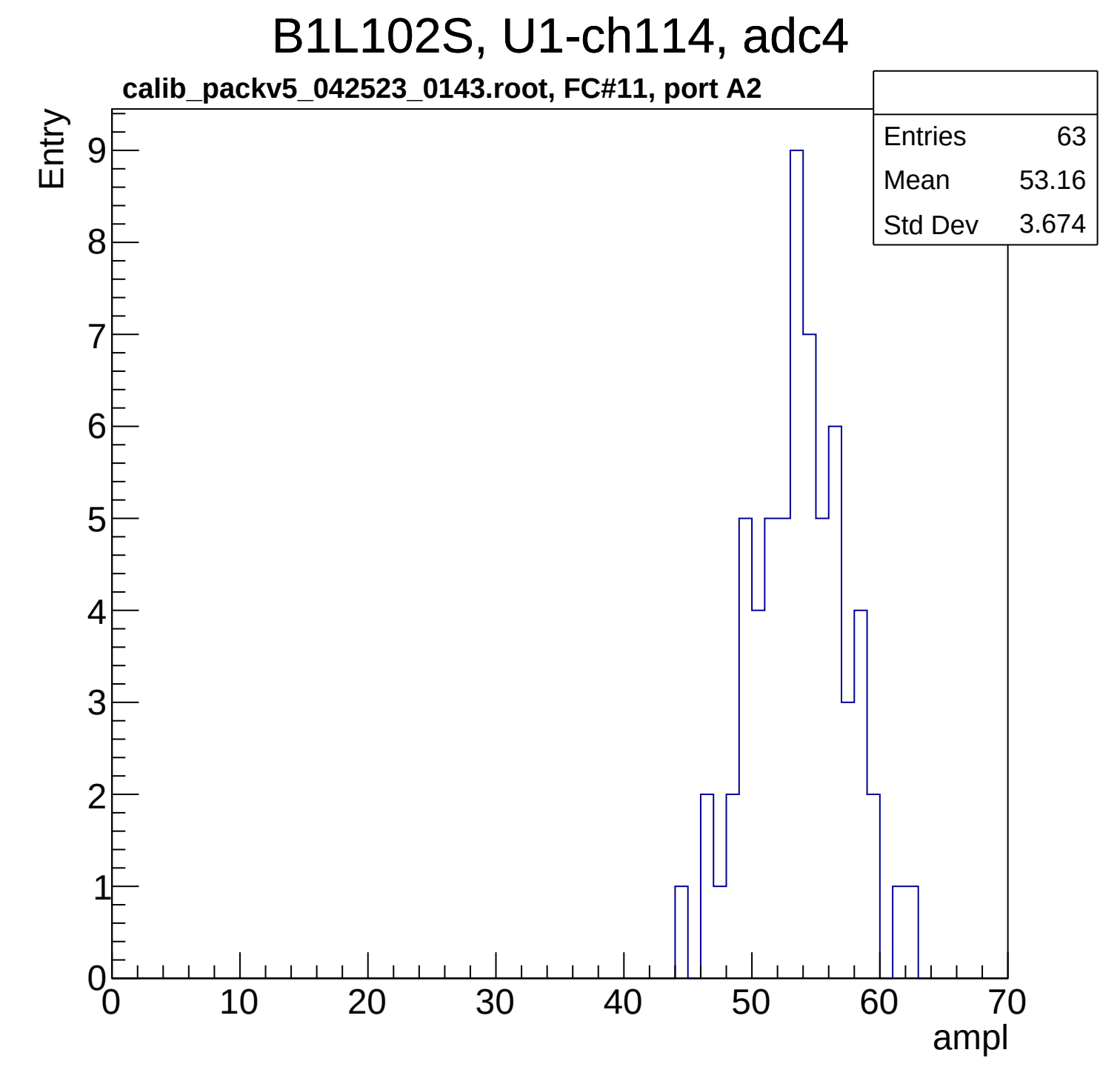

B1L102S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

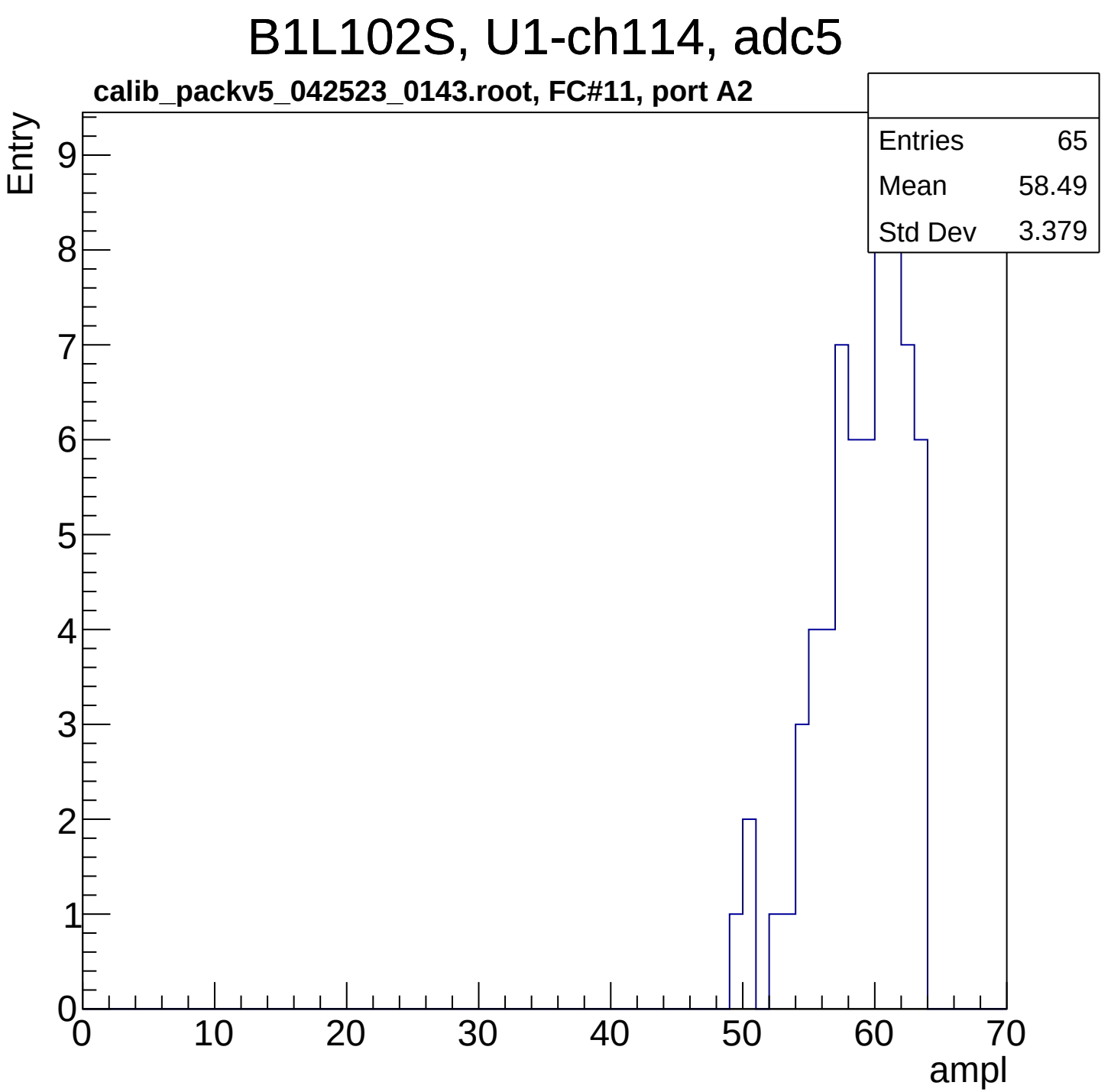

B1L102S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 26.87
Std Dev 3.778
Gaus mean : 27.6479
Gaus Width: 4.1449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

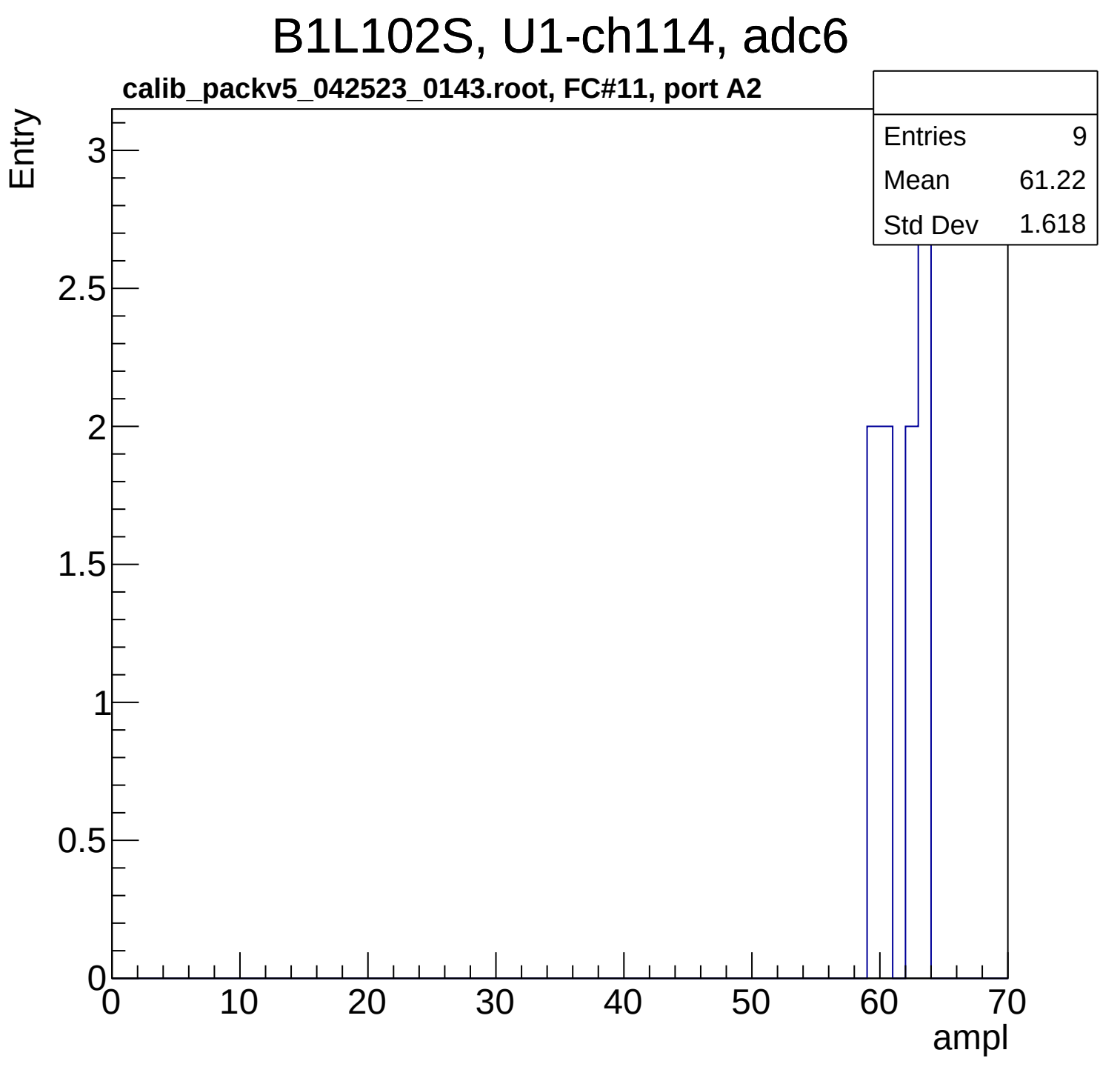

B1L102S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 34.43
Std Dev 3.786
Gaus mean : 34.9090
Gaus Width: 4.3159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

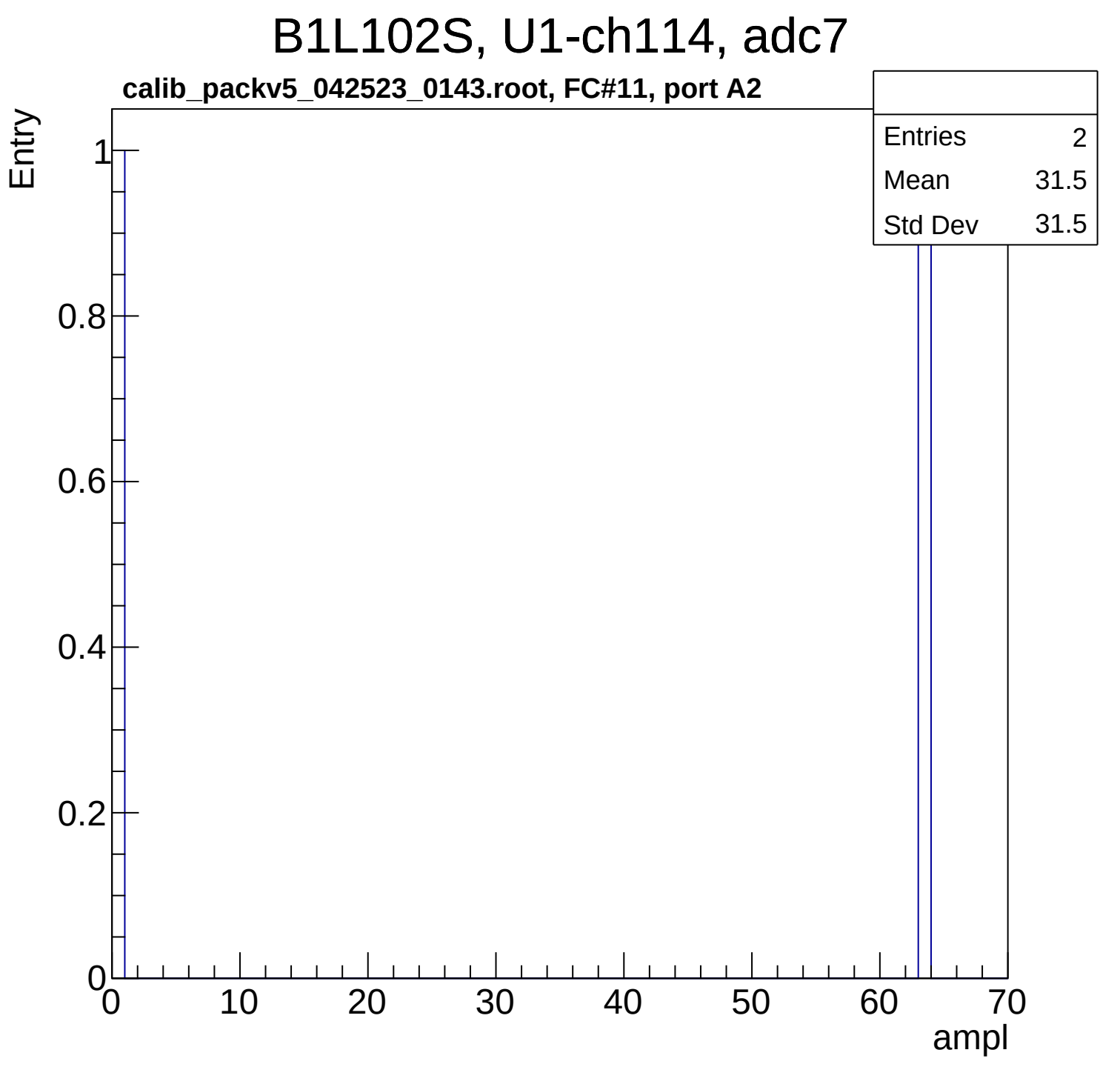

B1L102S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 40.53
Std Dev 3.644
Gaus mean : 41.3311
Gaus Width: 3.7438
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

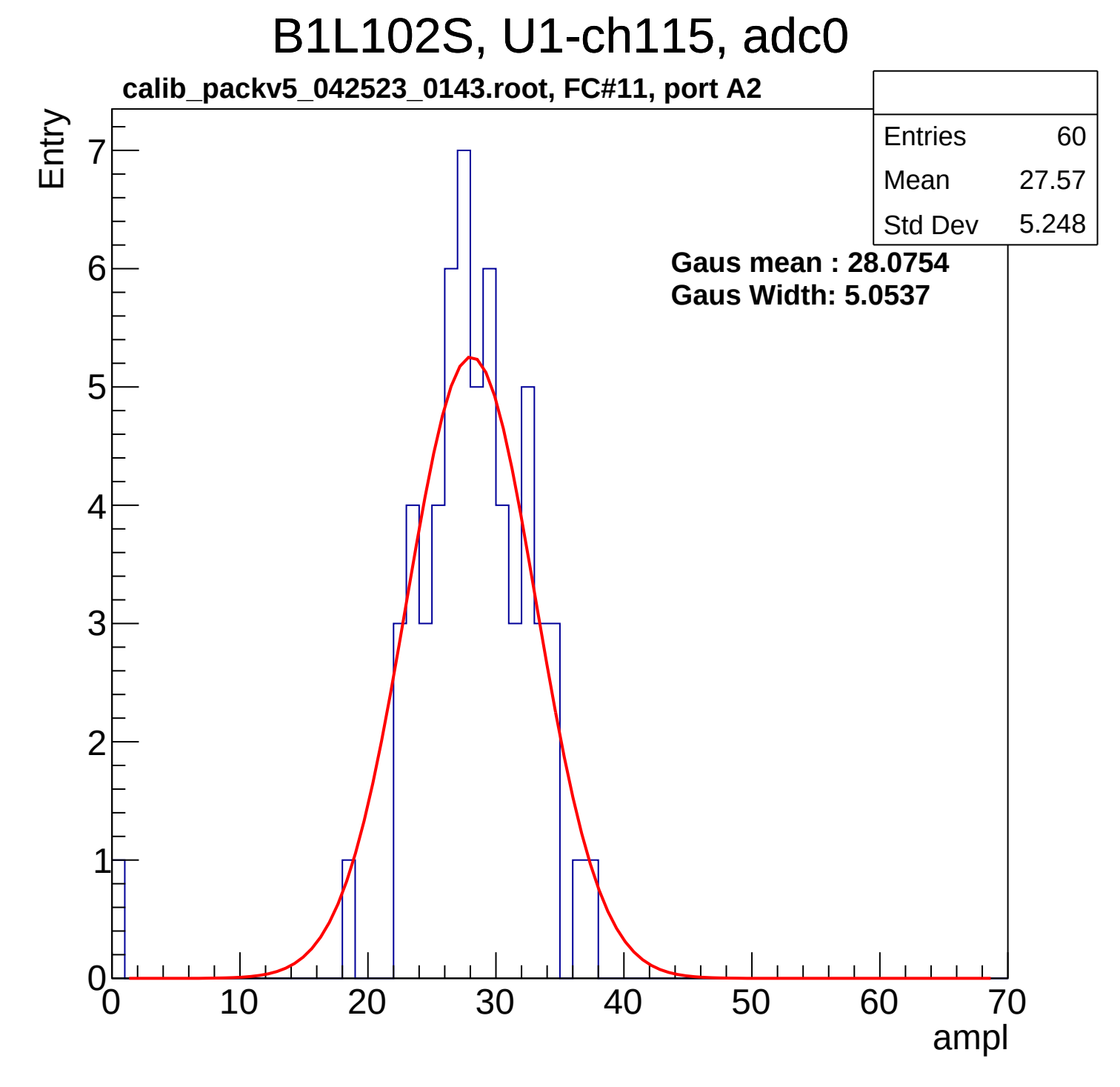

B1L102S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 46.73
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

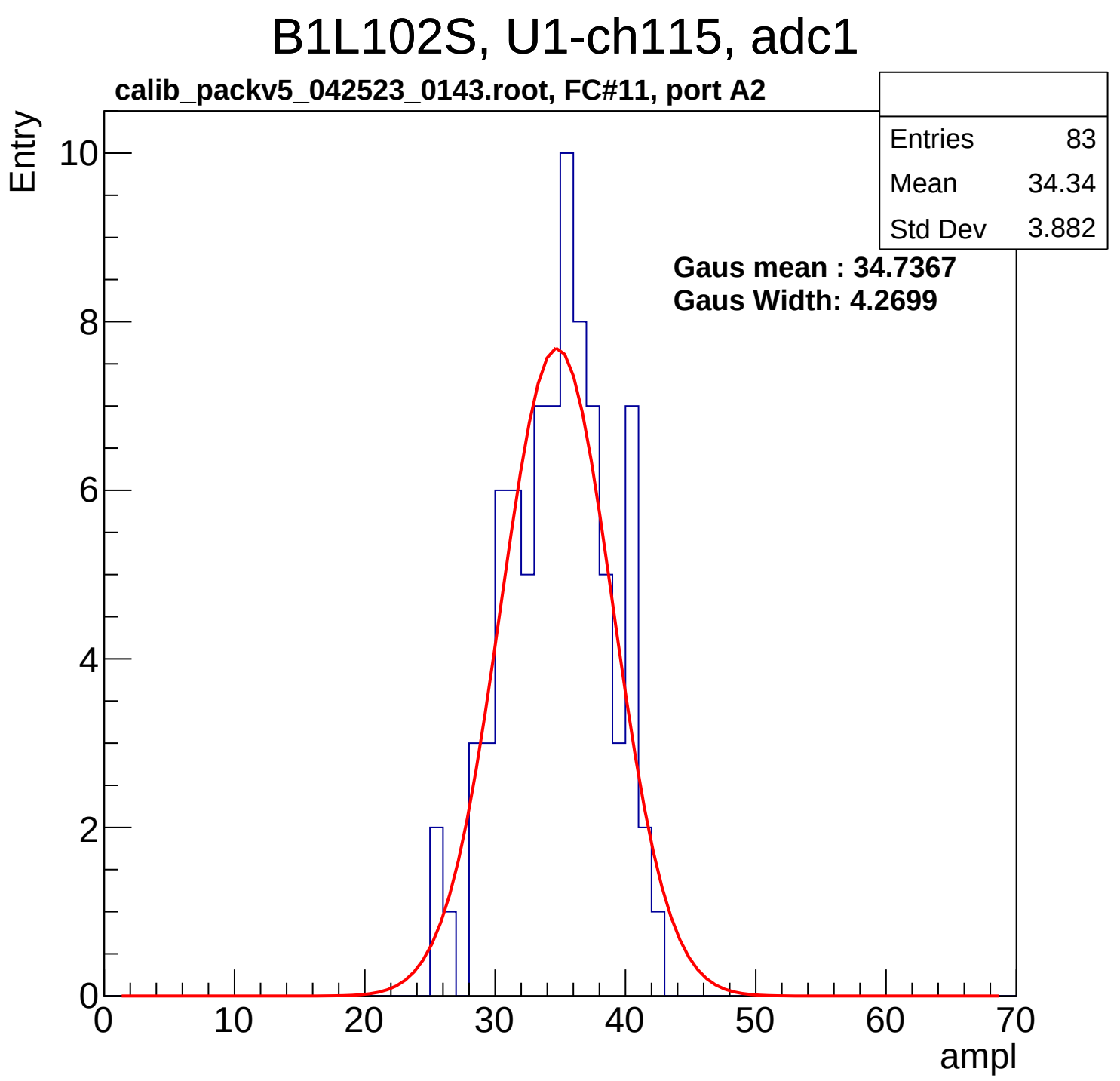

B1L102S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 53.16
Std Dev 3.674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

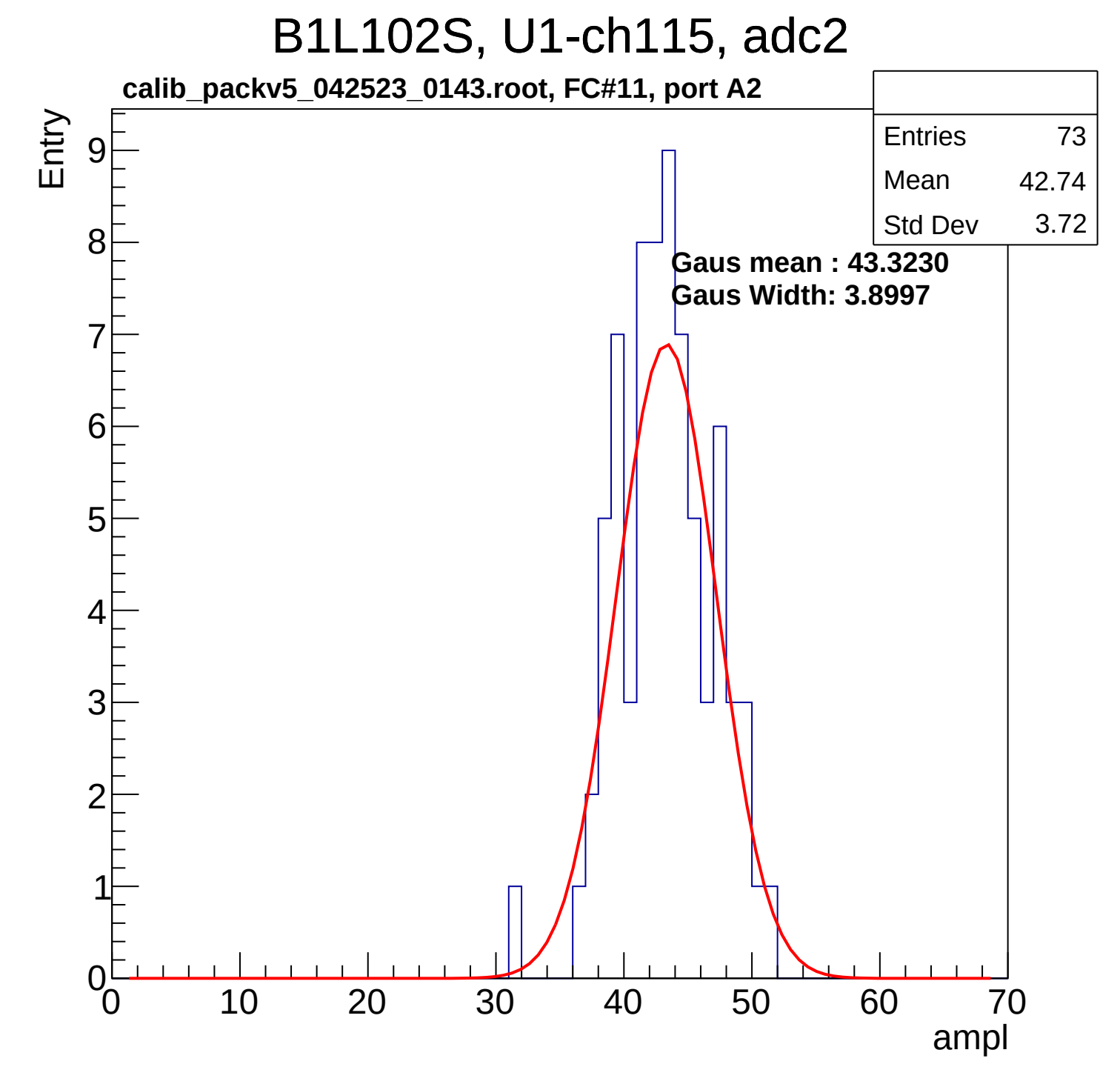

B1L102S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 58.49
Std Dev 3.379
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

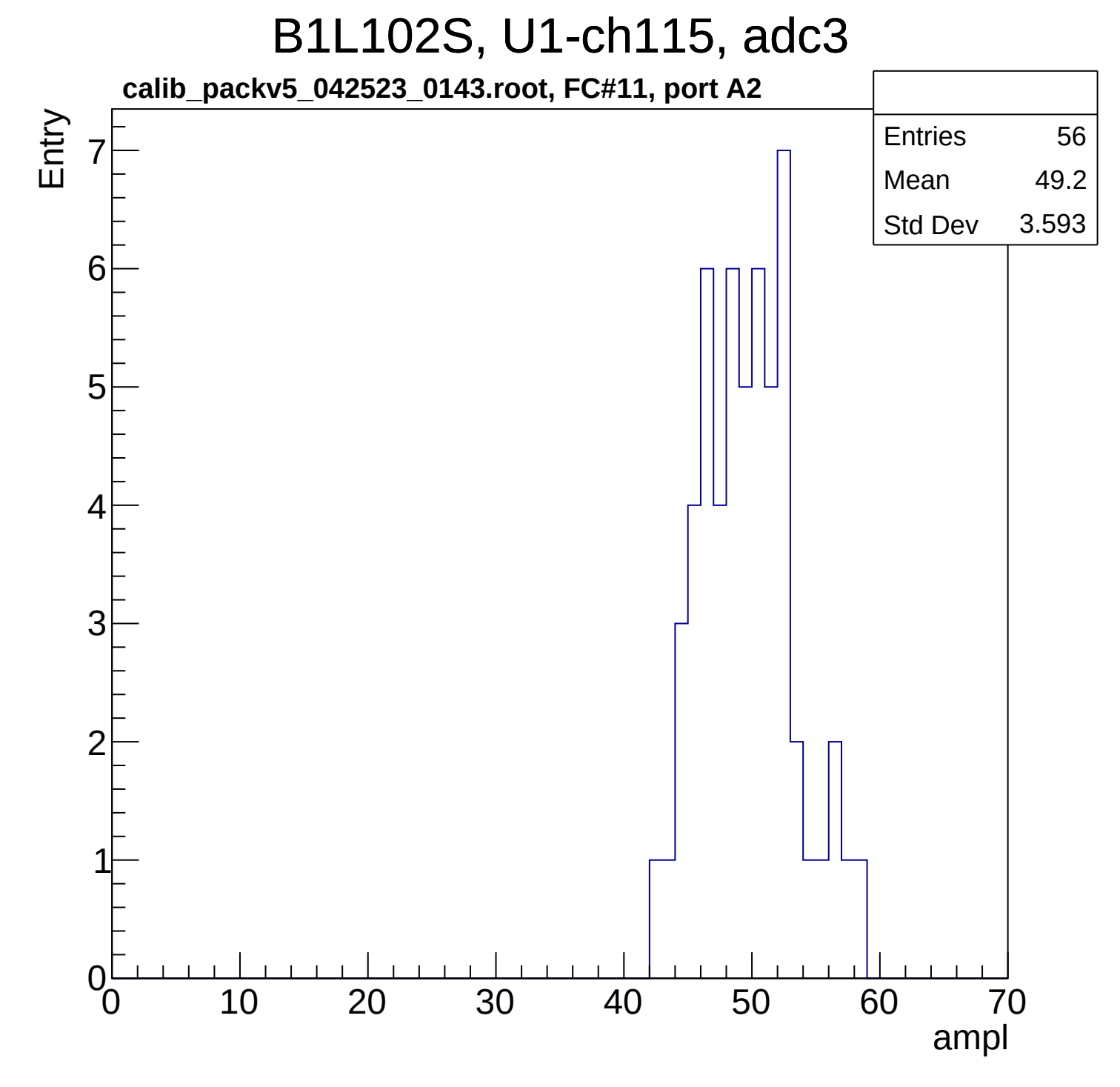

B1L102S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 9
Mean 61.22
Std Dev 1.618
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

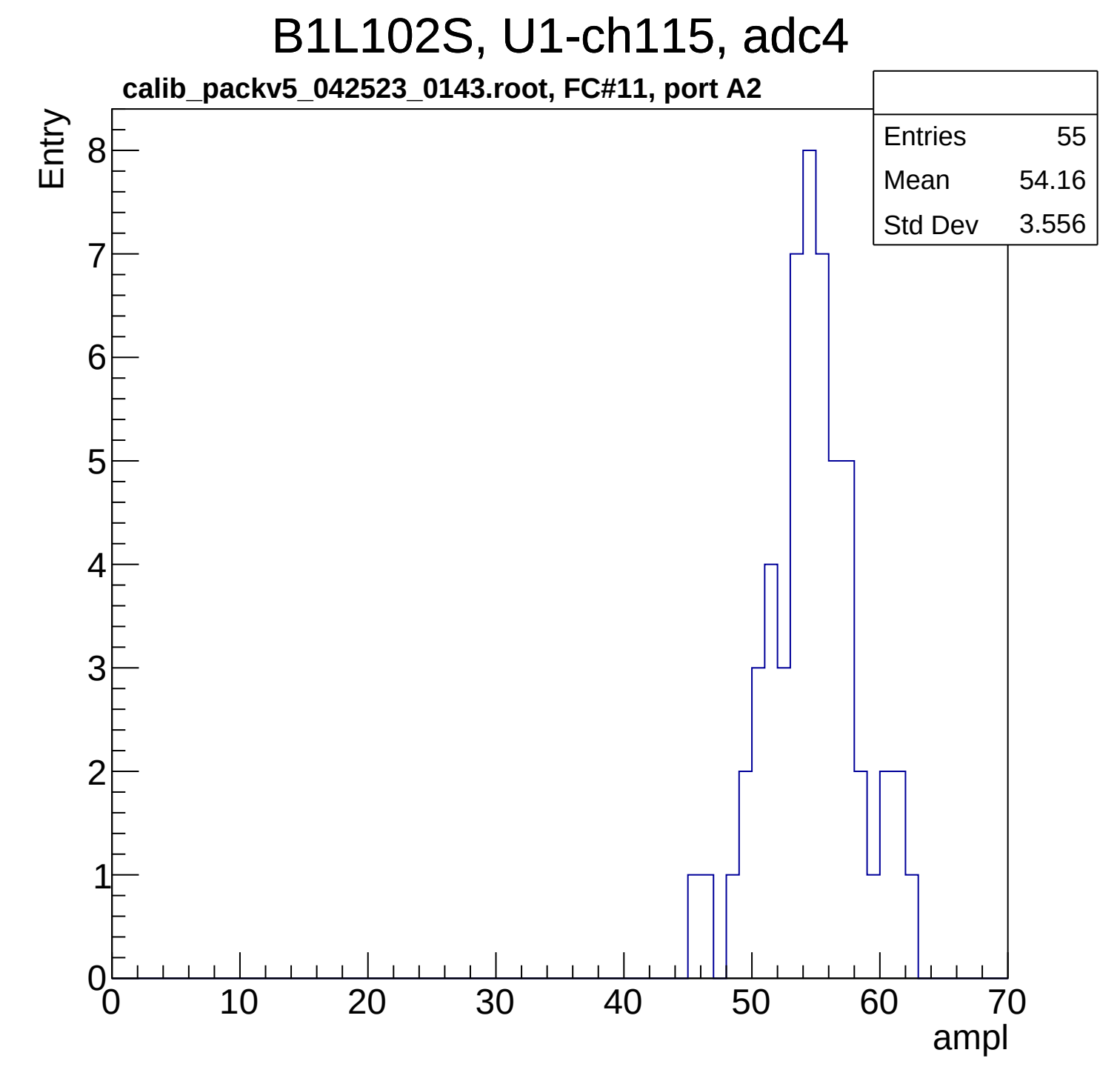

B1L102S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

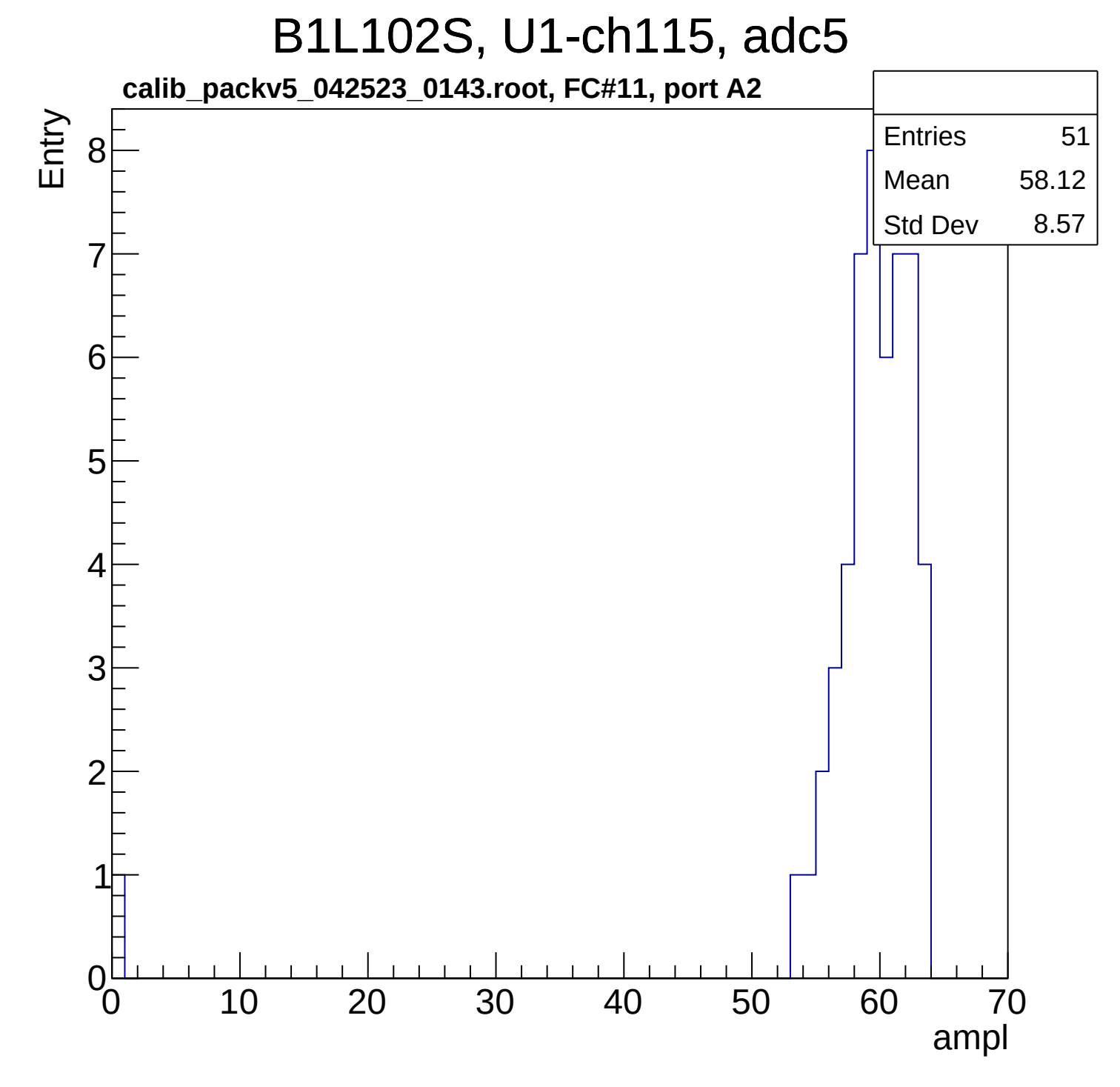

B1L102S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 27.57
Std Dev 5.248
Gaus mean : 28.0754
Gaus Width: 5.0537
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 83
Mean 34.34
Std Dev 3.882
Gaus mean : 34.7367
Gaus Width: 4.2699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 42.74
Std Dev 3.72
Gaus mean : 43.3230
Gaus Width: 3.8997
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 49.2
Std Dev 3.593
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 54.16
Std Dev 3.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 58.12
Std Dev 8.57
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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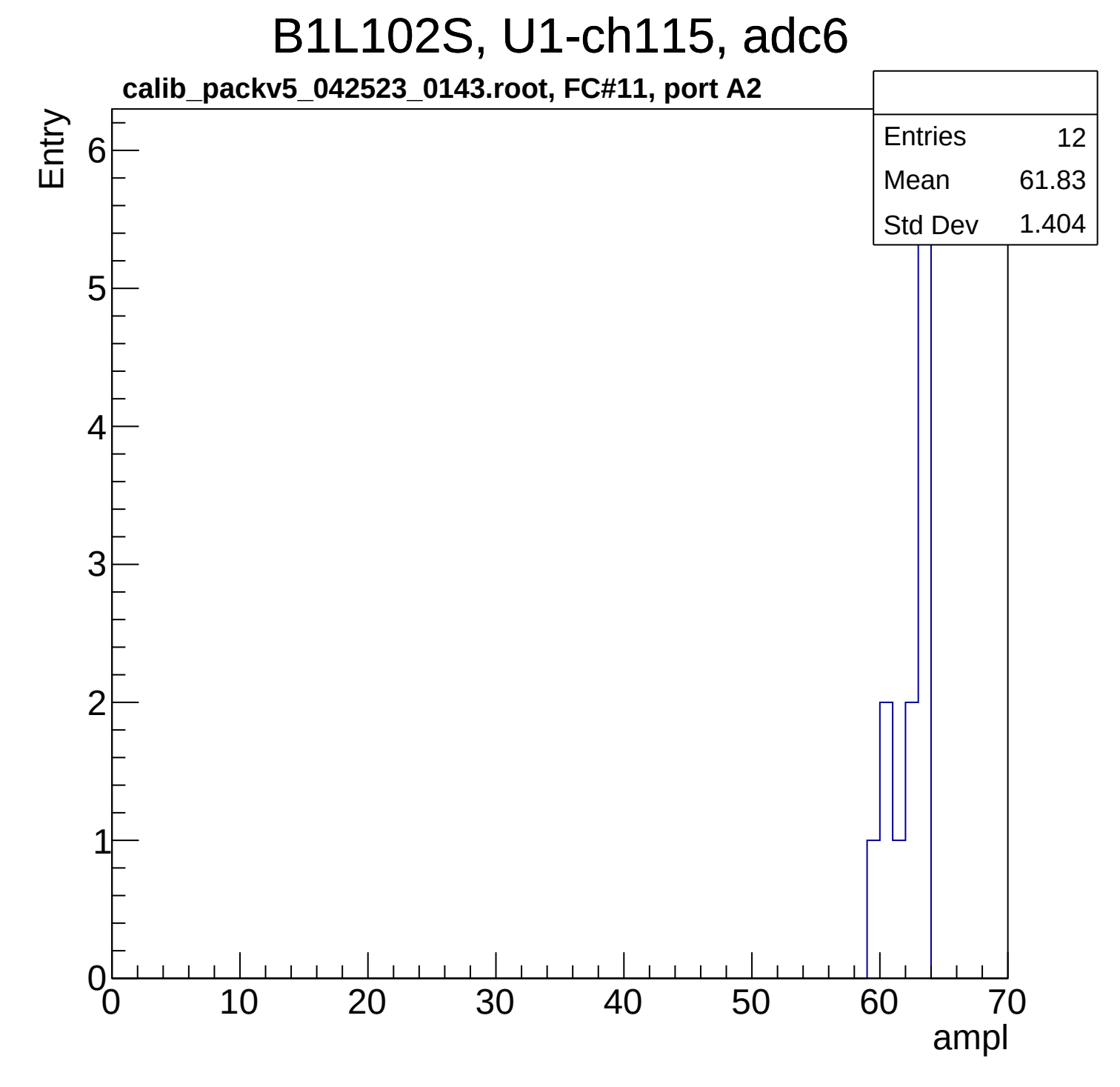
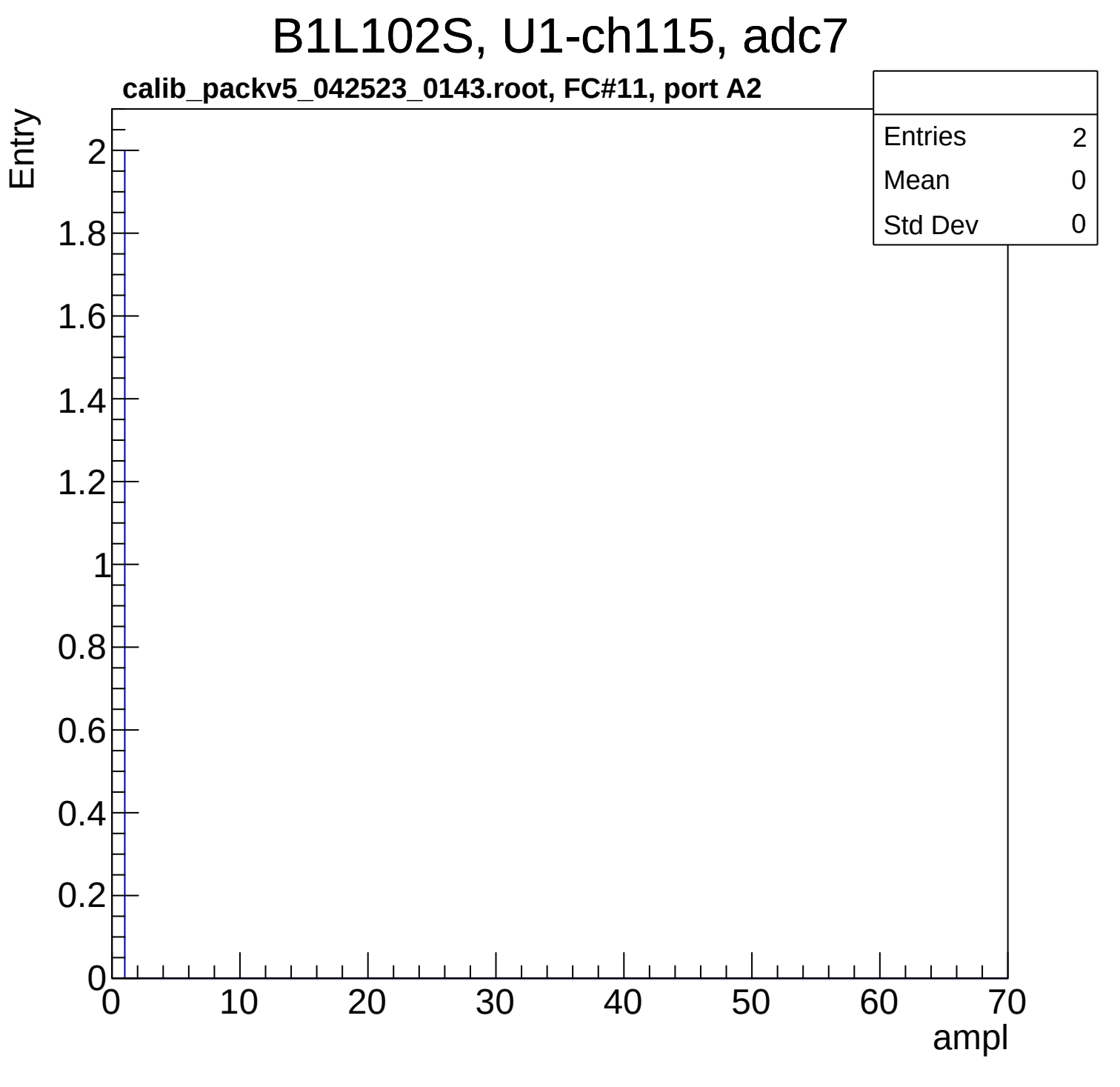
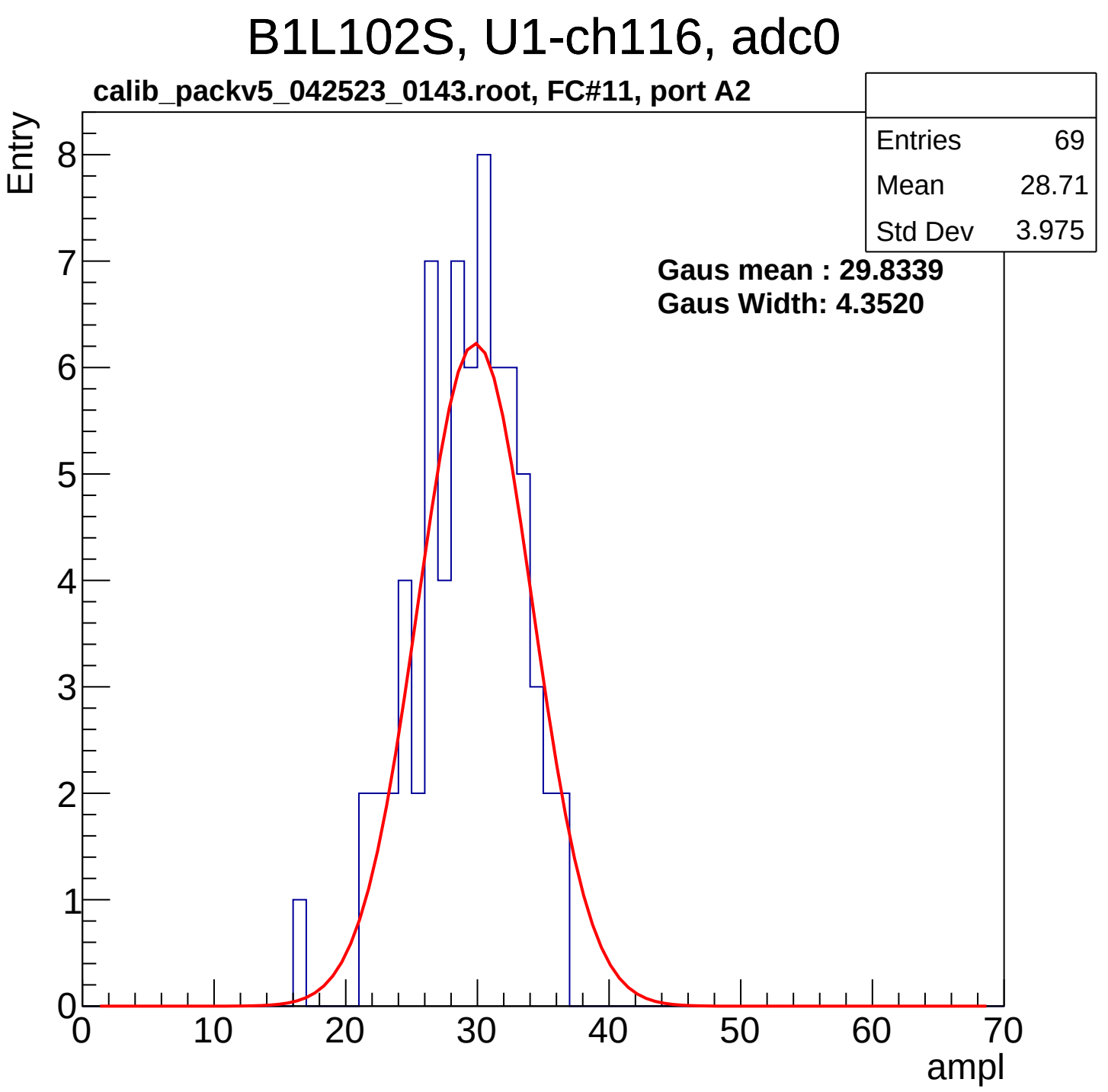
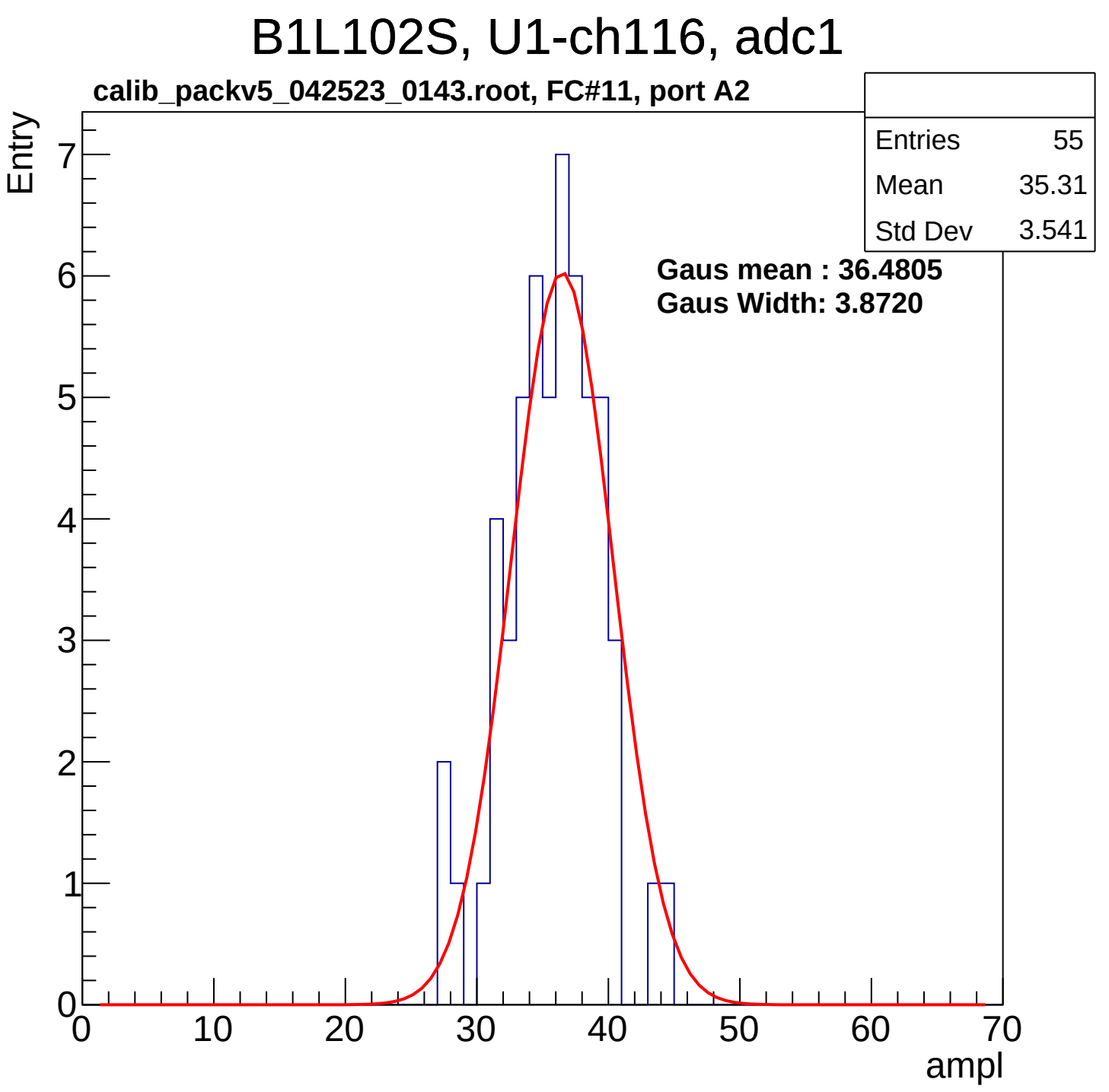

B1L102S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.83
Std Dev 1.404
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.71
Std Dev 3.975
Gaus mean : 29.8339
Gaus Width: 4.3520
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 35.31
Std Dev 3.541
Gaus mean : 36.4805
Gaus Width: 3.8720
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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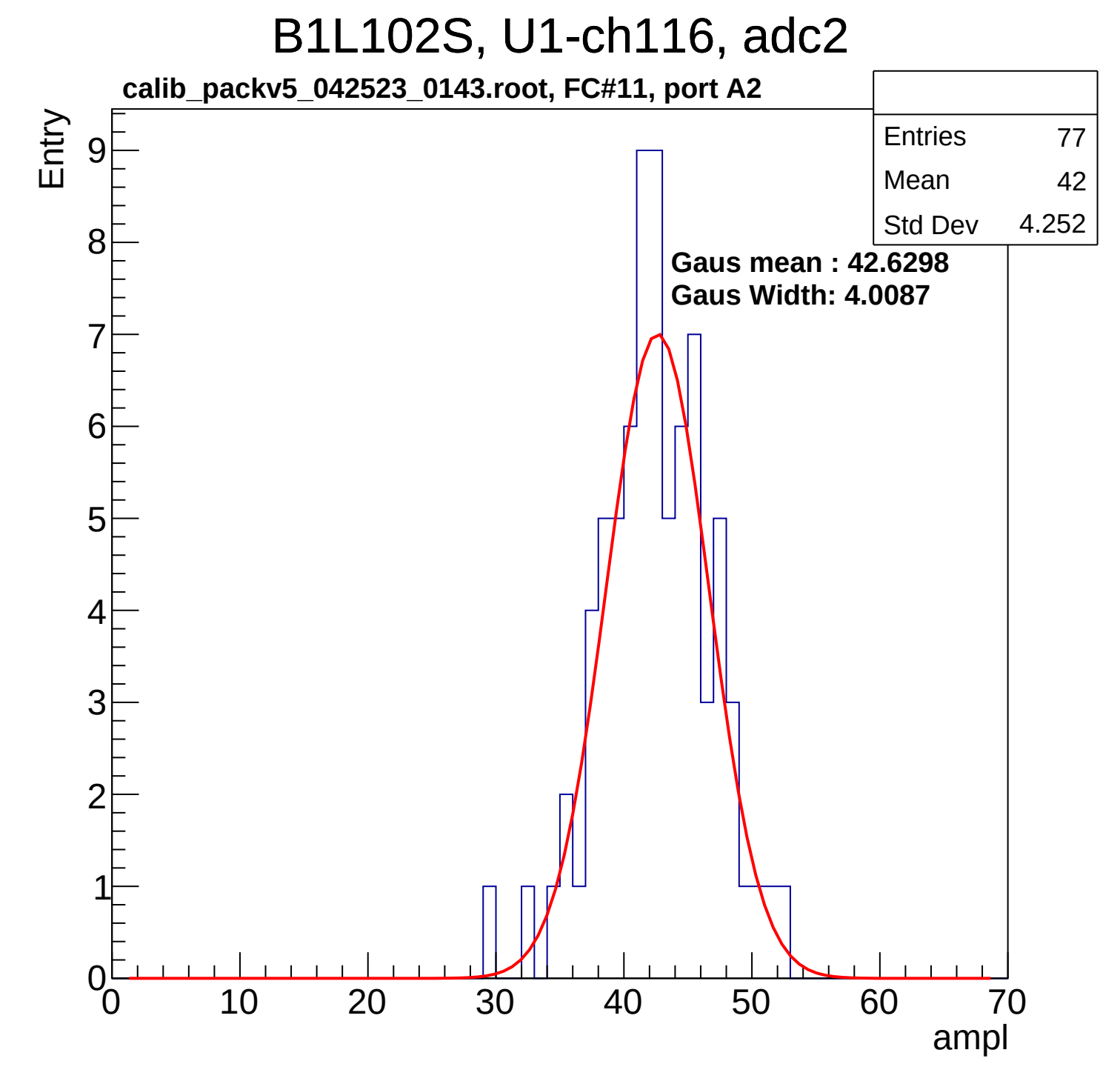
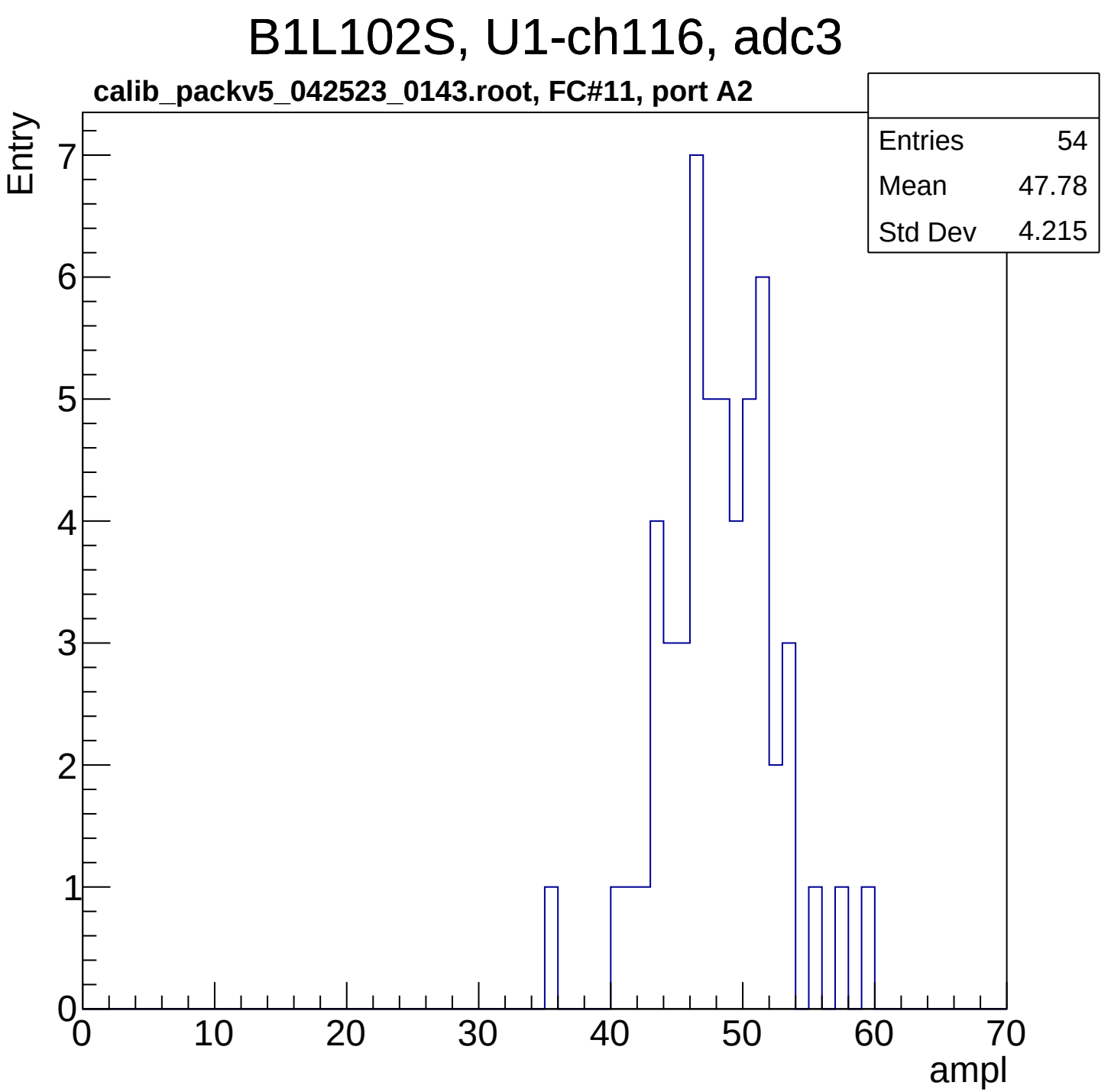
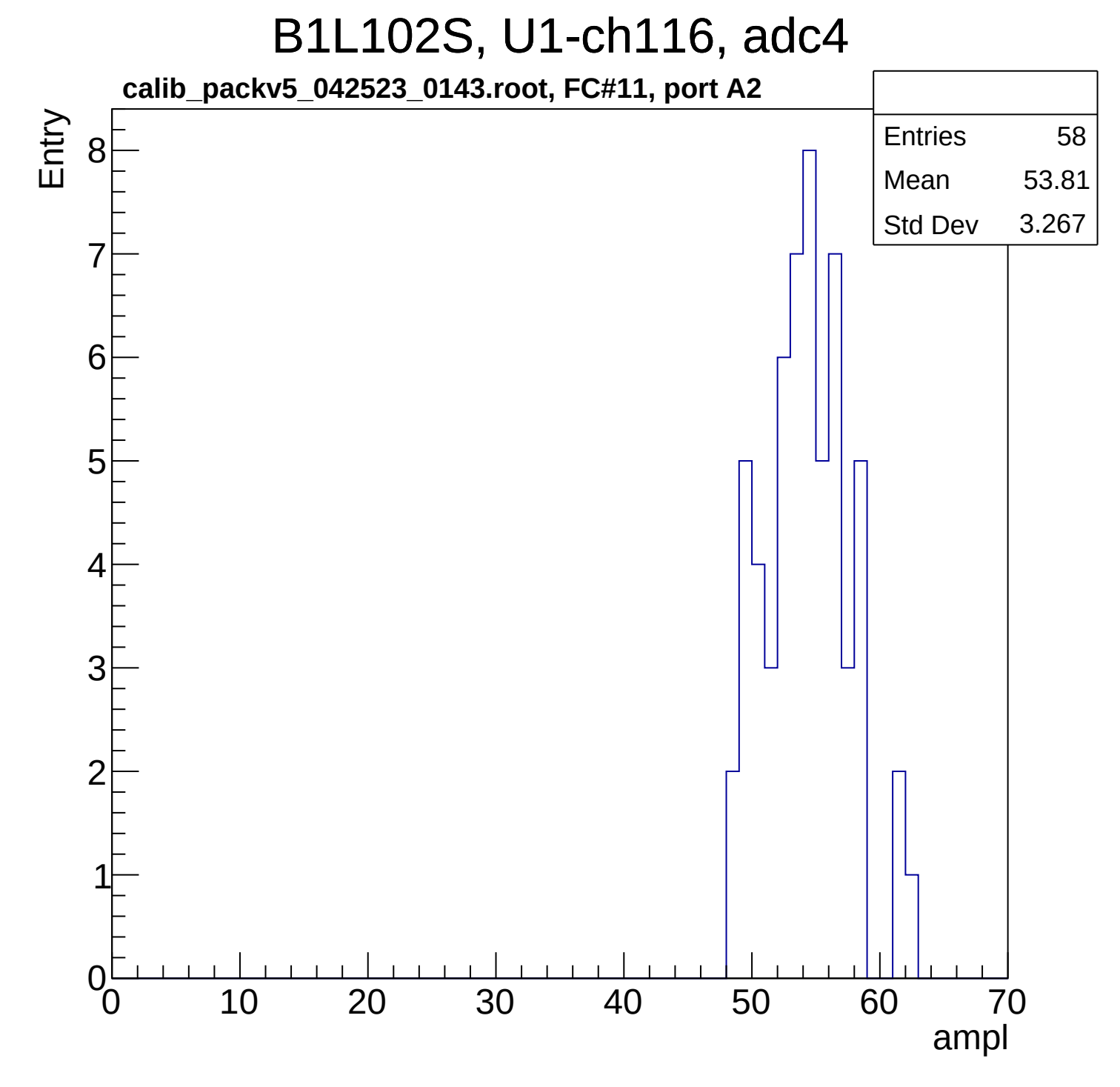

B1L102S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 42
Std Dev 4.252
Gaus mean : 42.6298
Gaus Width: 4.0087
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 47.78
Std Dev 4.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 53.81
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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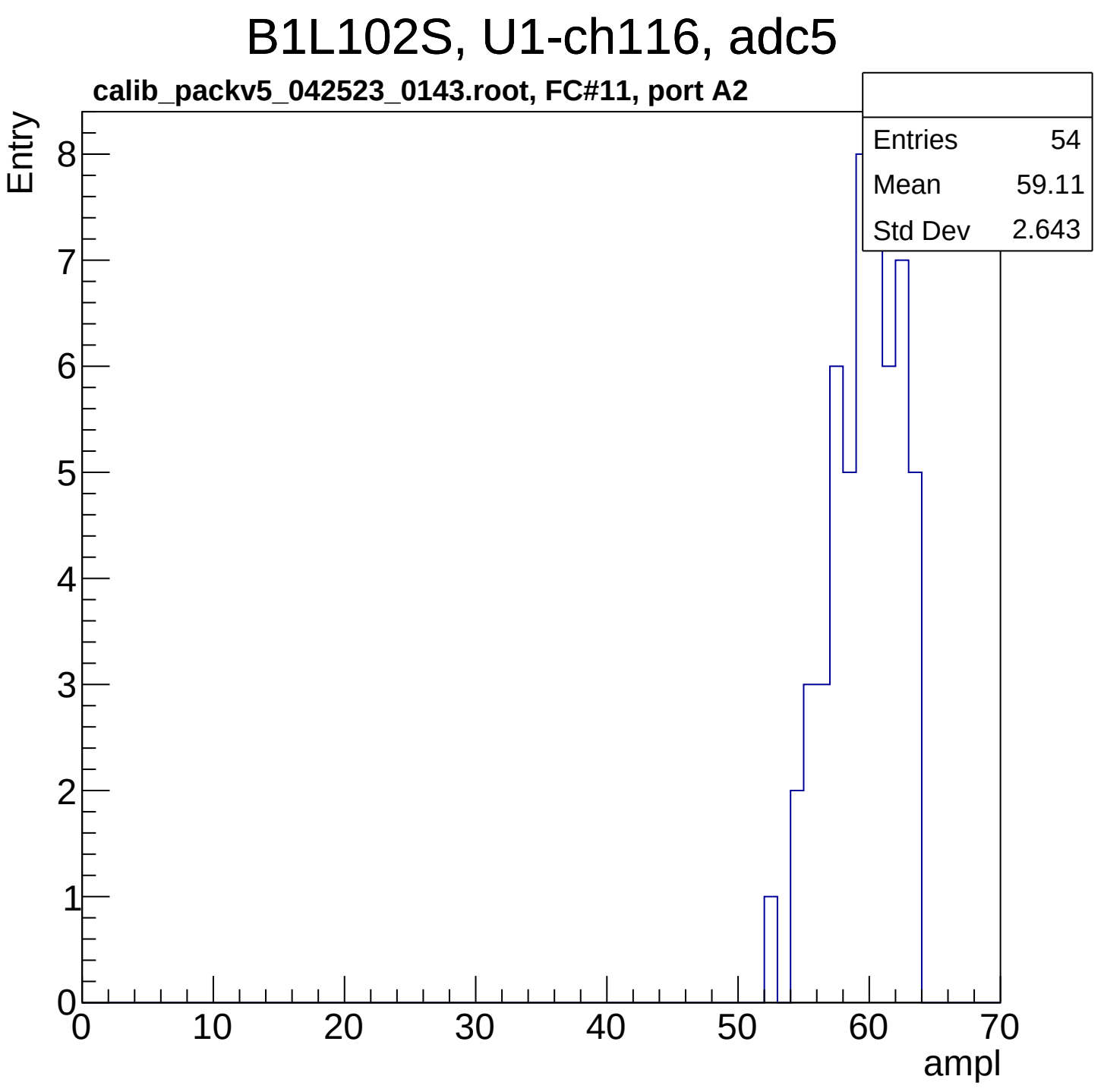
B1L102S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 59.11
Std Dev 2.643
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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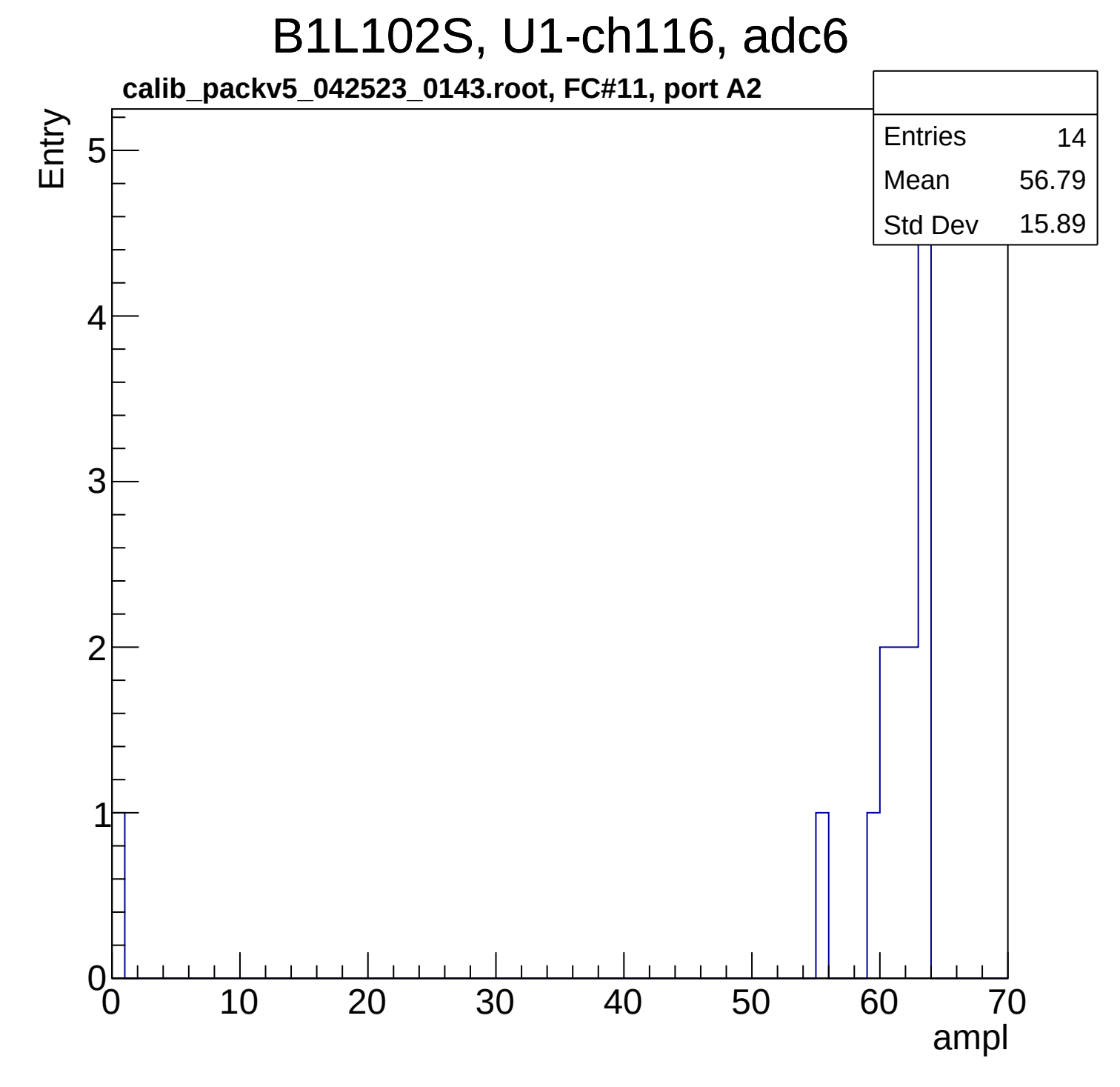
B1L102S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 56.79
Std Dev 15.89
Entry
ampl
0
1
2
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0
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30
40
50
60
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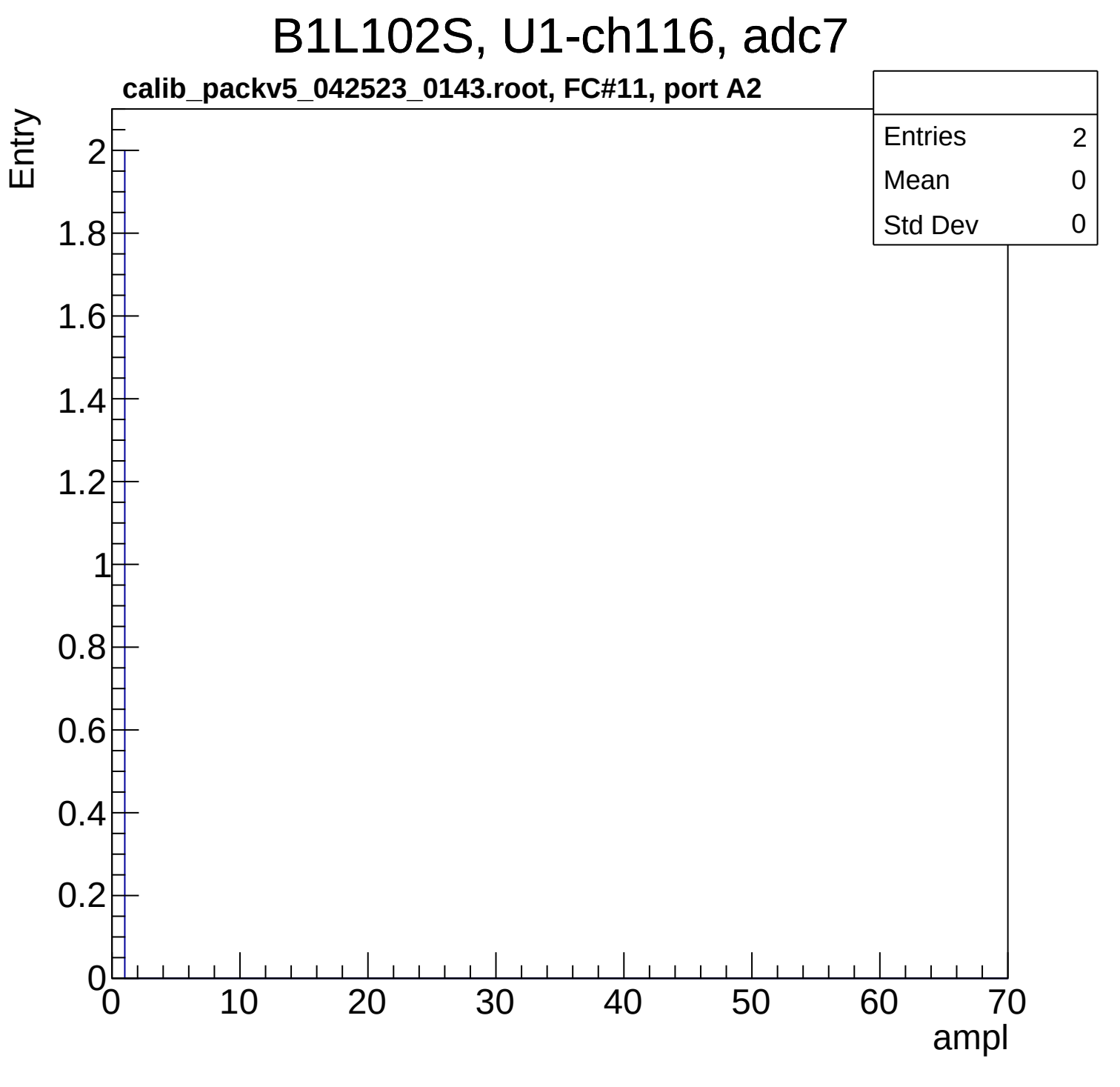
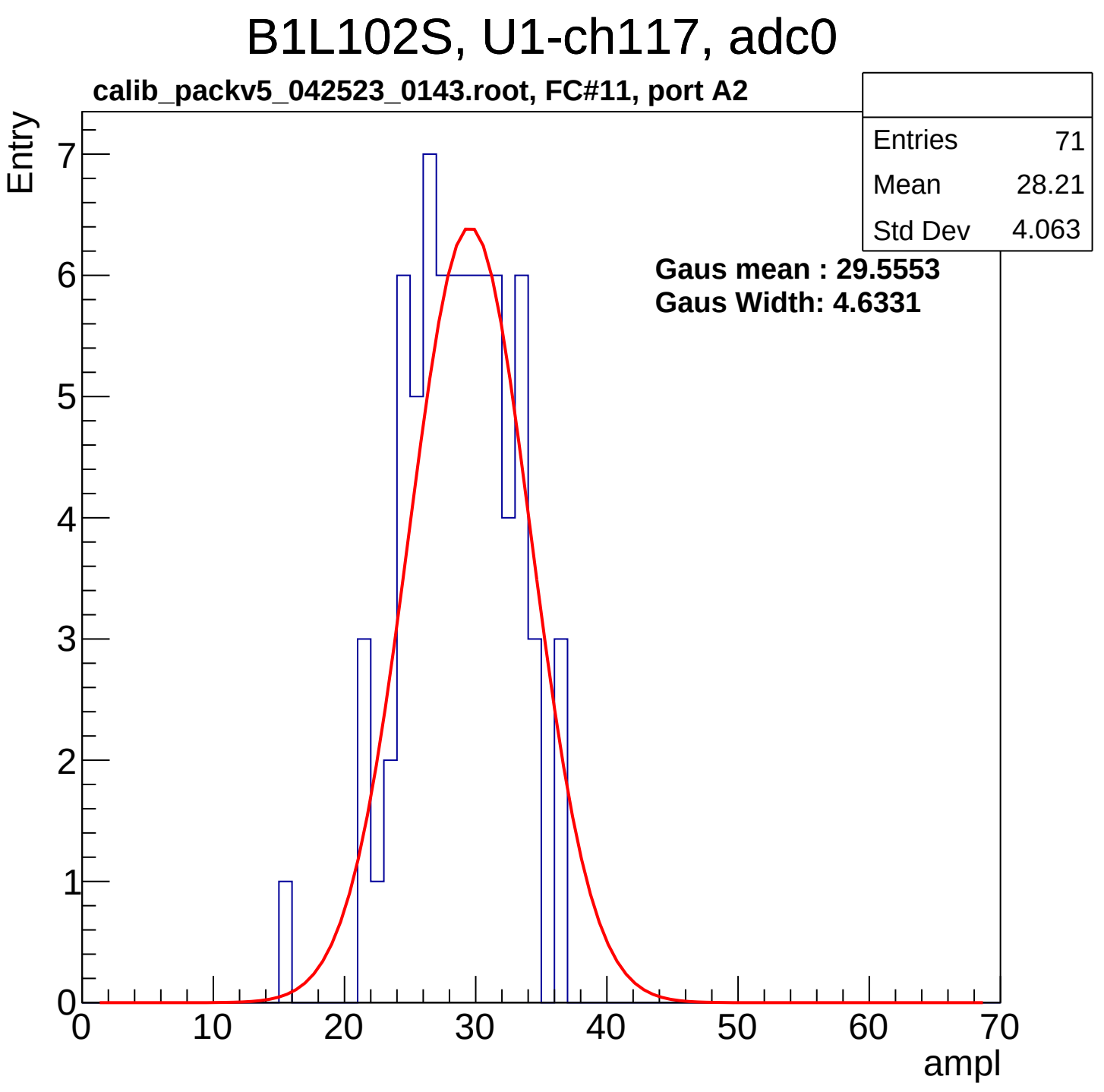
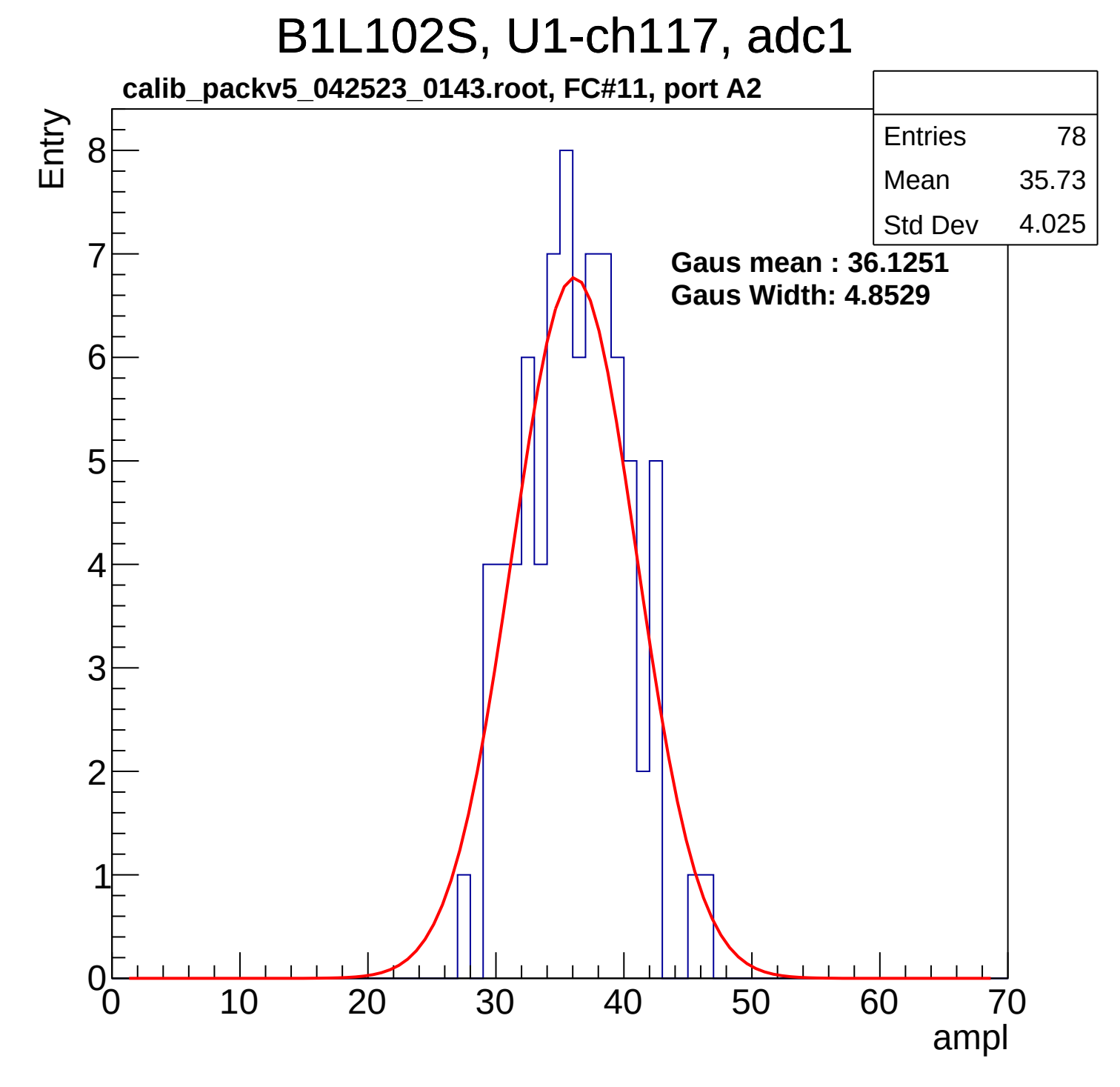
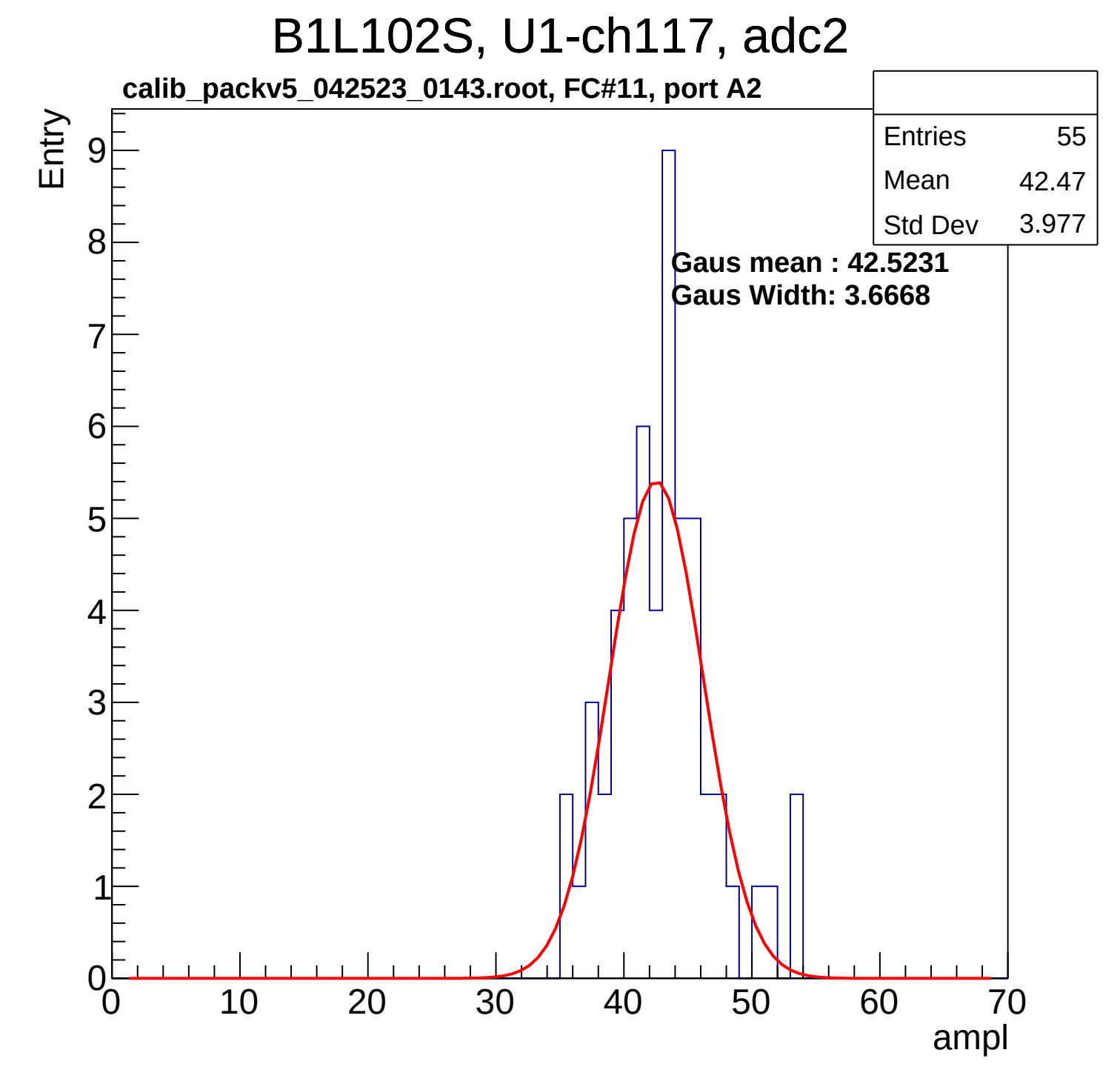
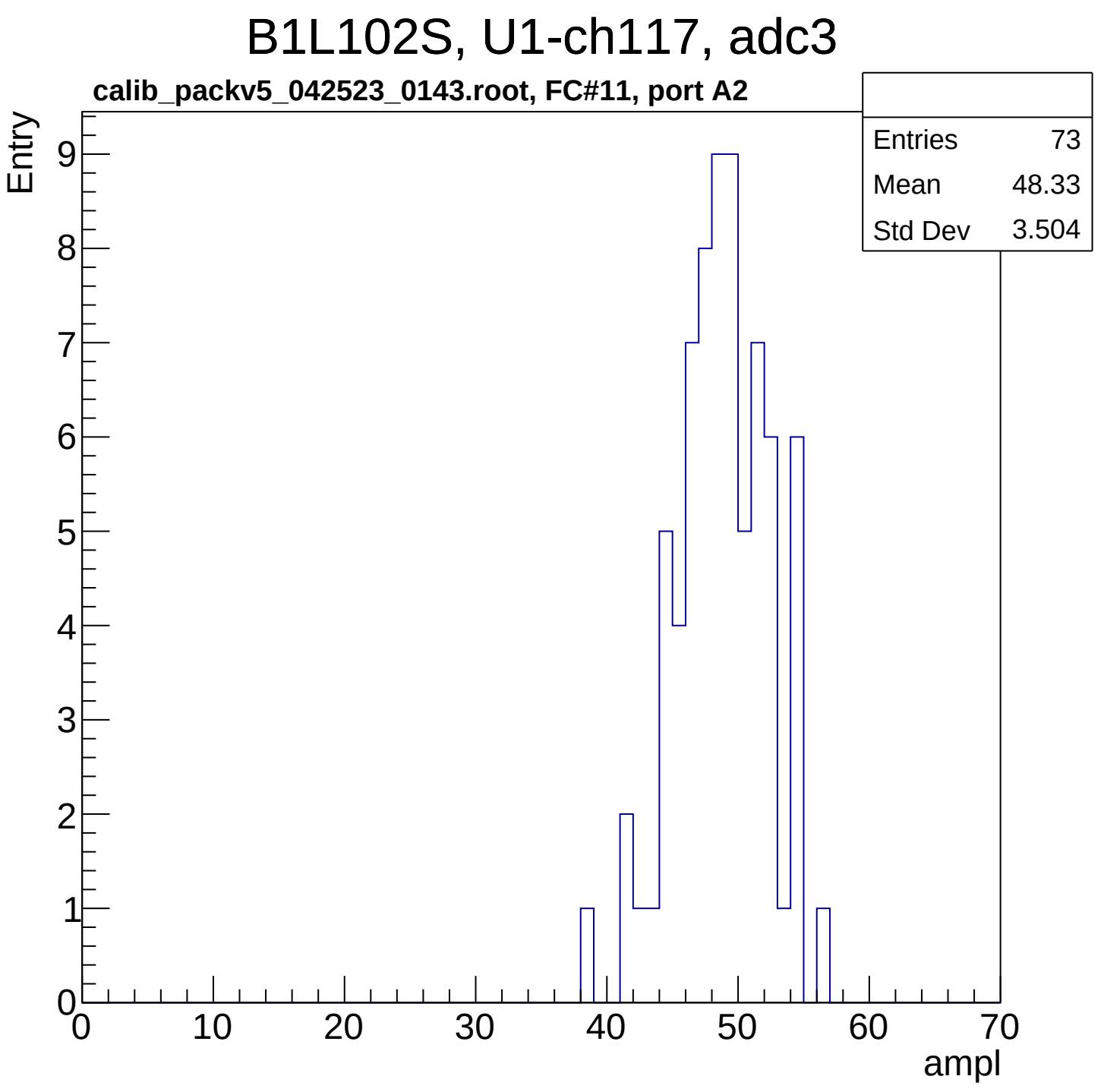

B1L102S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 28.21
Std Dev 4.063
Gaus mean : 29.5553
Gaus Width: 4.6331
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 78
Mean 35.73
Std Dev 4.025
Gaus mean : 36.1251
Gaus Width: 4.8529
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 42.47
Std Dev 3.977
Gaus mean : 42.5231
Gaus Width: 3.6668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 48.33
Std Dev 3.504
Entry
ampl
0
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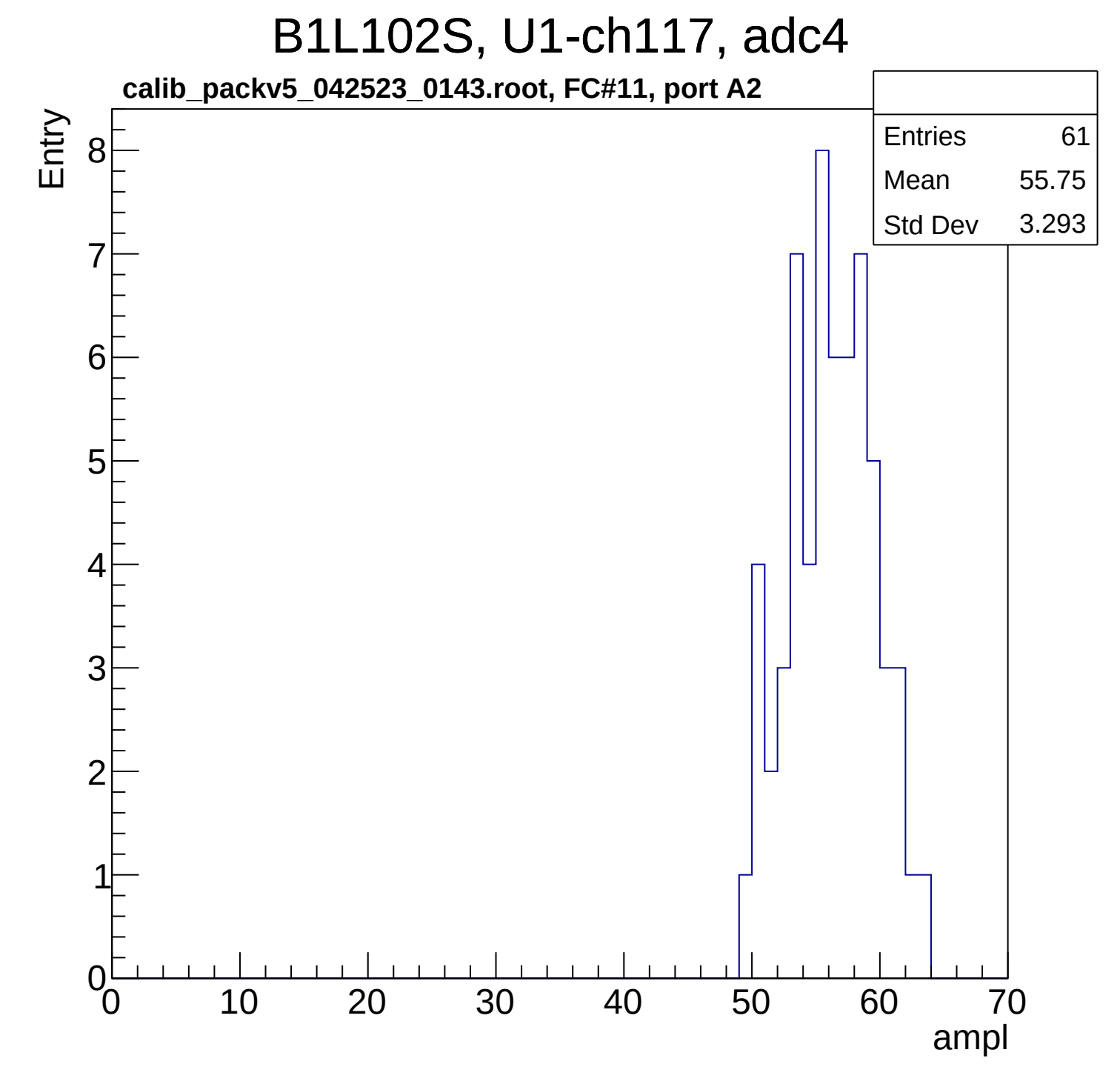
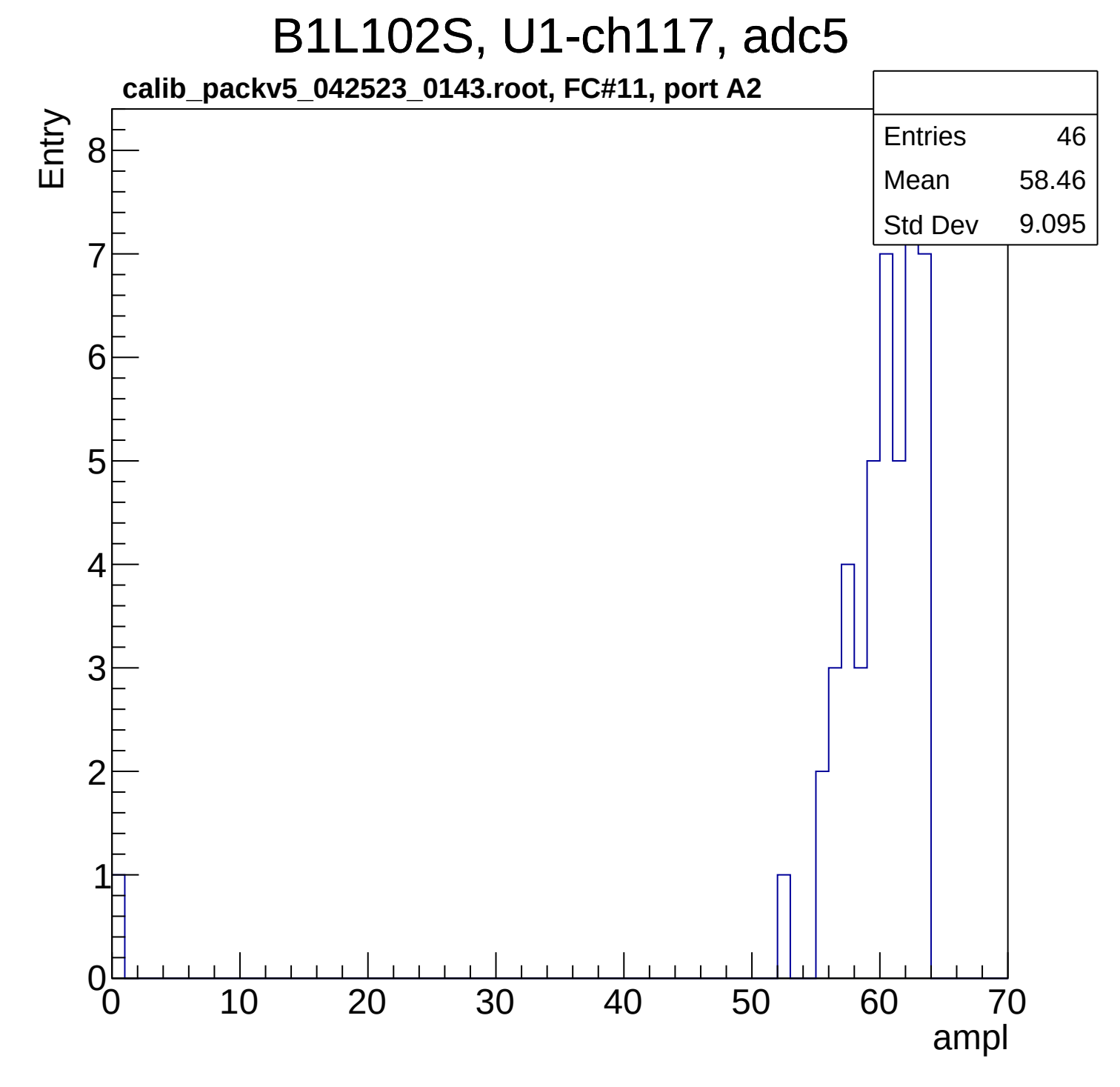
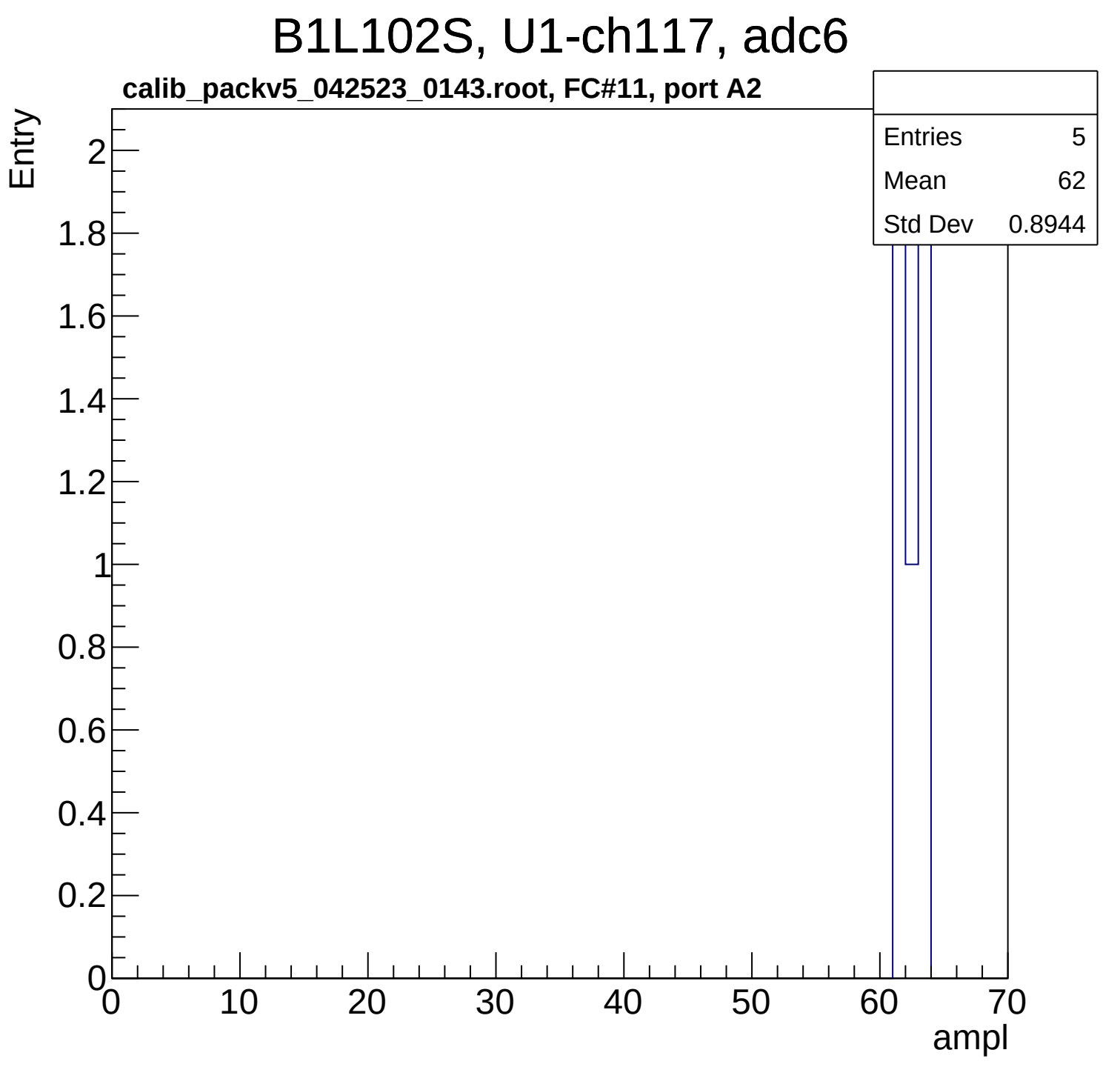
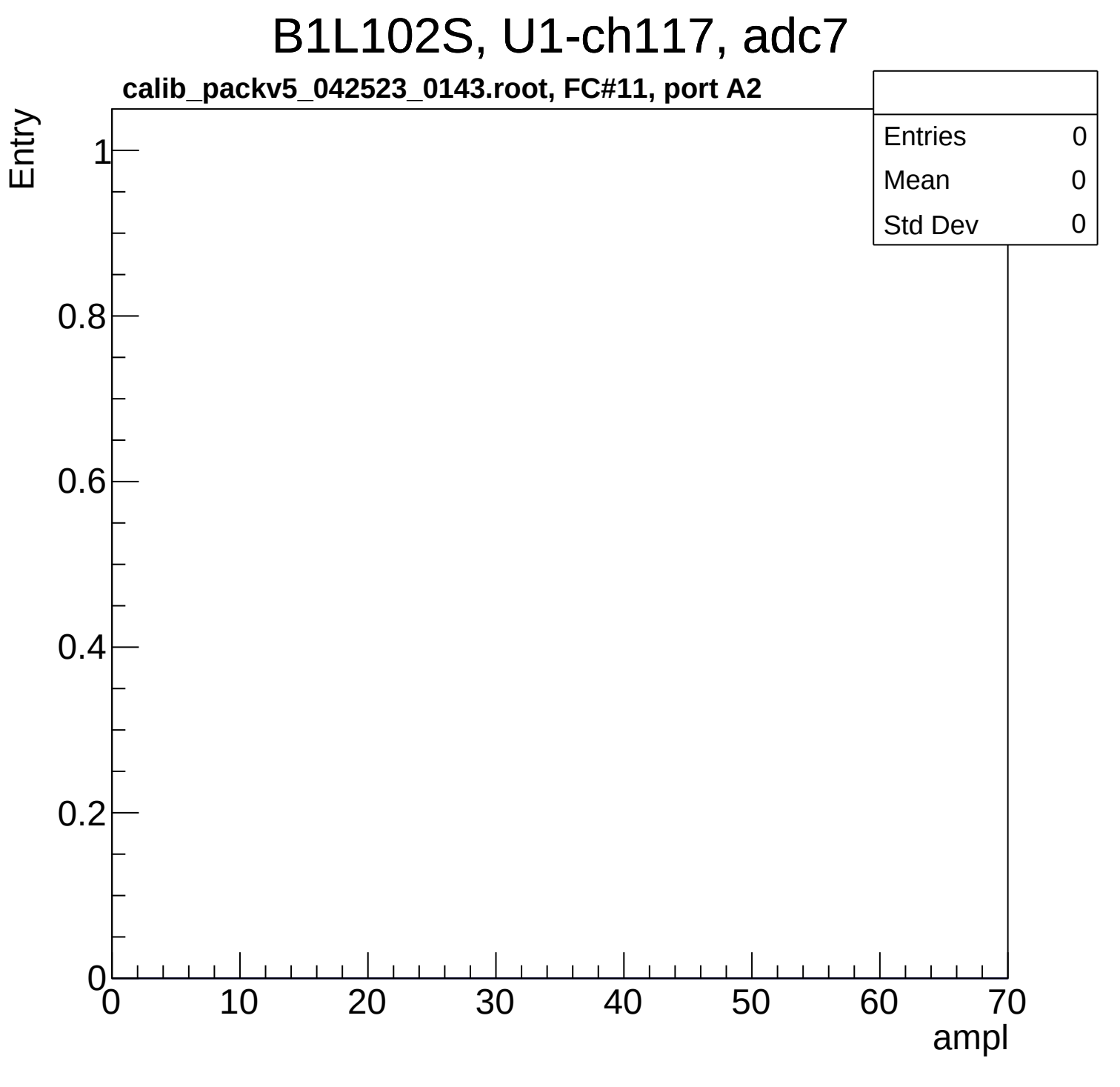
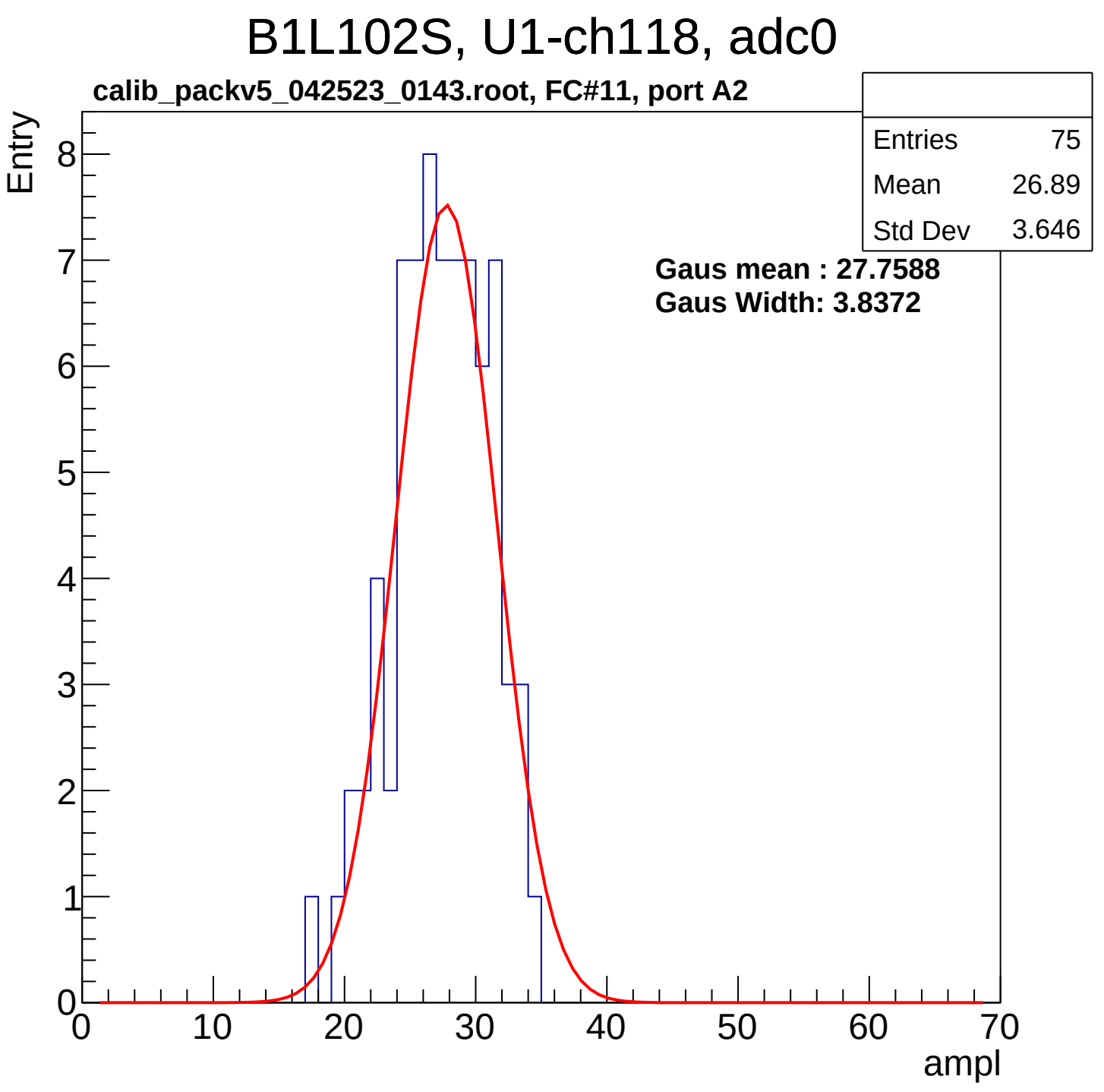
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

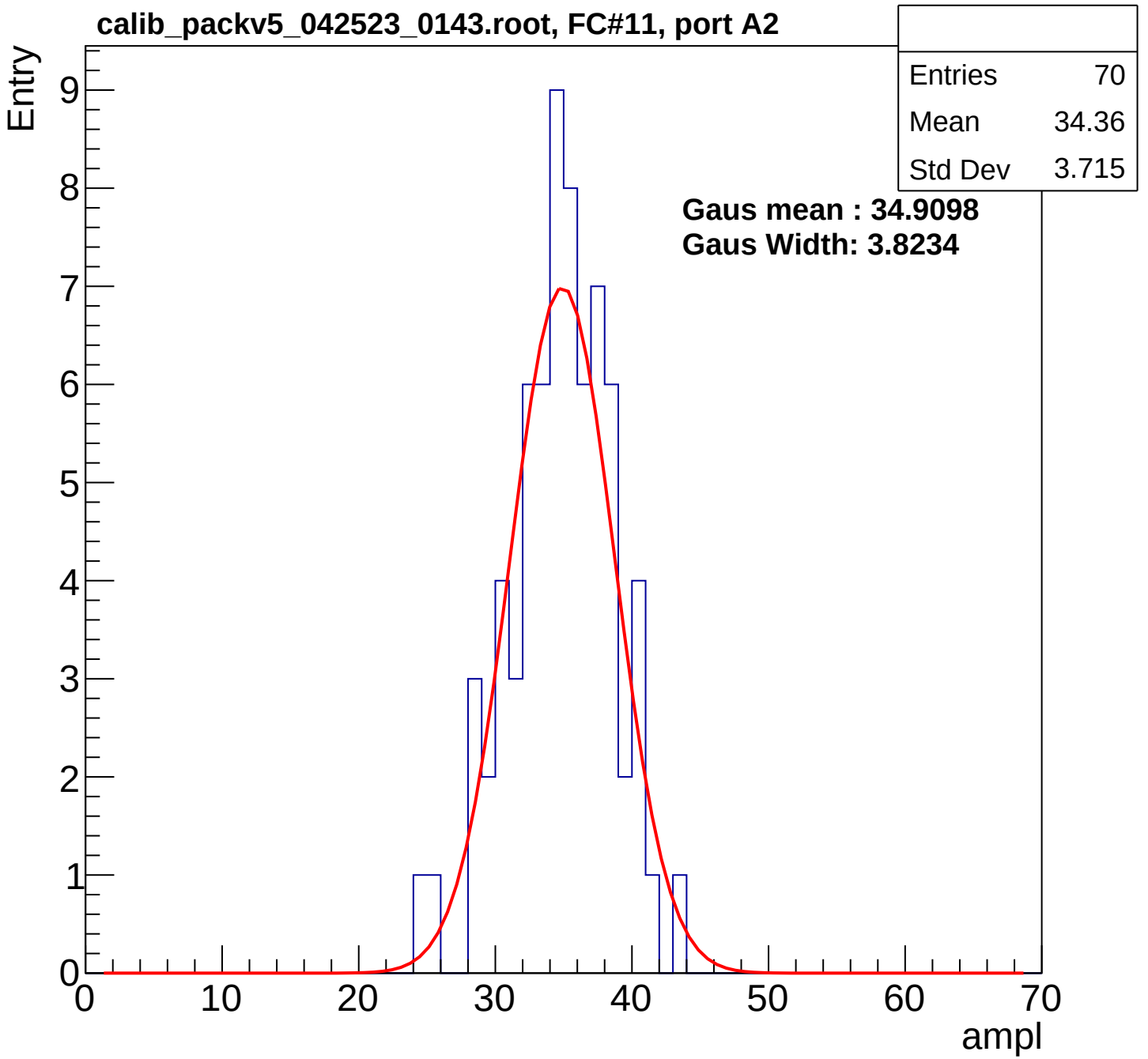

B1L102S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 61
Mean 55.75
Std Dev 3.293
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

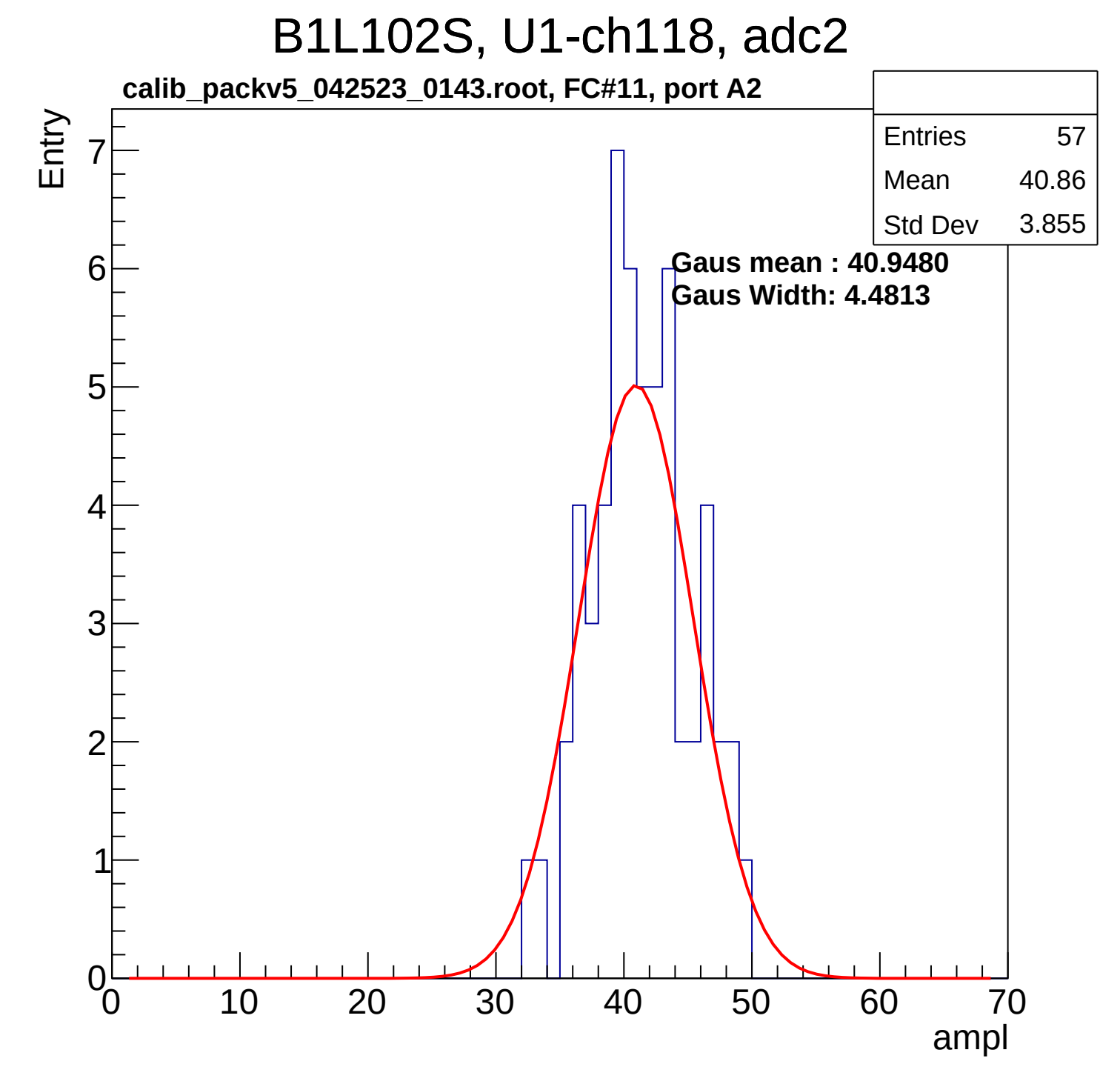

B1L102S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 46
Mean 58.46
Std Dev 9.095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

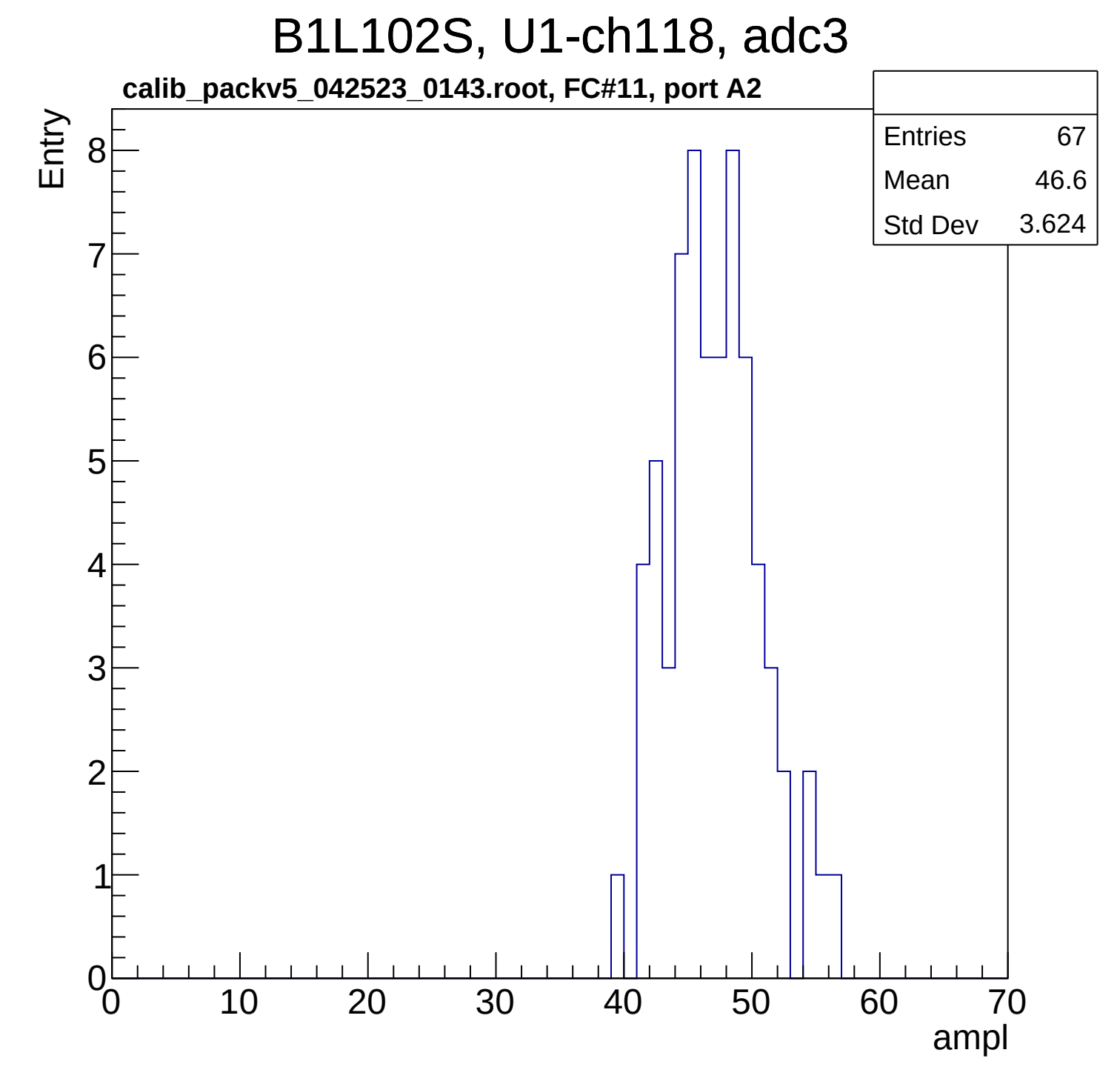

B1L102S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

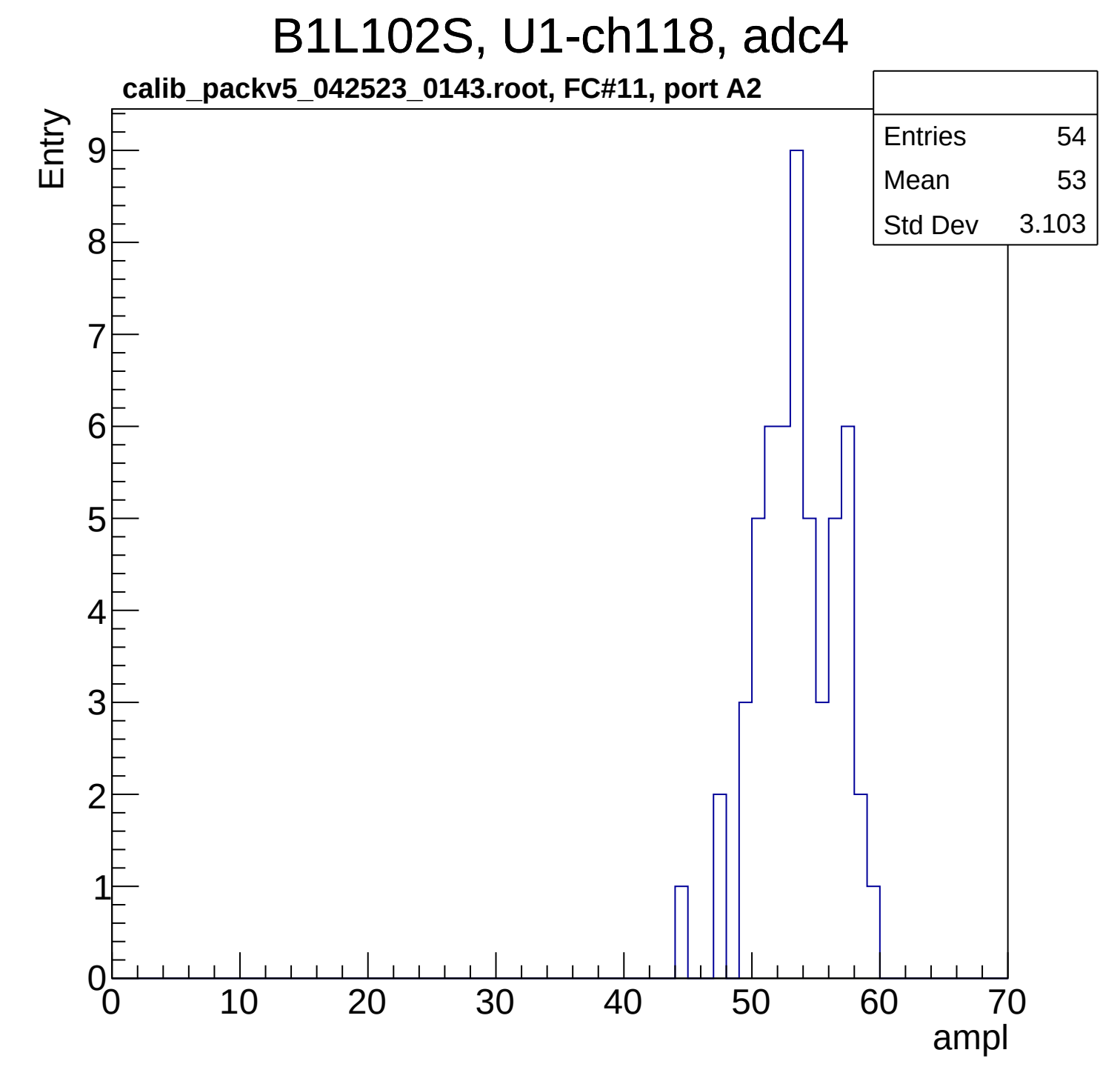

B1L102S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

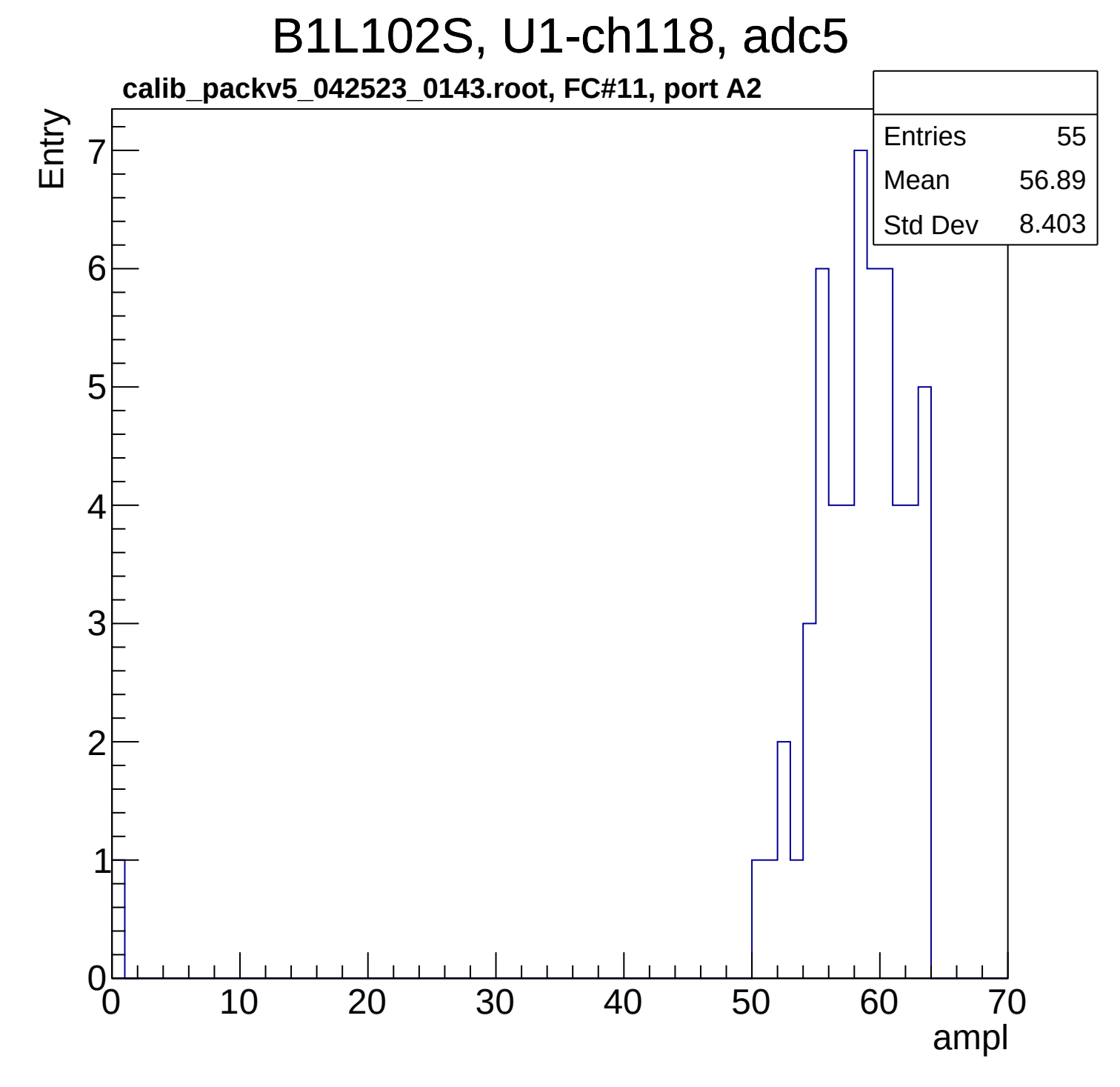

B1L102S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 75
Mean 26.89
Std Dev 3.646
Gaus mean : 27.7588
Gaus Width: 3.8372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 34.36
Std Dev 3.715
Gaus mean : 34.9098
Gaus Width: 3.8234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 40.86
Std Dev 3.855
Gaus mean : 40.9480
Gaus Width: 4.4813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 67
Mean 46.6
Std Dev 3.624
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 53
Std Dev 3.103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 56.89
Std Dev 8.403
Entry
ampl
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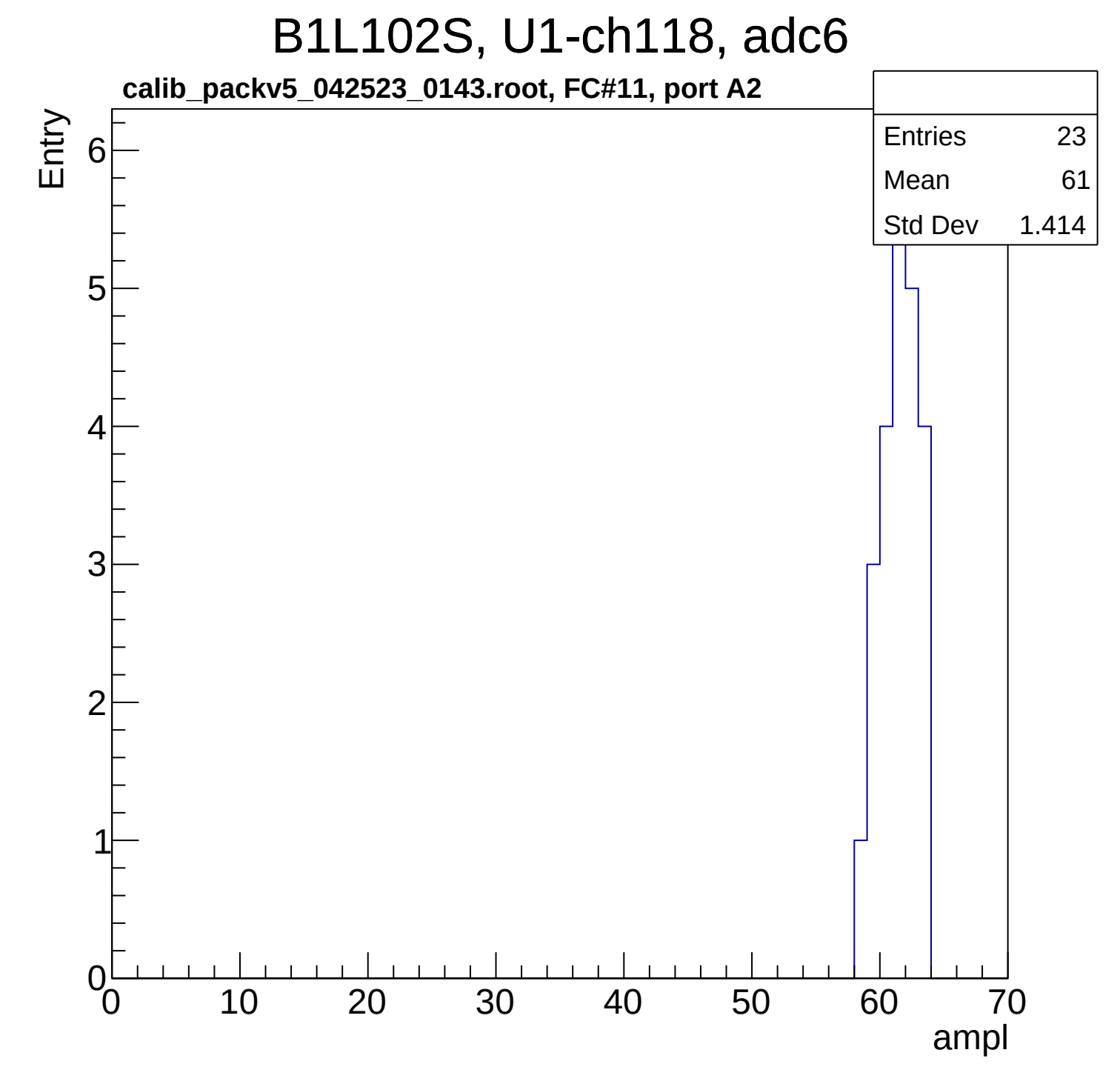
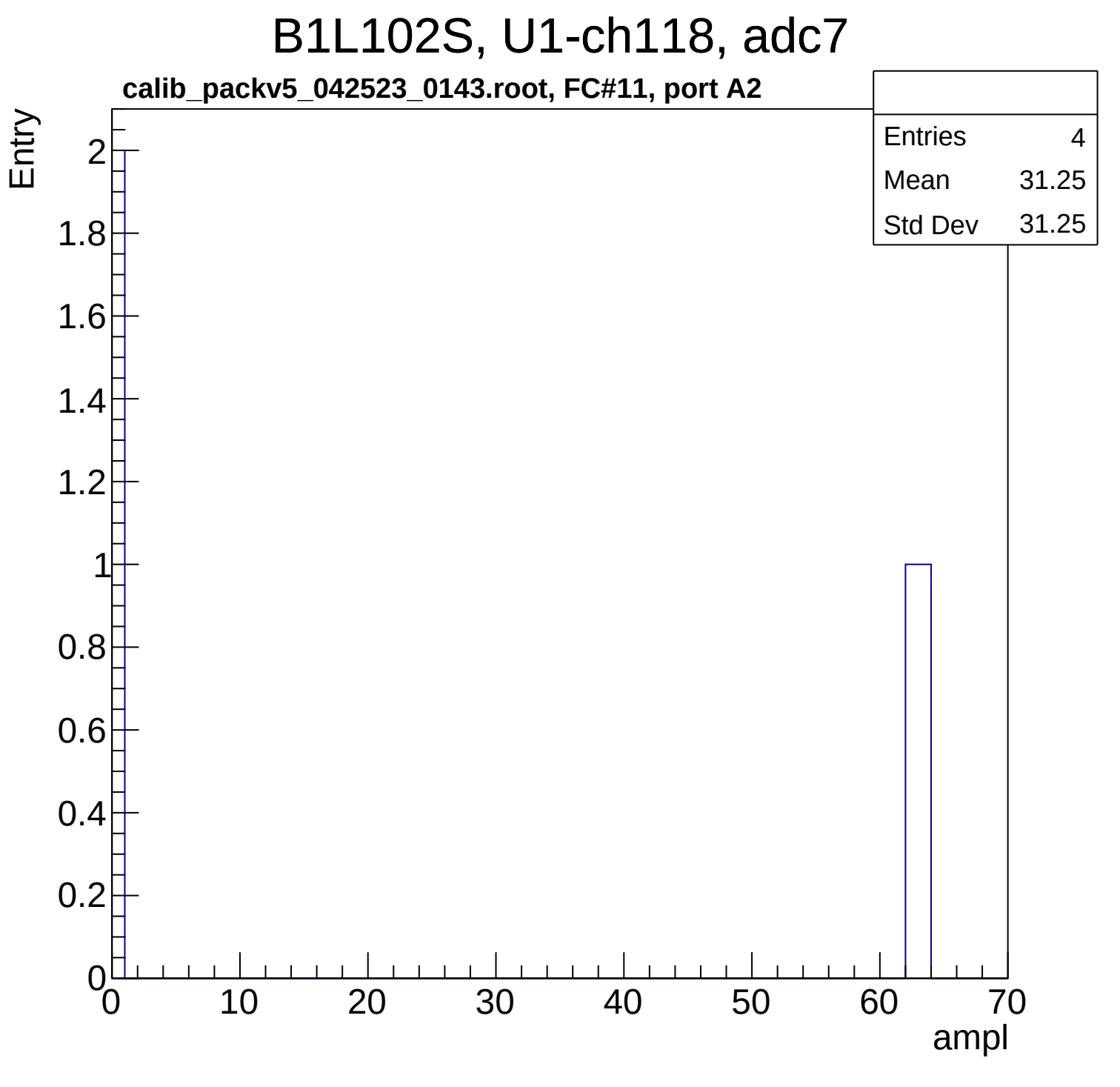
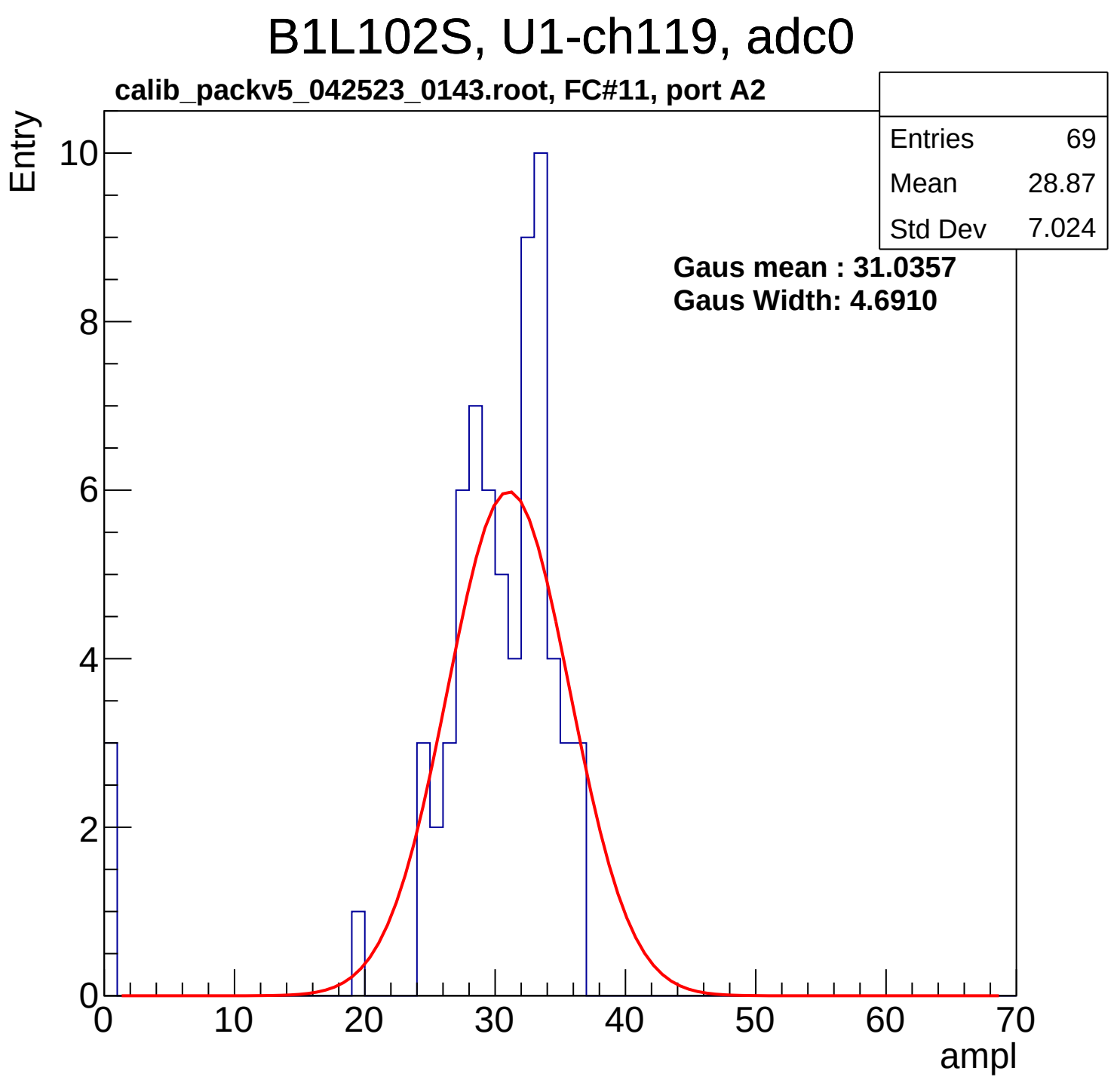
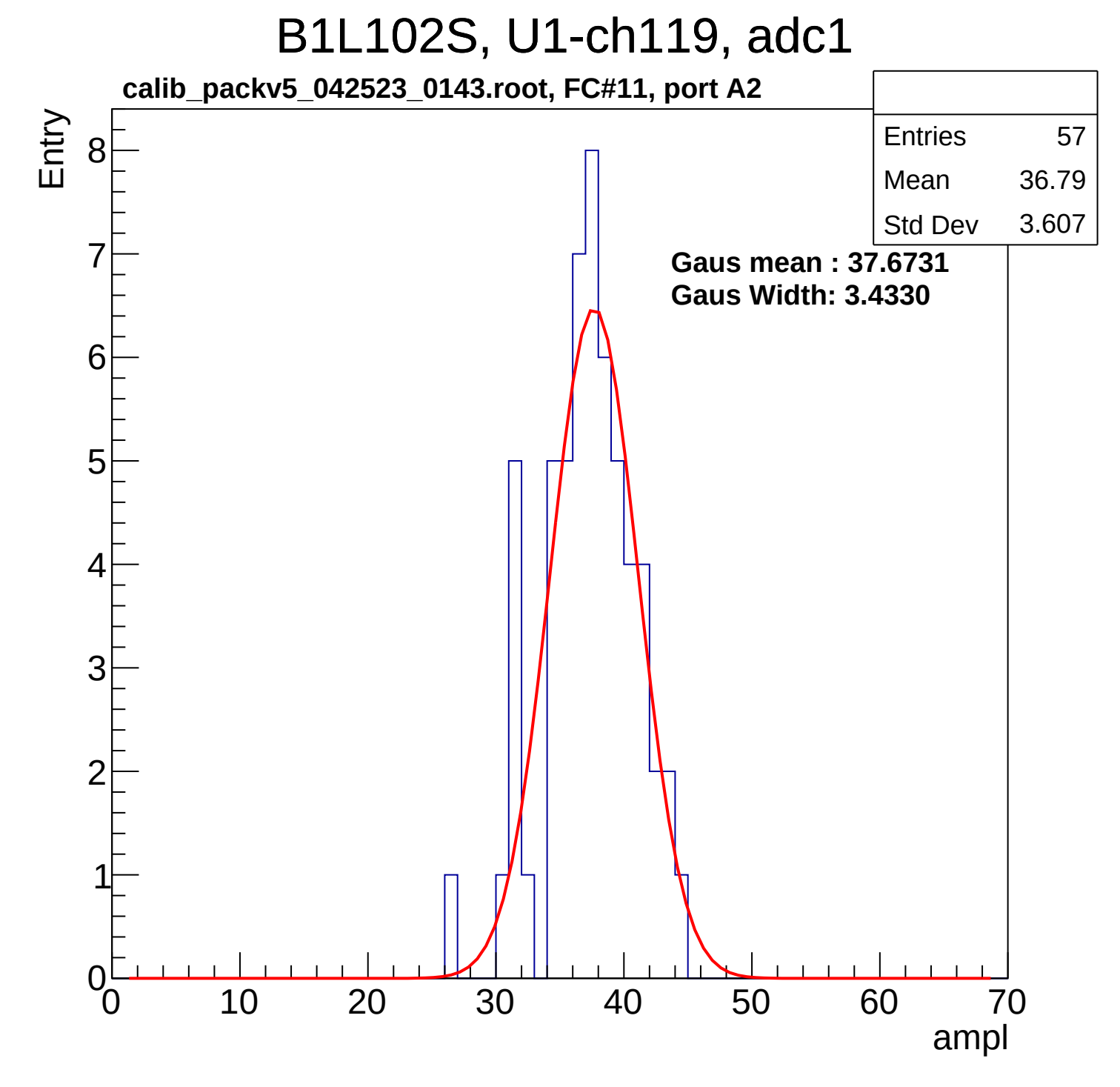
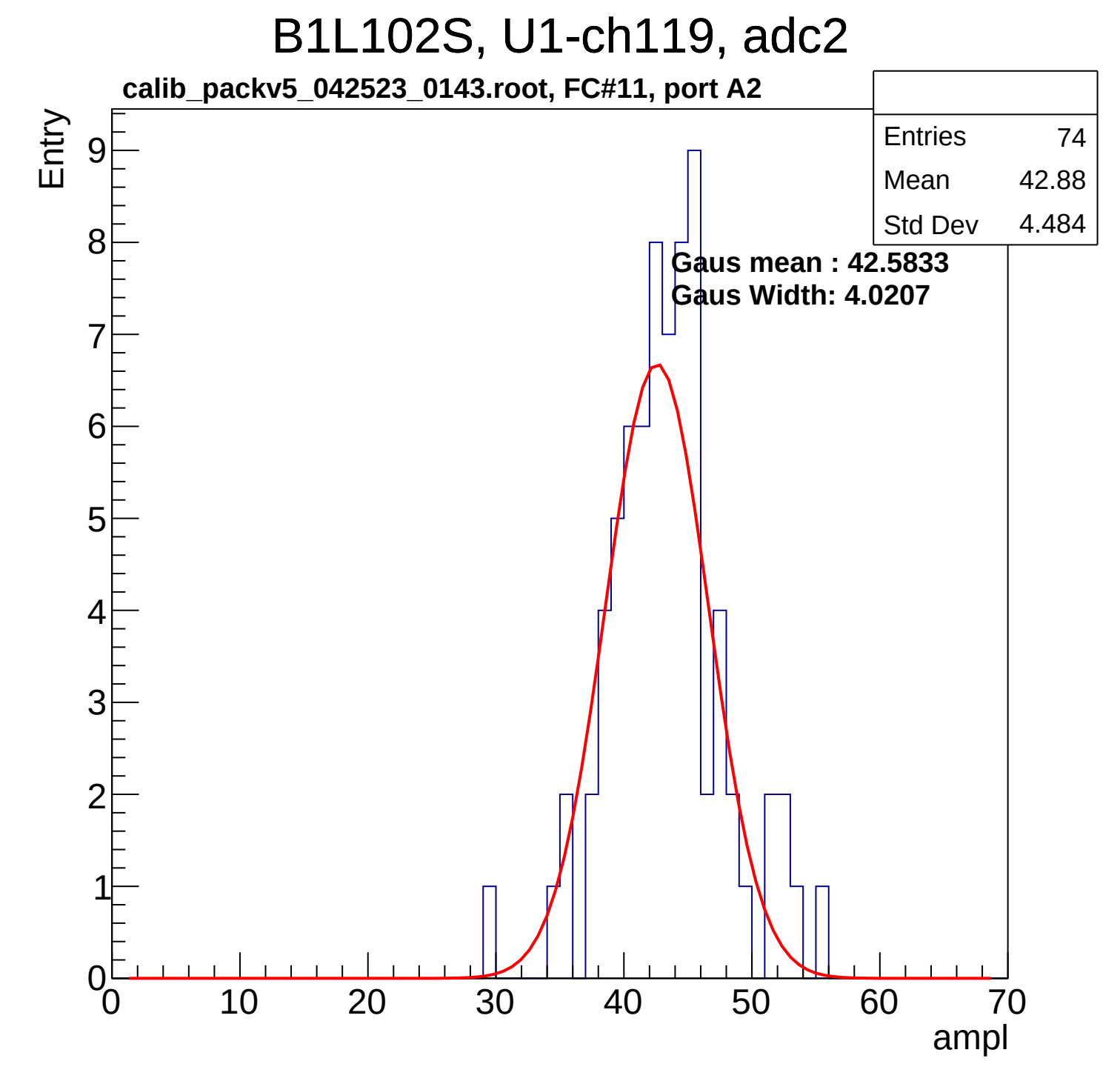
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

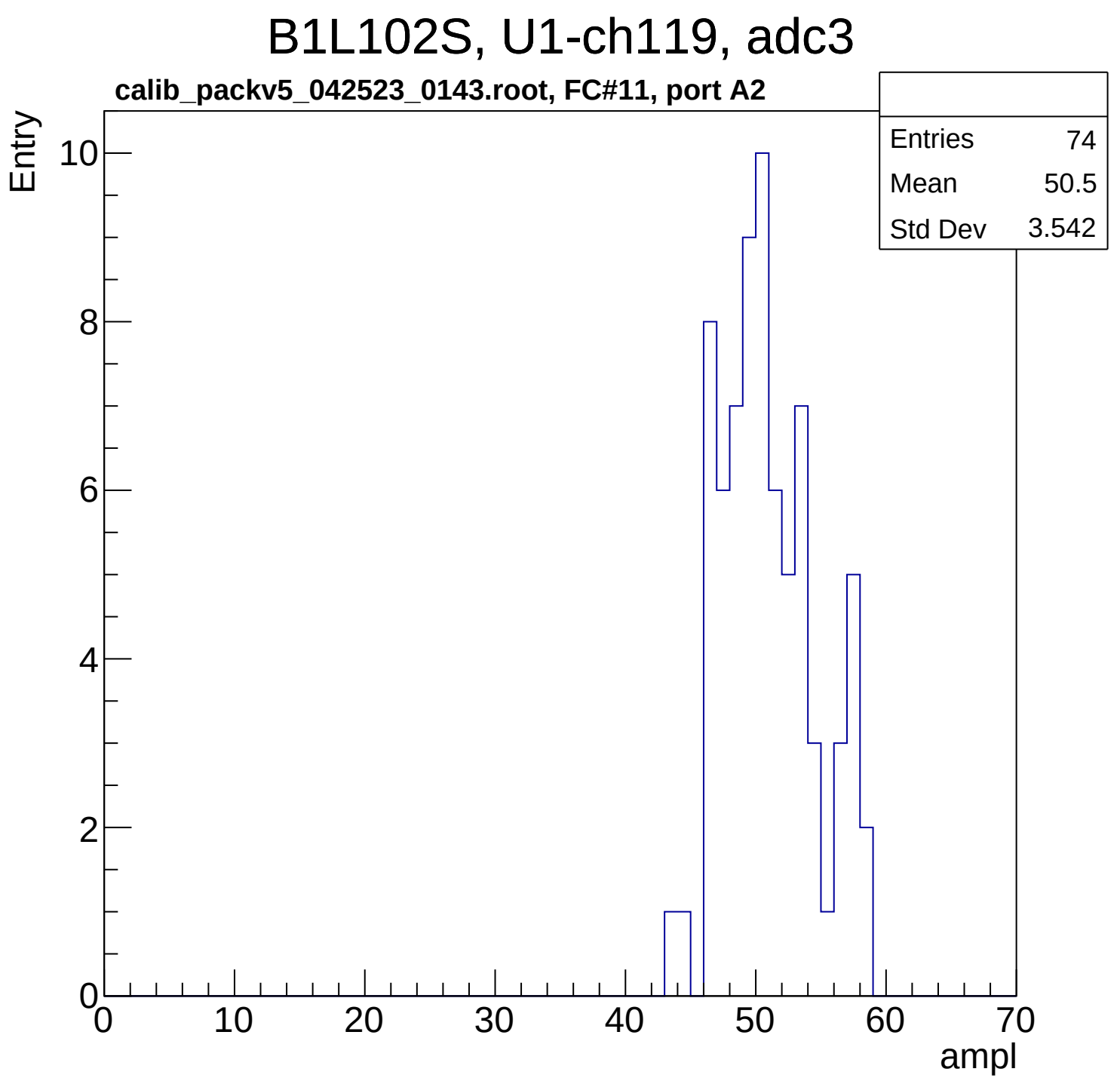

B1L102S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 23
Mean 61
Std Dev 1.414
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

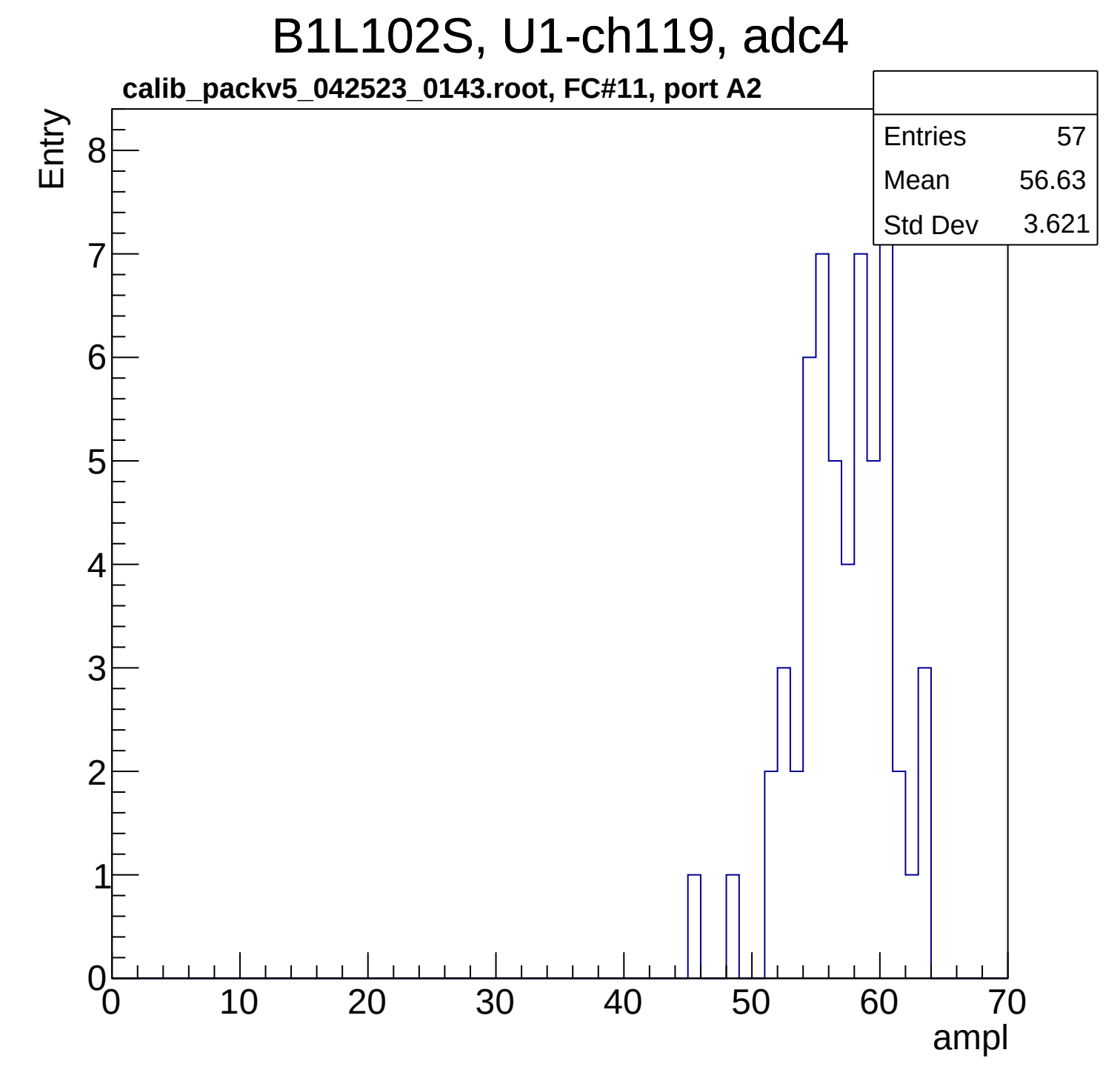

B1L102S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 28.87
Std Dev 7.024
Gaus mean : 31.0357
Gaus Width: 4.6910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 36.79
Std Dev 3.607
Gaus mean : 37.6731
Gaus Width: 3.4330
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 42.88
Std Dev 4.484
Gaus mean : 42.5833
Gaus Width: 4.0207
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 50.5
Std Dev 3.542
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 56.63
Std Dev 3.621
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
60
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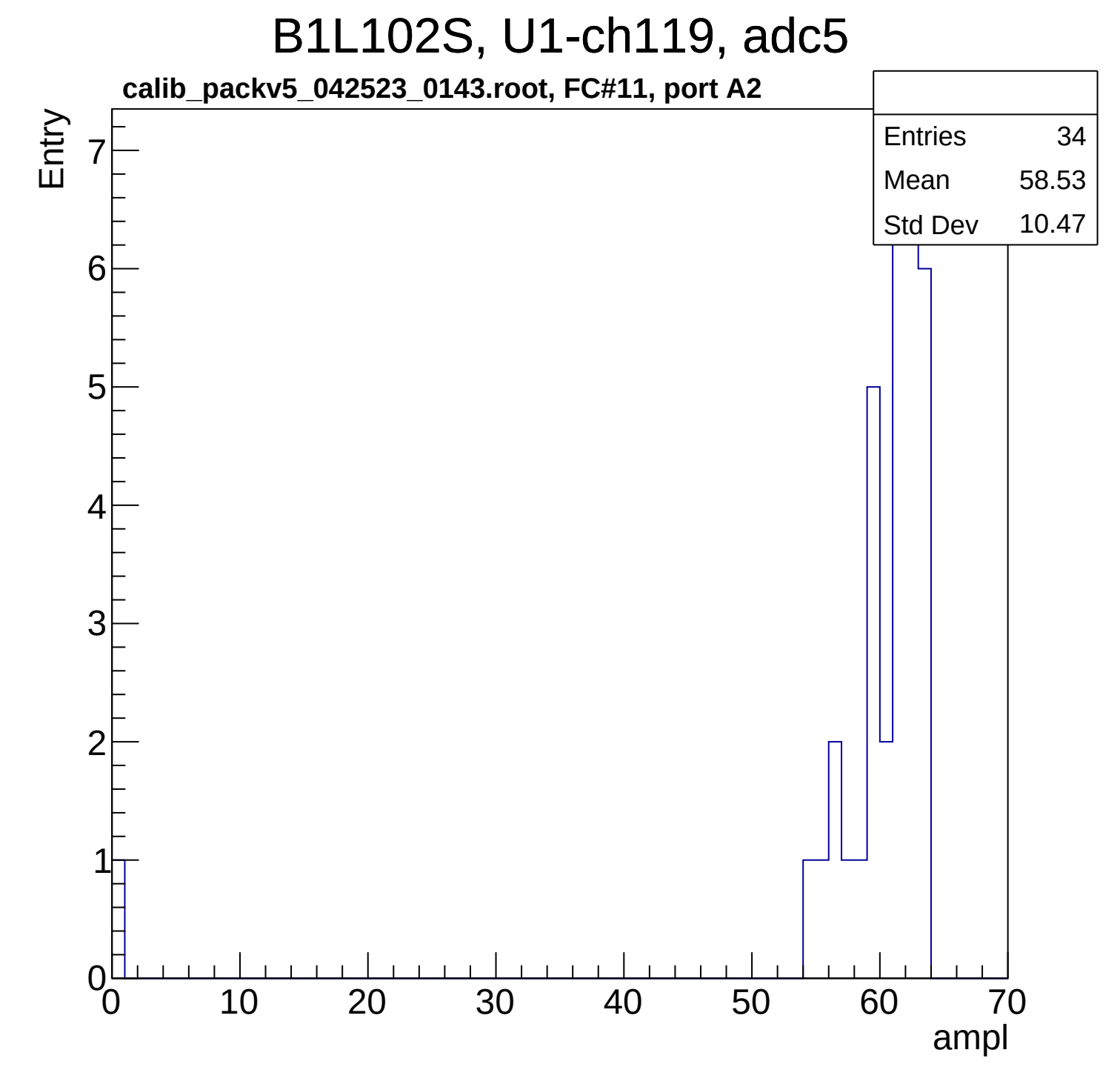
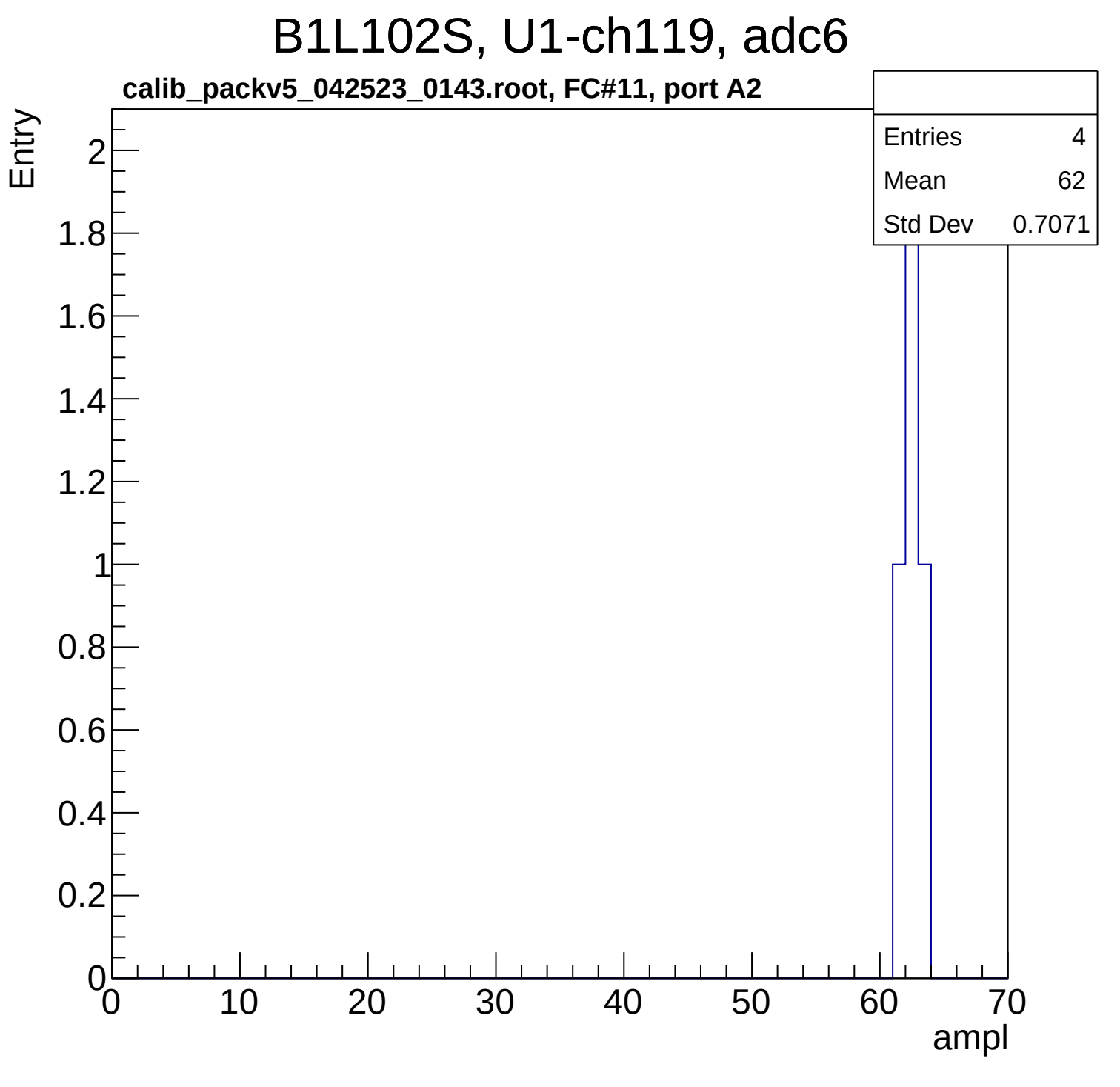
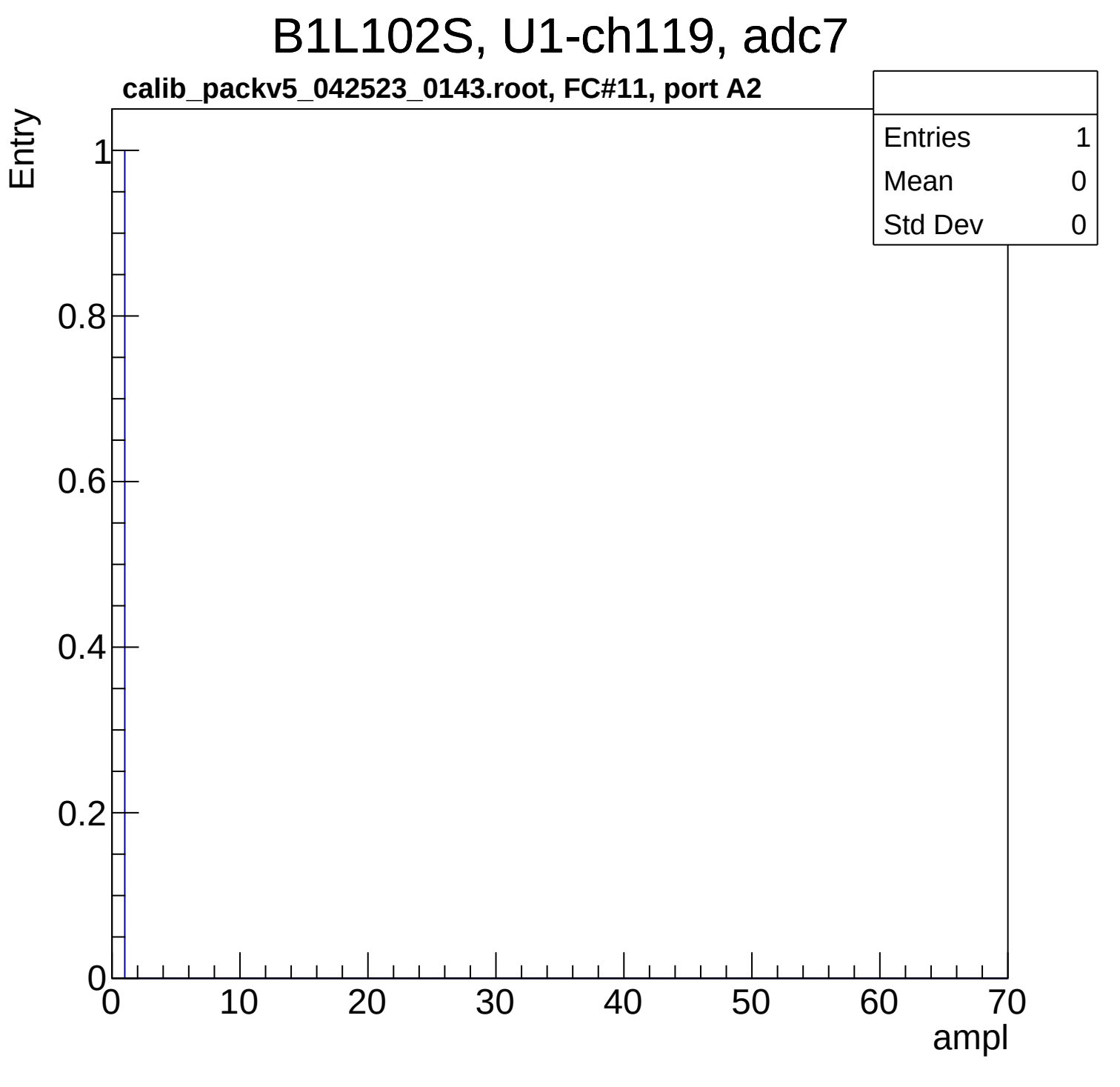
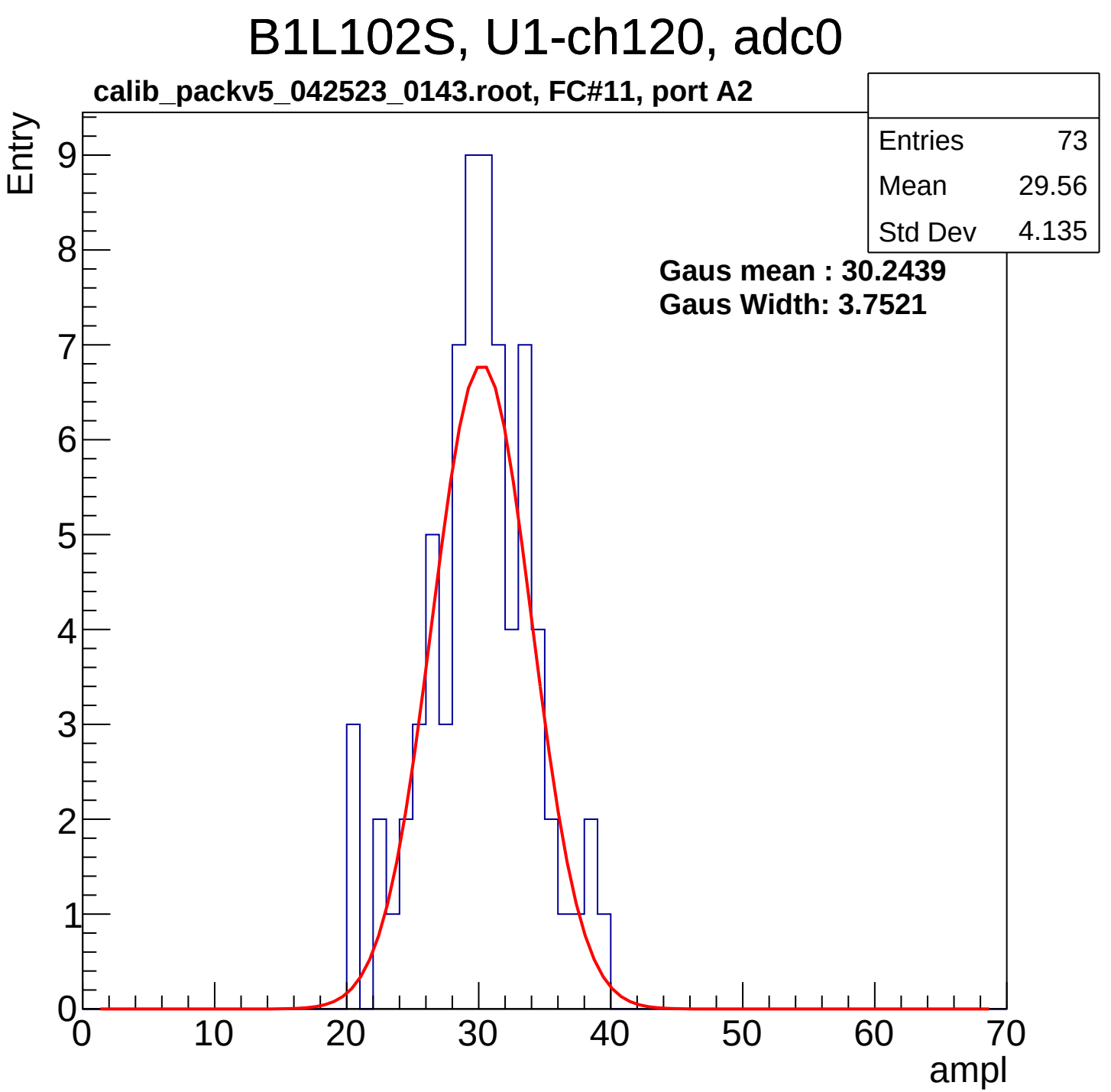
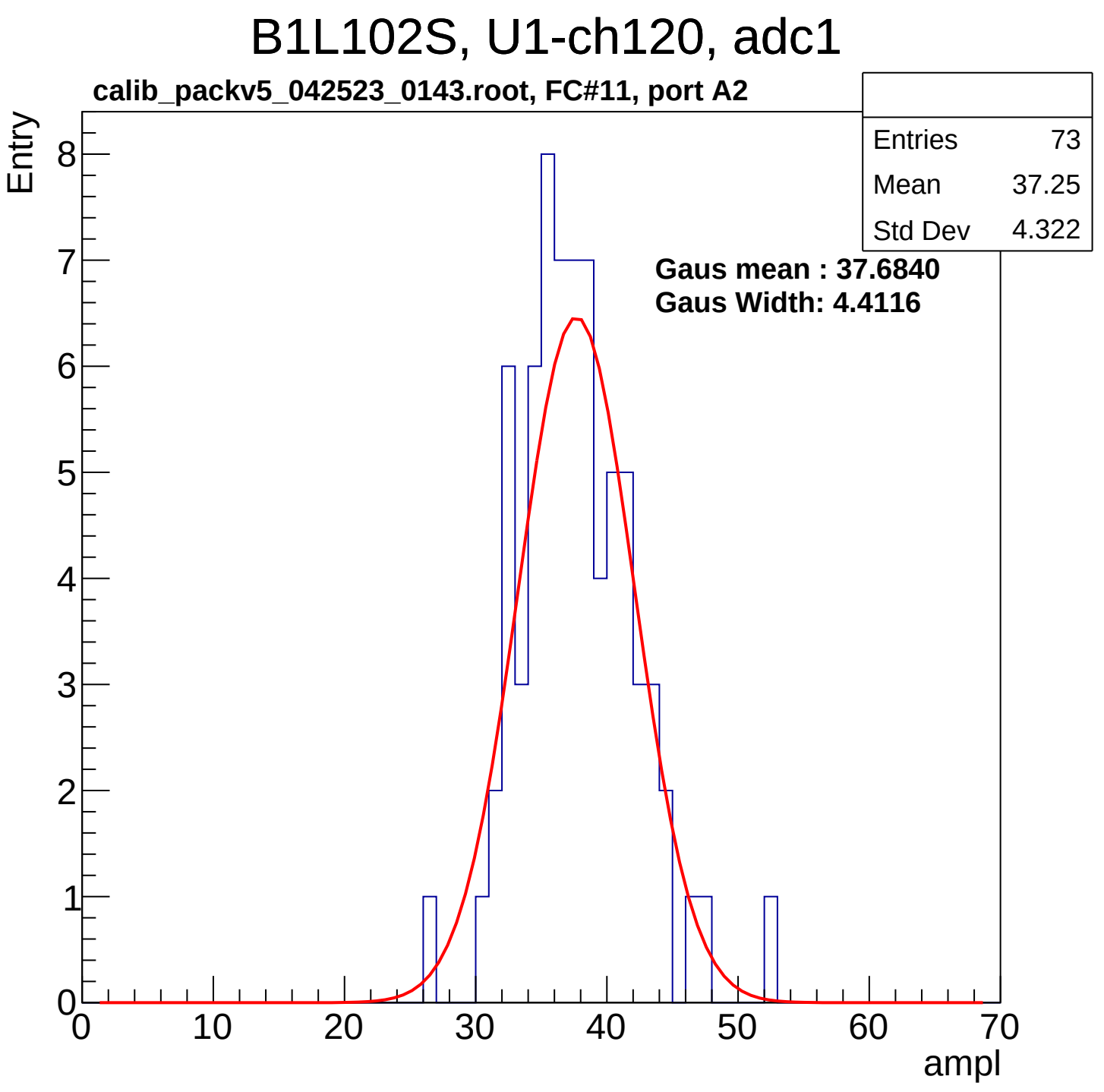
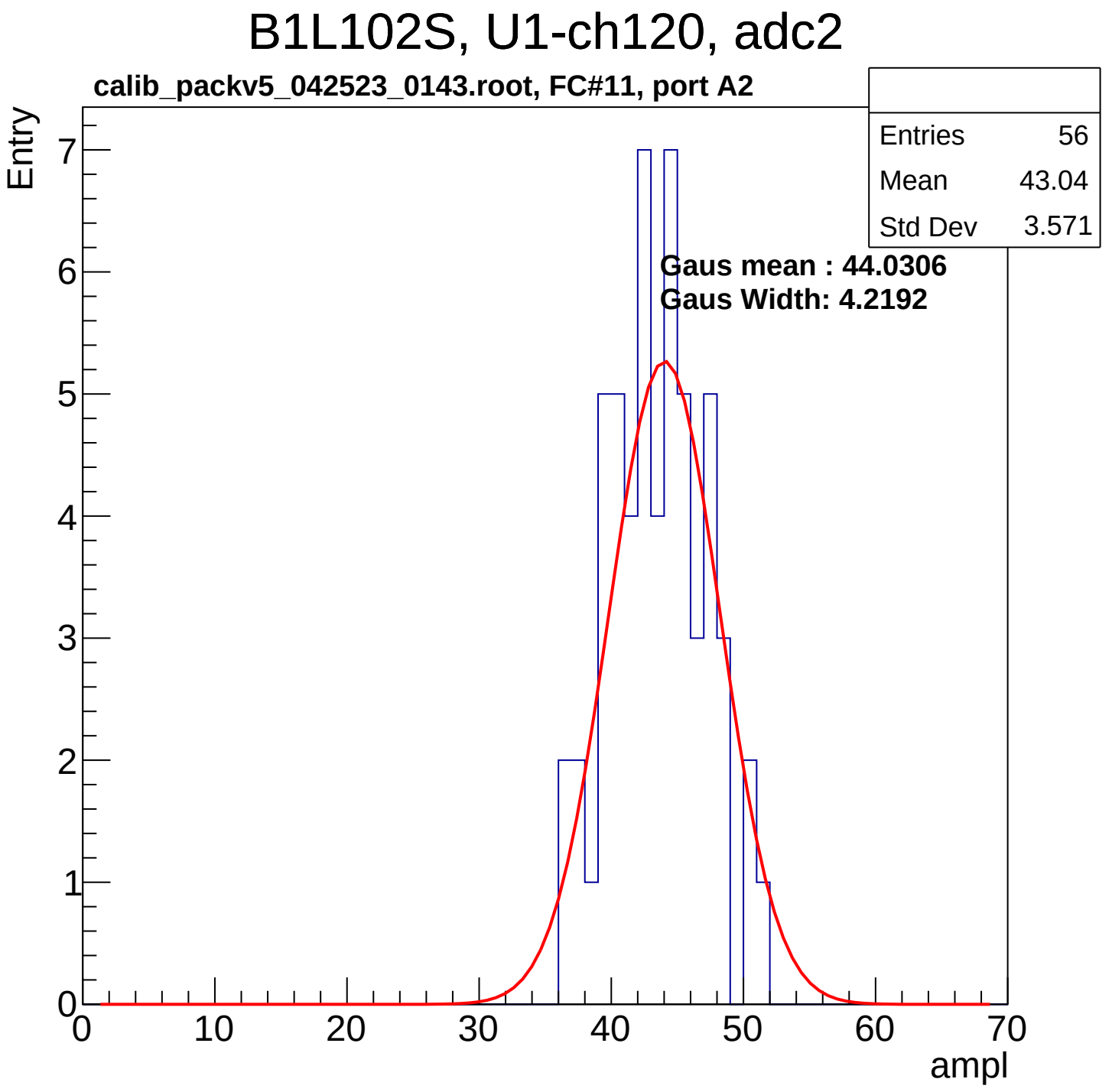

B1L102S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 34
Mean 58.53
Std Dev 10.47
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

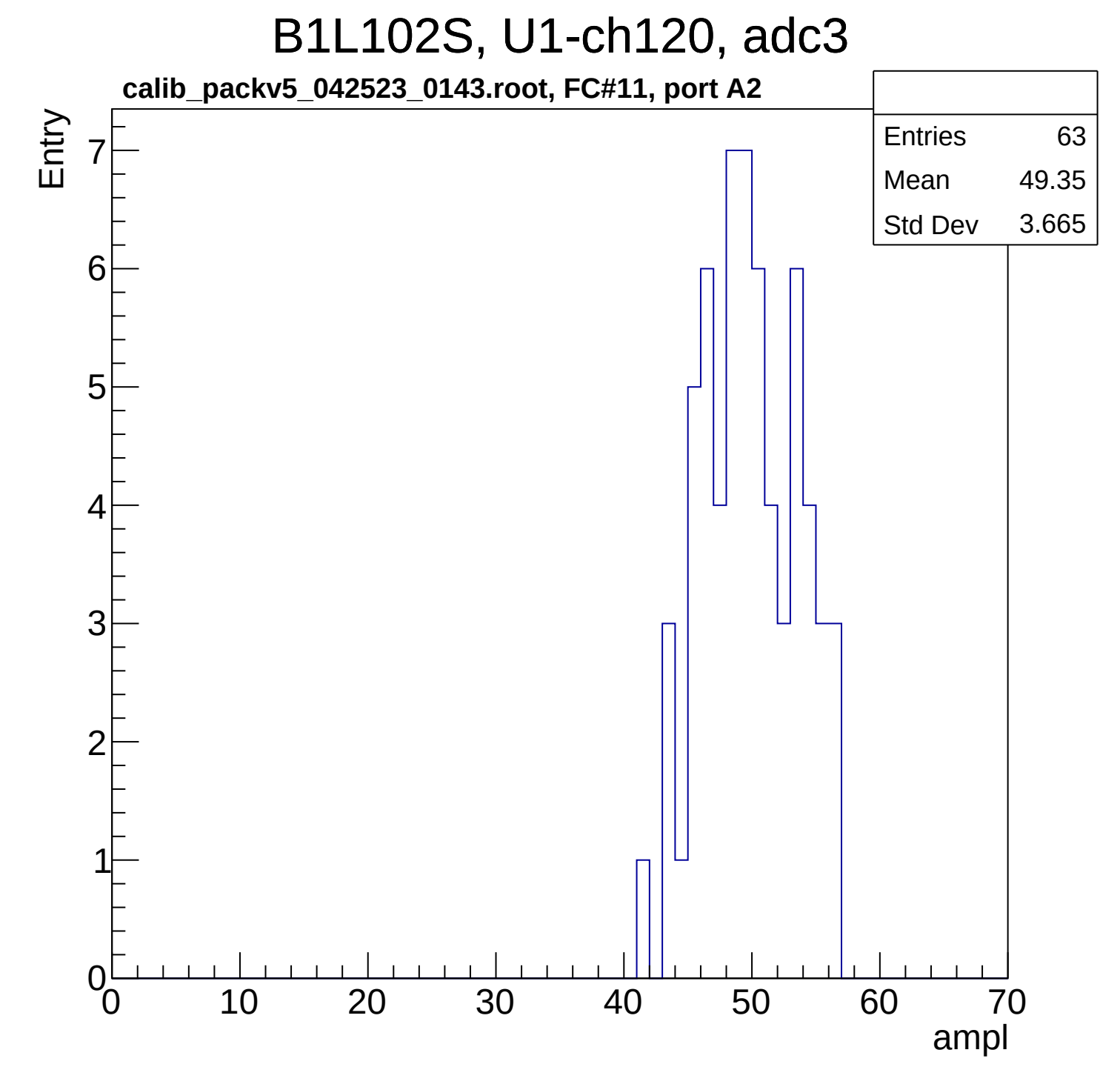

B1L102S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

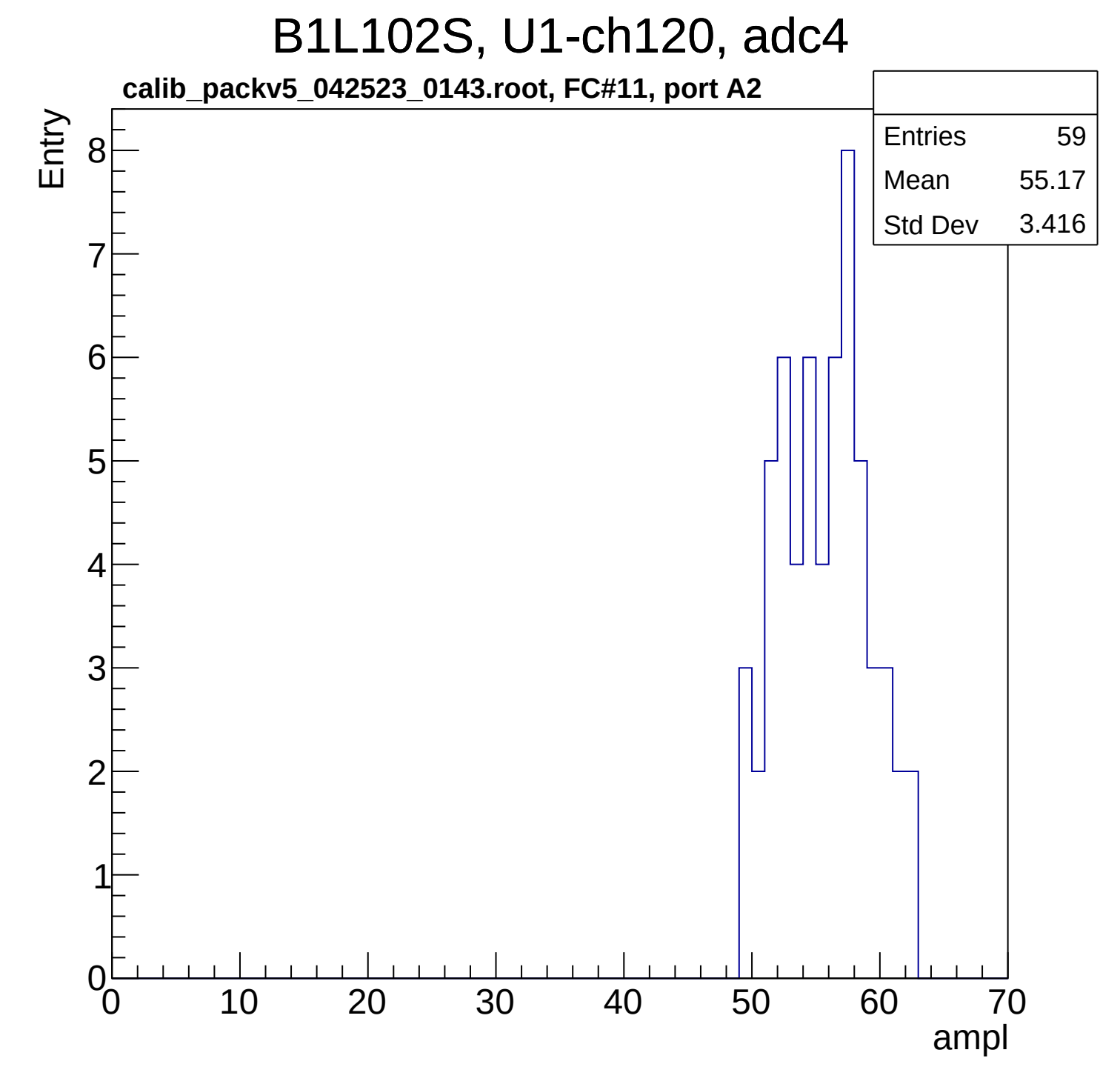

B1L102S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

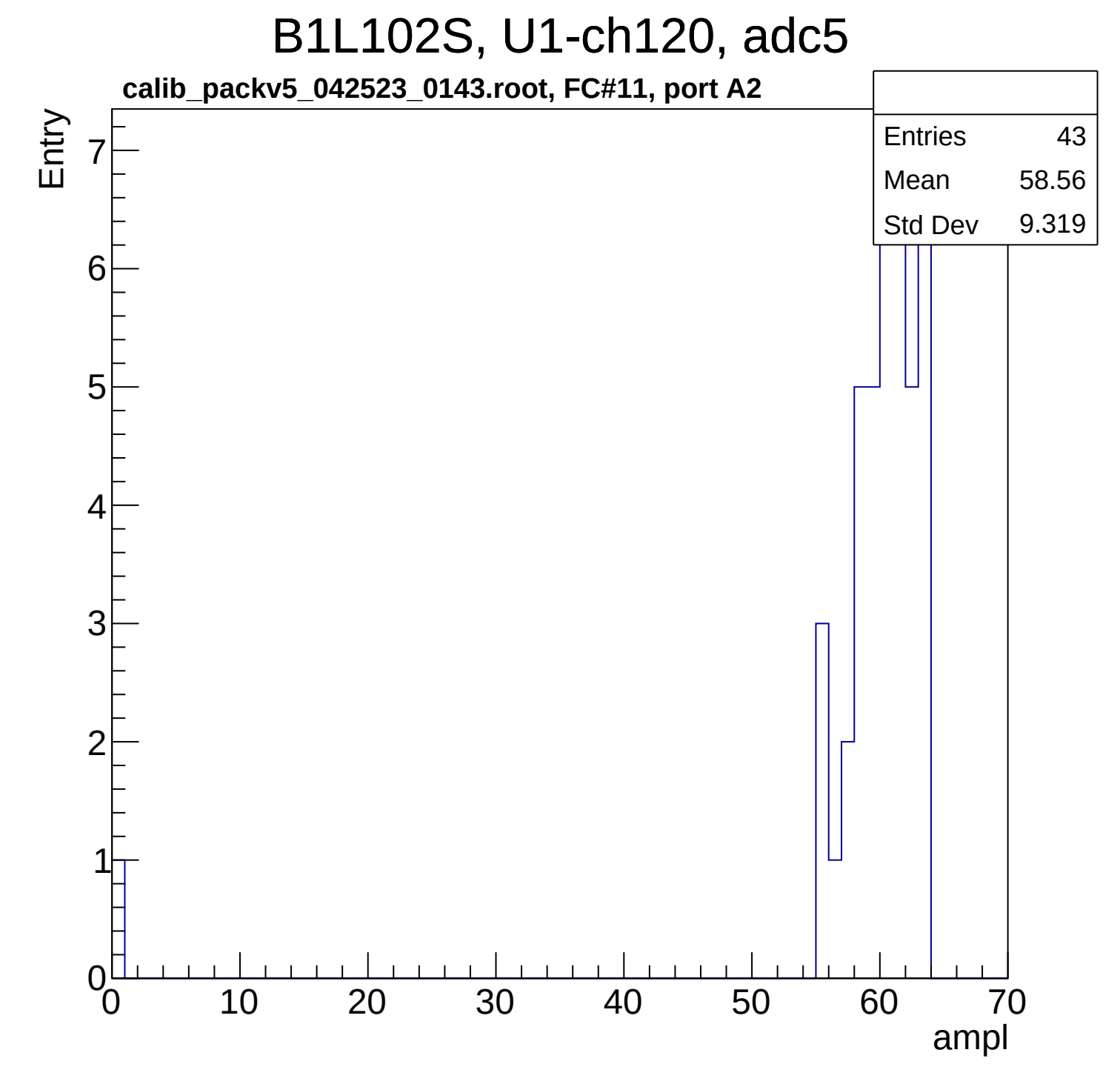

B1L102S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 29.56
Std Dev 4.135
Gaus mean : 30.2439
Gaus Width: 3.7521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

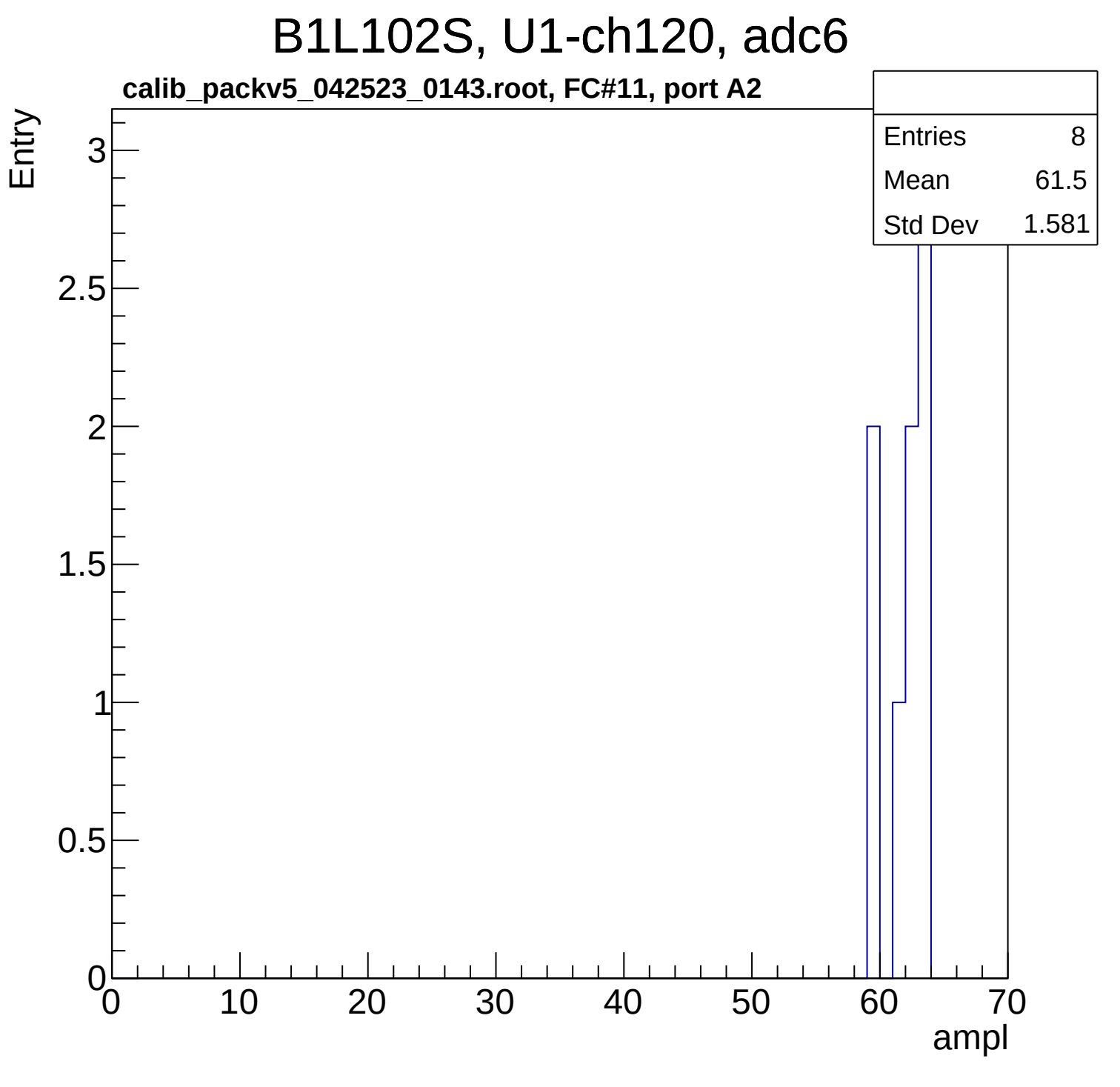

B1L102S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 37.25
Std Dev 4.322
Gaus mean : 37.6840
Gaus Width: 4.4116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

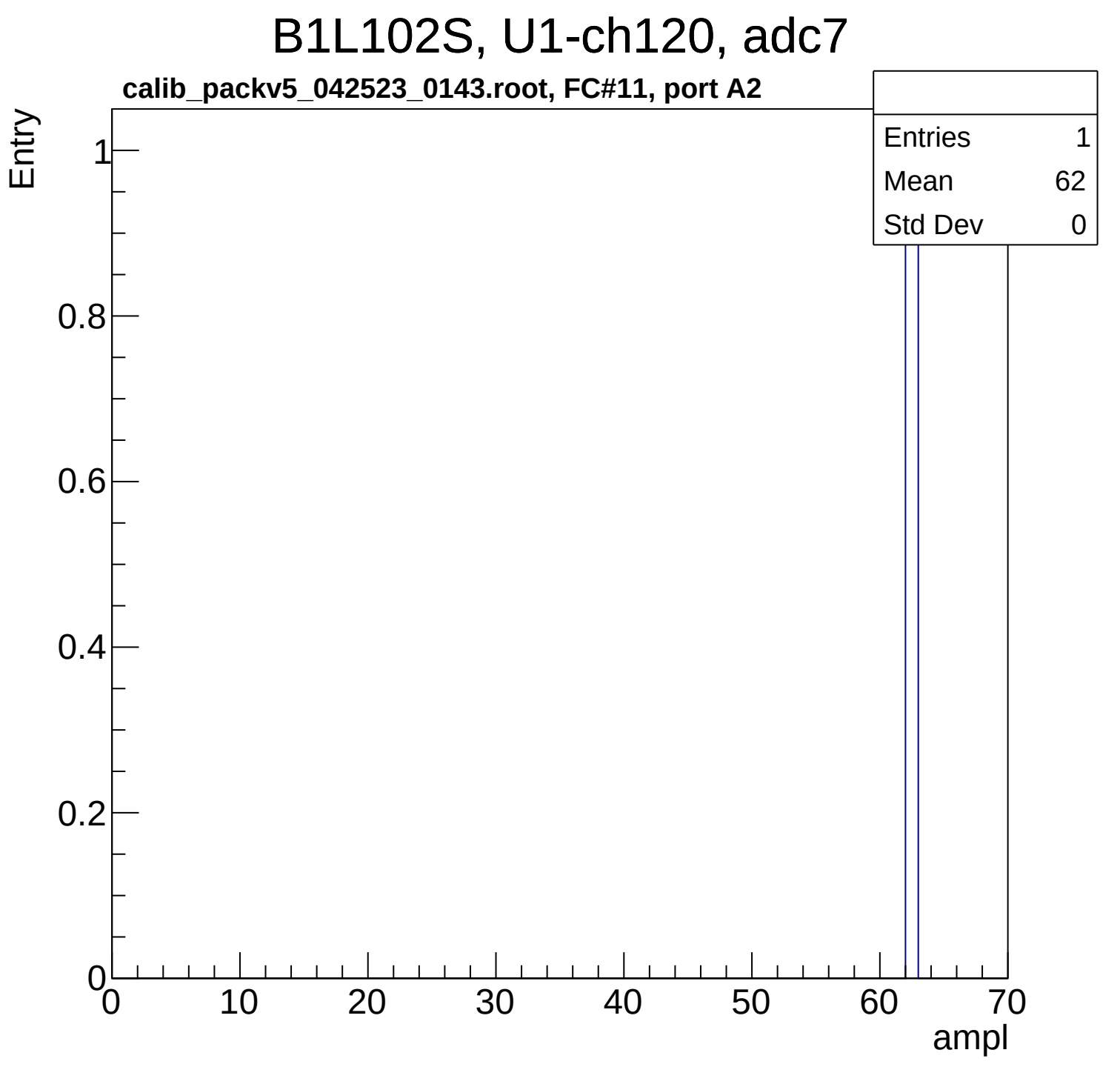

B1L102S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 43.04
Std Dev 3.571
Gaus mean : 44.0306
Gaus Width: 4.2192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

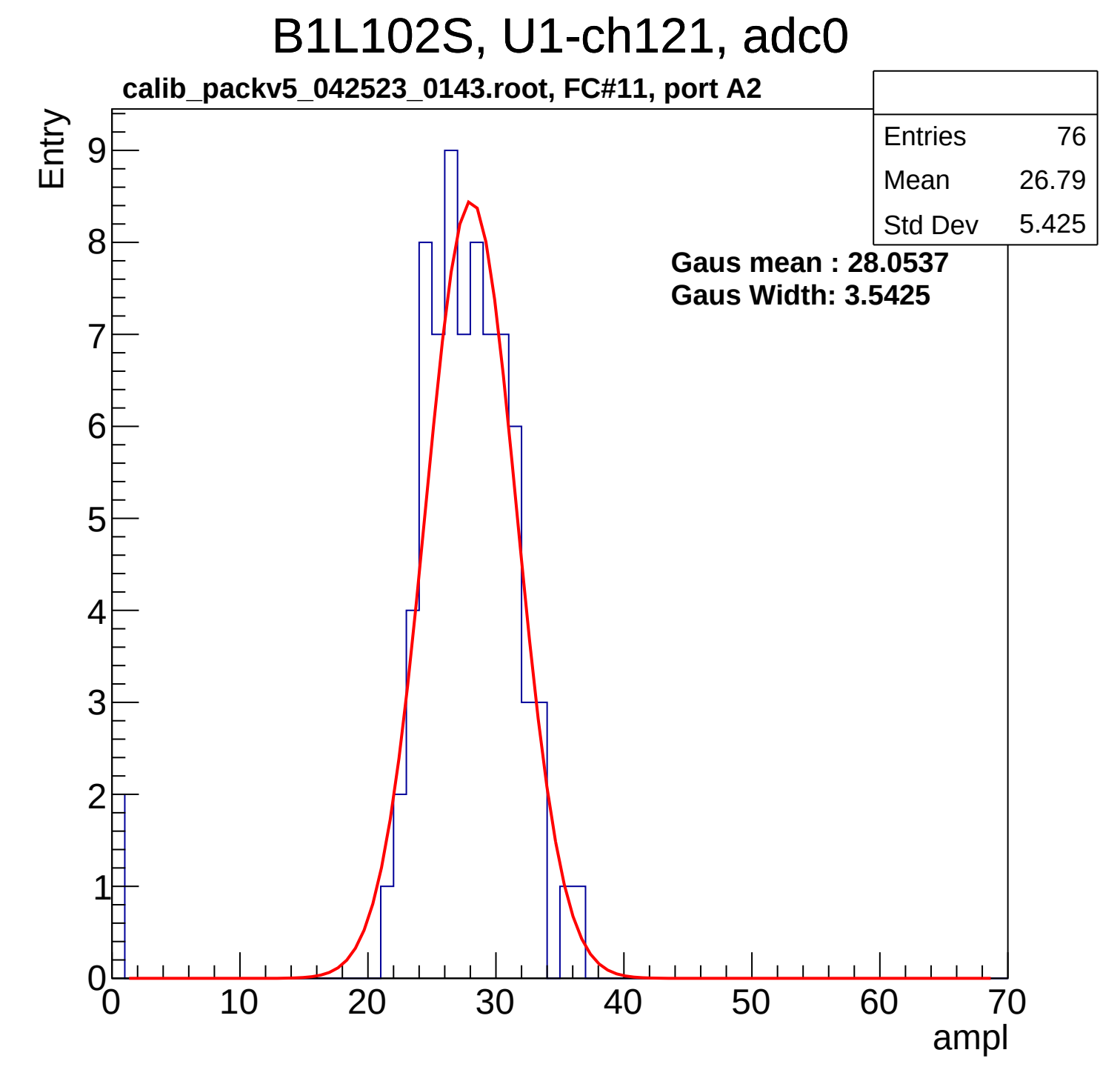

B1L102S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 49.35
Std Dev 3.665
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

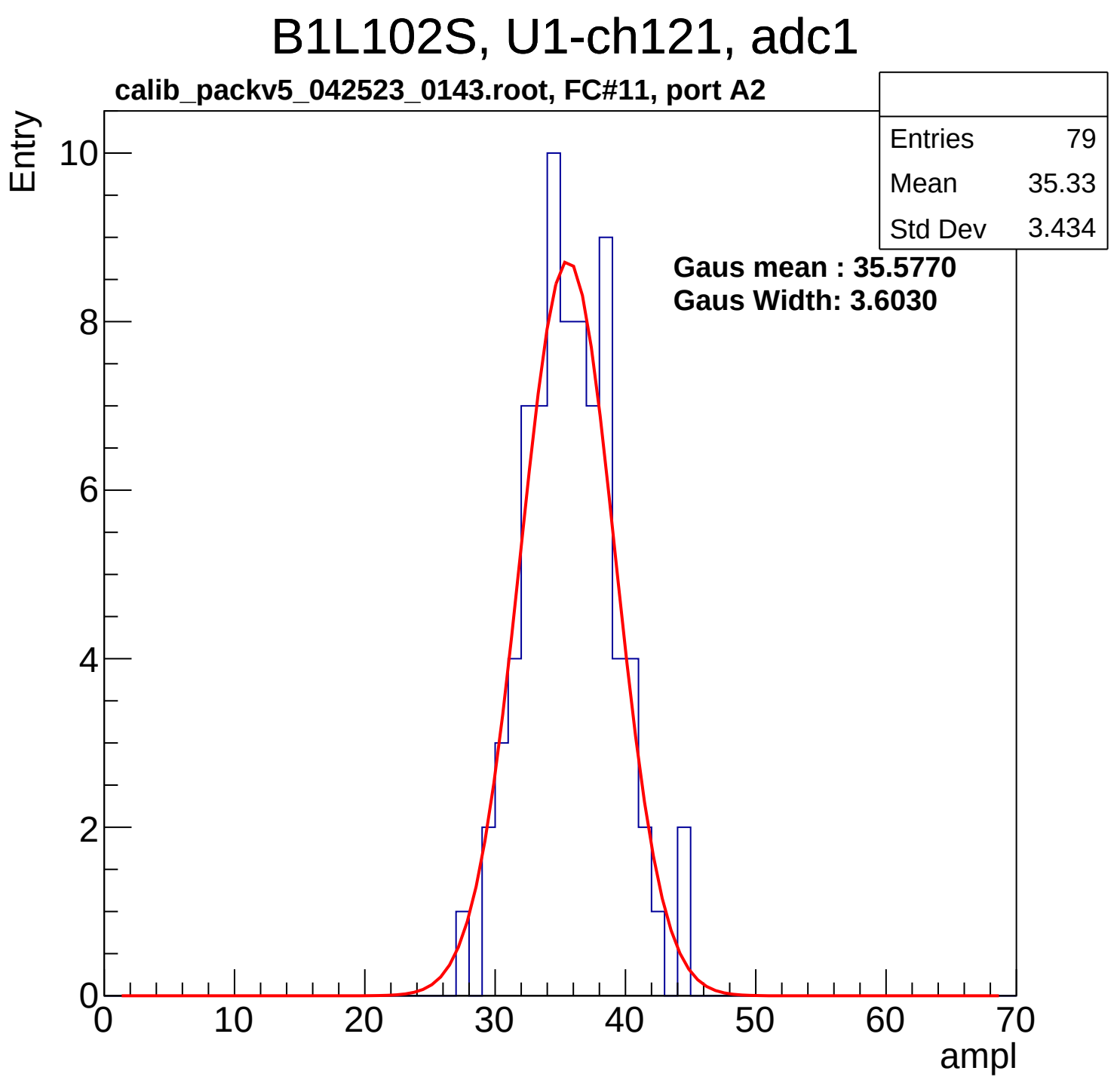

B1L102S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 55.17
Std Dev 3.416
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

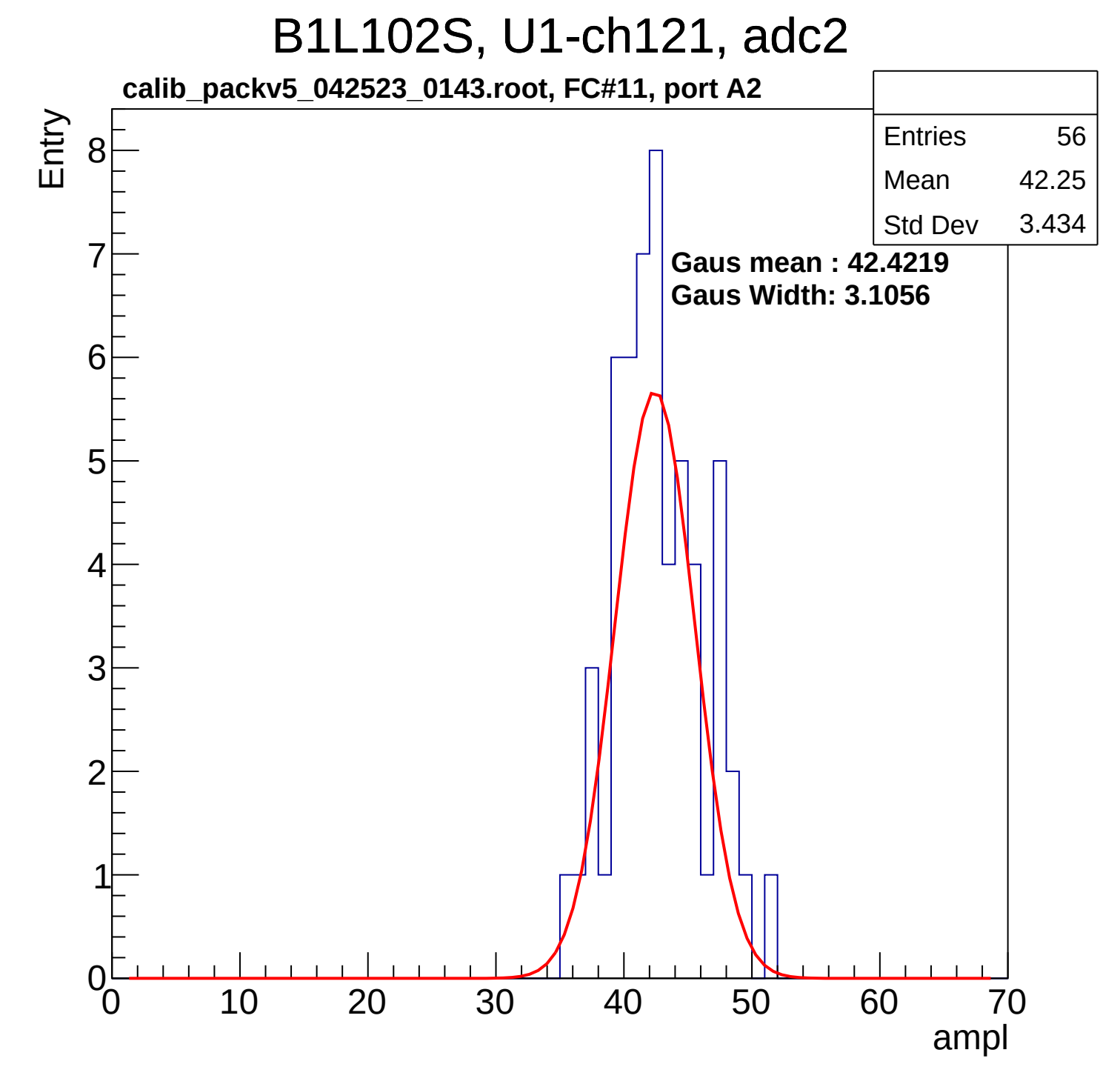

B1L102S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 43
Mean 58.56
Std Dev 9.319
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

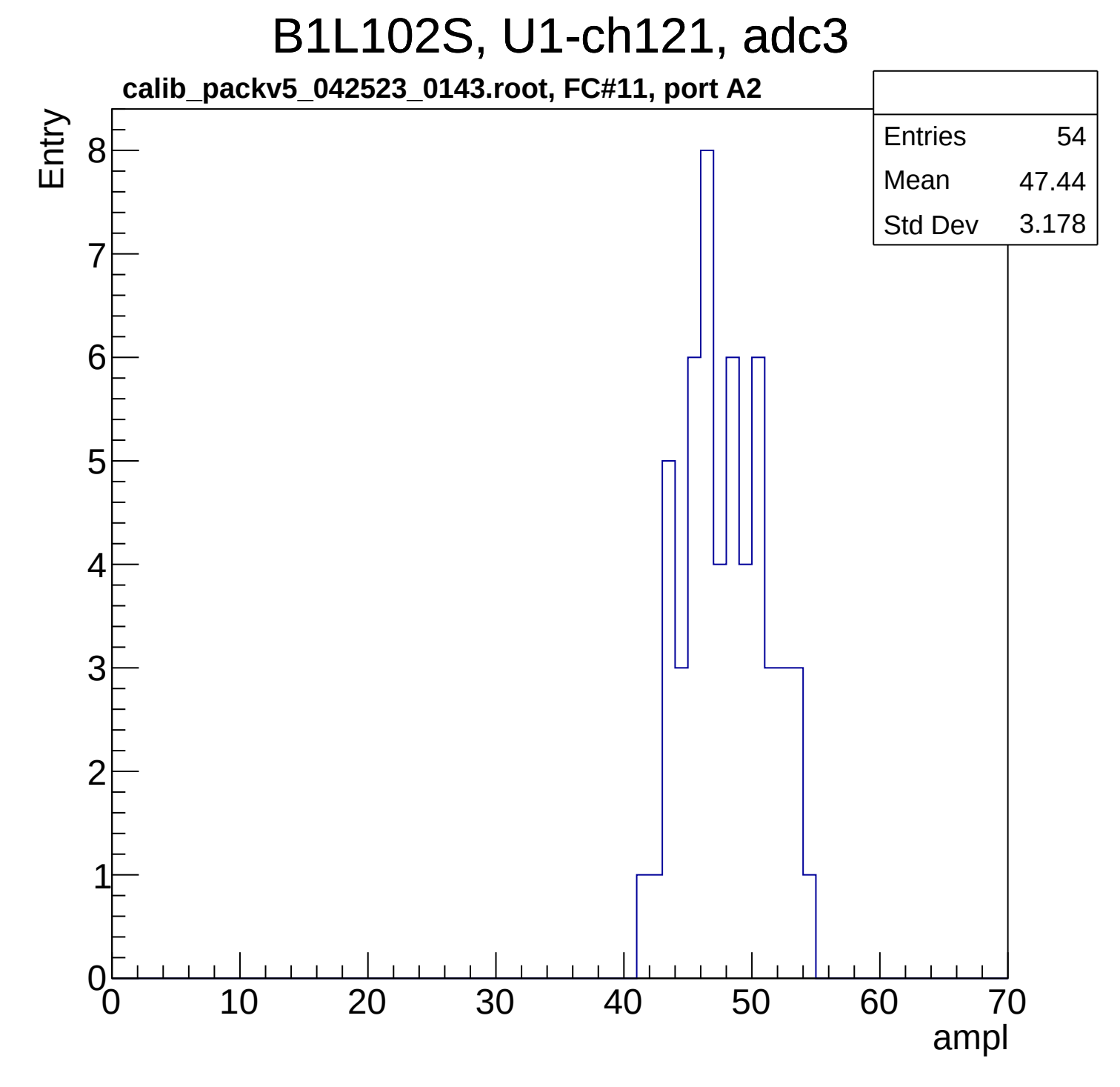

B1L102S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61.5
Std Dev 1.581
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

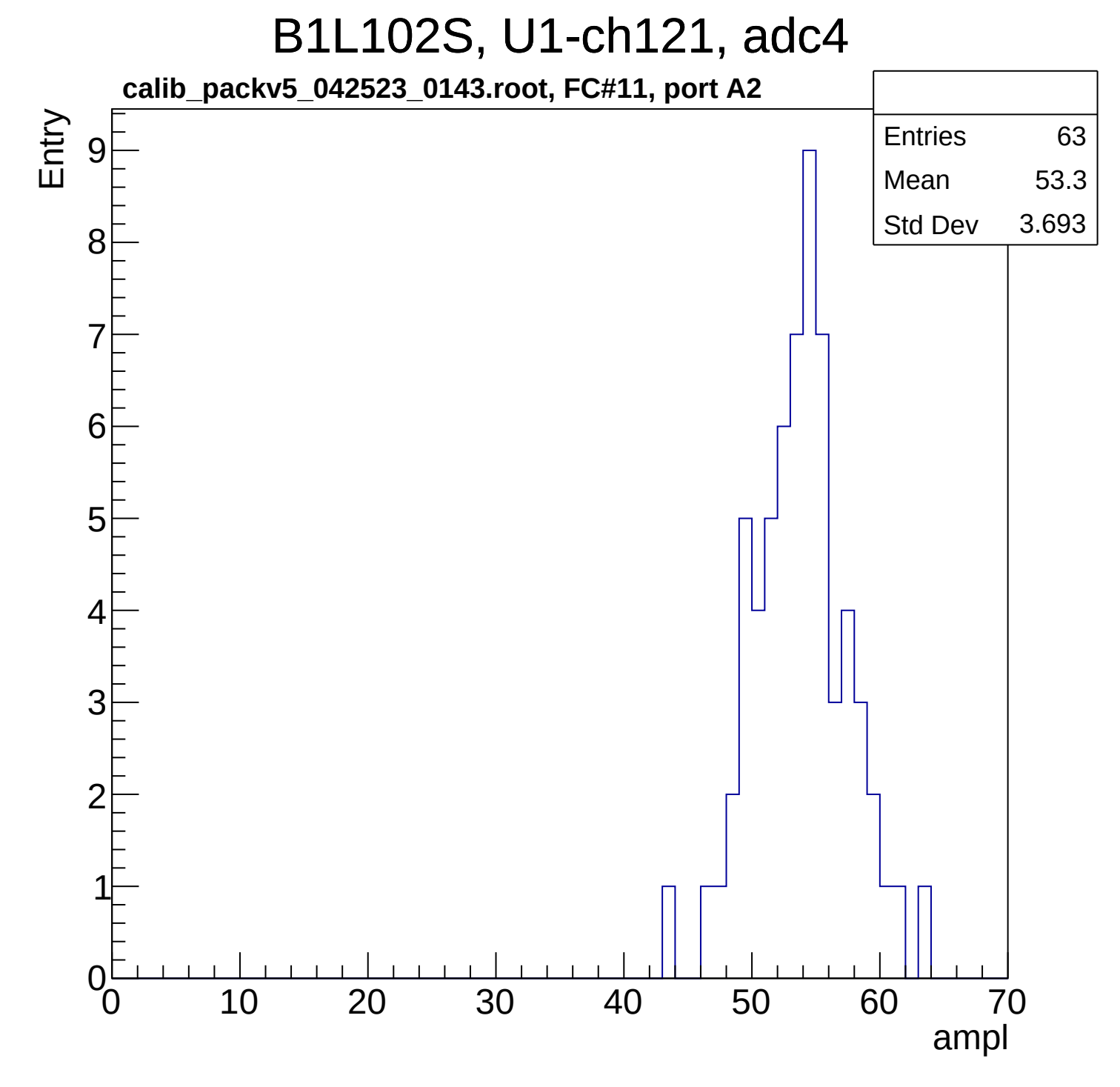

B1L102S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

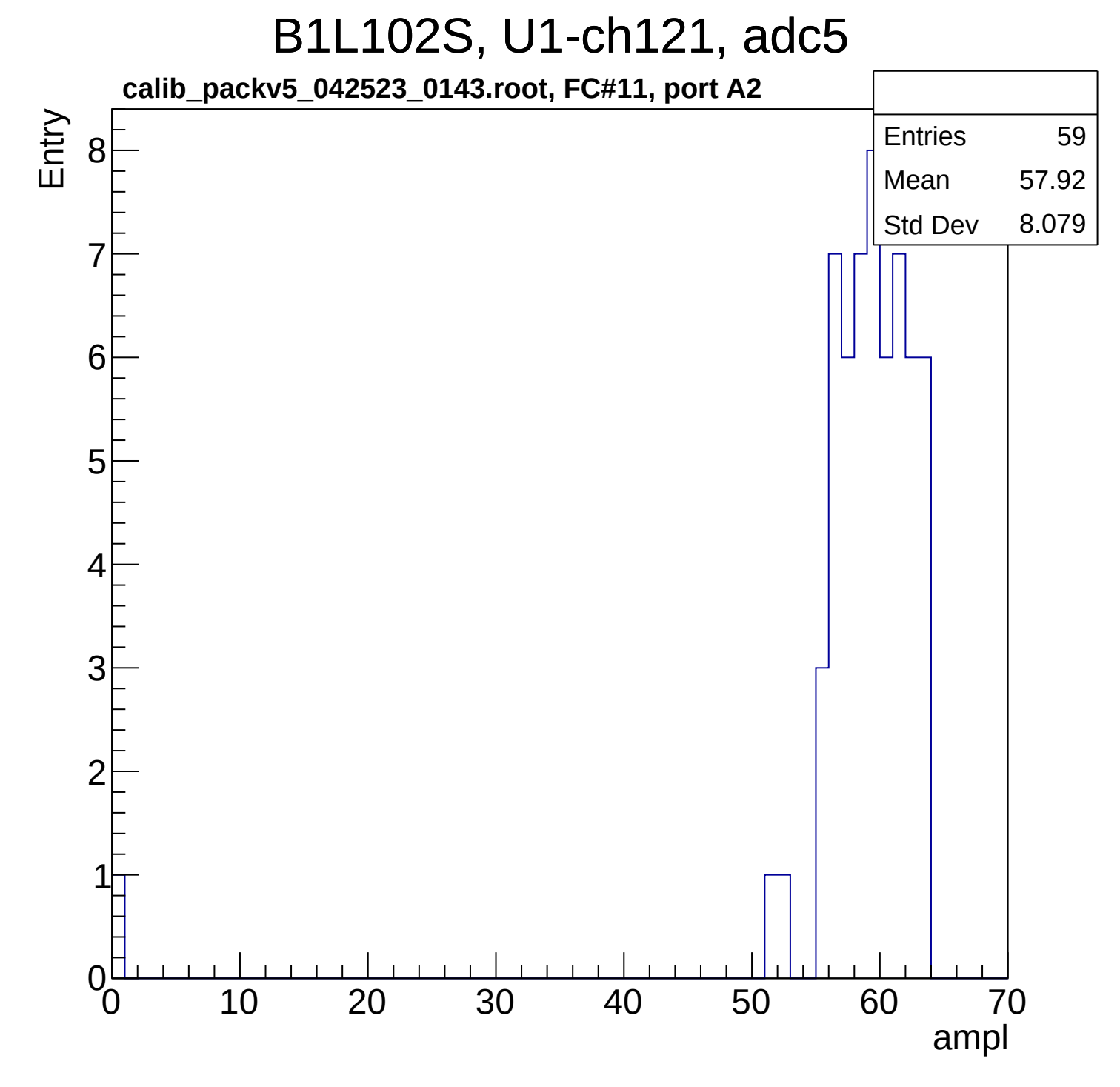

B1L102S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 76
Mean 26.79
Std Dev 5.425
Gaus mean : 28.0537
Gaus Width: 3.5425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

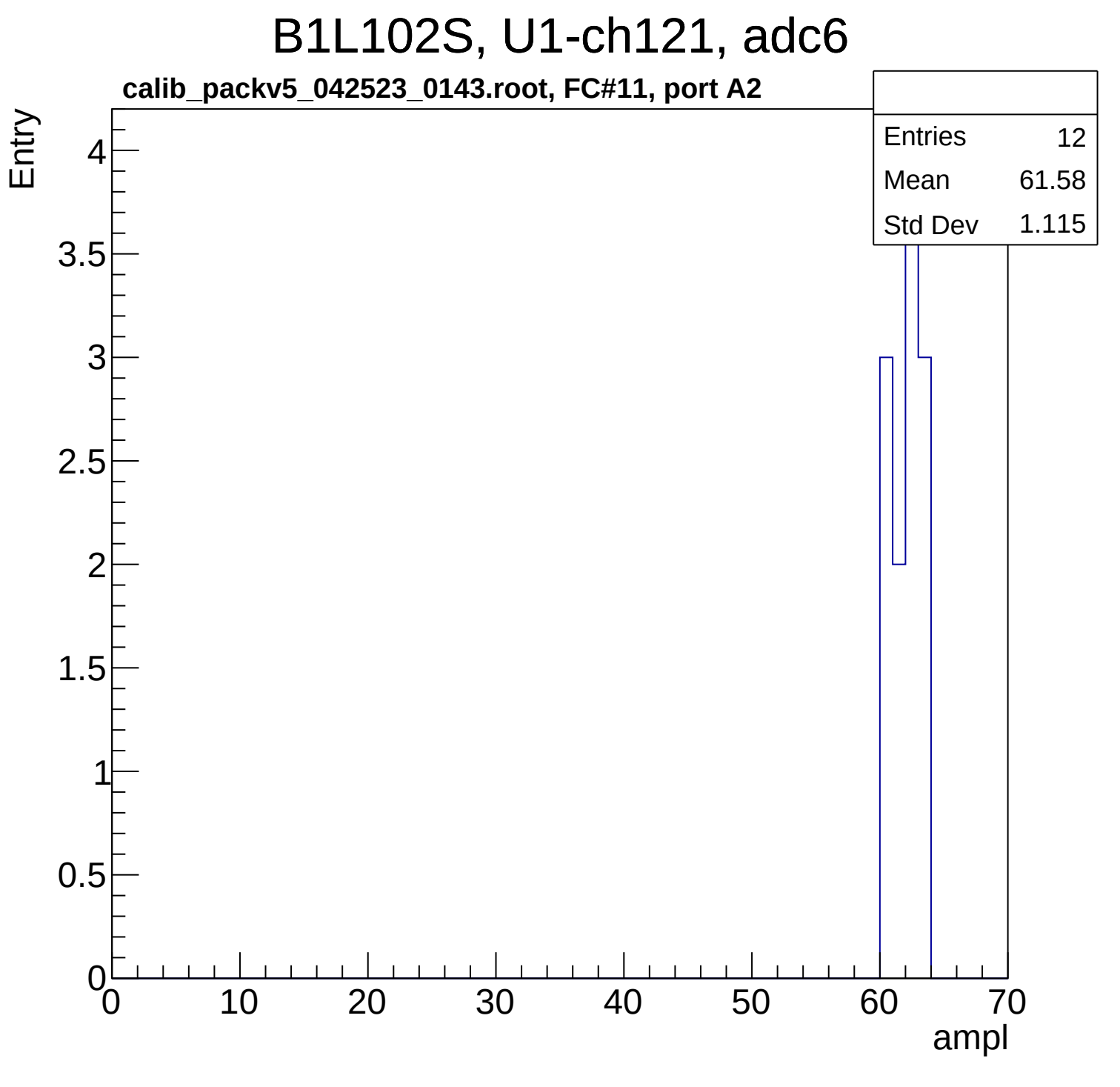

B1L102S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 79
Mean 35.33
Std Dev 3.434
Gaus mean : 35.5770
Gaus Width: 3.6030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

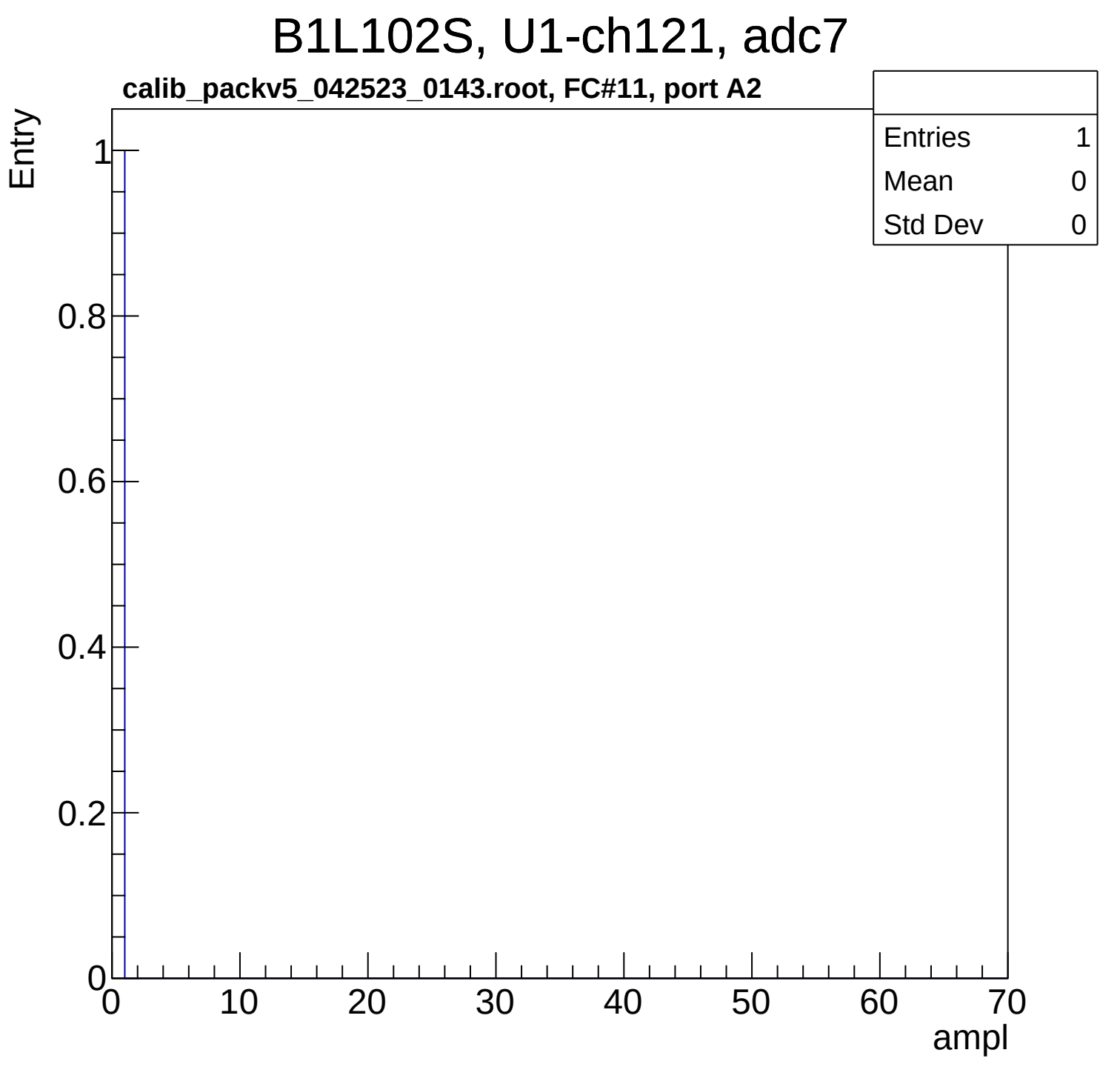

B1L102S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 56
Mean 42.25
Std Dev 3.434
Gaus mean : 42.4219
Gaus Width: 3.1056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

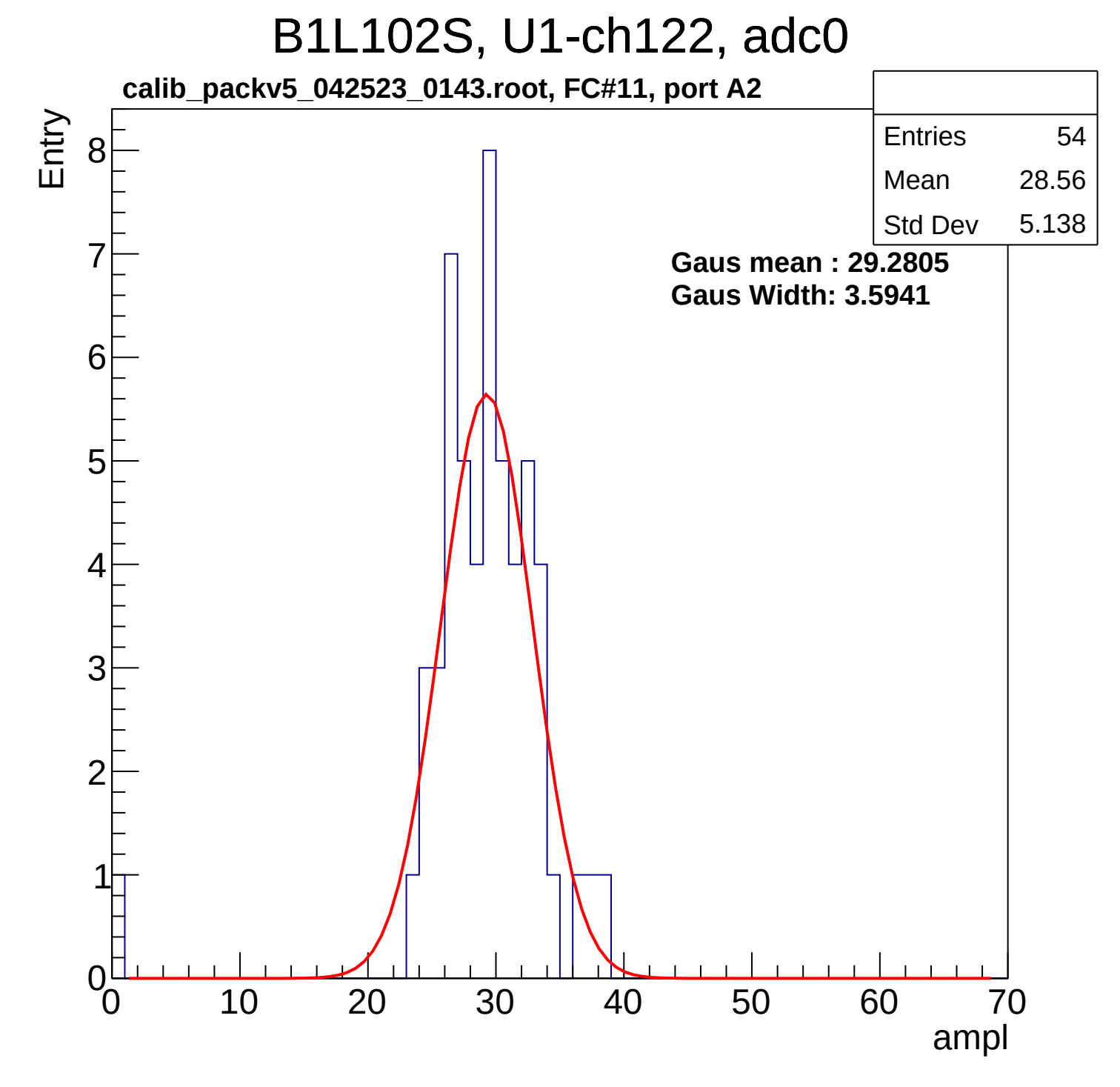

B1L102S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 47.44
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

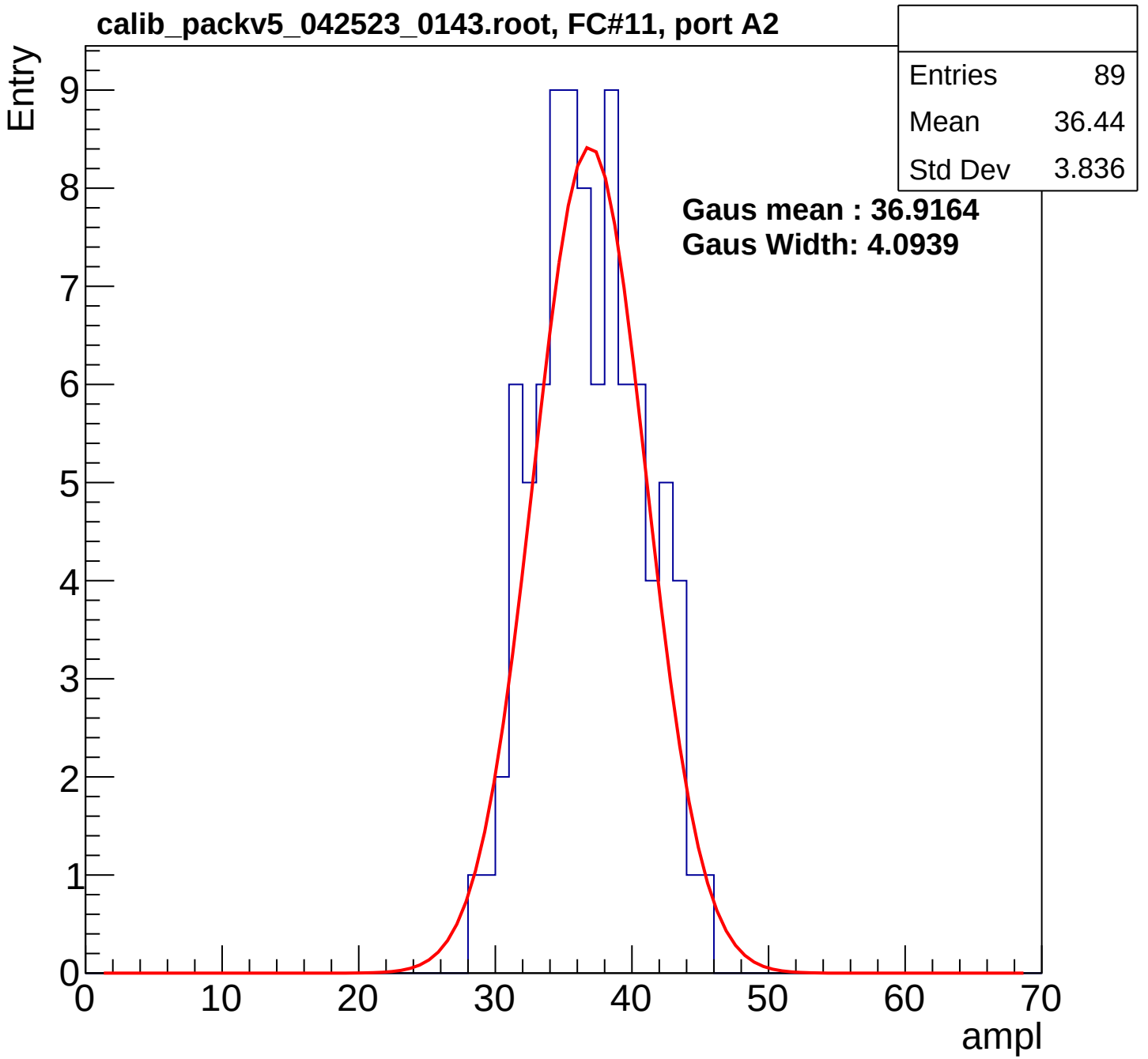

B1L102S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 53.3
Std Dev 3.693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

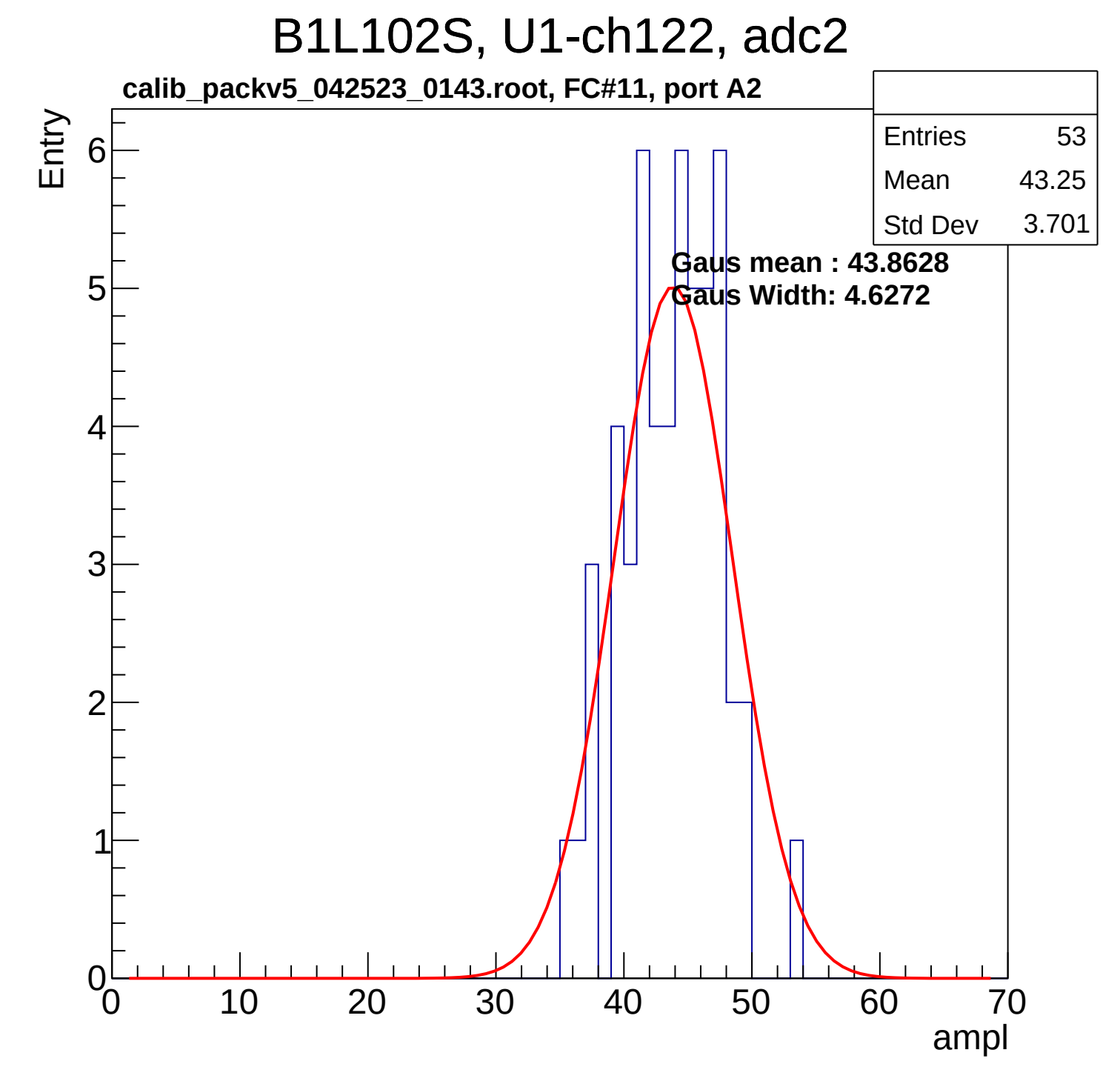

B1L102S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 57.92
Std Dev 8.079
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

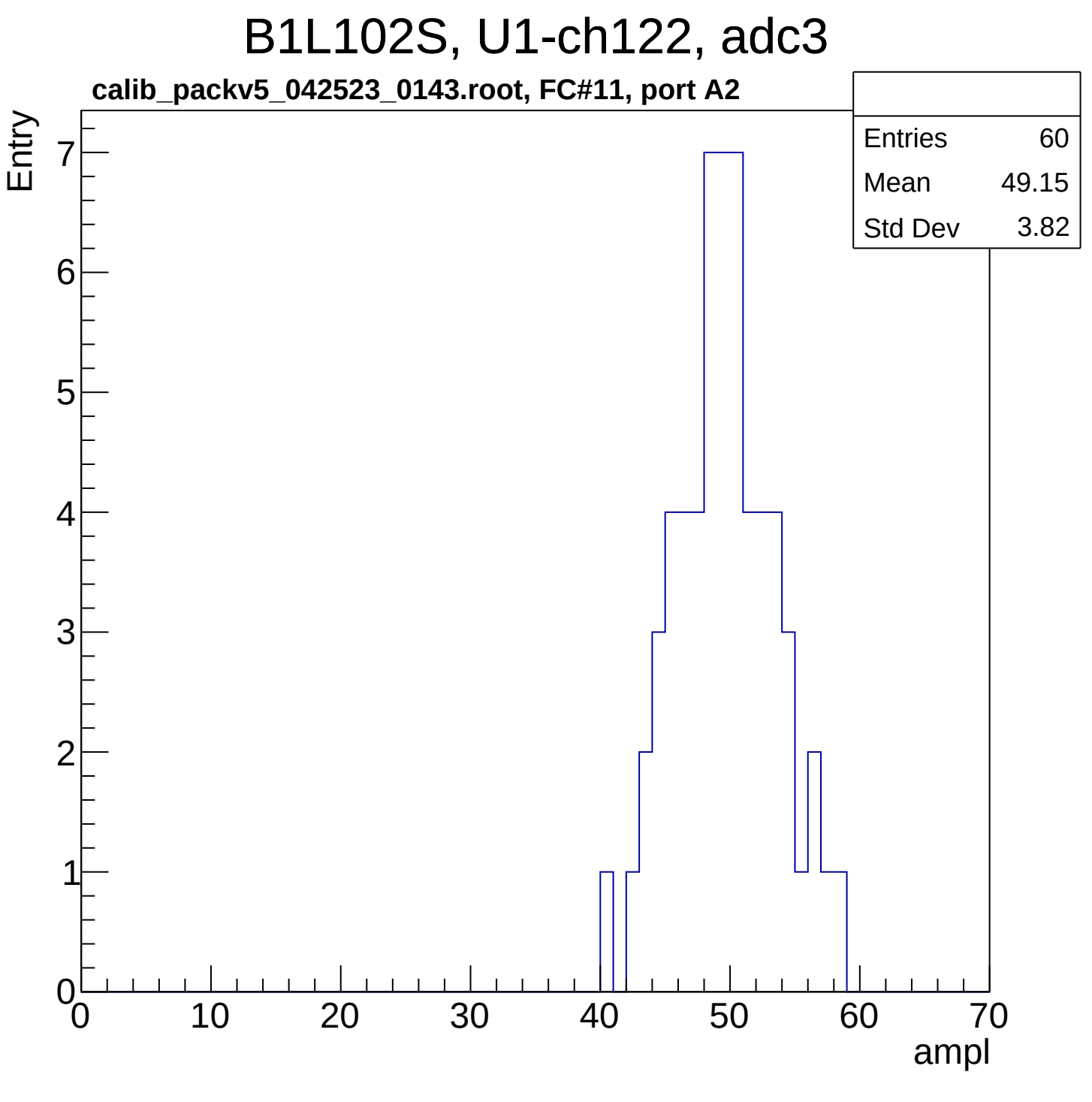

B1L102S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.58
Std Dev 1.115
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

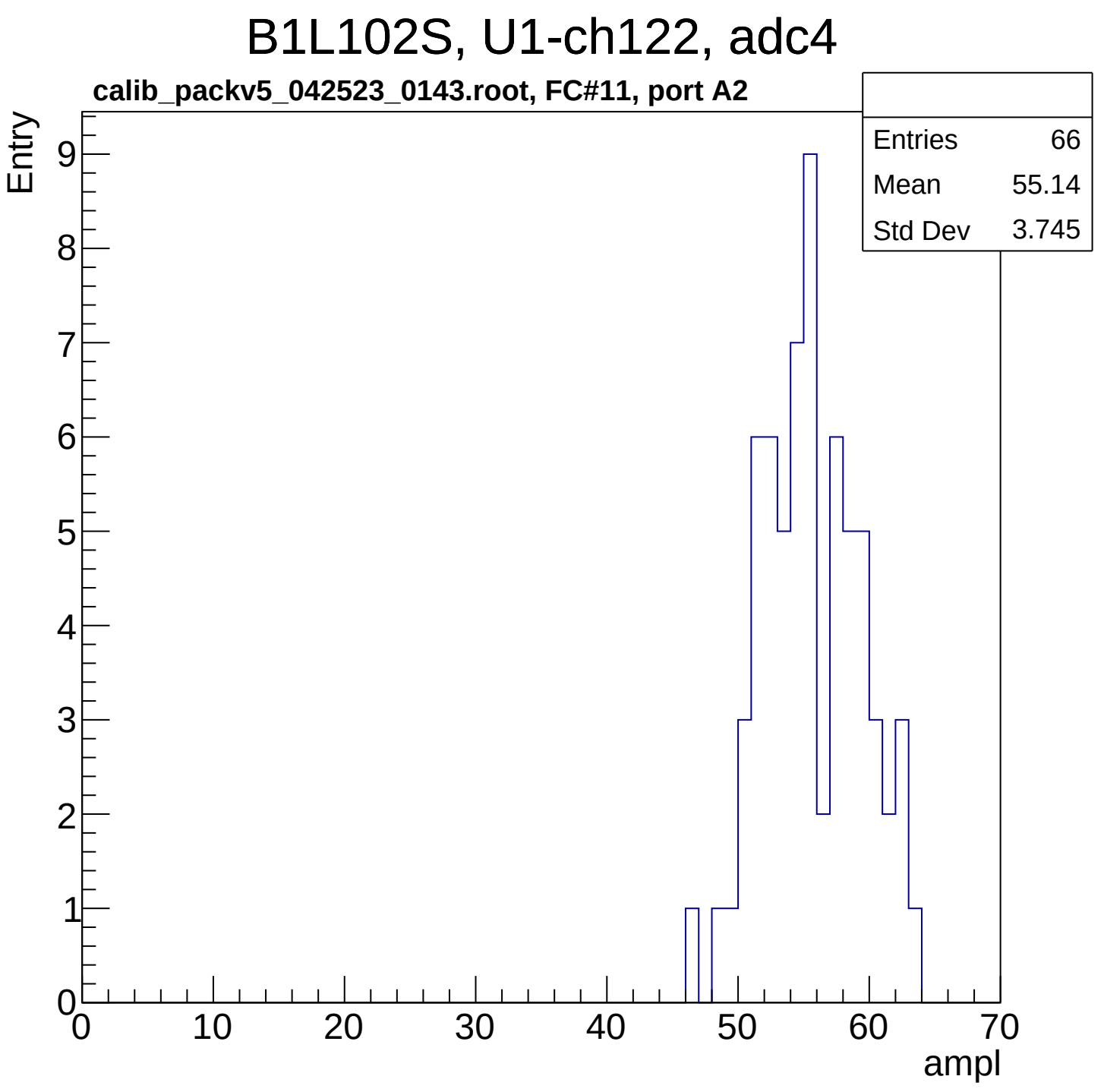

B1L102S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

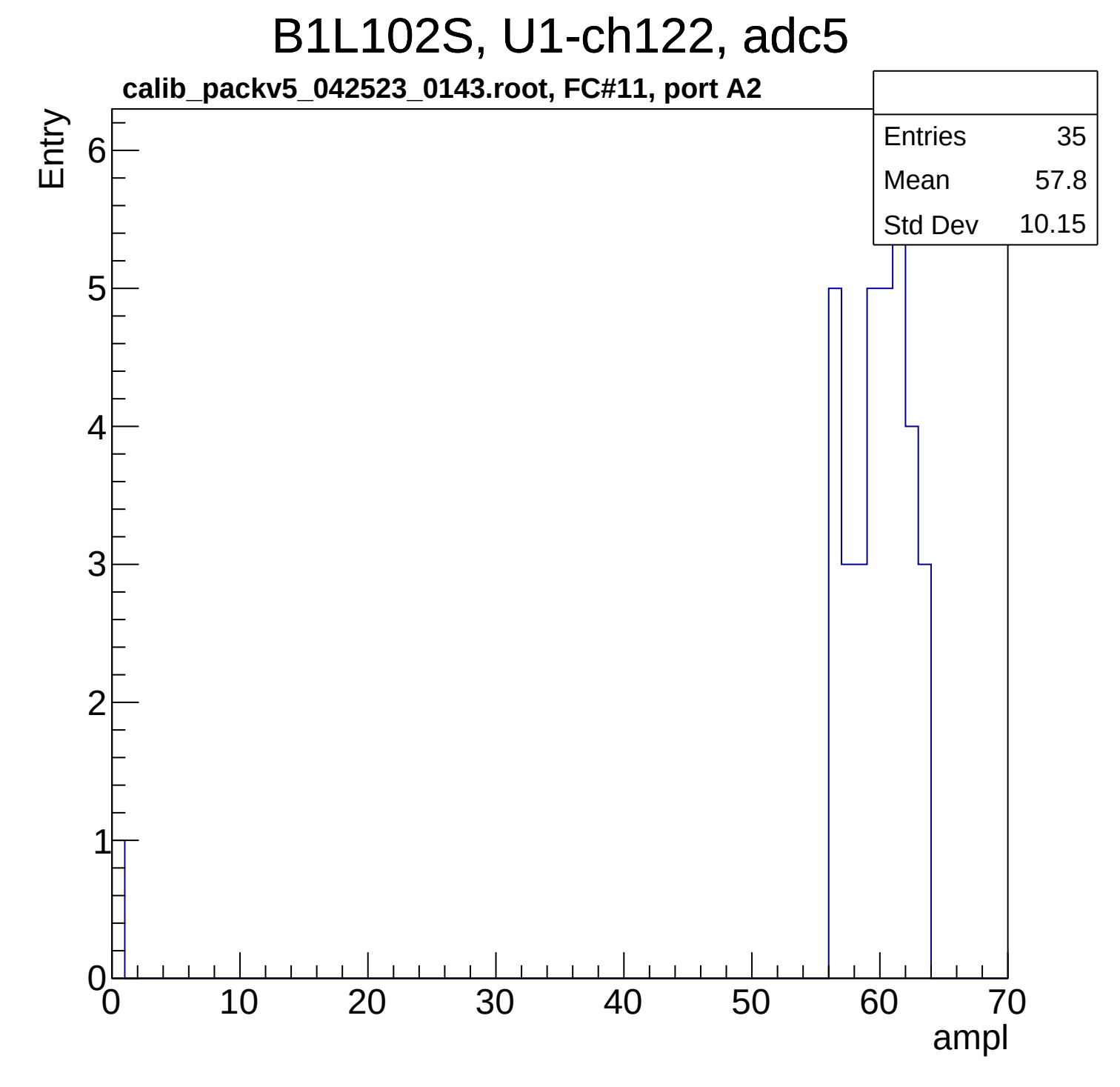

B1L102S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 54
Mean 28.56
Std Dev 5.138
Gaus mean : 29.2805
Gaus Width: 3.5941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

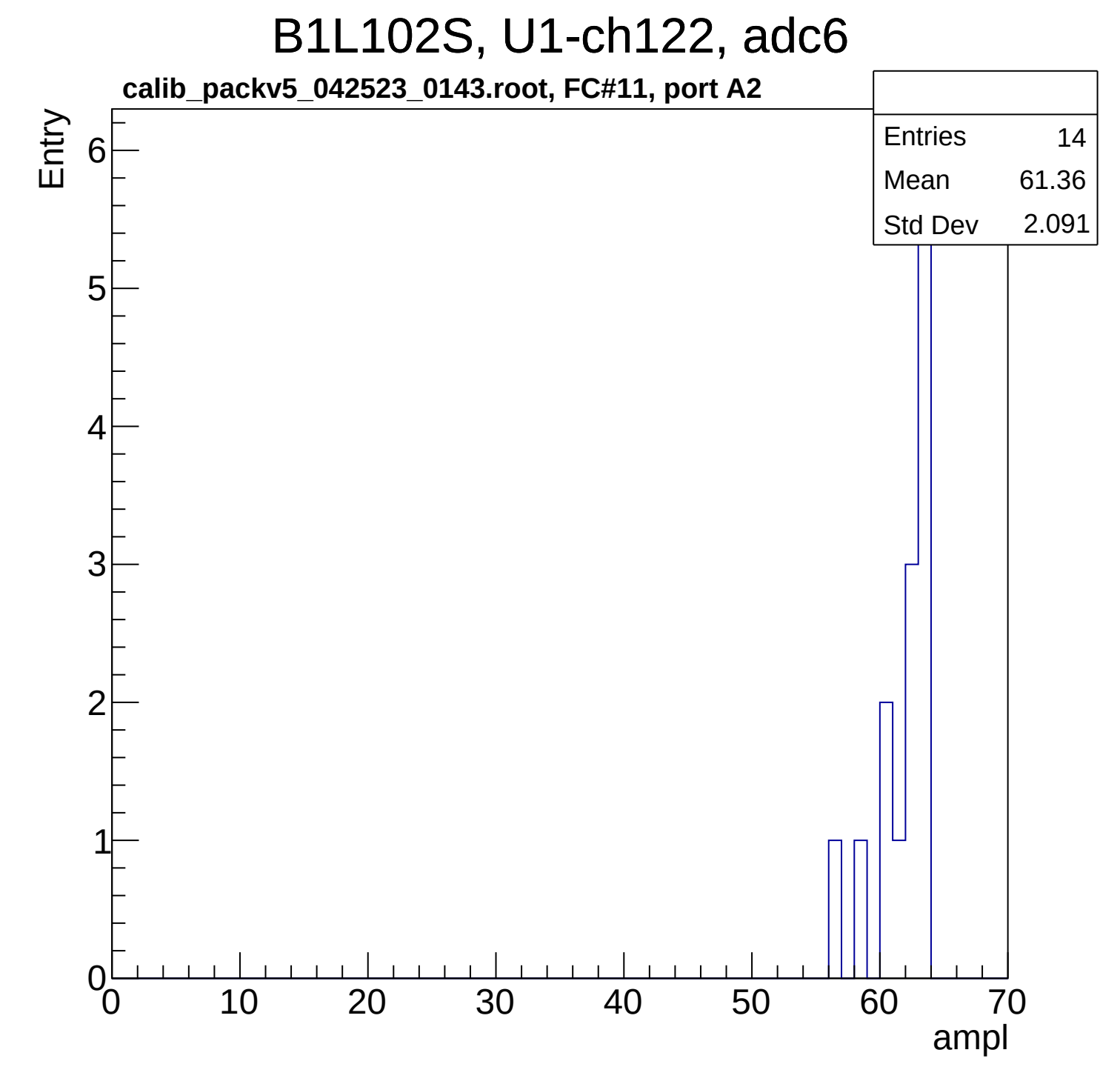

B1L102S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 89
Mean 36.44
Std Dev 3.836
Gaus mean : 36.9164
Gaus Width: 4.0939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

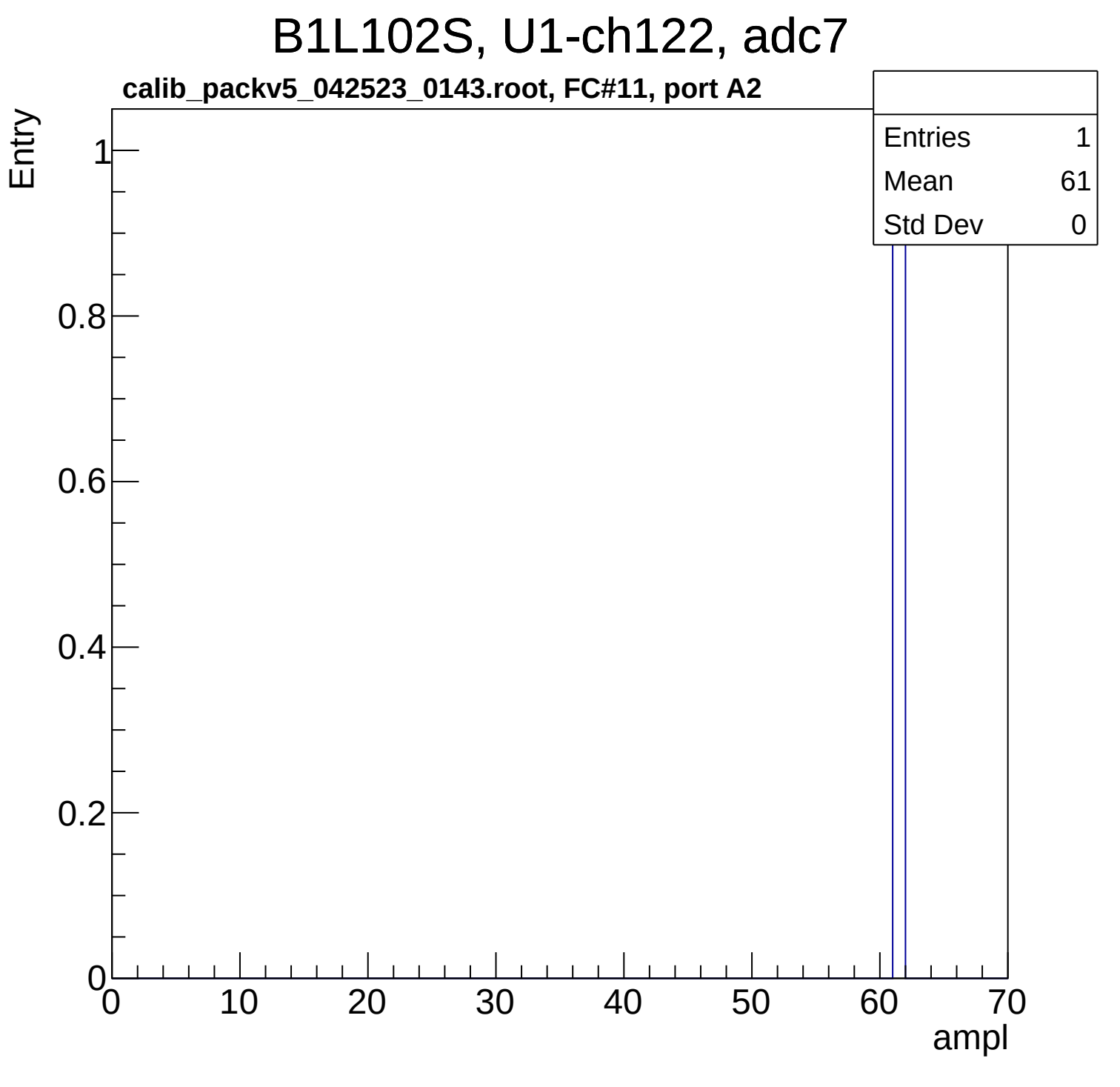

B1L102S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 53
Mean 43.25
Std Dev 3.701
Gaus mean : 43.8628
Gaus Width: 4.6272
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L102S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 49.15
Std Dev 3.82
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 66
Mean 55.14
Std Dev 3.745
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 35
Mean 57.8
Std Dev 10.15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 14
Mean 61.36
Std Dev 2.091
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 61
Std Dev 0
Entry
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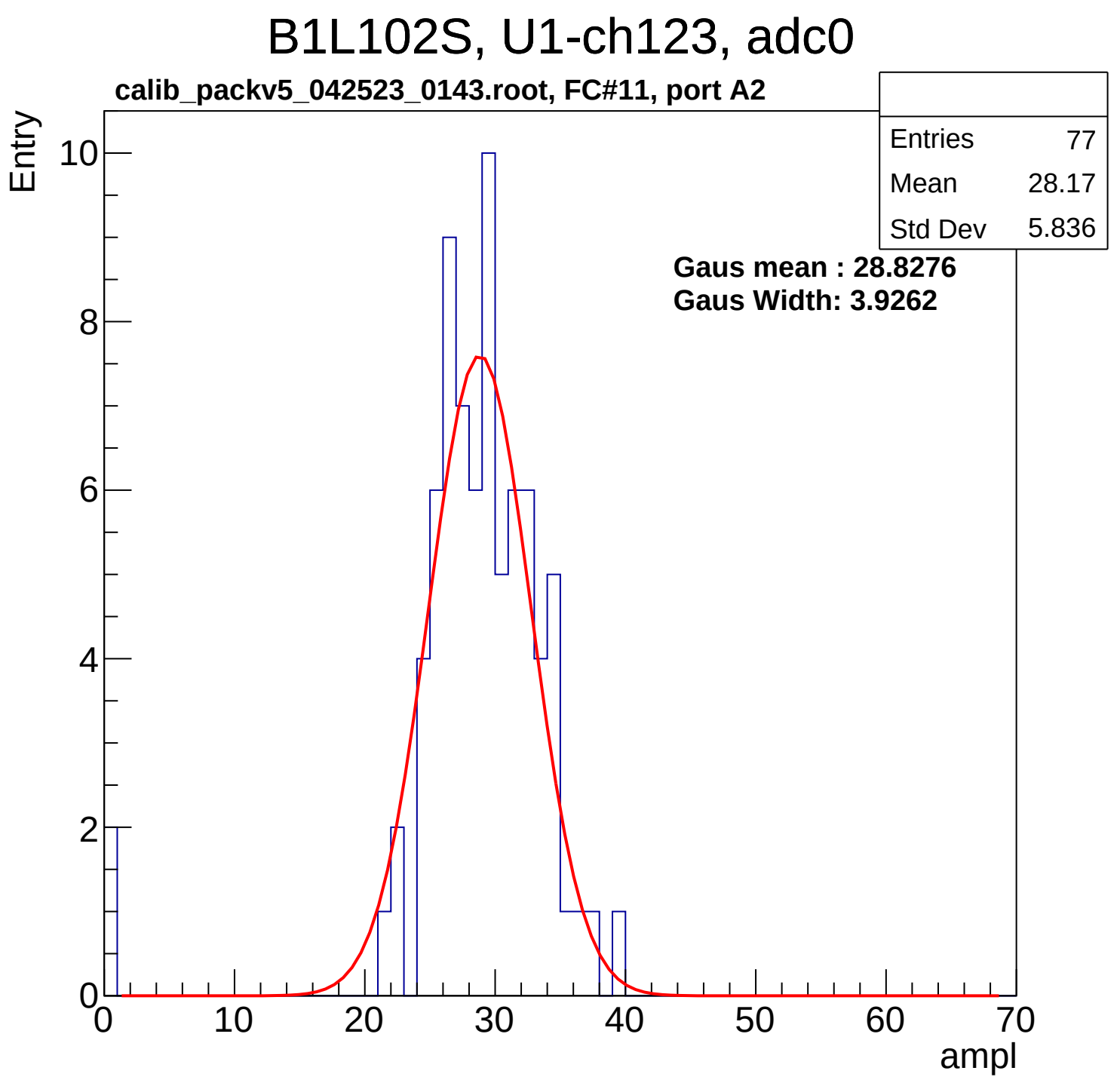
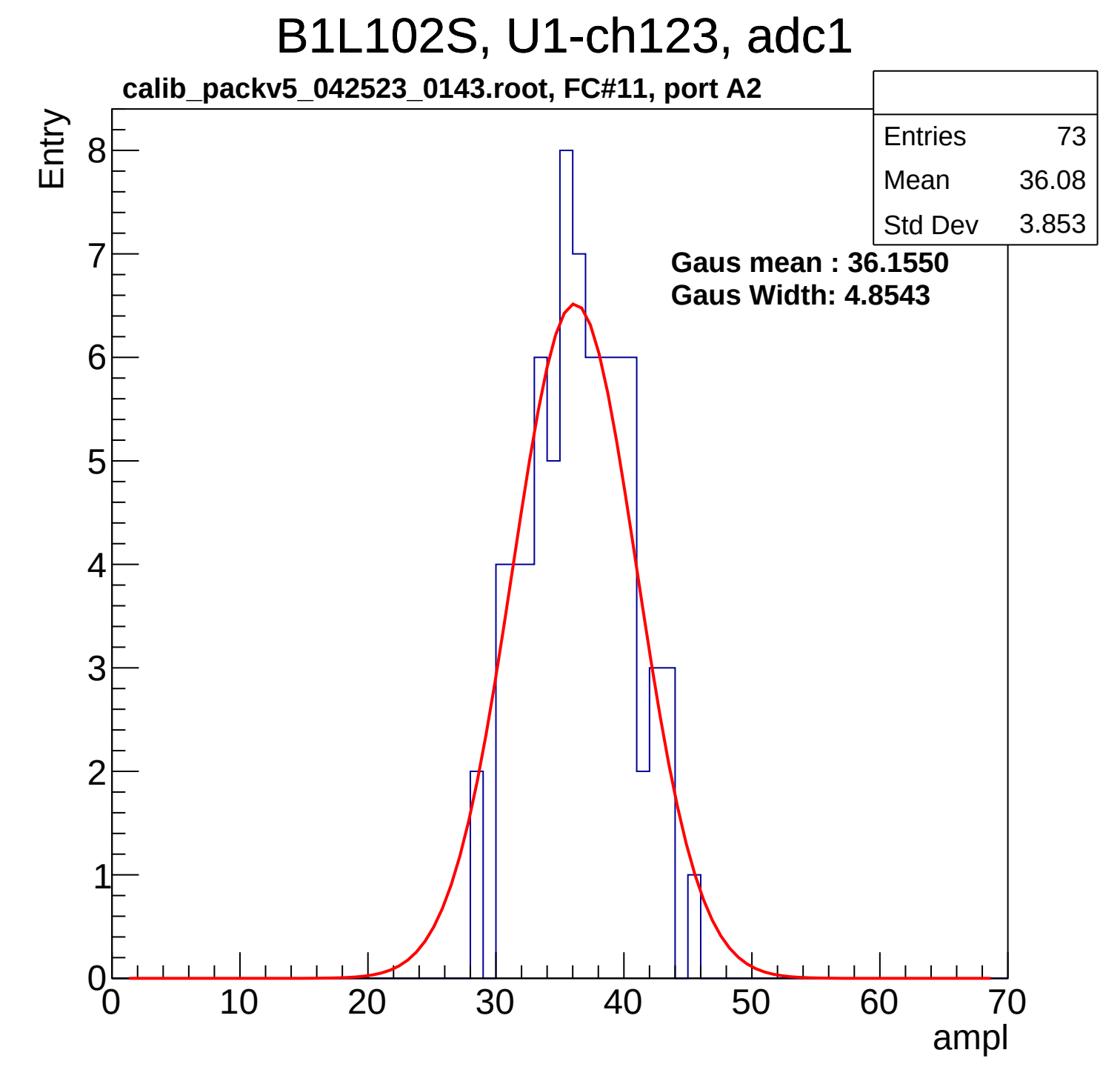
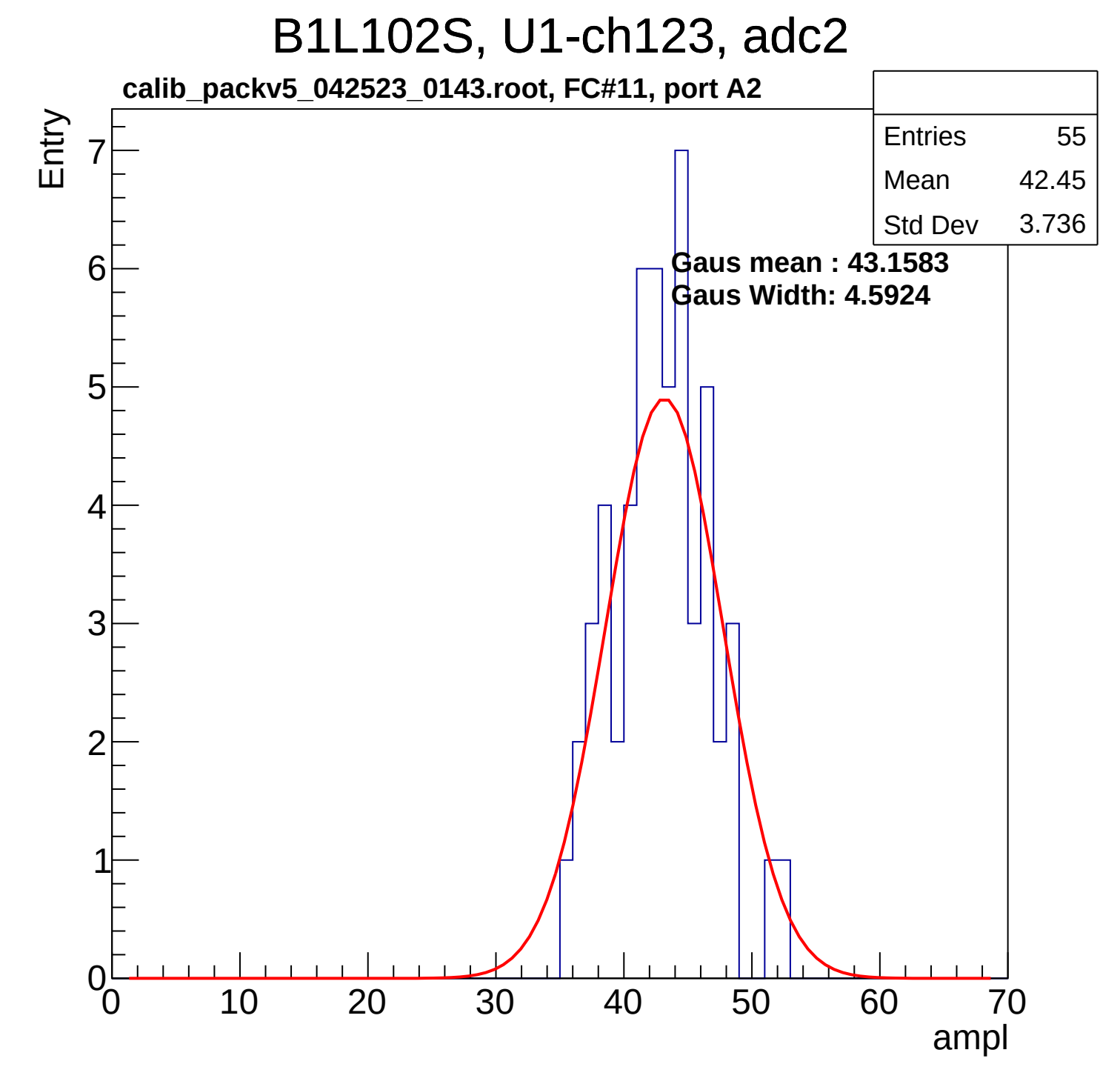
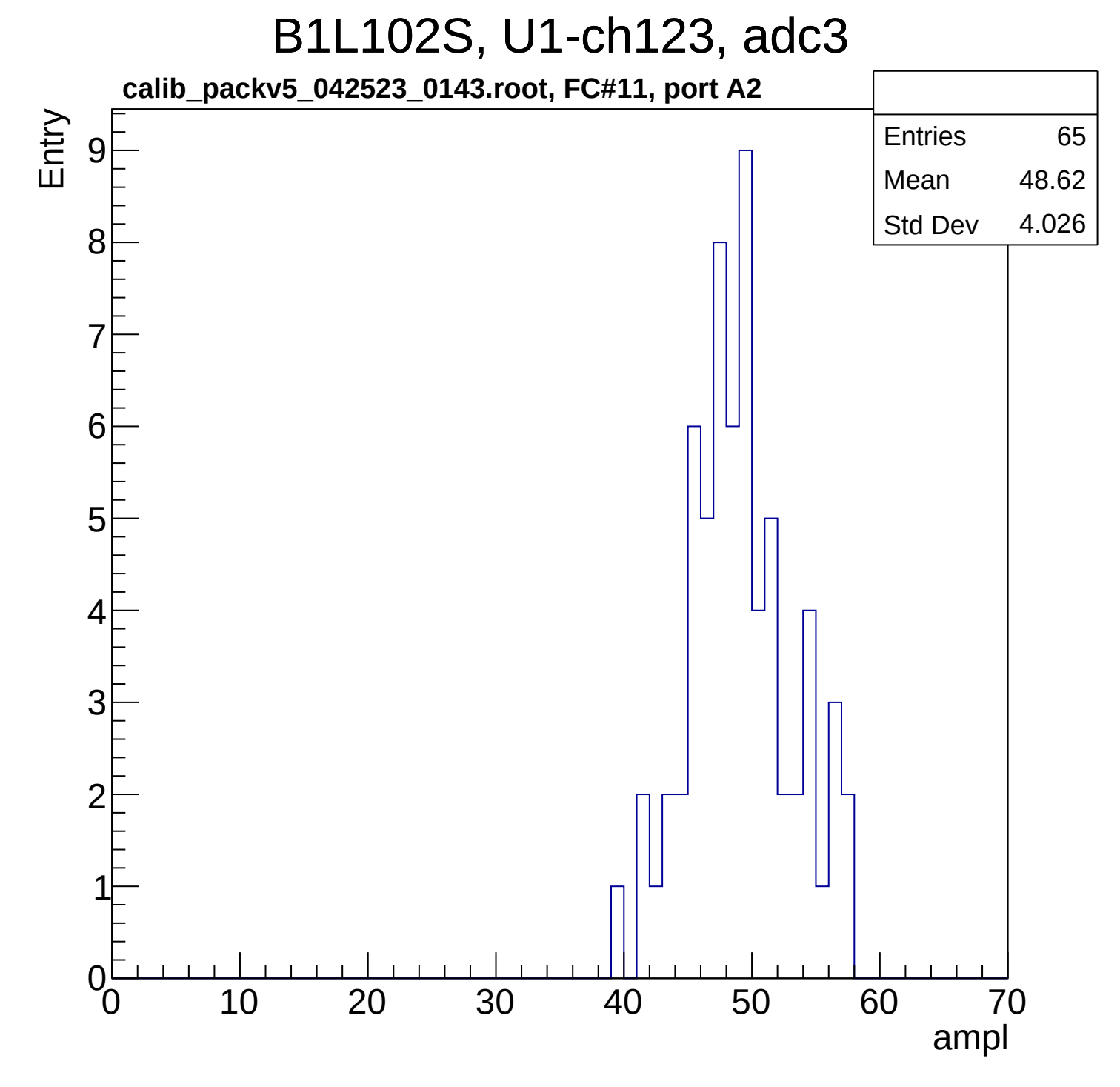
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 77
Mean 28.17
Std Dev 5.836
Gaus mean : 28.8276
Gaus Width: 3.9262
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 36.08
Std Dev 3.853
Gaus mean : 36.1550
Gaus Width: 4.8543
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 55
Mean 42.45
Std Dev 3.736
Gaus mean : 43.1583
Gaus Width: 4.5924
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 65
Mean 48.62
Std Dev 4.026
Entry
ampl
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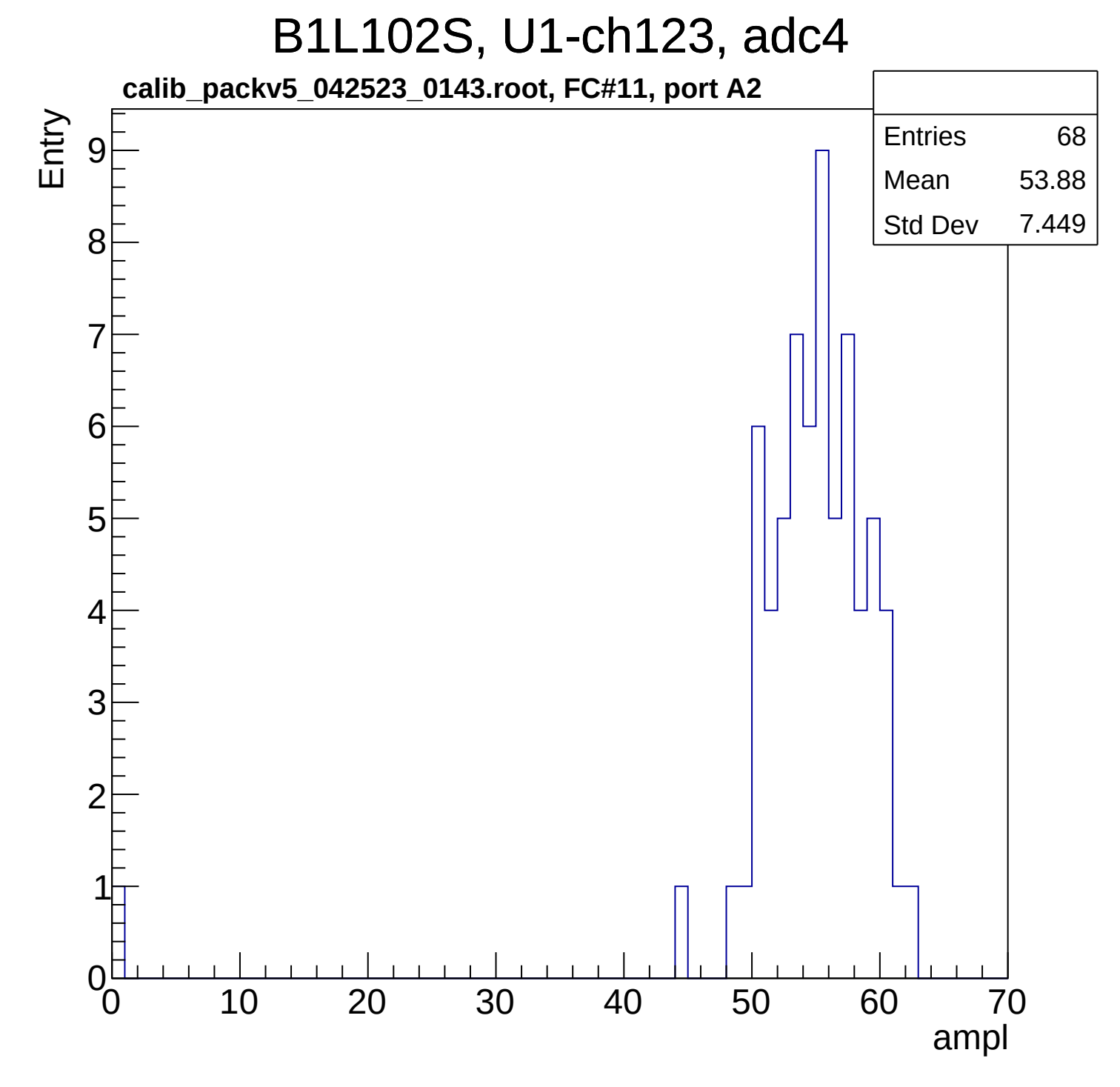
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L102S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 53.88
Std Dev 7.449
Entry
ampl
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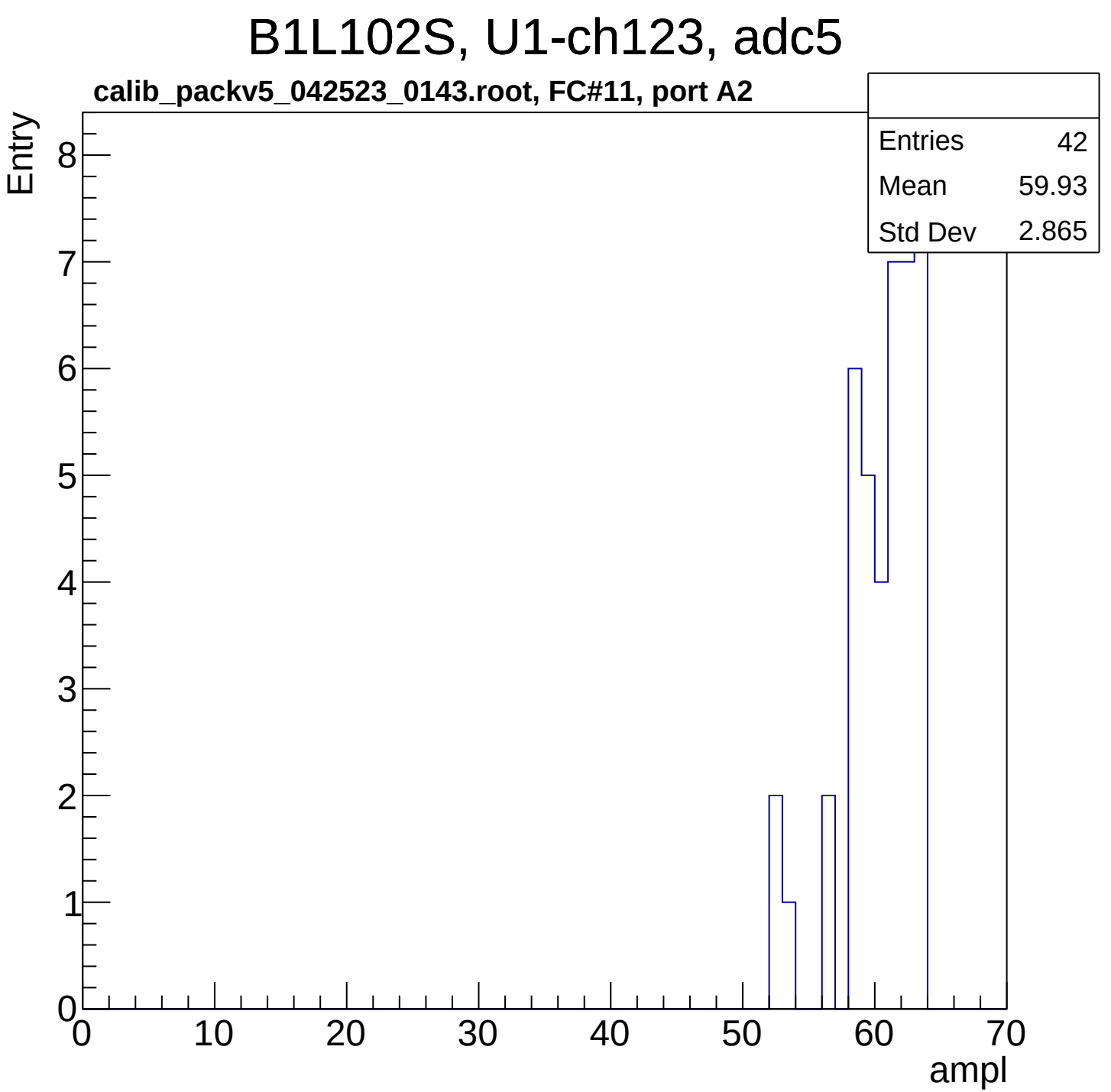
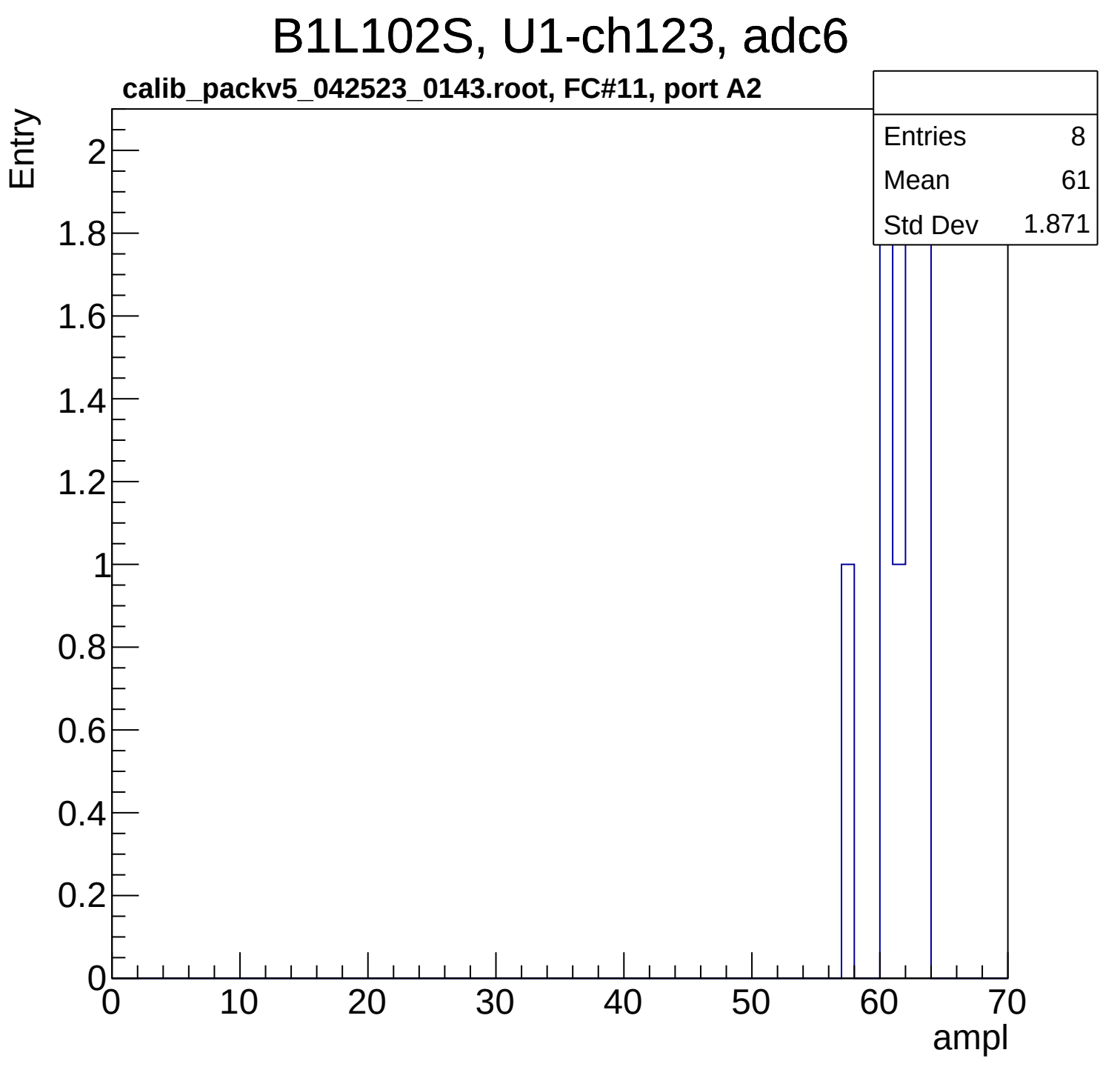
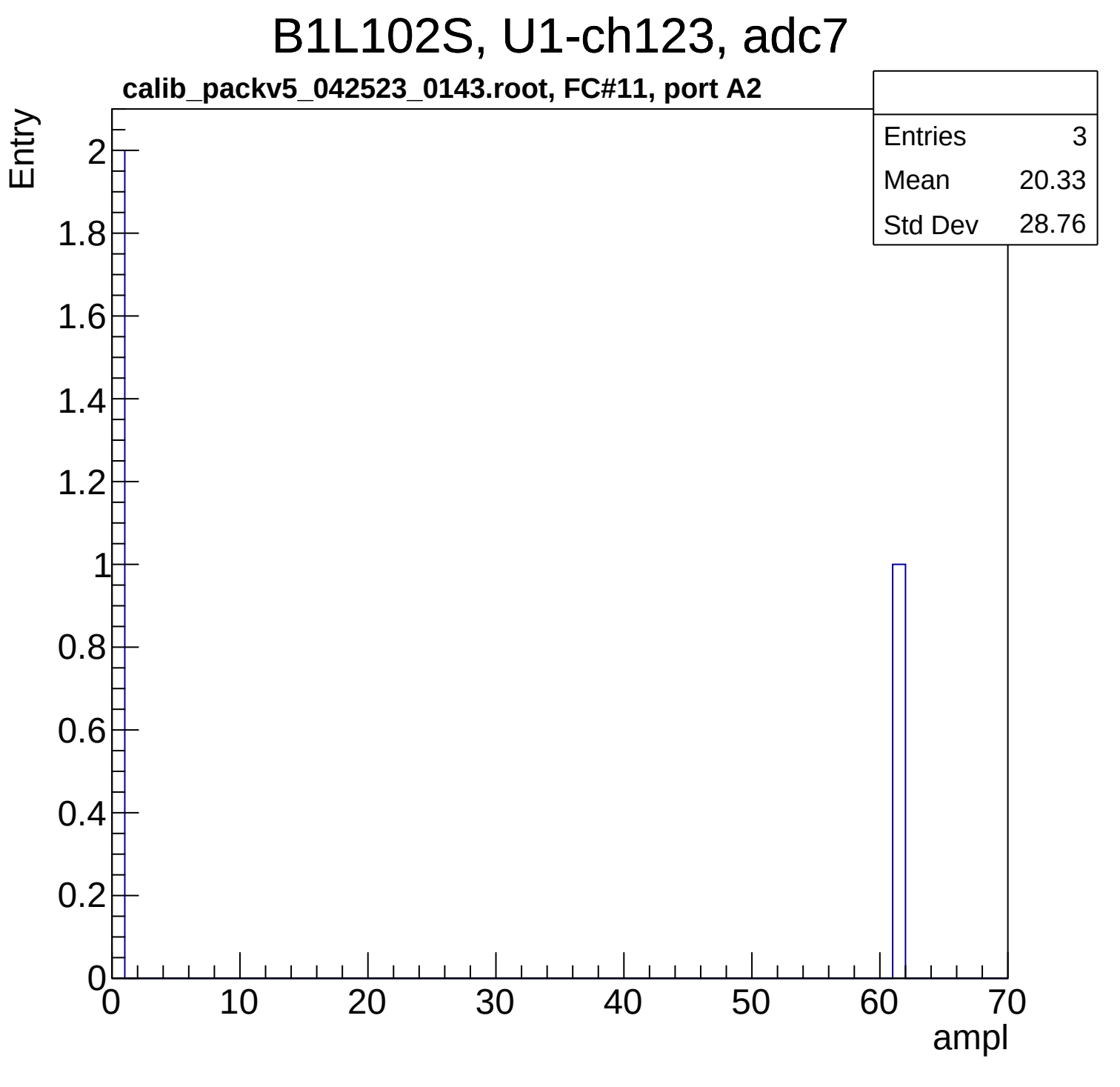
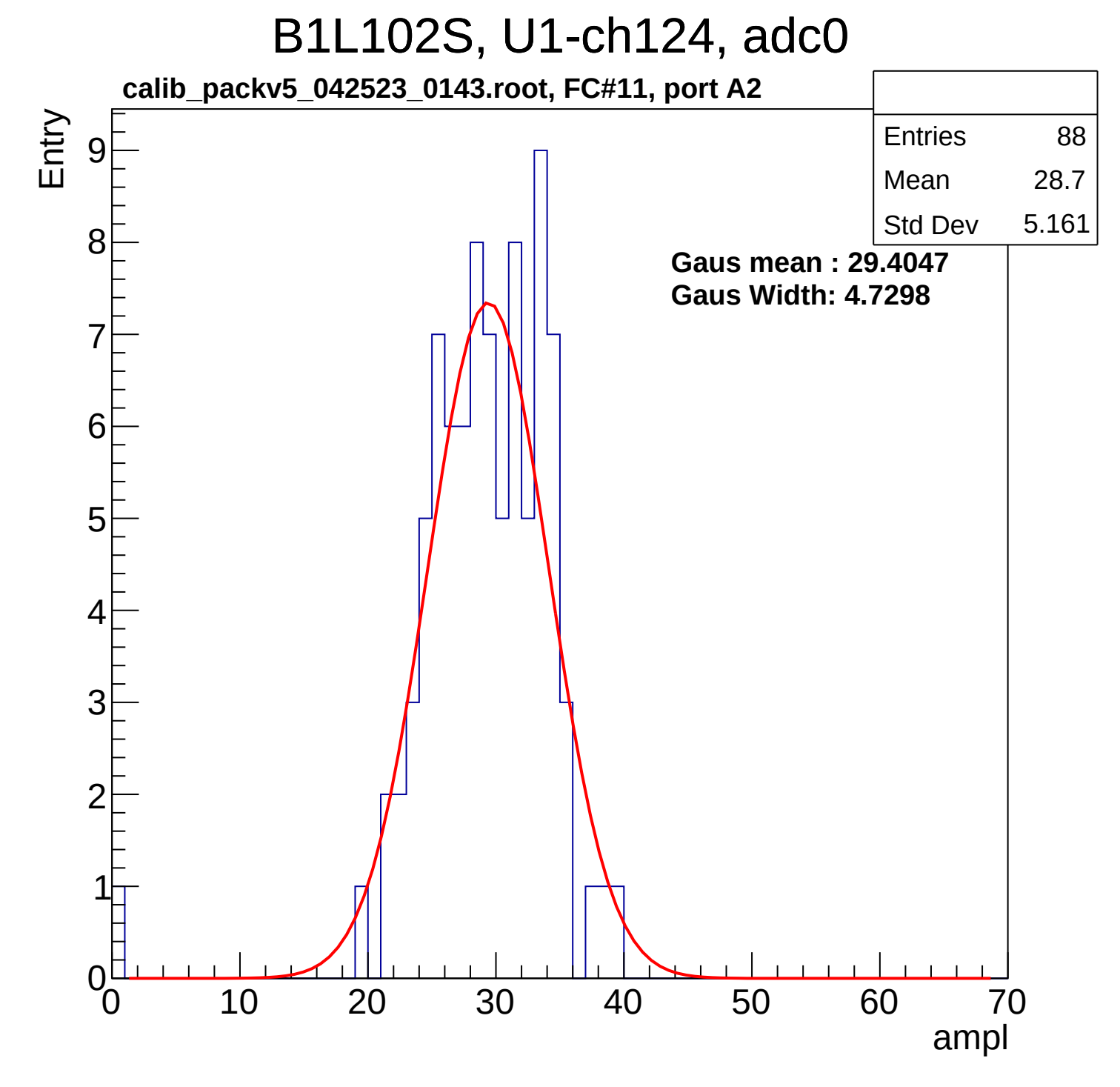
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 42
Mean 59.93
Std Dev 2.865
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 8
Mean 61
Std Dev 1.871
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 3
Mean 20.33
Std Dev 28.76
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L102S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 88
Mean 28.7
Std Dev 5.161
Gaus mean : 29.4047
Gaus Width: 4.7298
Entry
ampl
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1
2
3
4
5
6
7
8
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0
10
20
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40
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60
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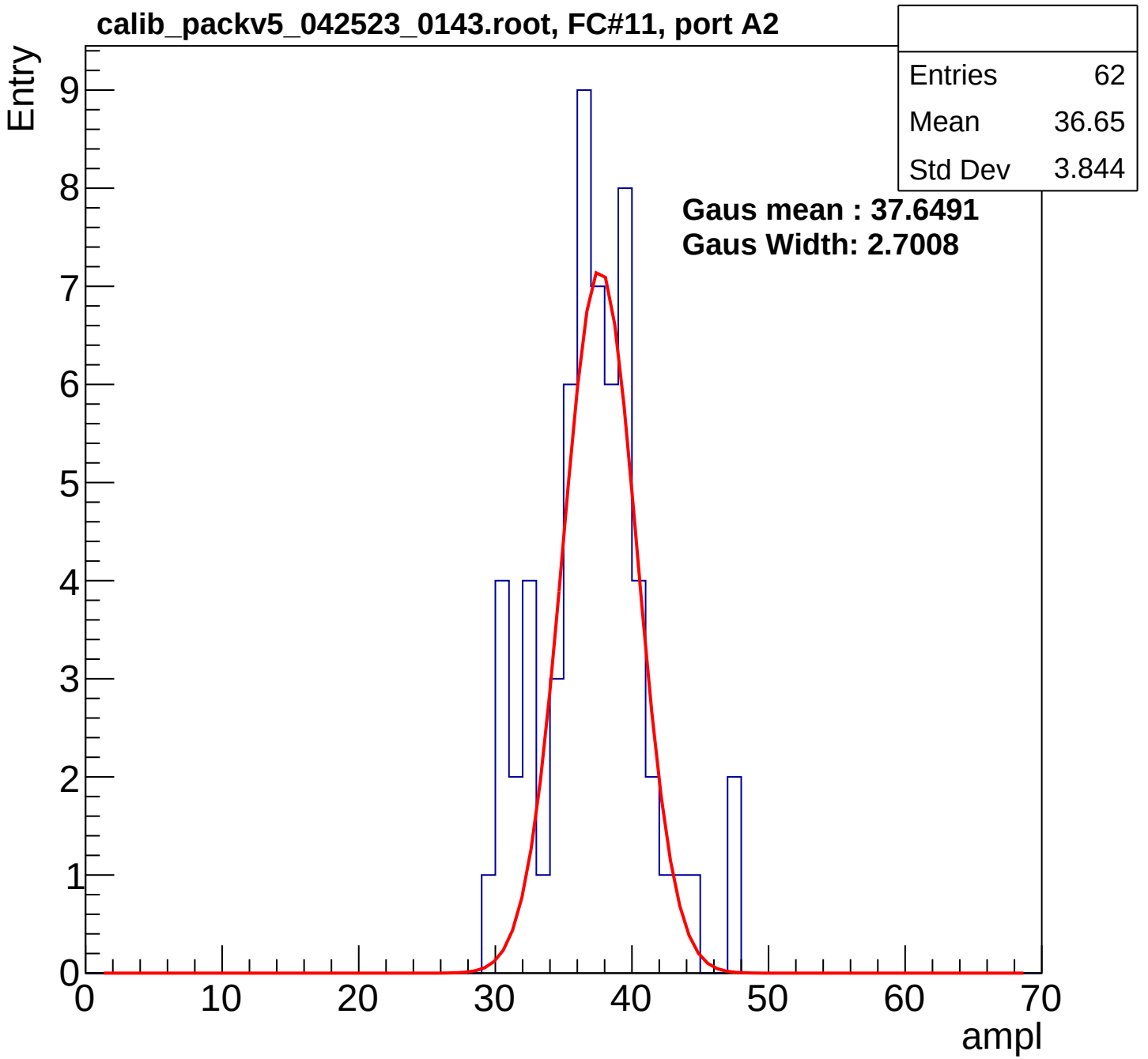
B1L102S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 36.65
Std Dev 3.844
Gaus mean : 37.6491
Gaus Width: 2.7008
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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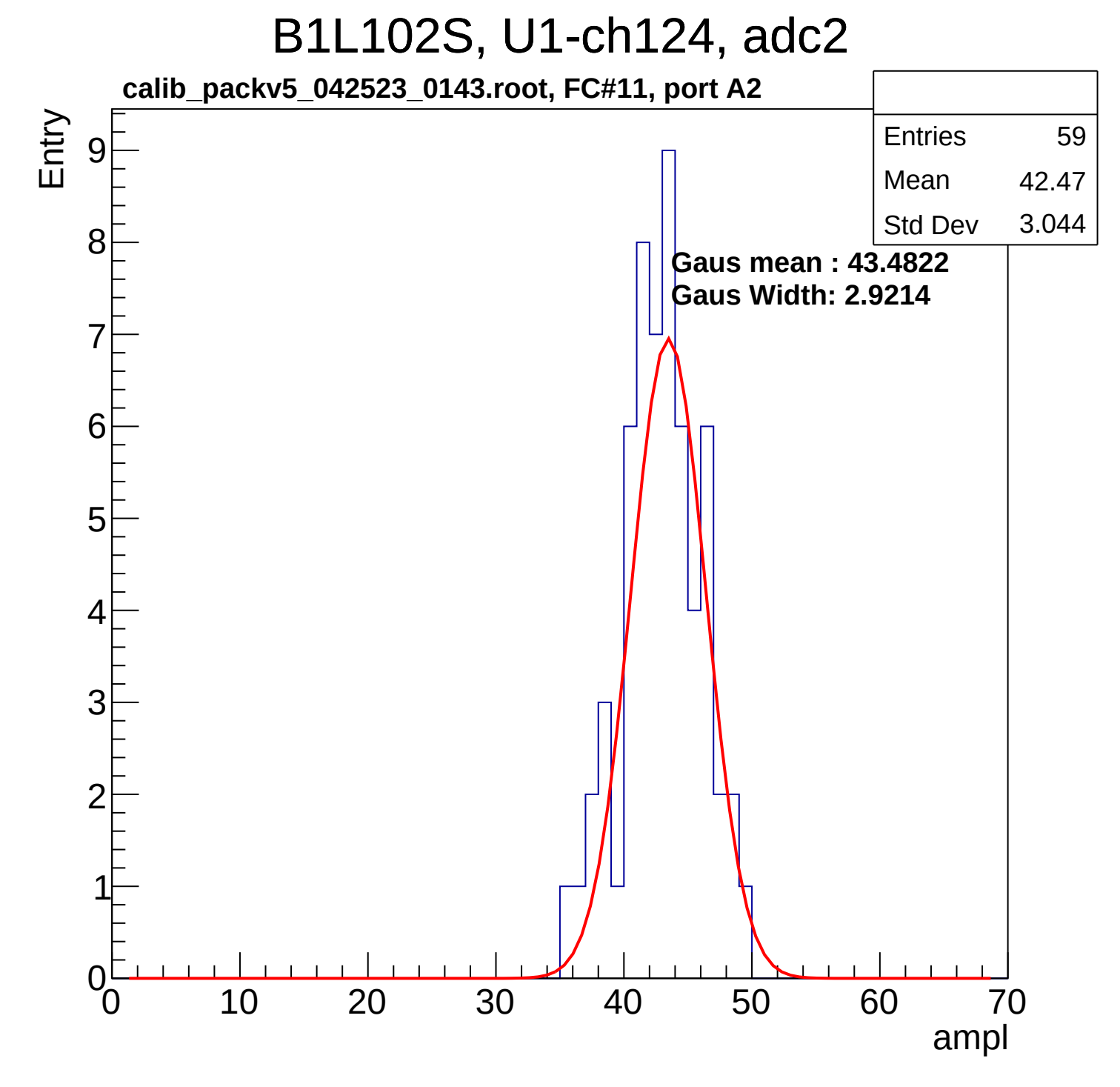
B1L102S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 59
Mean 42.47
Std Dev 3.044
Gaus mean : 43.4822
Gaus Width: 2.9214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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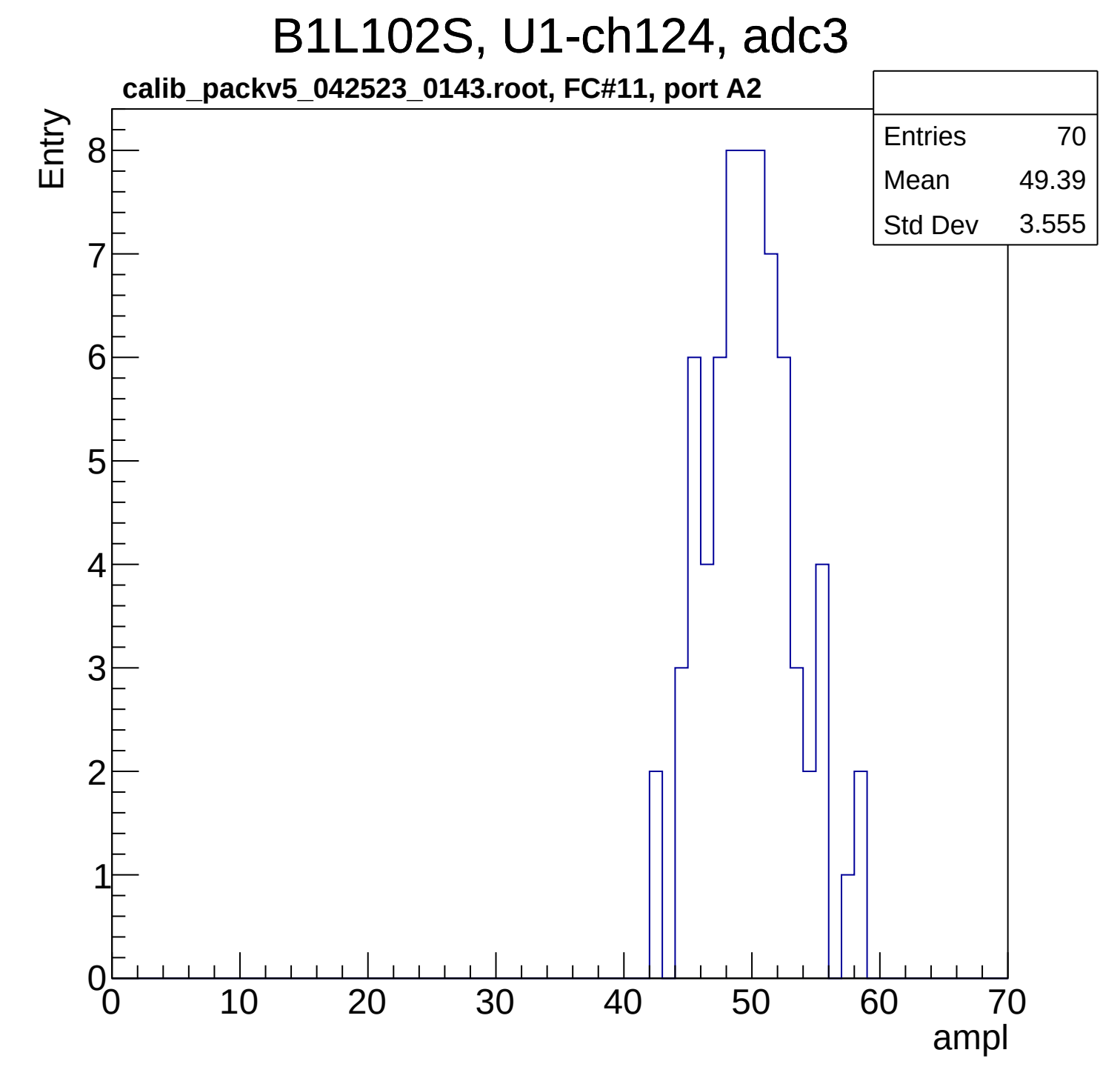
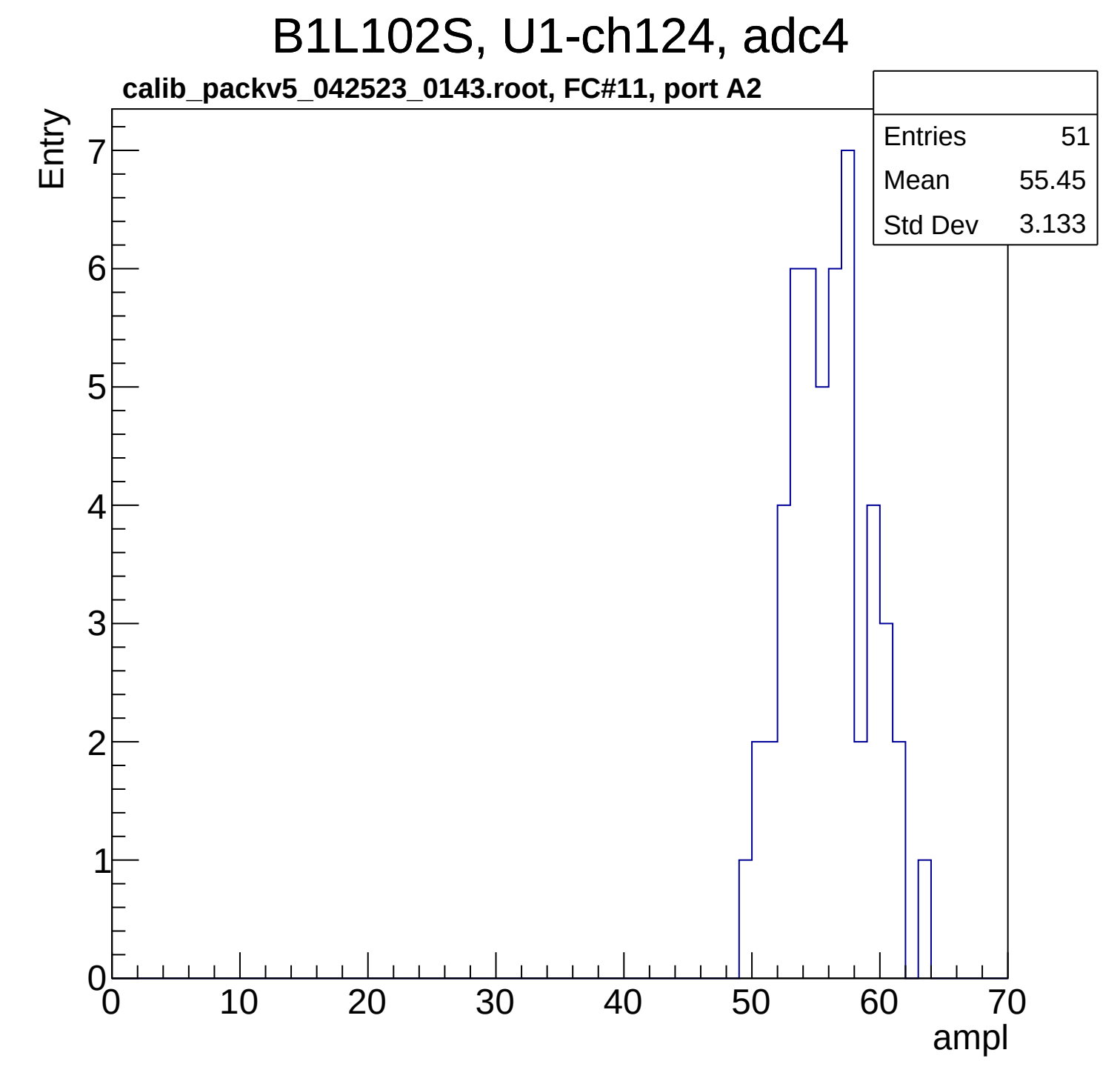
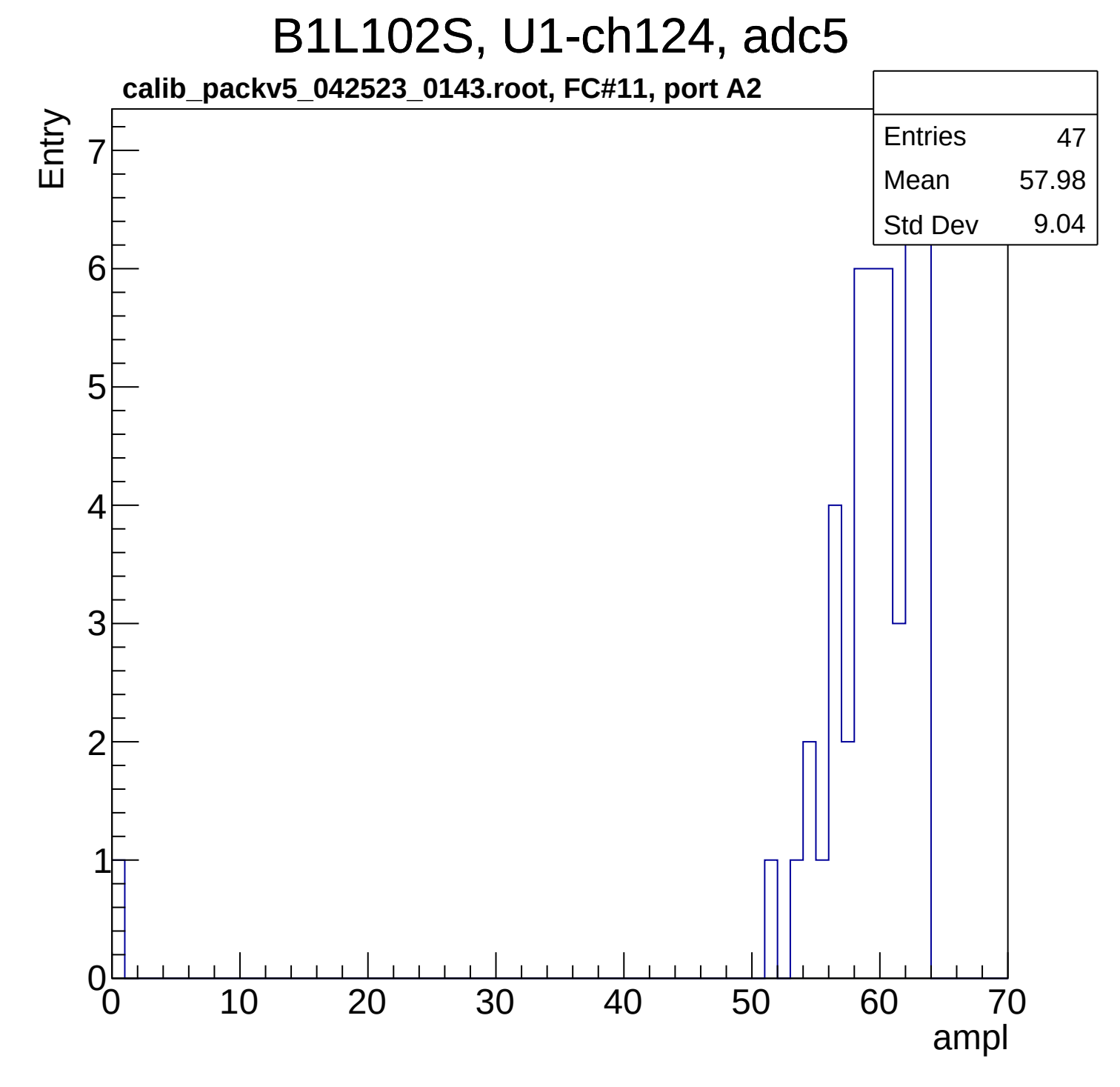

B1L102S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 70
Mean 49.39
Std Dev 3.555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 51
Mean 55.45
Std Dev 3.133
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 47
Mean 57.98
Std Dev 9.04
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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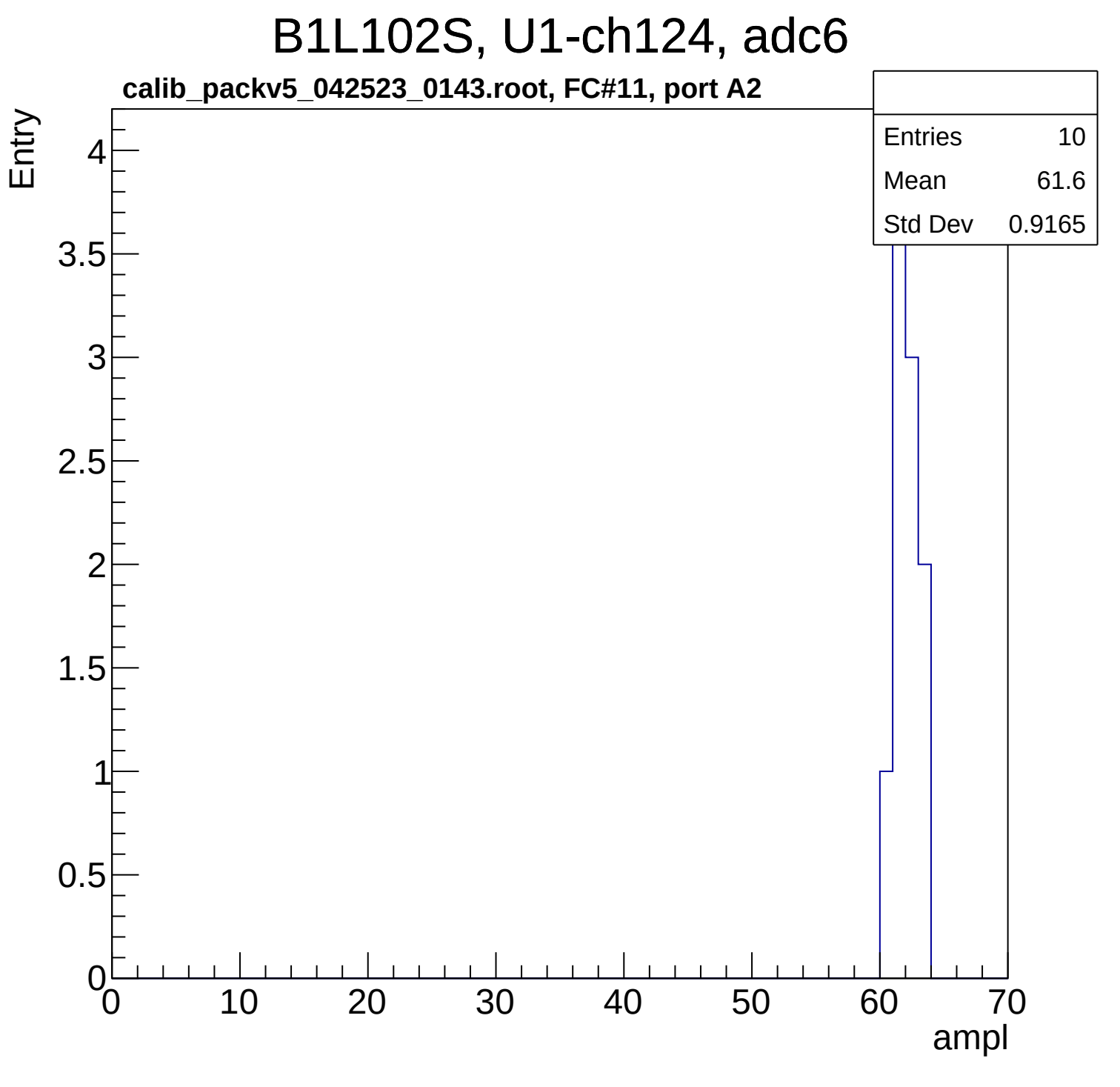
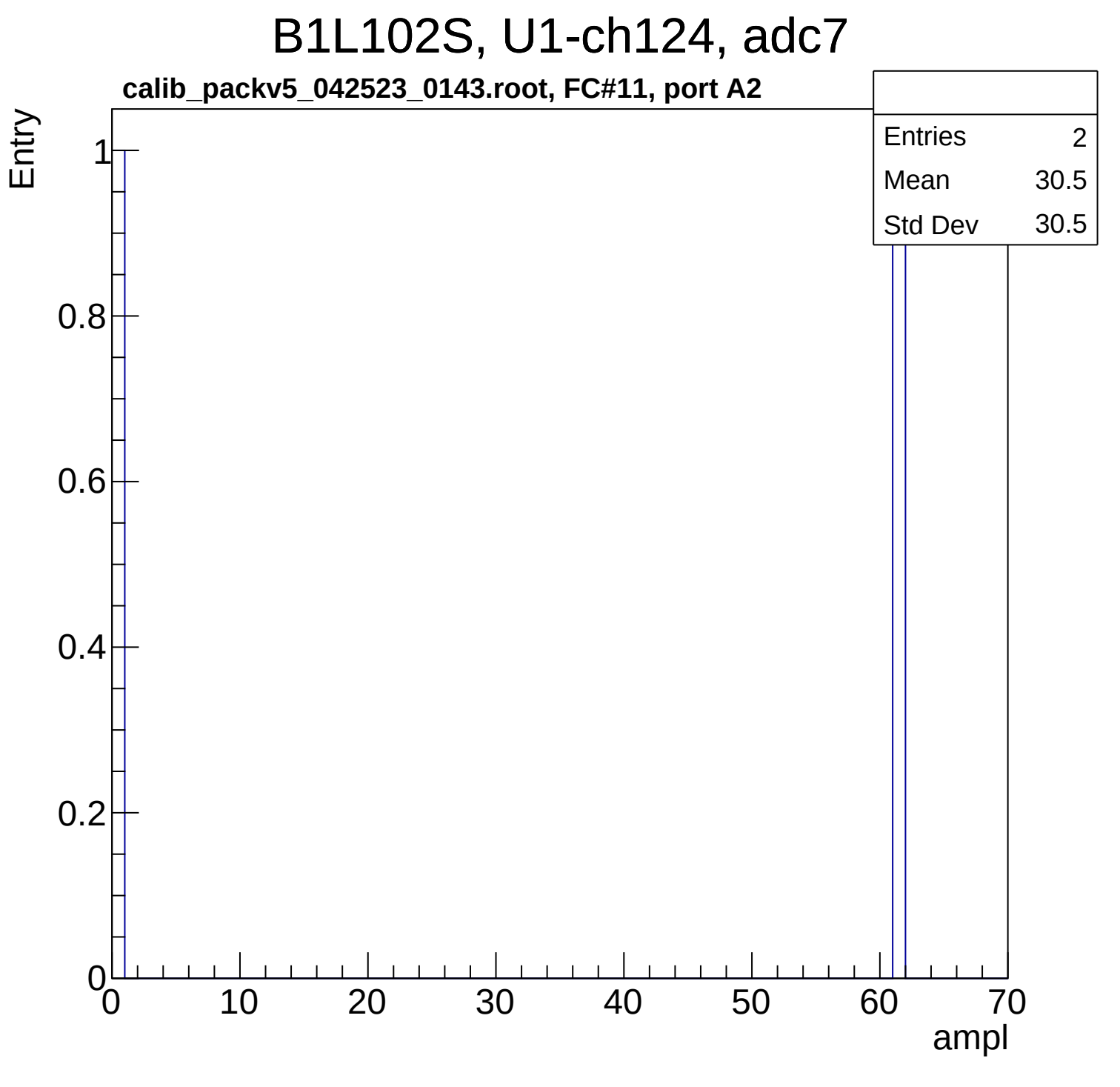
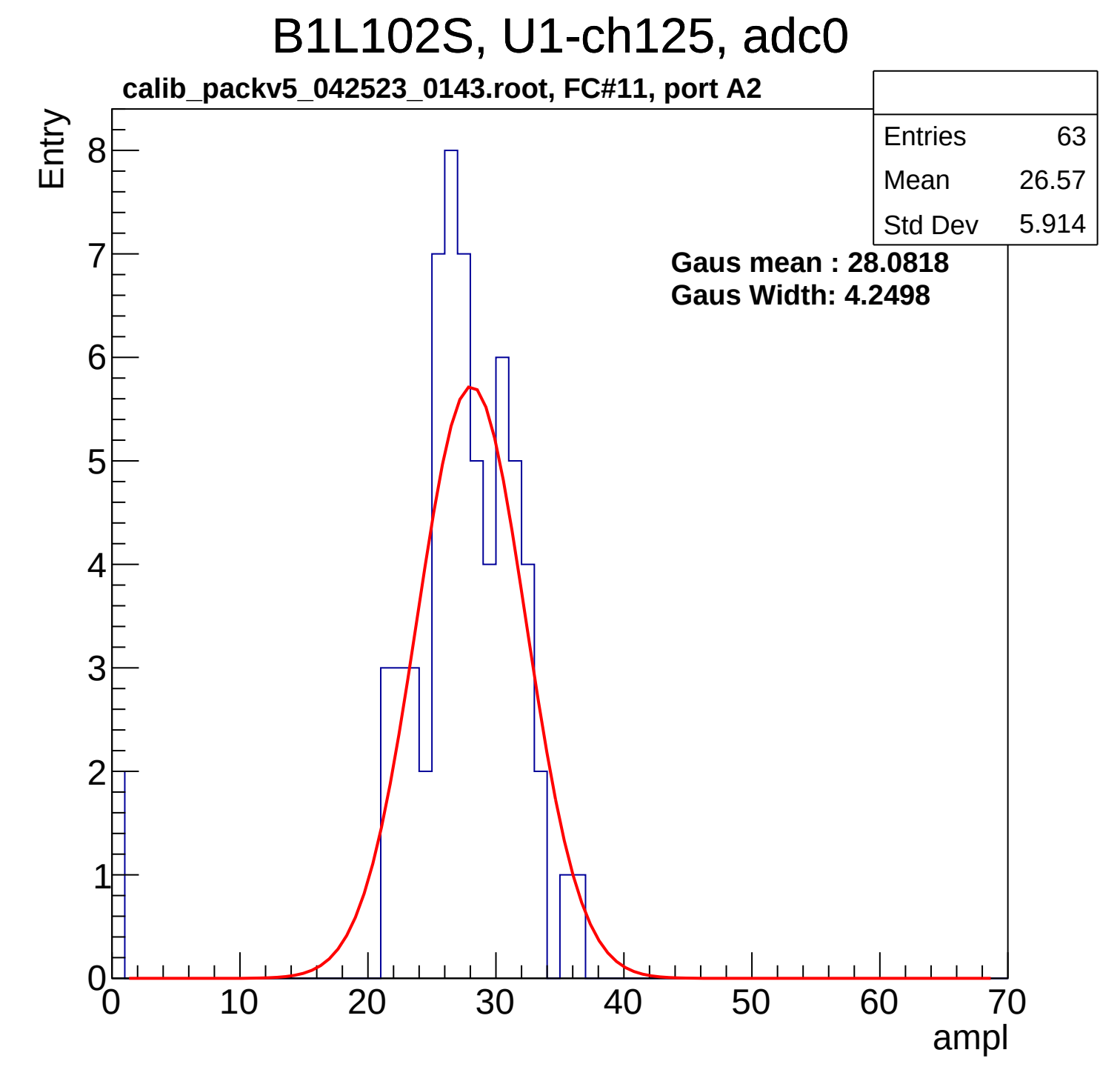
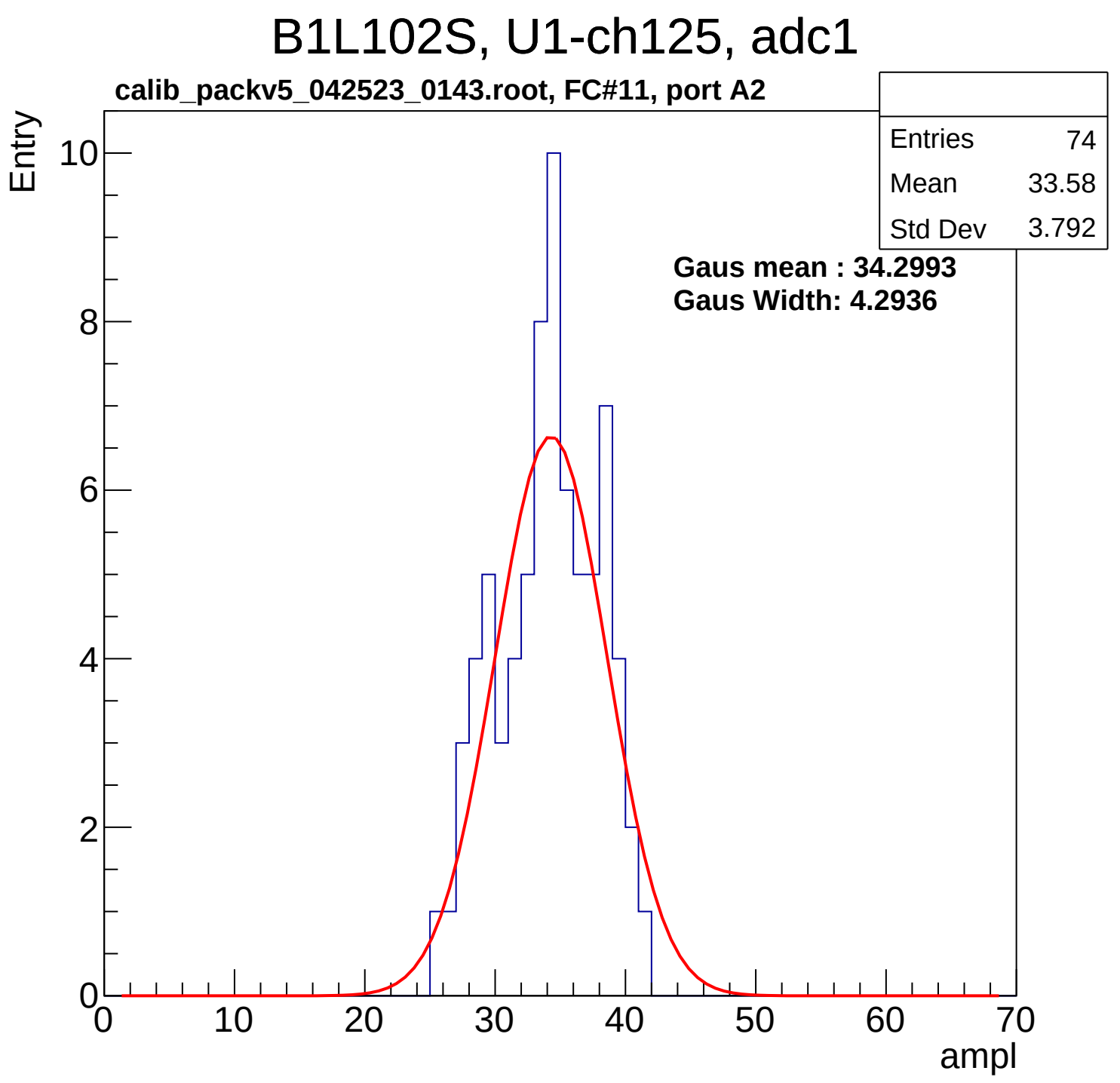
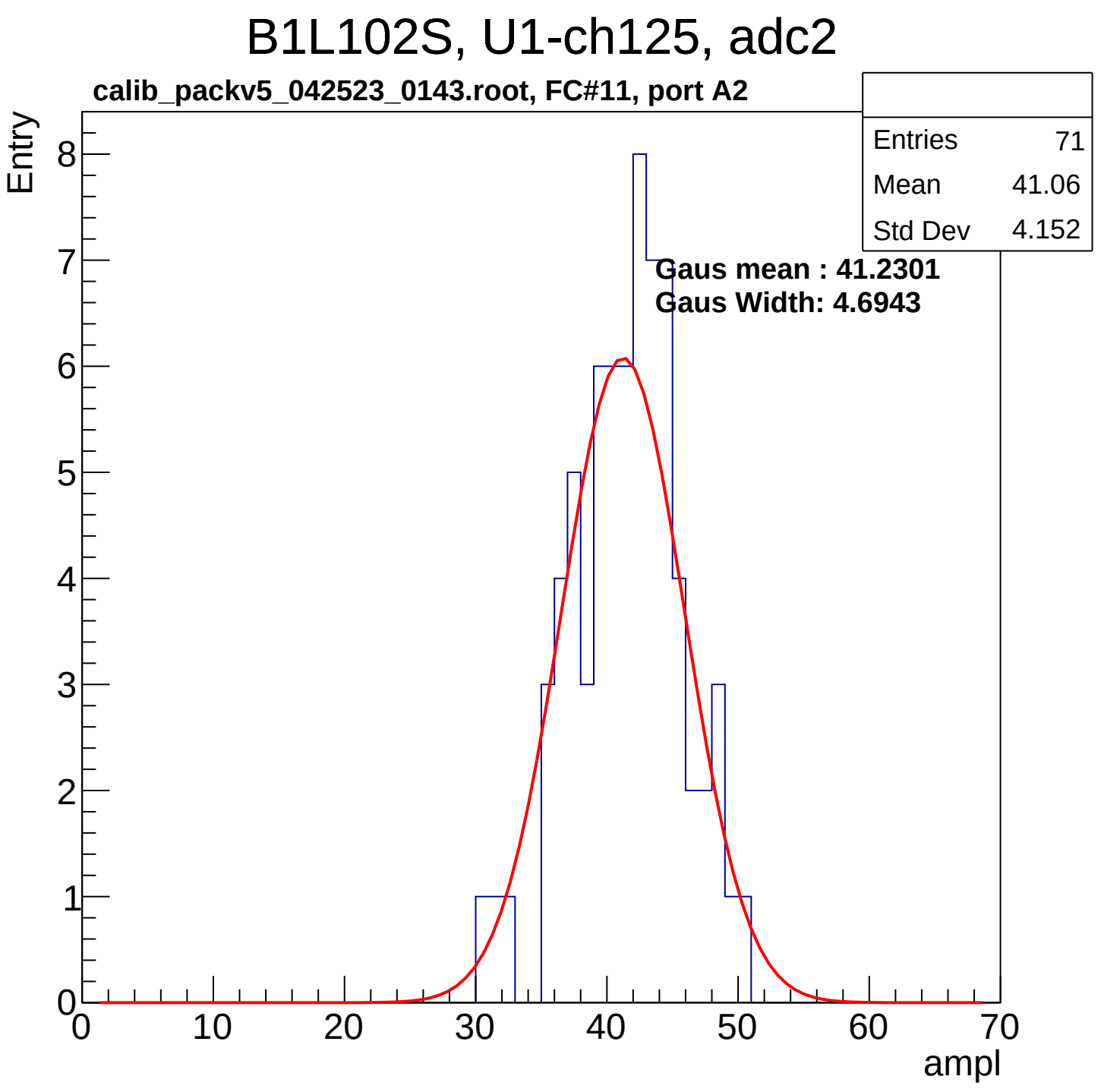

B1L102S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 10
Mean 61.6
Std Dev 0.9165
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L102S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 63
Mean 26.57
Std Dev 5.914
Gaus mean : 28.0818
Gaus Width: 4.2498
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 33.58
Std Dev 3.792
Gaus mean : 34.2993
Gaus Width: 4.2936
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 41.06
Std Dev 4.152
Gaus mean : 41.2301
Gaus Width: 4.6943
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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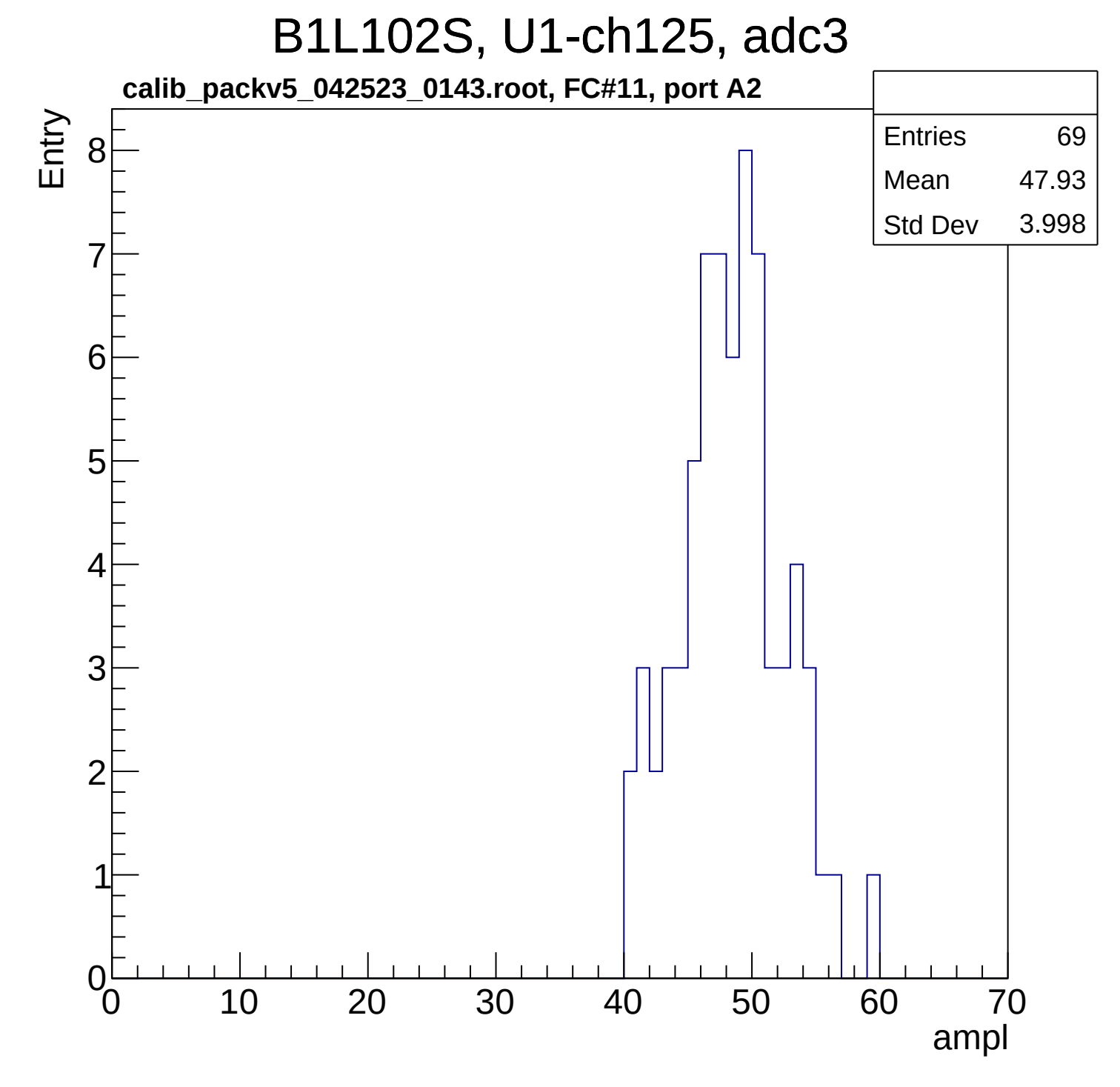
B1L102S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 69
Mean 47.93
Std Dev 3.998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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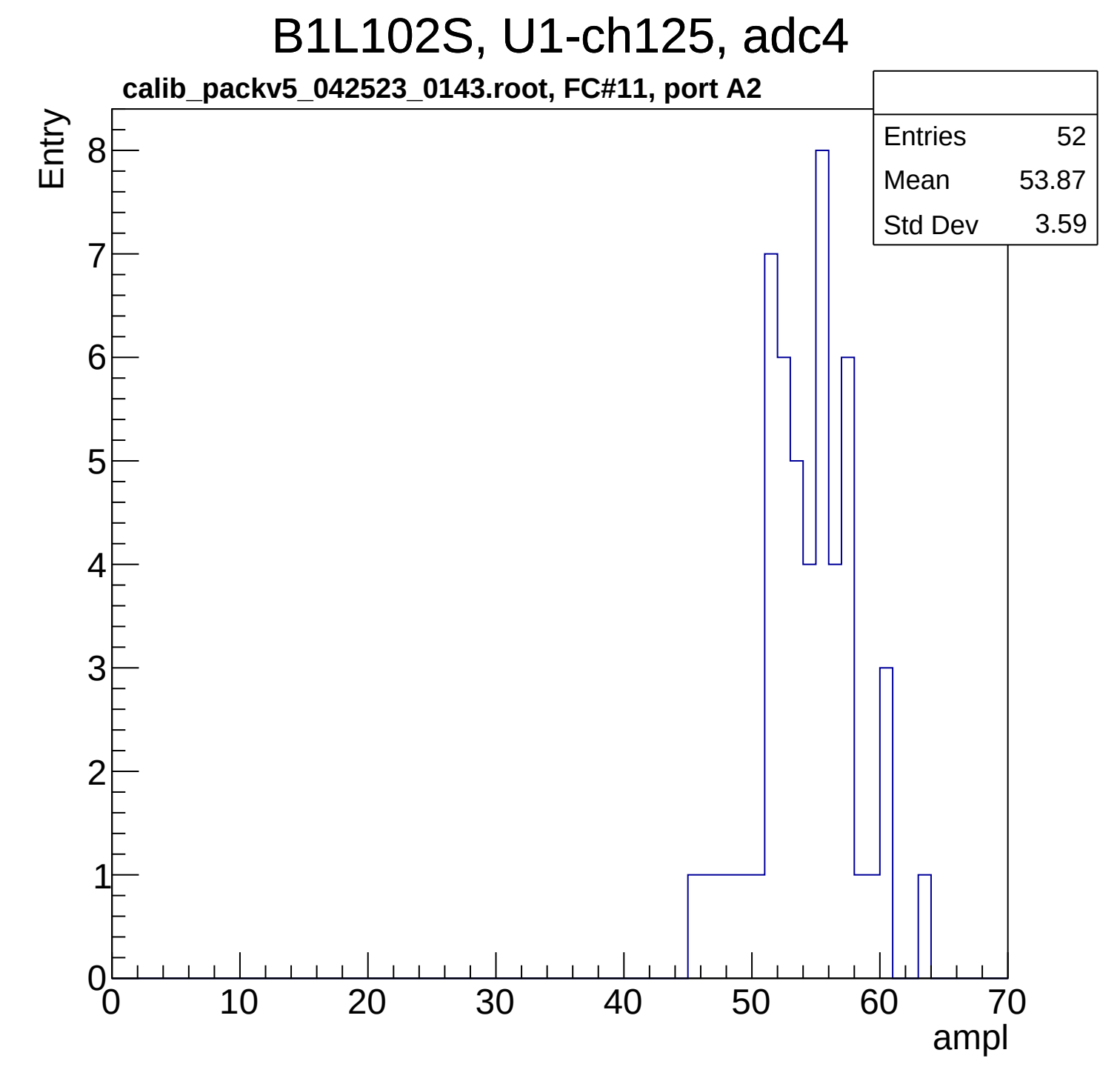
B1L102S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 52
Mean 53.87
Std Dev 3.59
Entry
ampl
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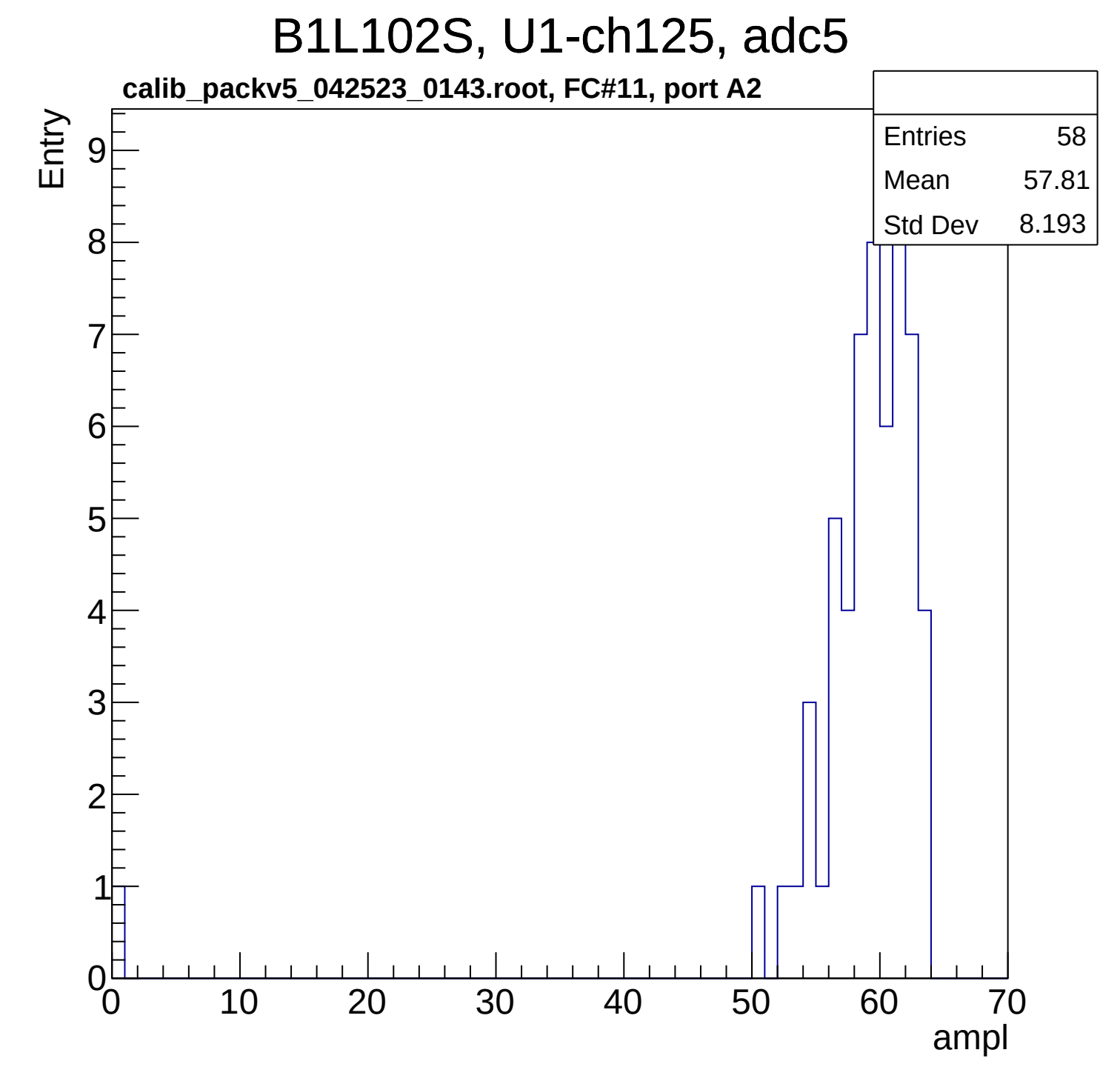
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 57.81
Std Dev 8.193
Entry
ampl
0
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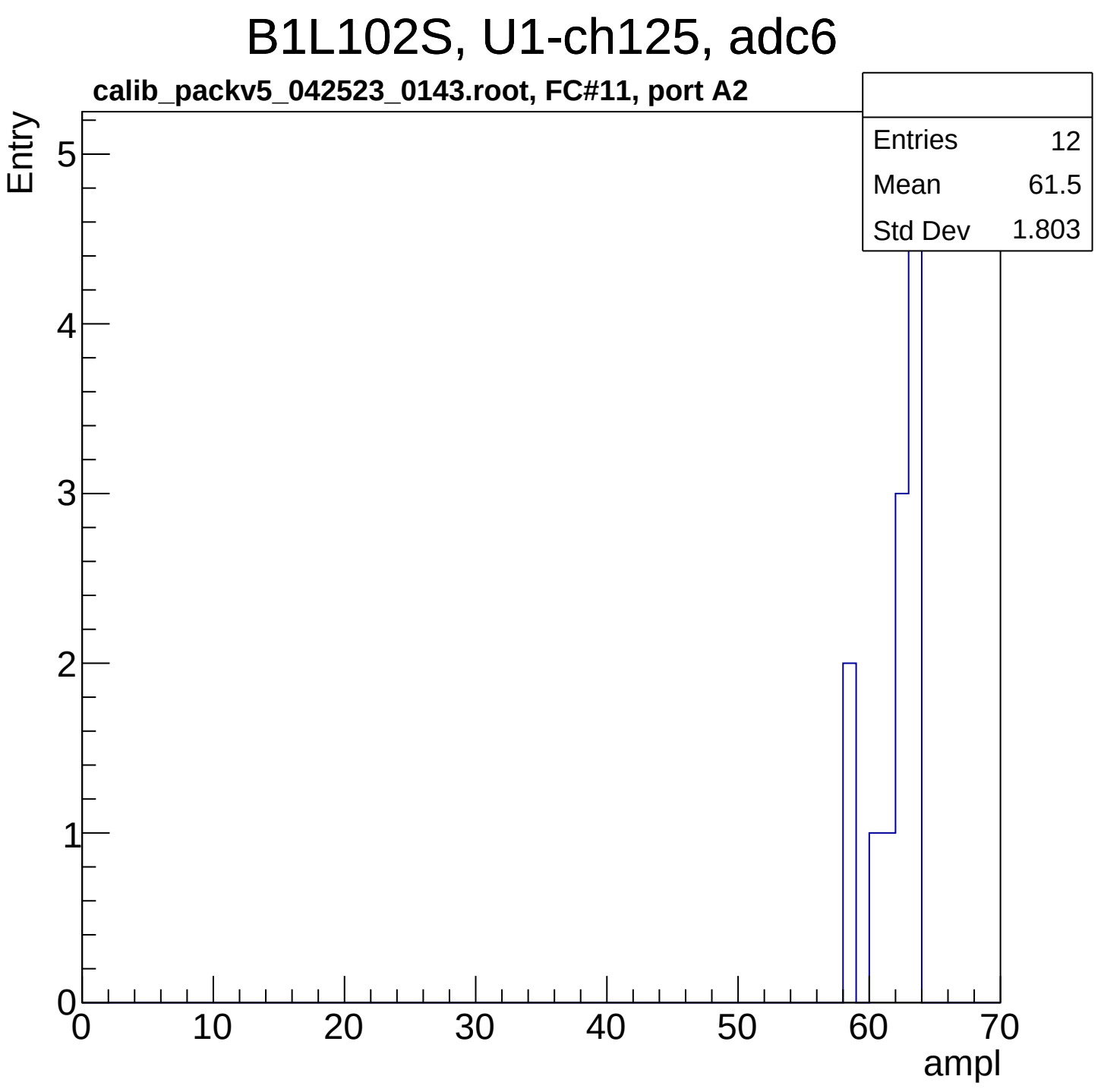
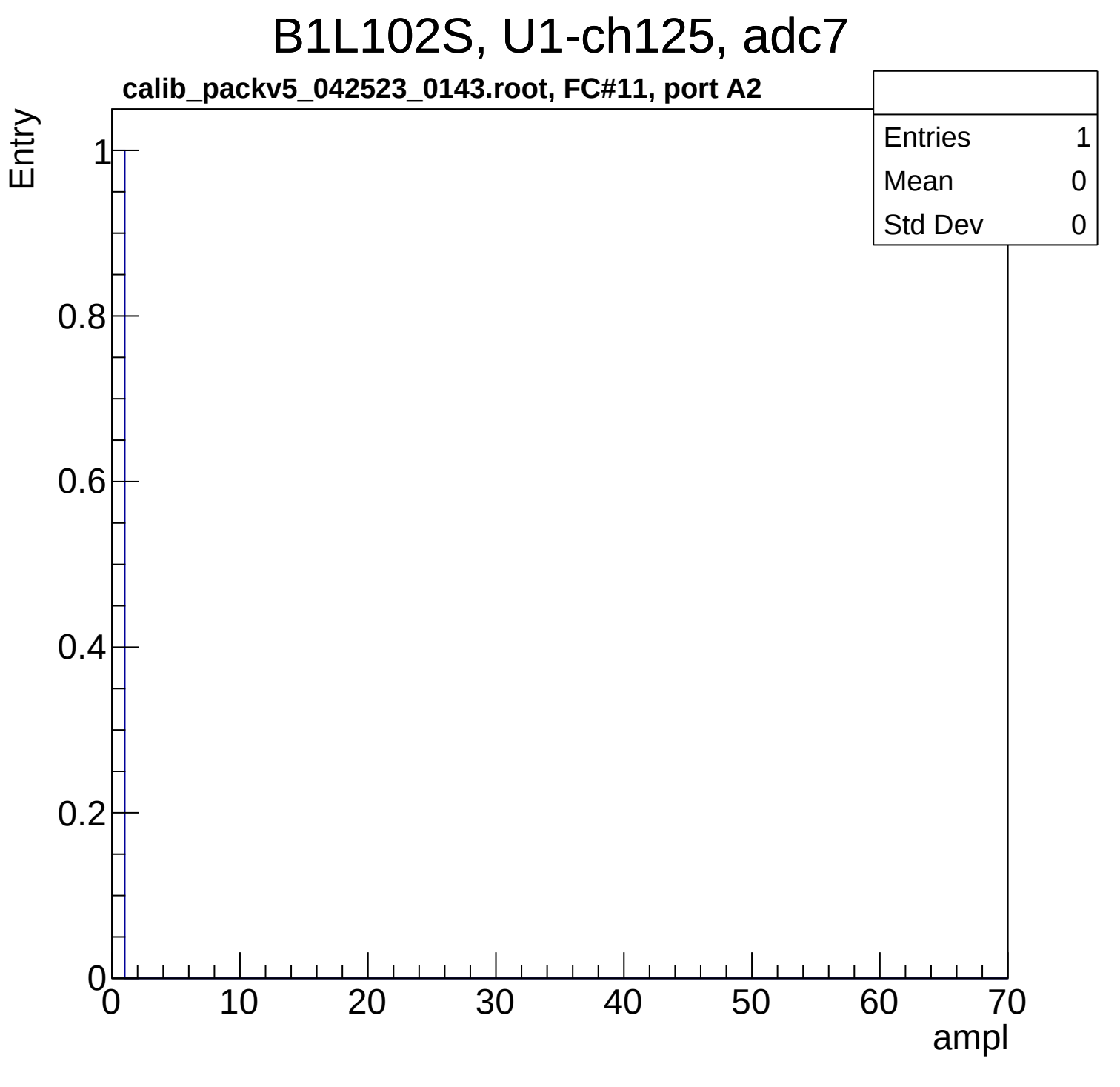
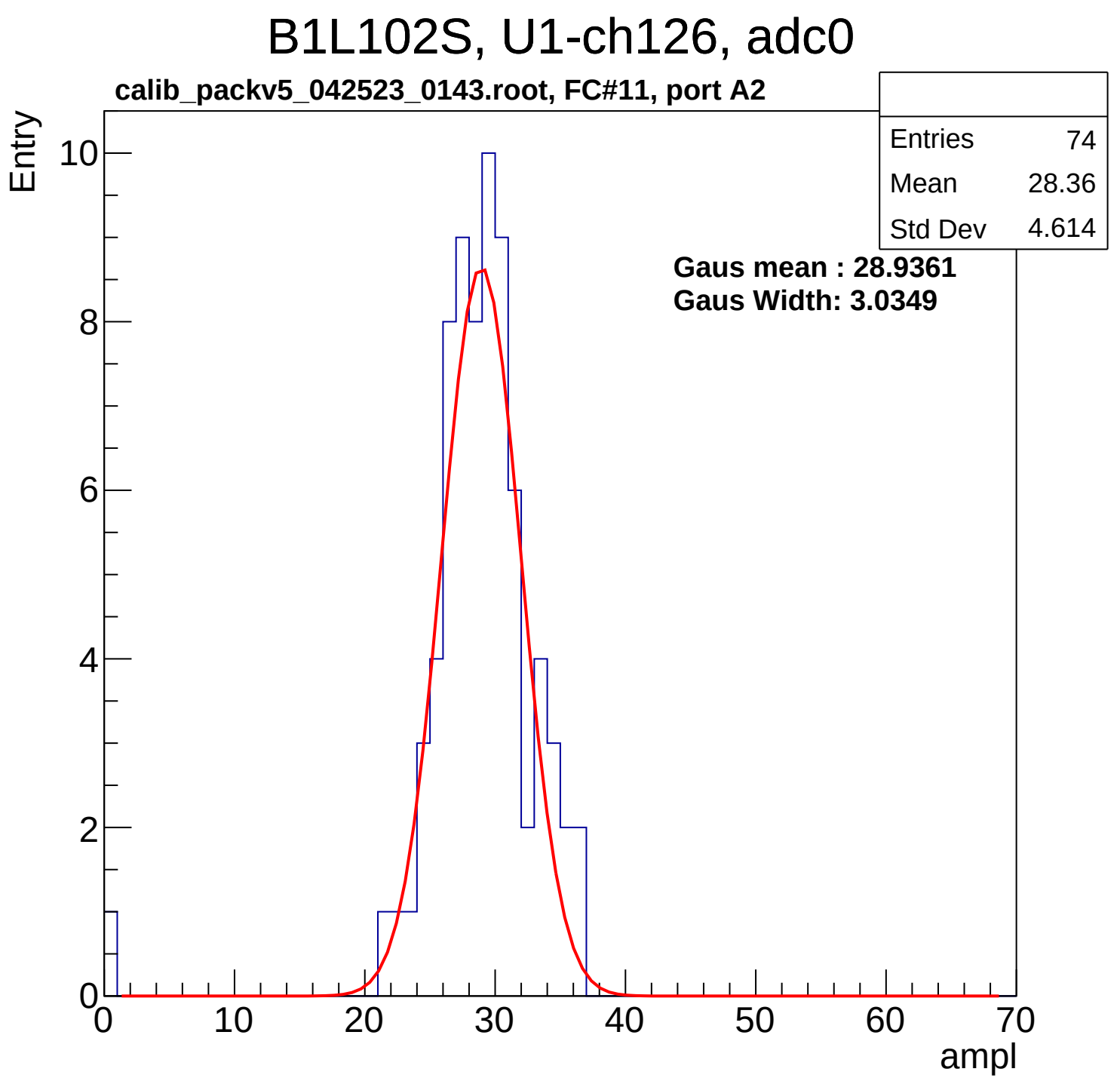
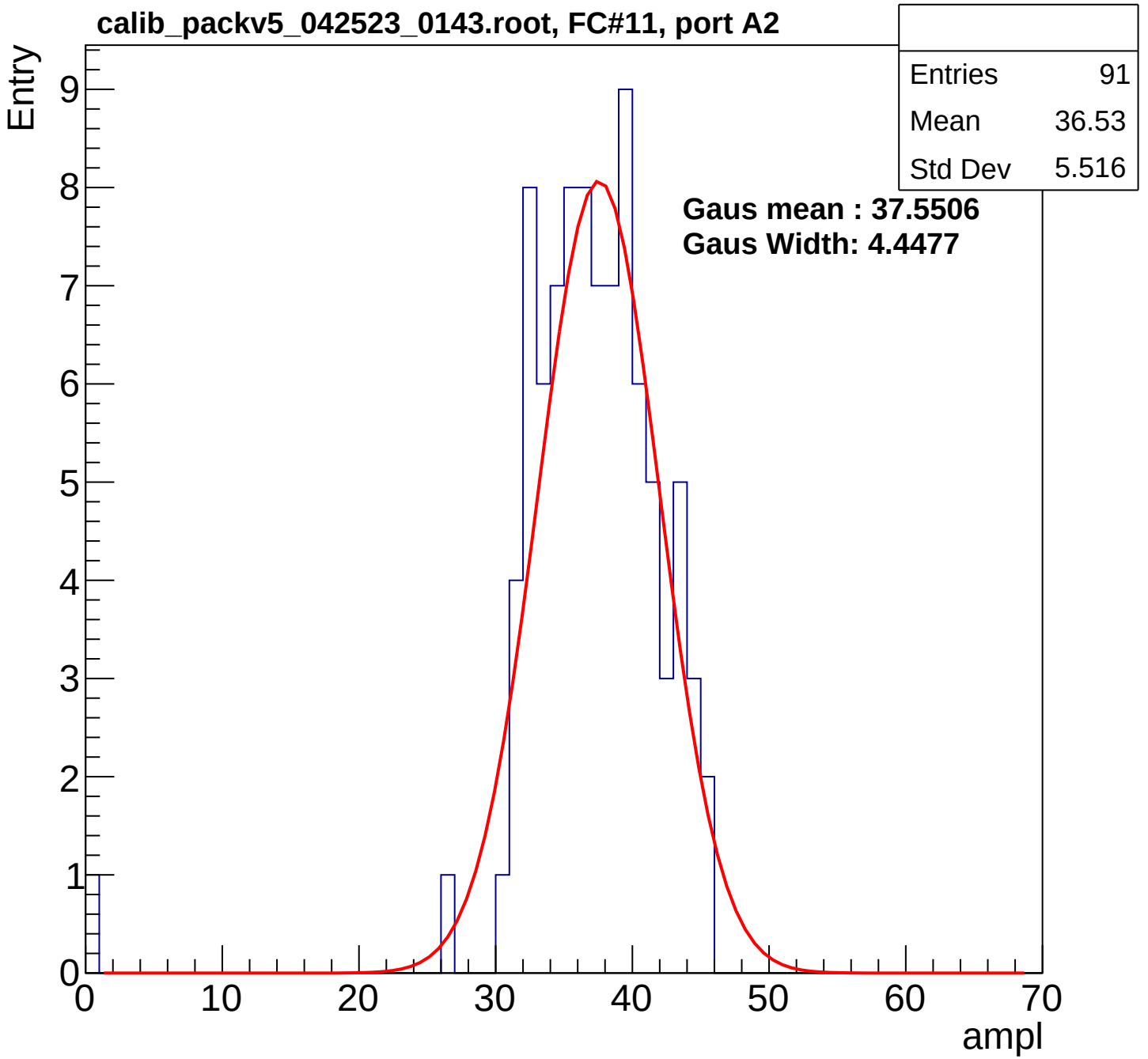
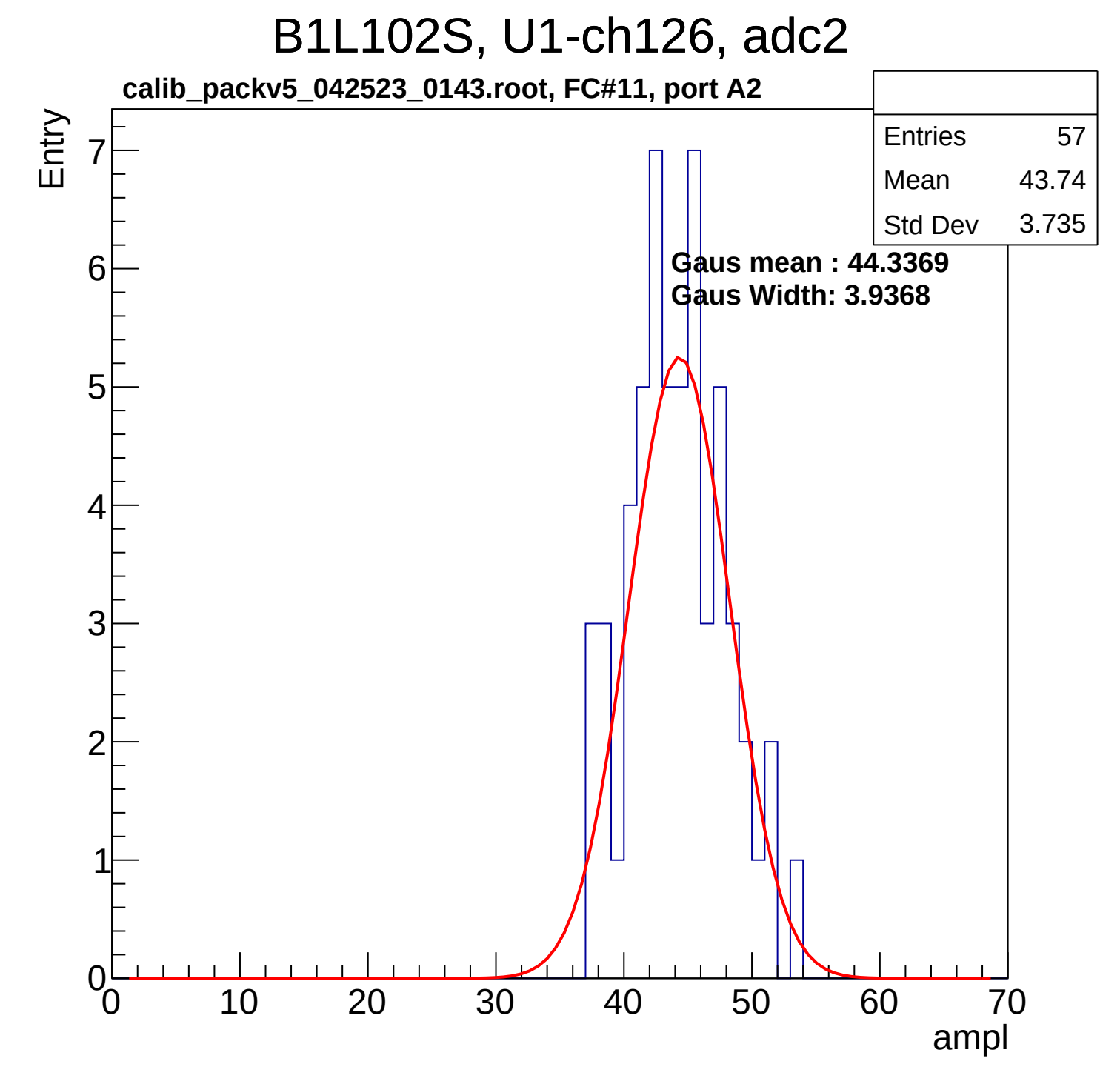
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 12
Mean 61.5
Std Dev 1.803
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 74
Mean 28.36
Std Dev 4.614
Gaus mean : 28.9361
Gaus Width: 3.0349
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L102S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 91
Mean 36.53
Std Dev 5.516
Gaus mean : 37.5506
Gaus Width: 4.4477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 57
Mean 43.74
Std Dev 3.735
Gaus mean : 44.3369
Gaus Width: 3.9368
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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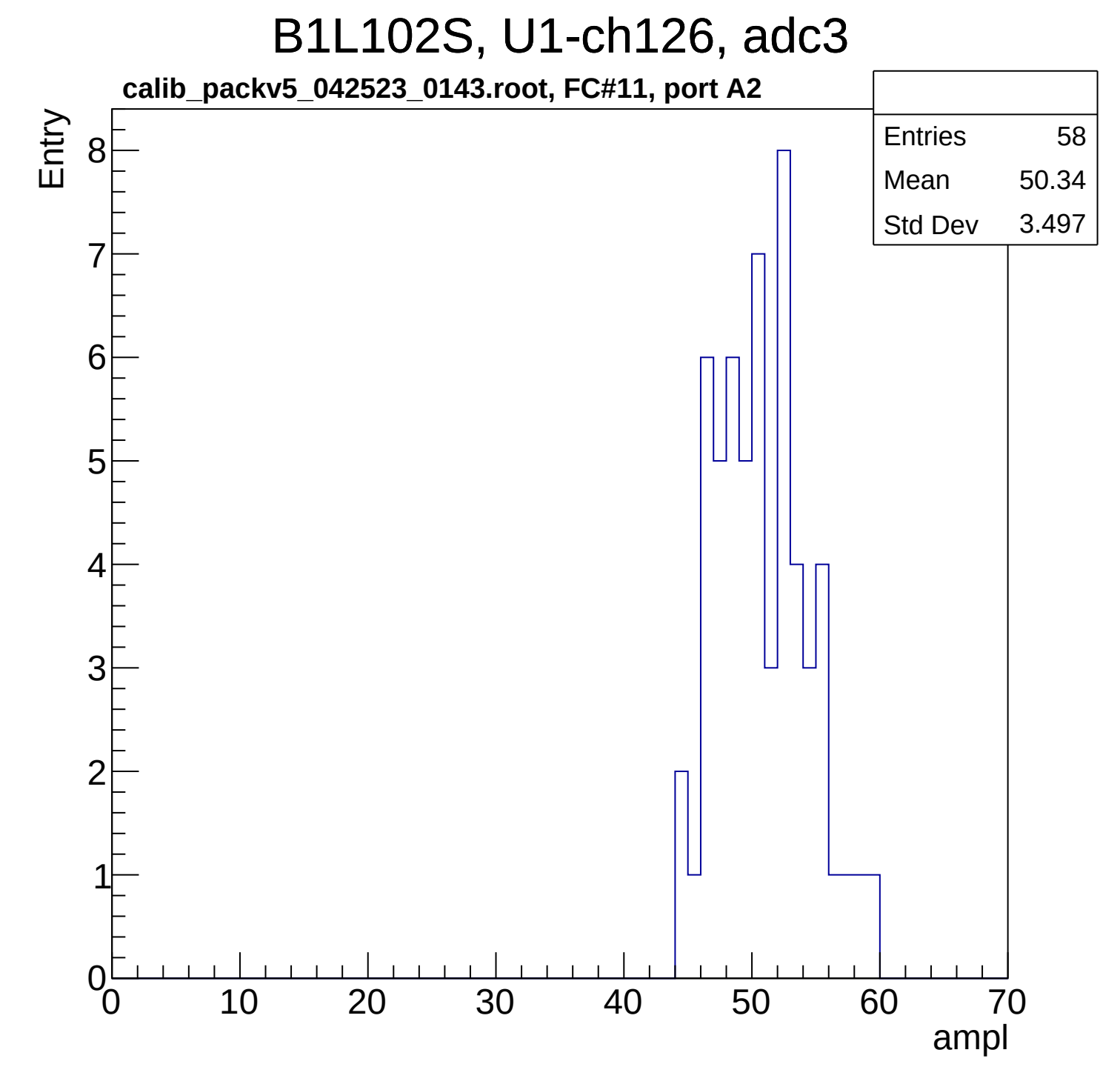
B1L102S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 58
Mean 50.34
Std Dev 3.497
Entry
ampl
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1
2
3
4
5
6
7
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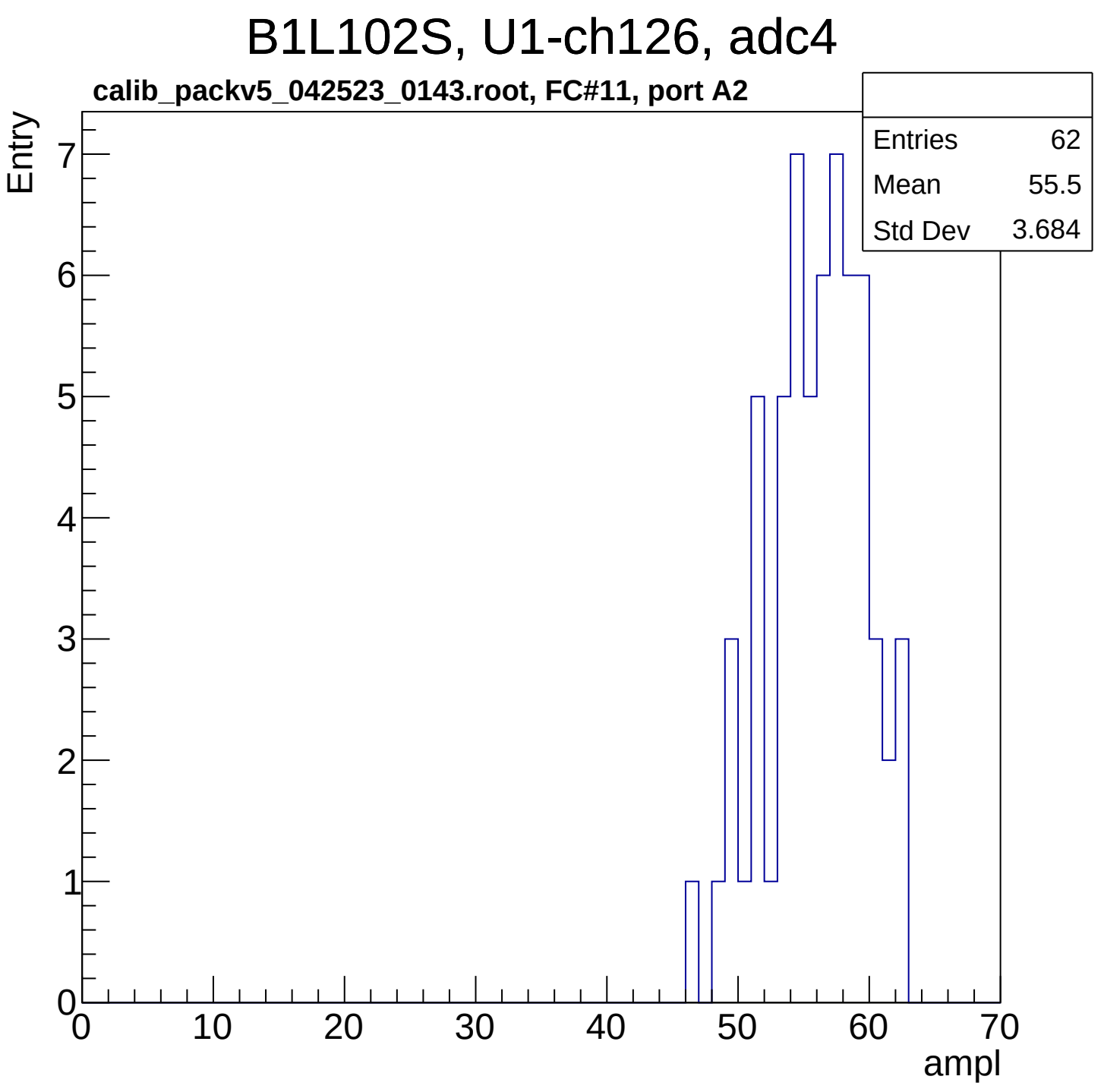
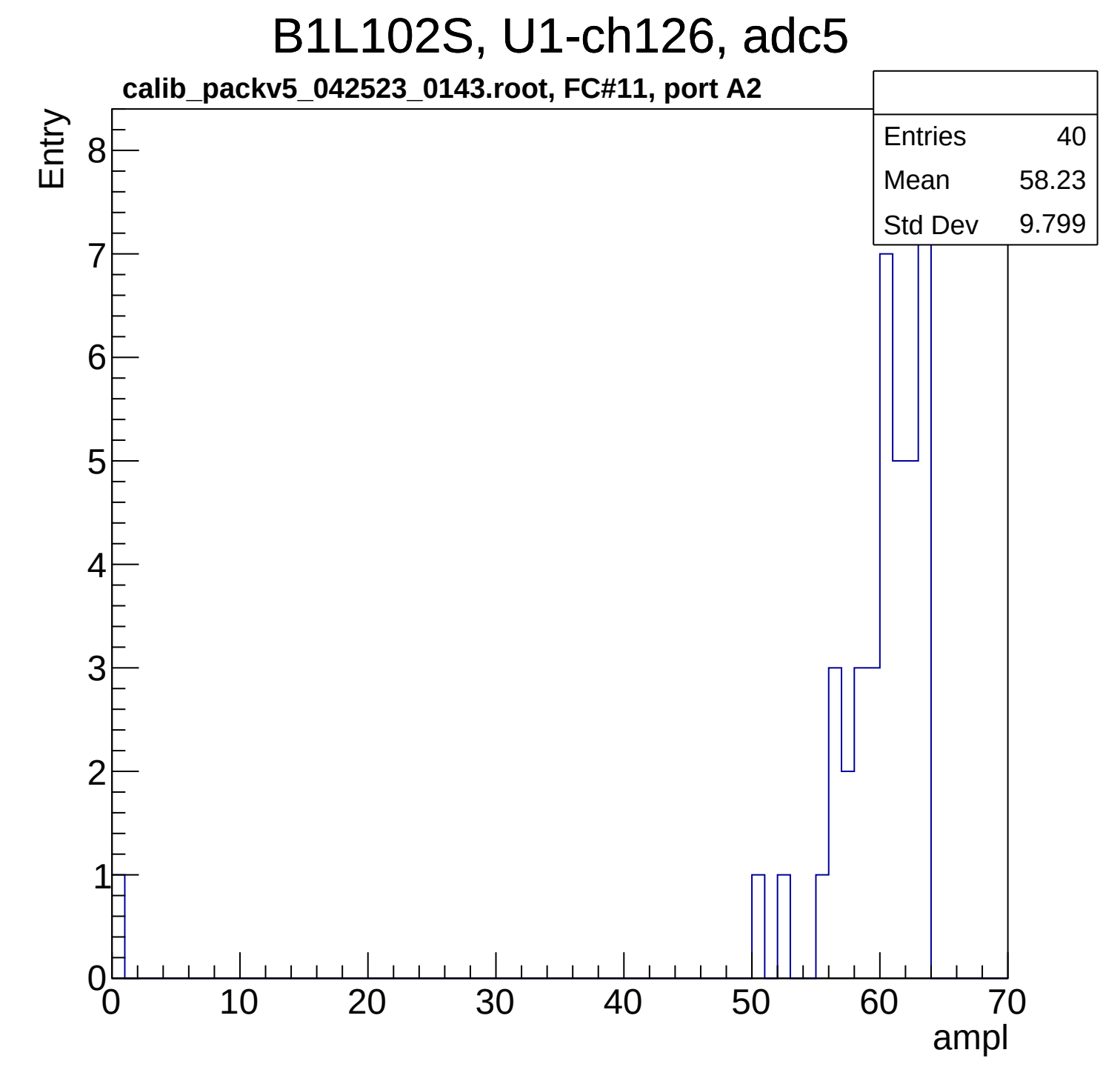
0
10
20
30
40
50
60
70


B1L102S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 55.5
Std Dev 3.684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L102S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 40
Mean 58.23
Std Dev 9.799
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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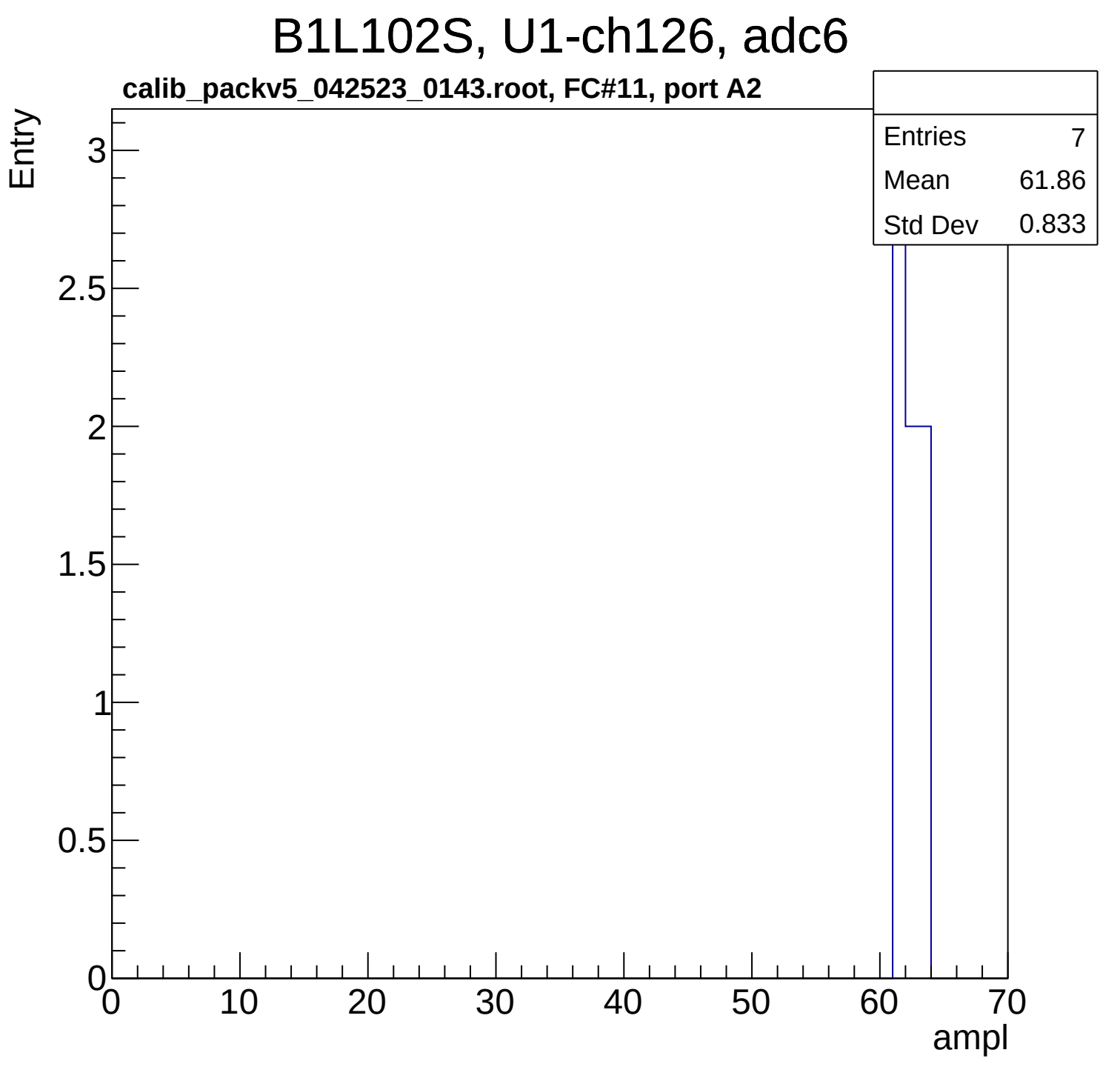
40
50
60
70


B1L102S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 7
Mean 61.86
Std Dev 0.833
Entry
ampl
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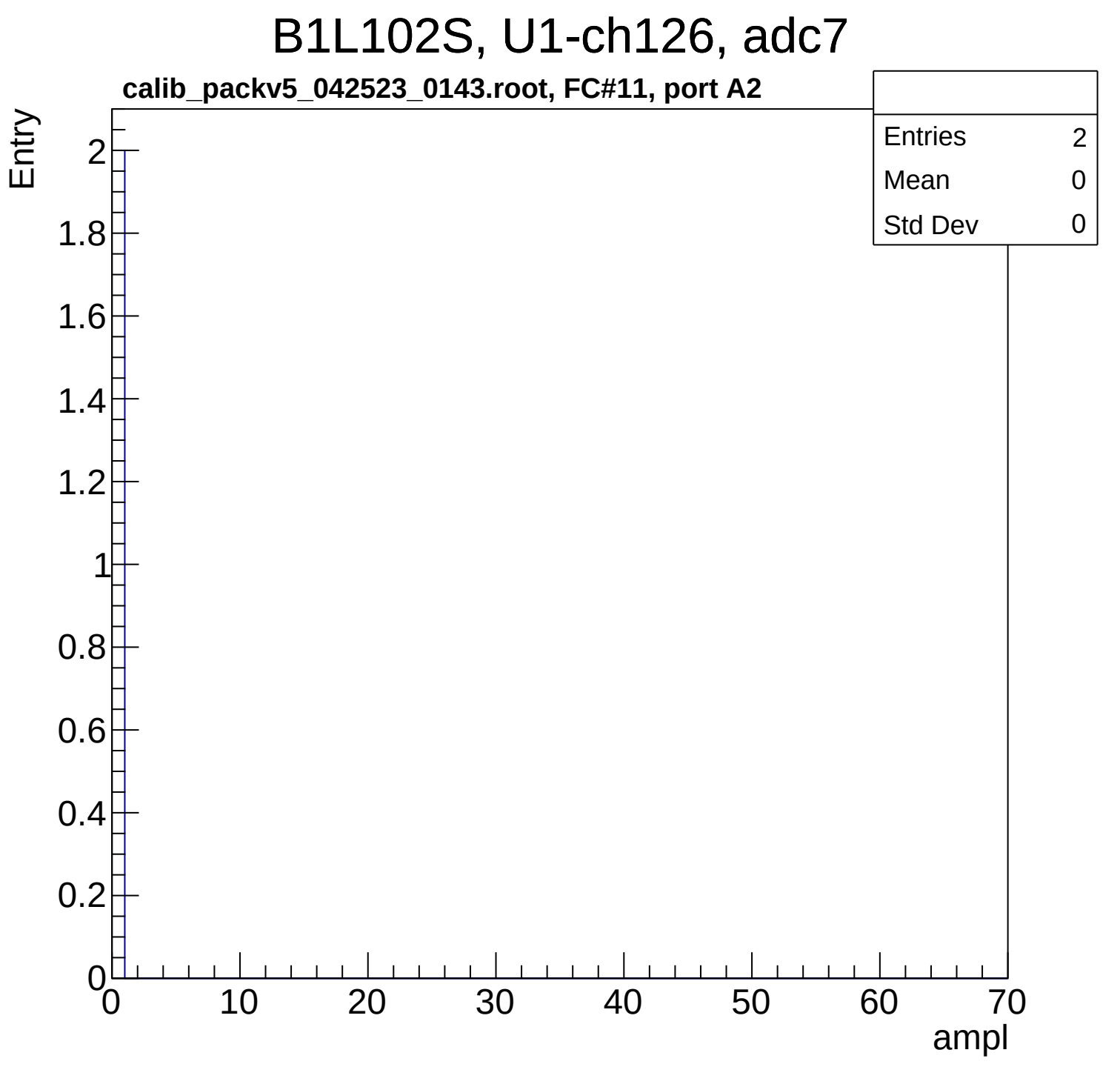
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L102S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
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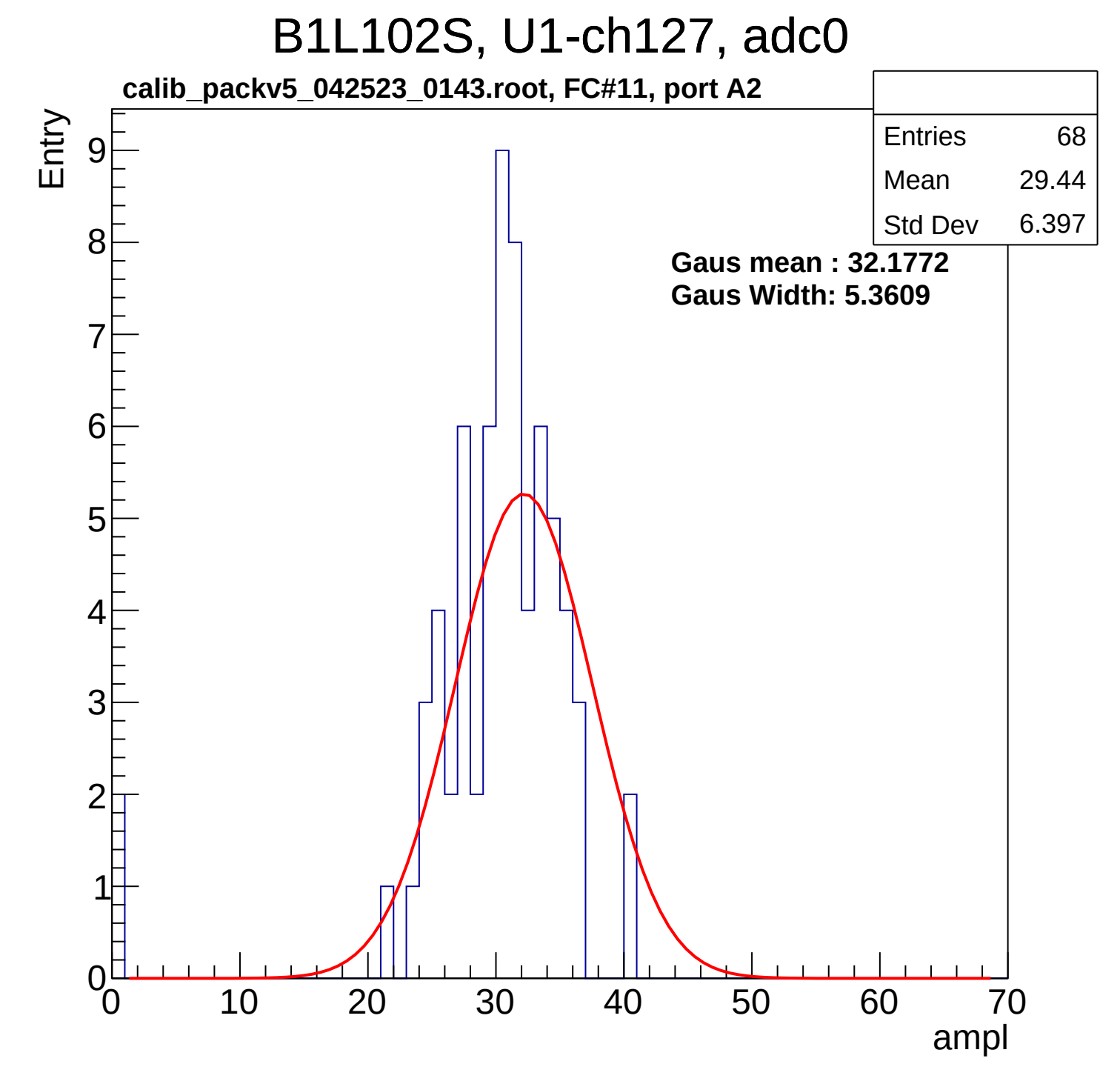
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#11, port A2
Entries 68
Mean 29.44
Std Dev 6.397
Gaus mean : 32.1772
Gaus Width: 5.3609
Entry
ampl
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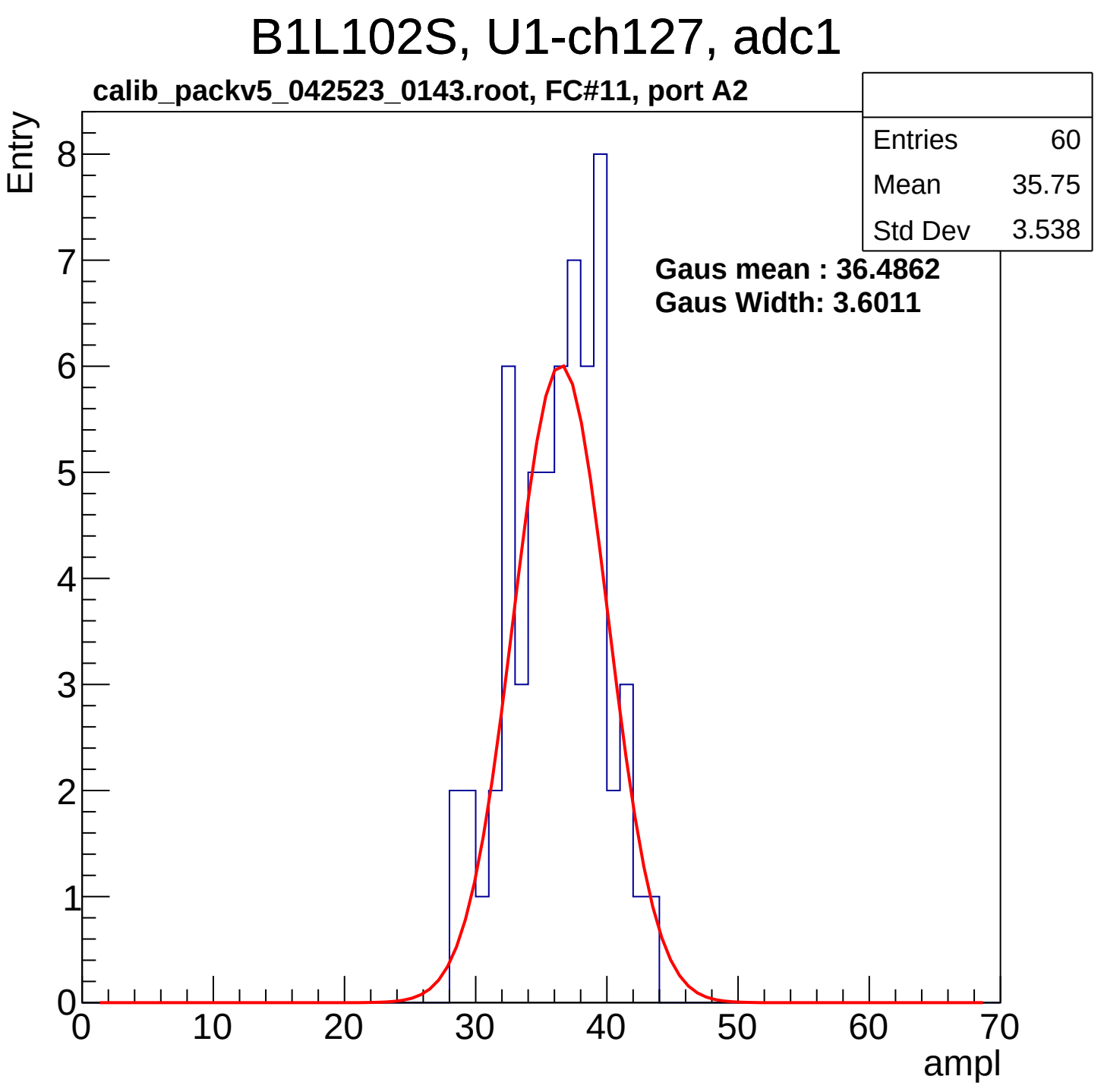
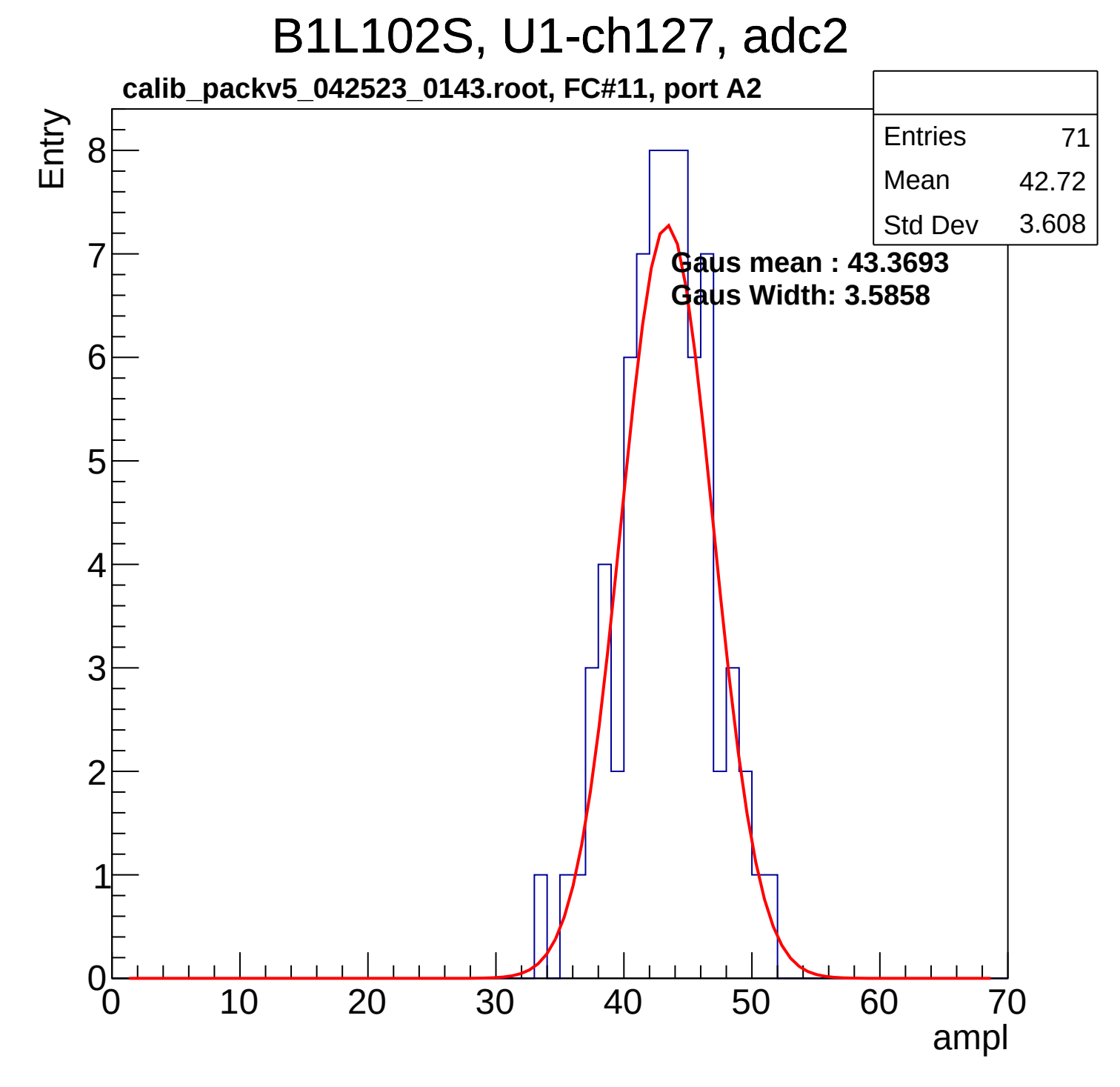
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L102S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#11, port A2
Entries 60
Mean 35.75
Std Dev 3.538
Gaus mean : 36.4862
Gaus Width: 3.6011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L102S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#11, port A2
Entries 71
Mean 42.72
Std Dev 3.608
Gaus mean : 43.3693
Gaus Width: 3.5858
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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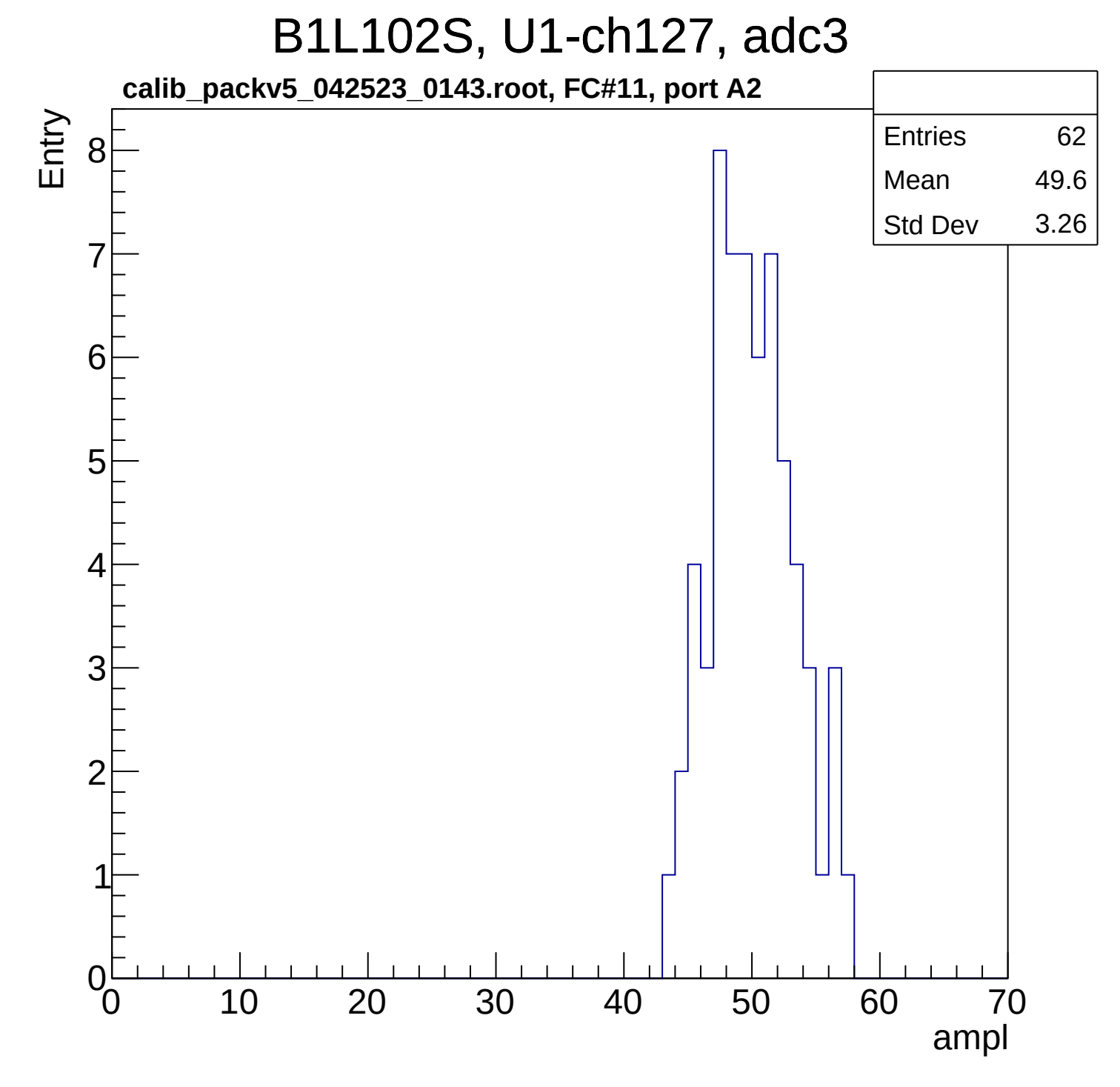
40
50
60
70


B1L102S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#11, port A2
Entries 62
Mean 49.6
Std Dev 3.26
Entry
ampl
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2
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4
5
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7
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50
60
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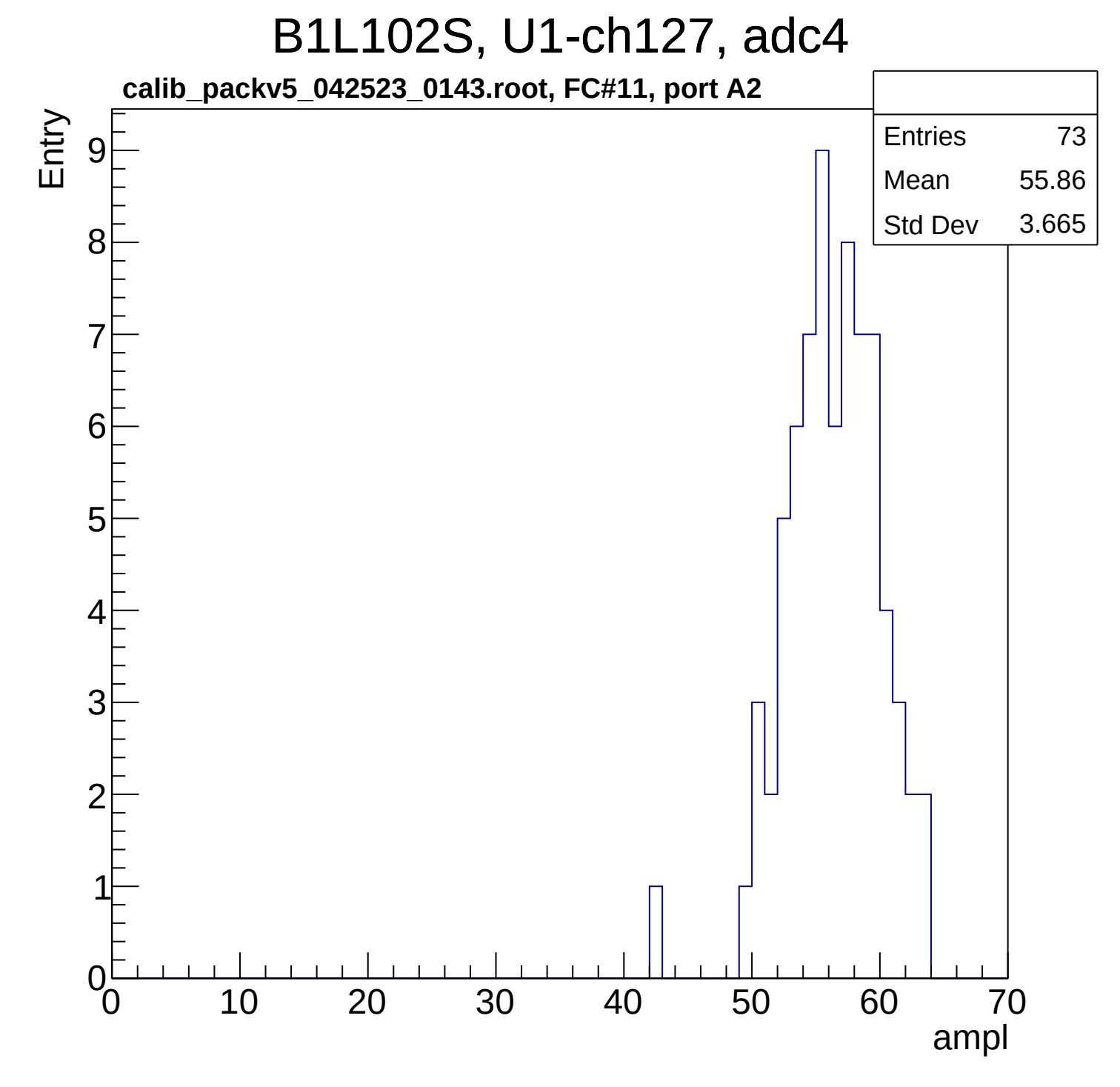
B1L102S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#11, port A2
Entries 73
Mean 55.86
Std Dev 3.665
Entry
ampl
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6
7
8
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0
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20
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40
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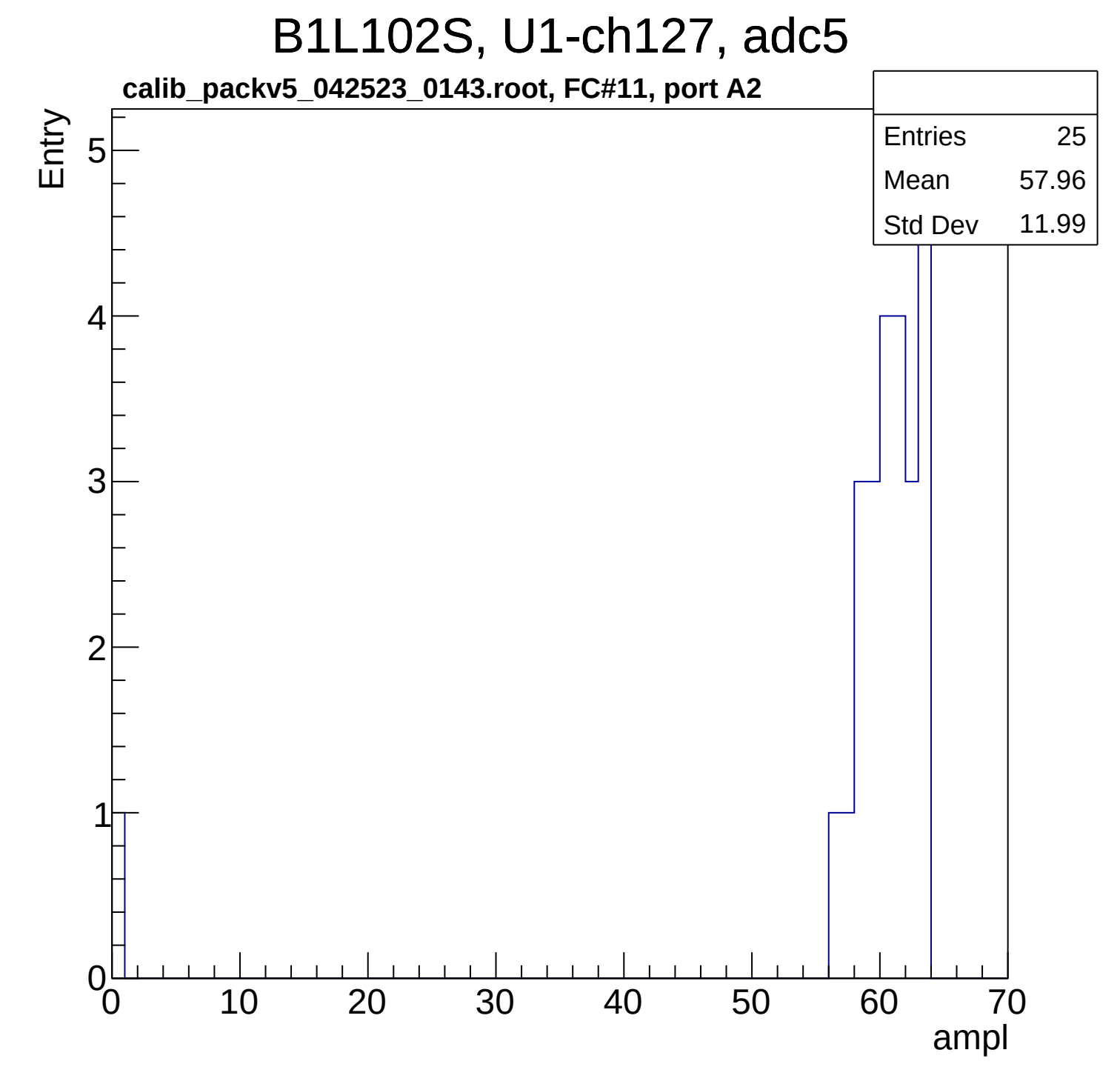
B1L102S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#11, port A2
Entries 25
Mean 57.96
Std Dev 11.99
Entry
0
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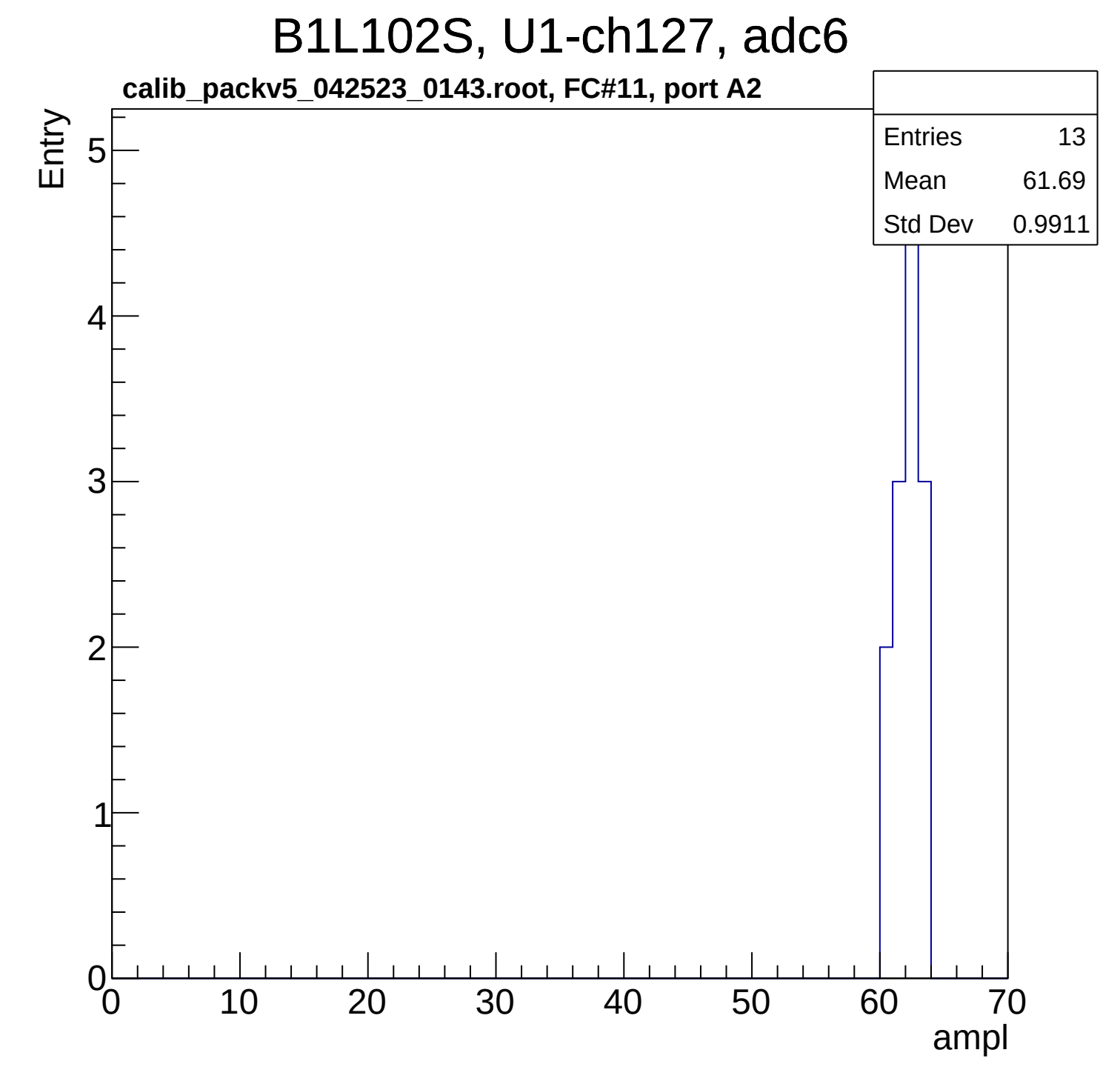
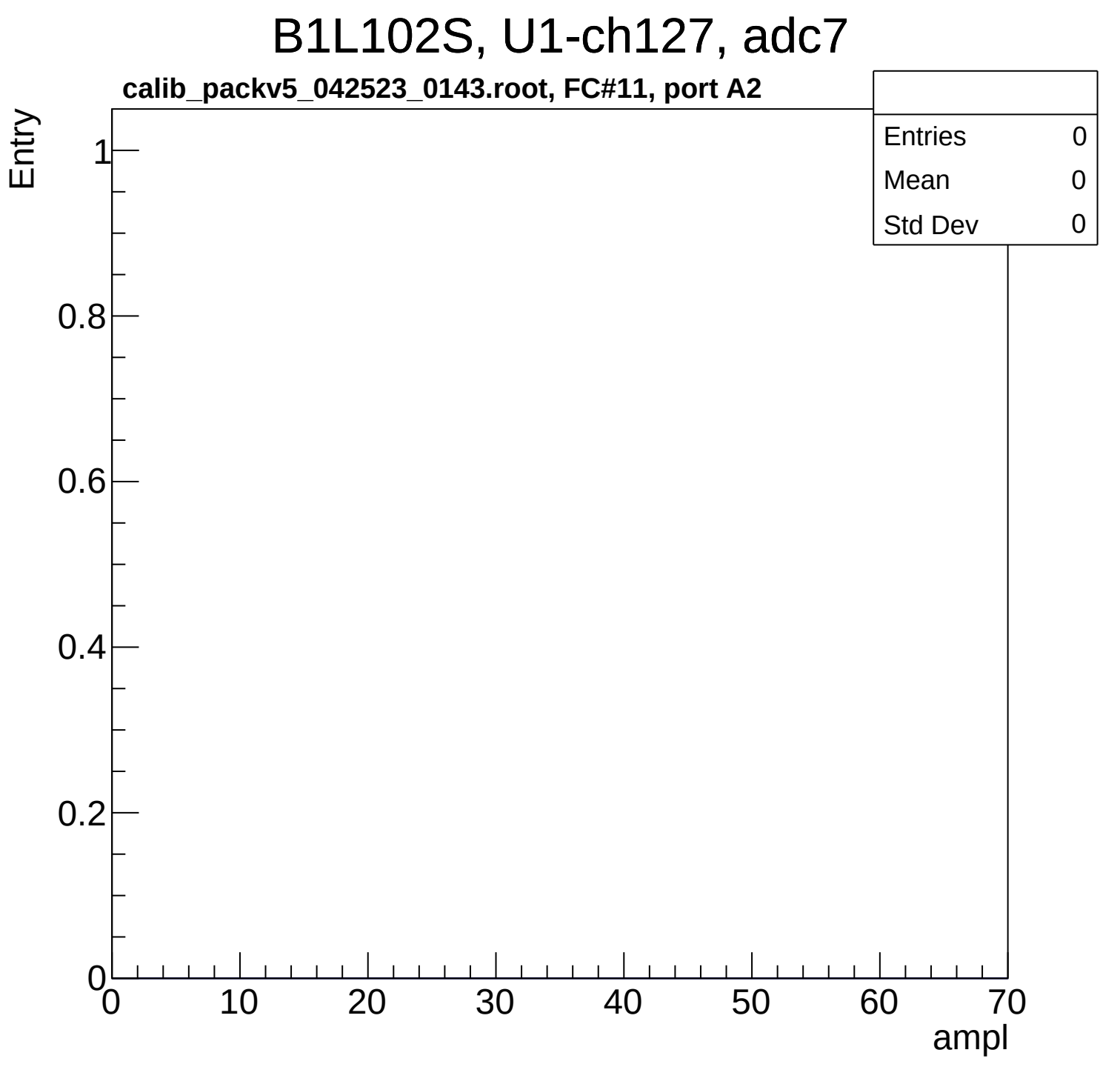
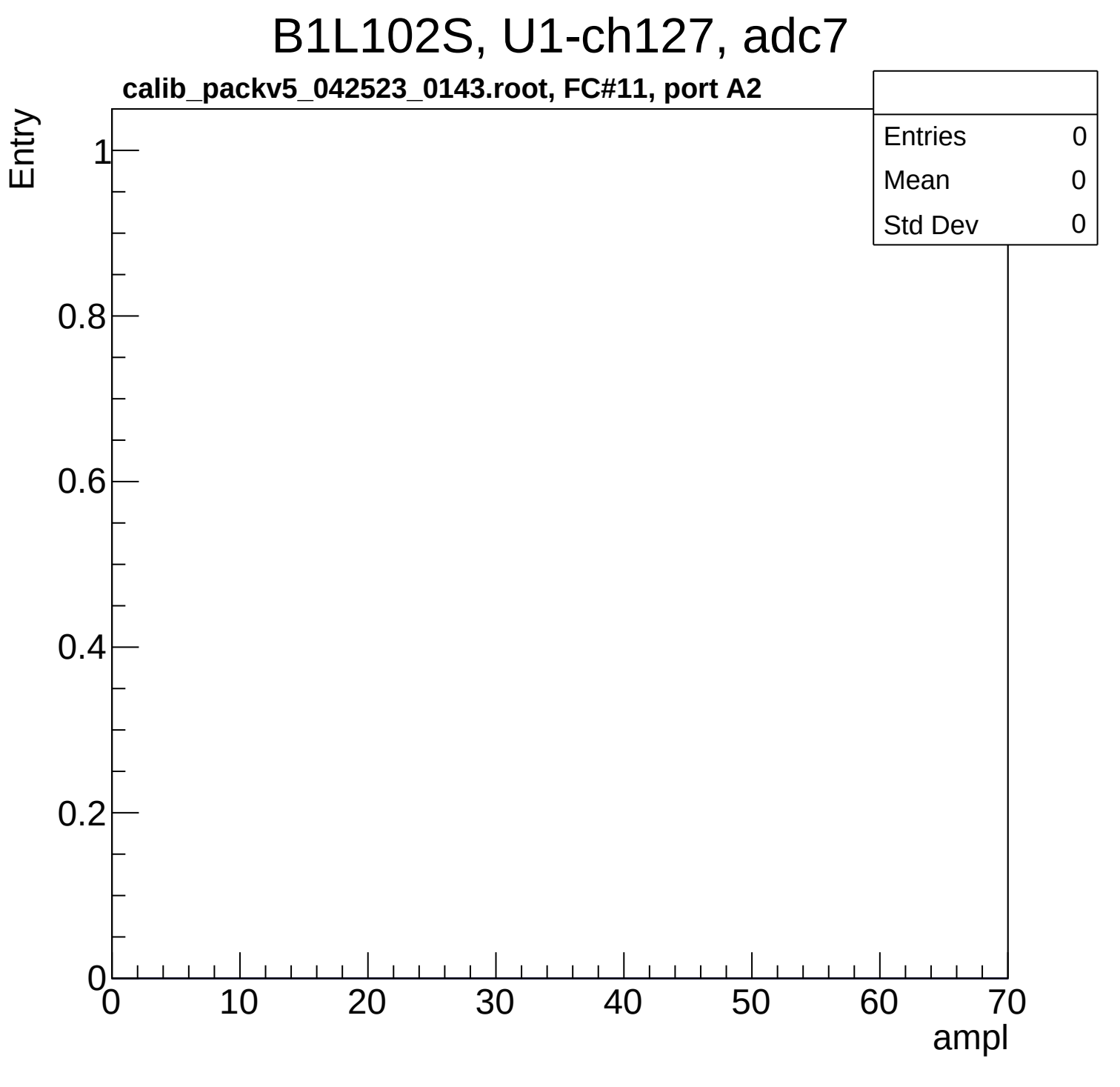
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#11, port A2
Entries 13
Mean 61.69
Std Dev 0.9911
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L102S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L102S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#11, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl