

B1L002S, U8-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.07
Std Dev	12.1

Turn on : 23.6993

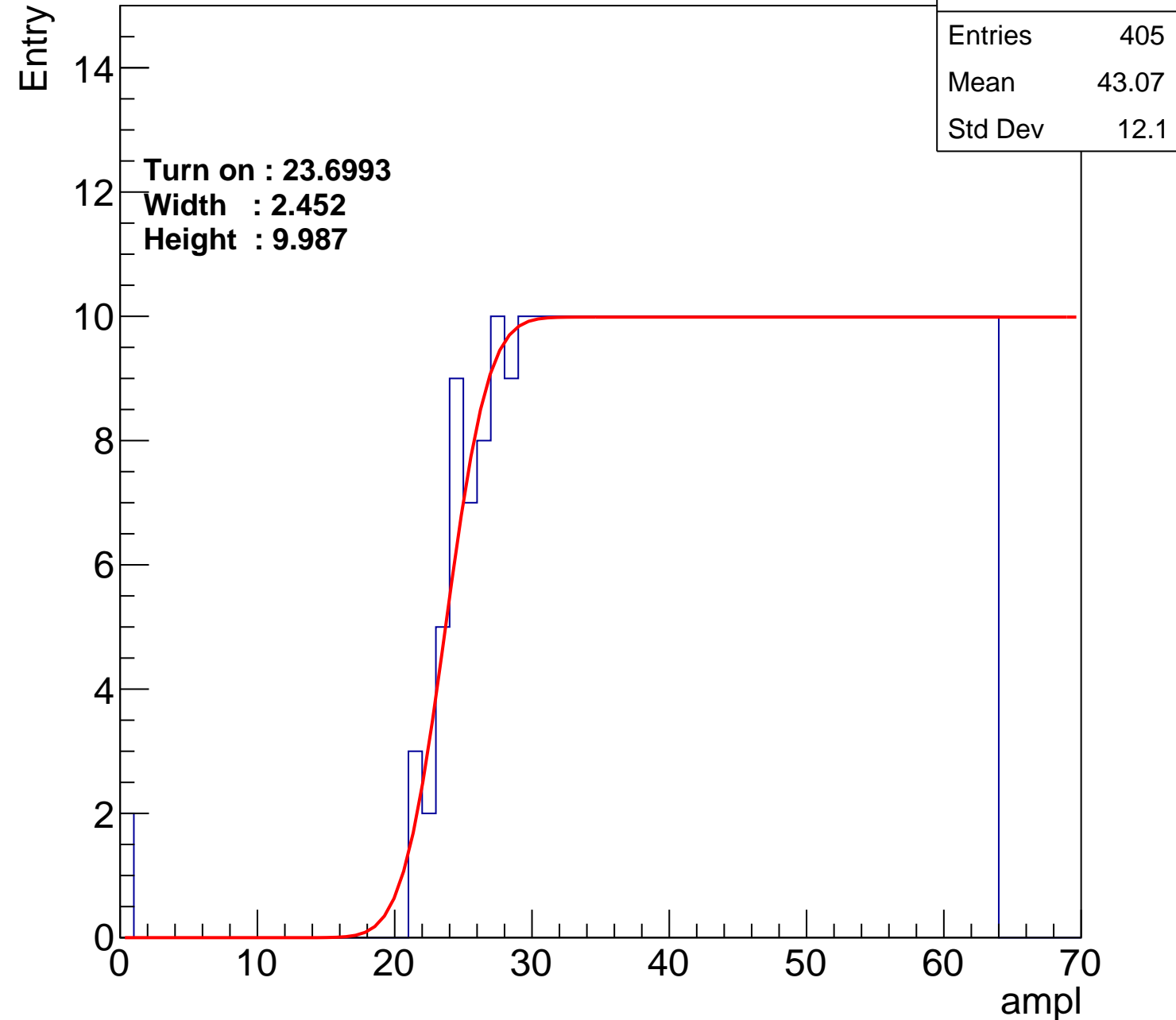
Width : 2.452

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.71
Std Dev	12.05

Turn on : 25.6373

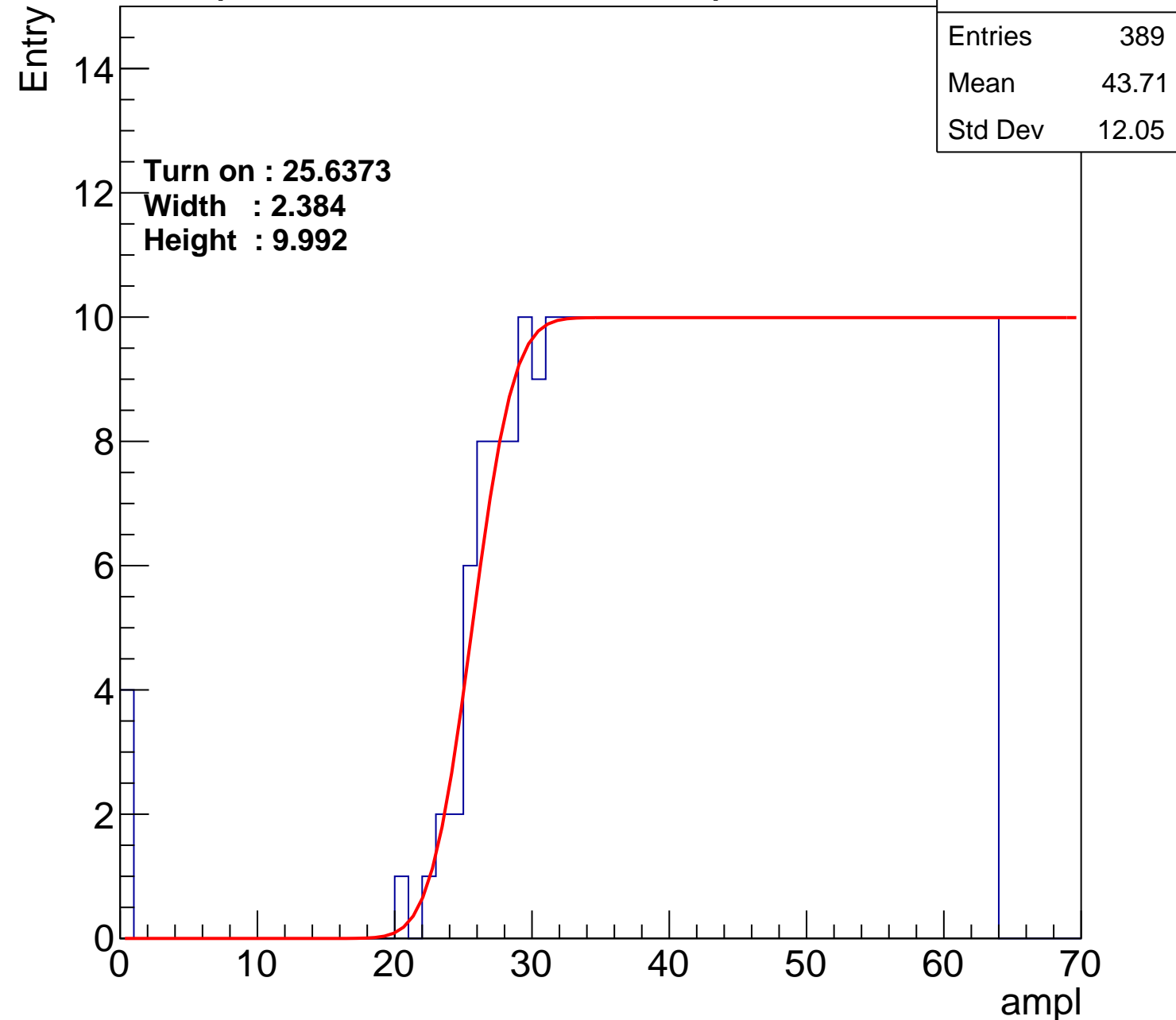
Width : 2.384

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.29
Std Dev	11.28

Turn on : 26.1684

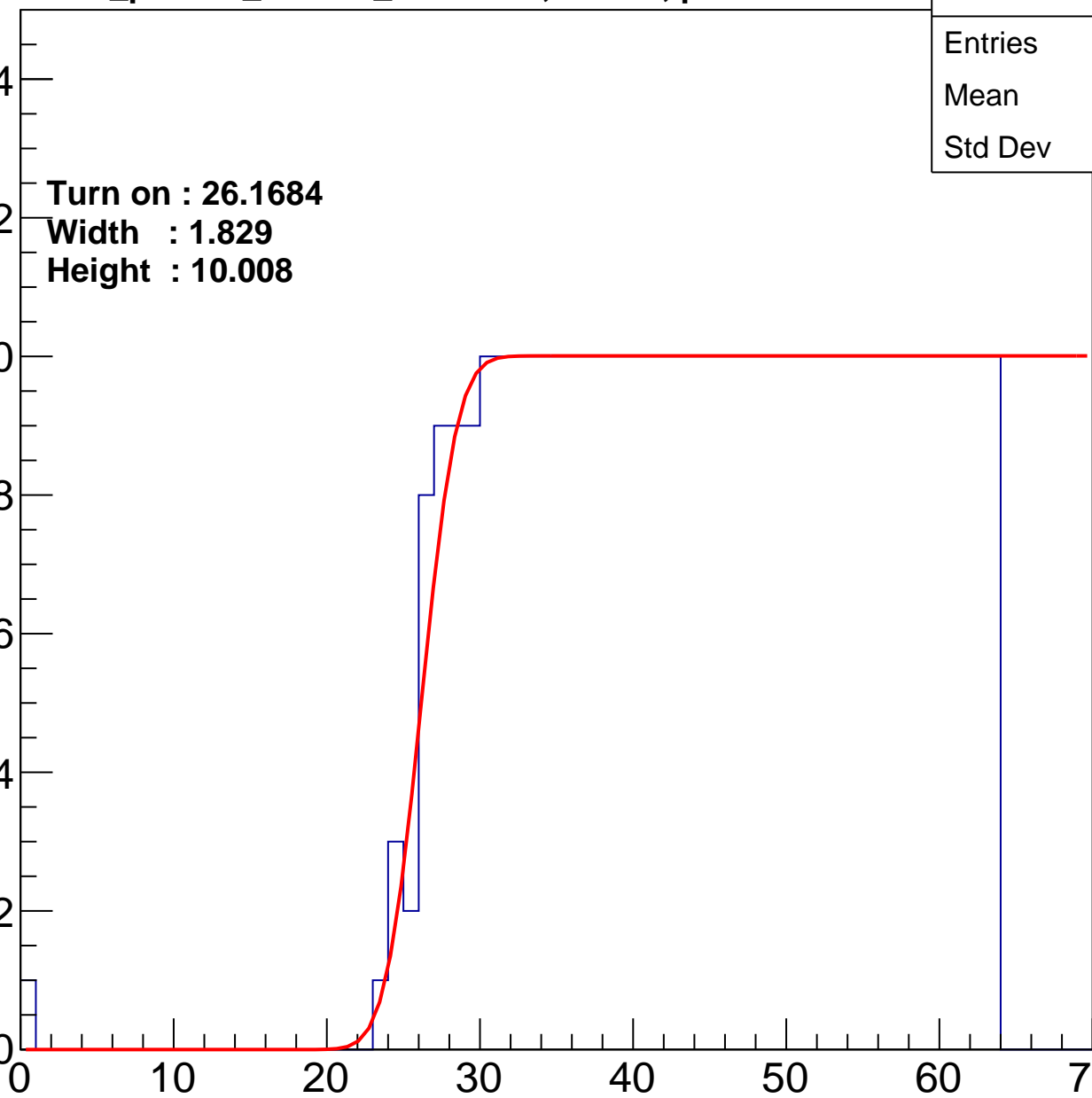
Width : 1.829

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch3

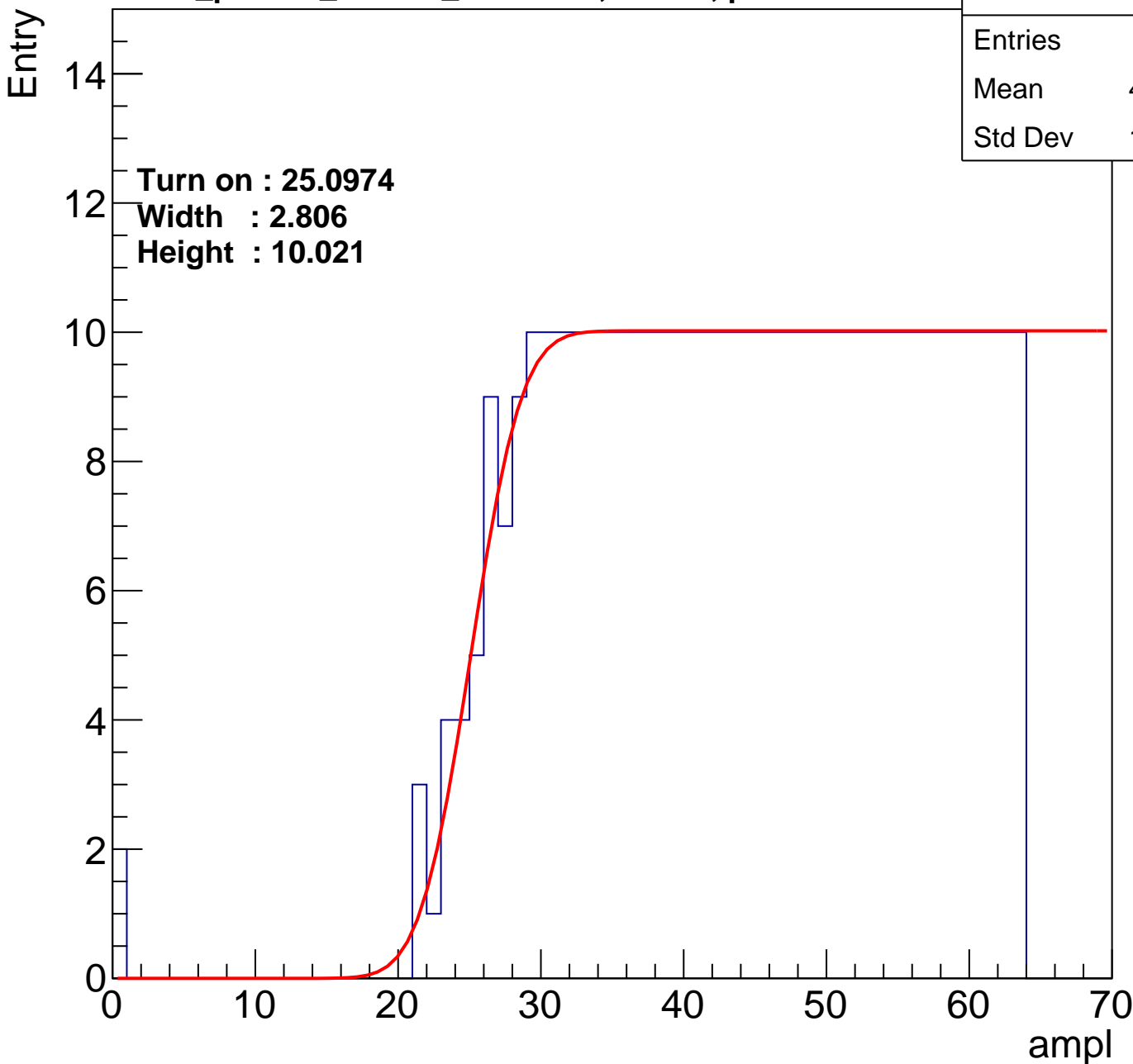
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.0974

Width : 2.806

Height : 10.021

Entries	394
Mean	43.59
Std Dev	11.86



B1L002S, U8-ch4

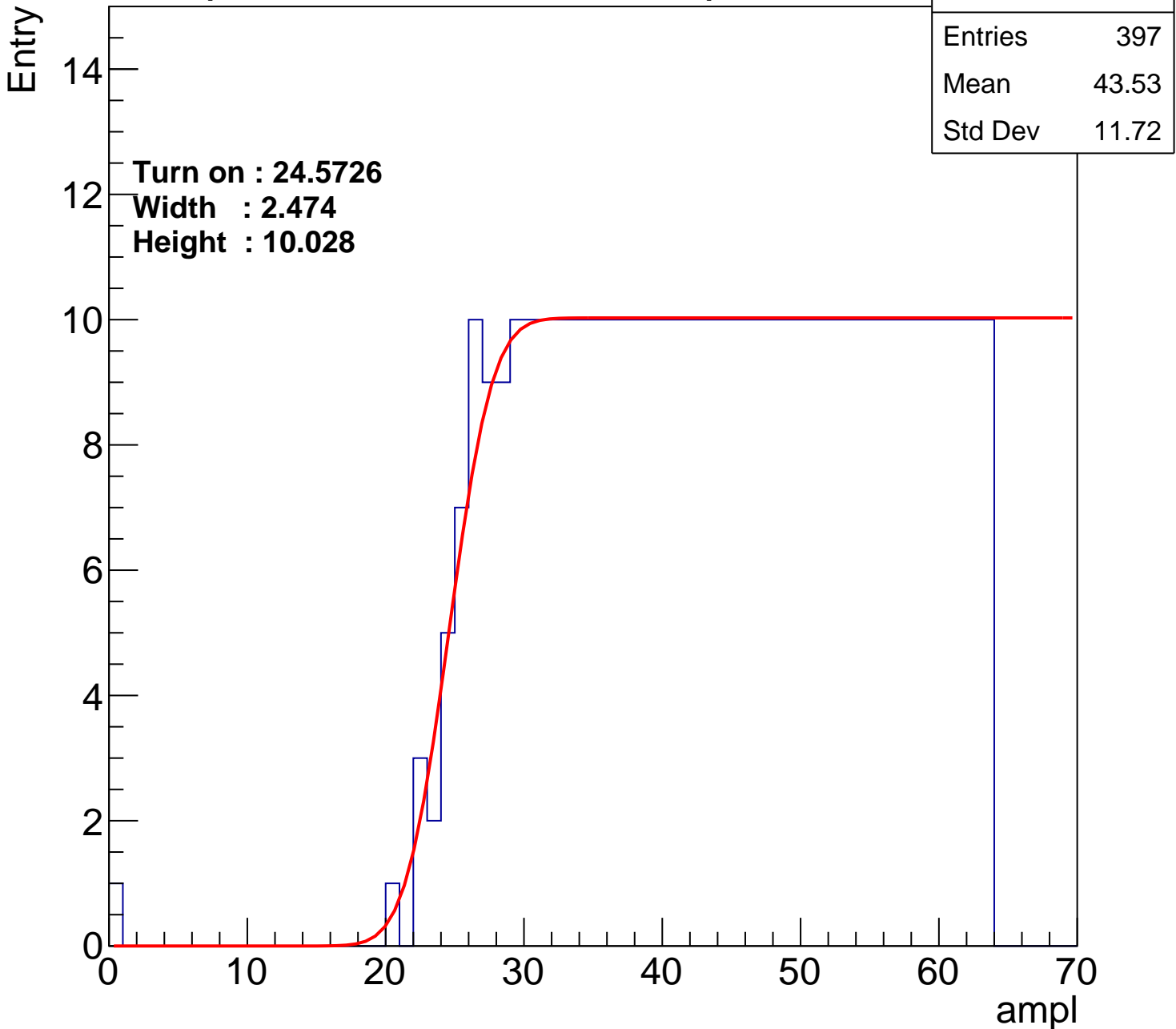
calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.53
Std Dev	11.72

Turn on : 24.5726

Width : 2.474

Height : 10.028



B1L002S, U8-ch5

calib_packv5_042523_0143.root, FC#10, port B3

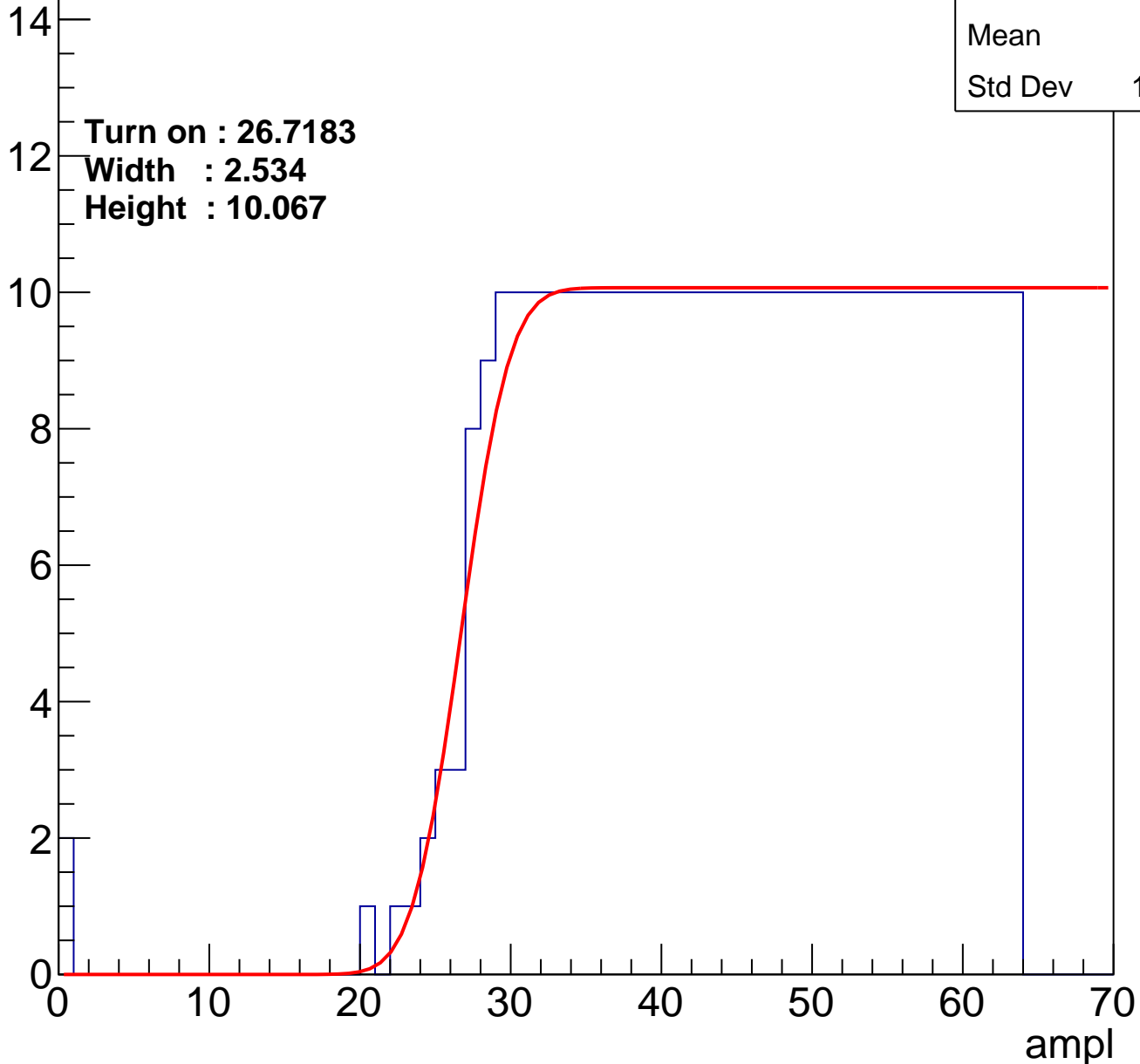
Entries	380
Mean	44.3
Std Dev	11.46

Turn on : 26.7183

Width : 2.534

Height : 10.067

Entry



B1L002S, U8-ch6

calib_packv5_042523_0143.root, FC#10, port B3

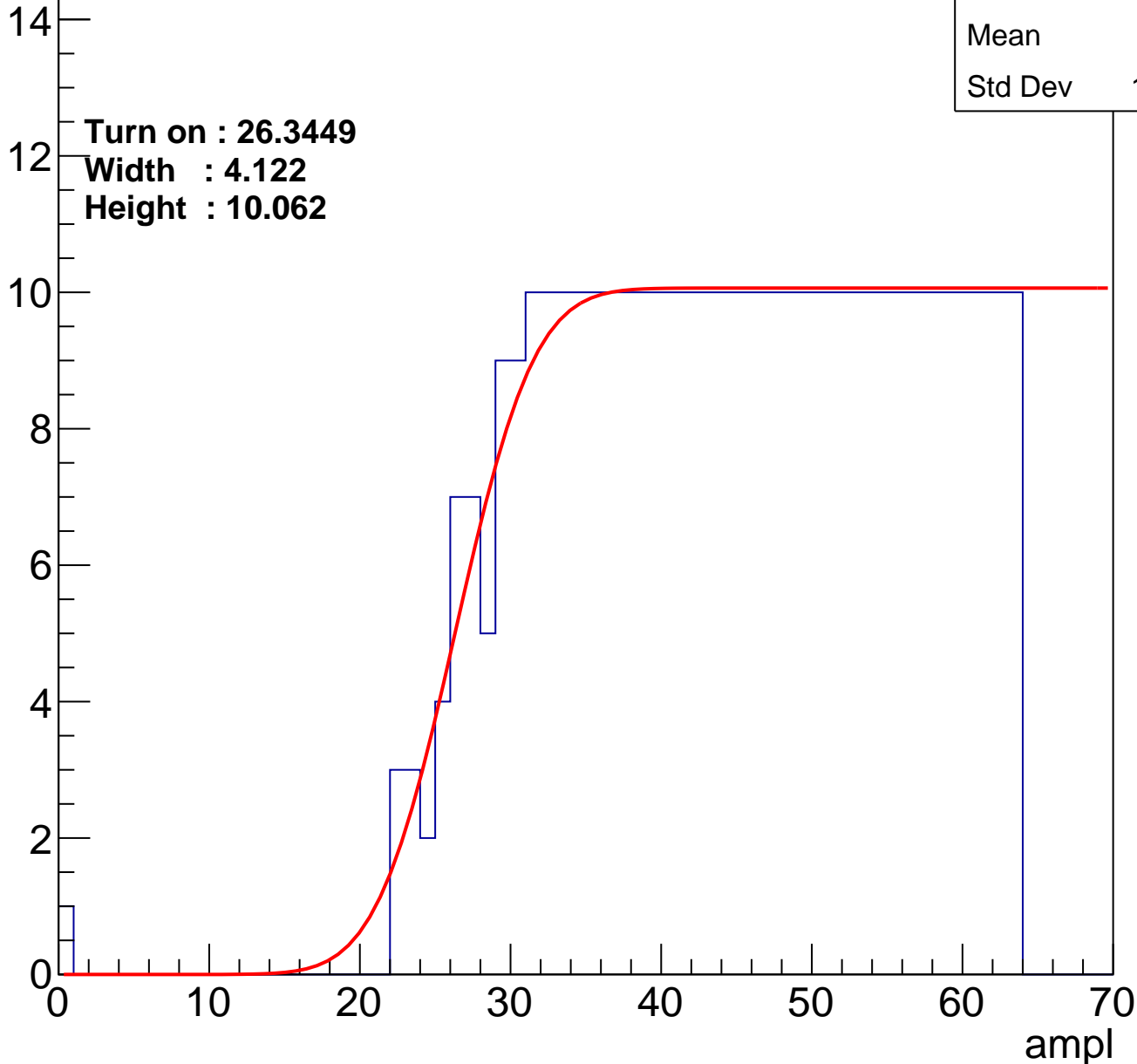
Entries	380
Mean	44.3
Std Dev	11.38

Turn on : 26.3449

Width : 4.122

Height : 10.062

Entry



B1L002S, U8-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.3999

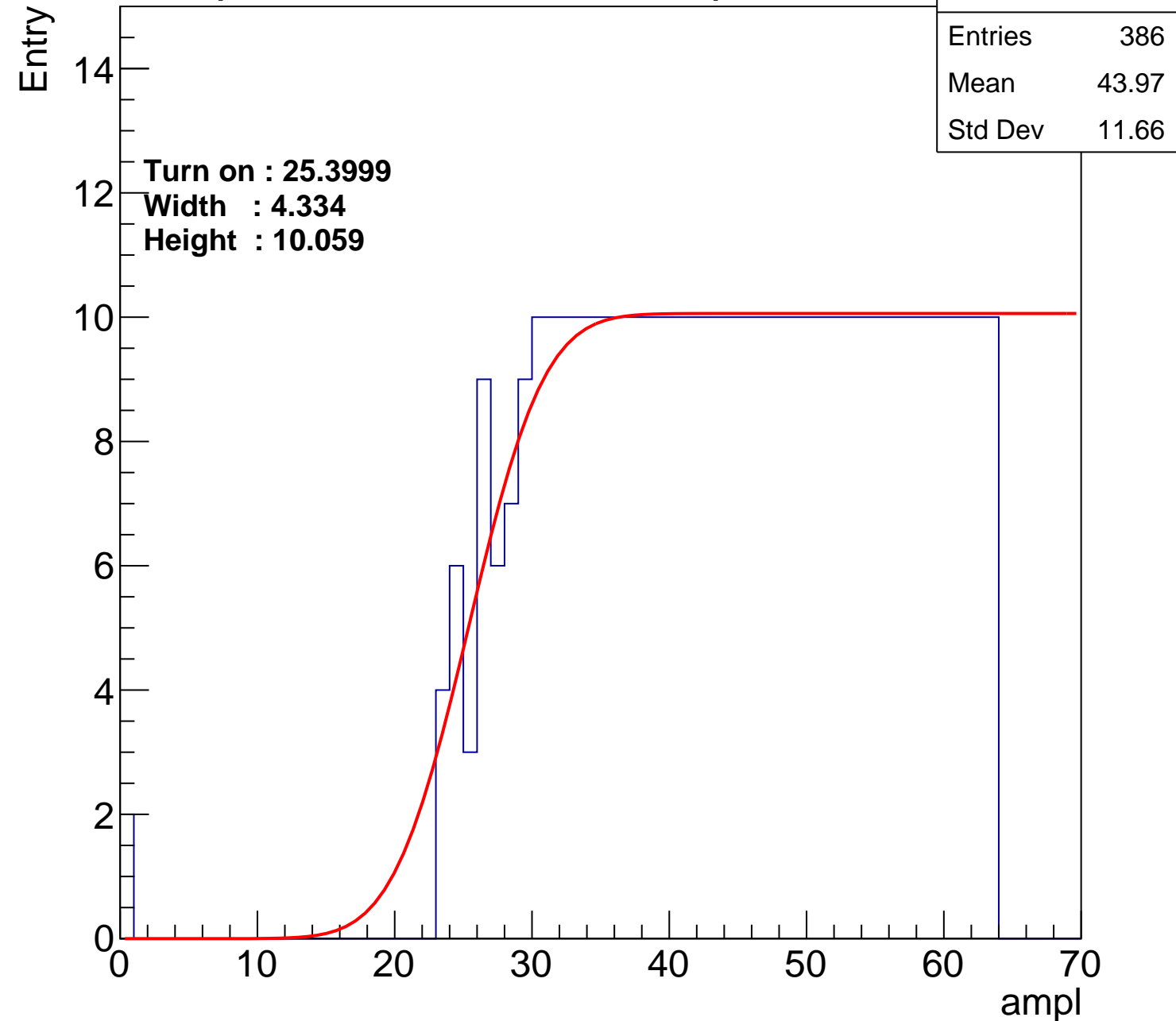
Width : 4.334

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 27.8292

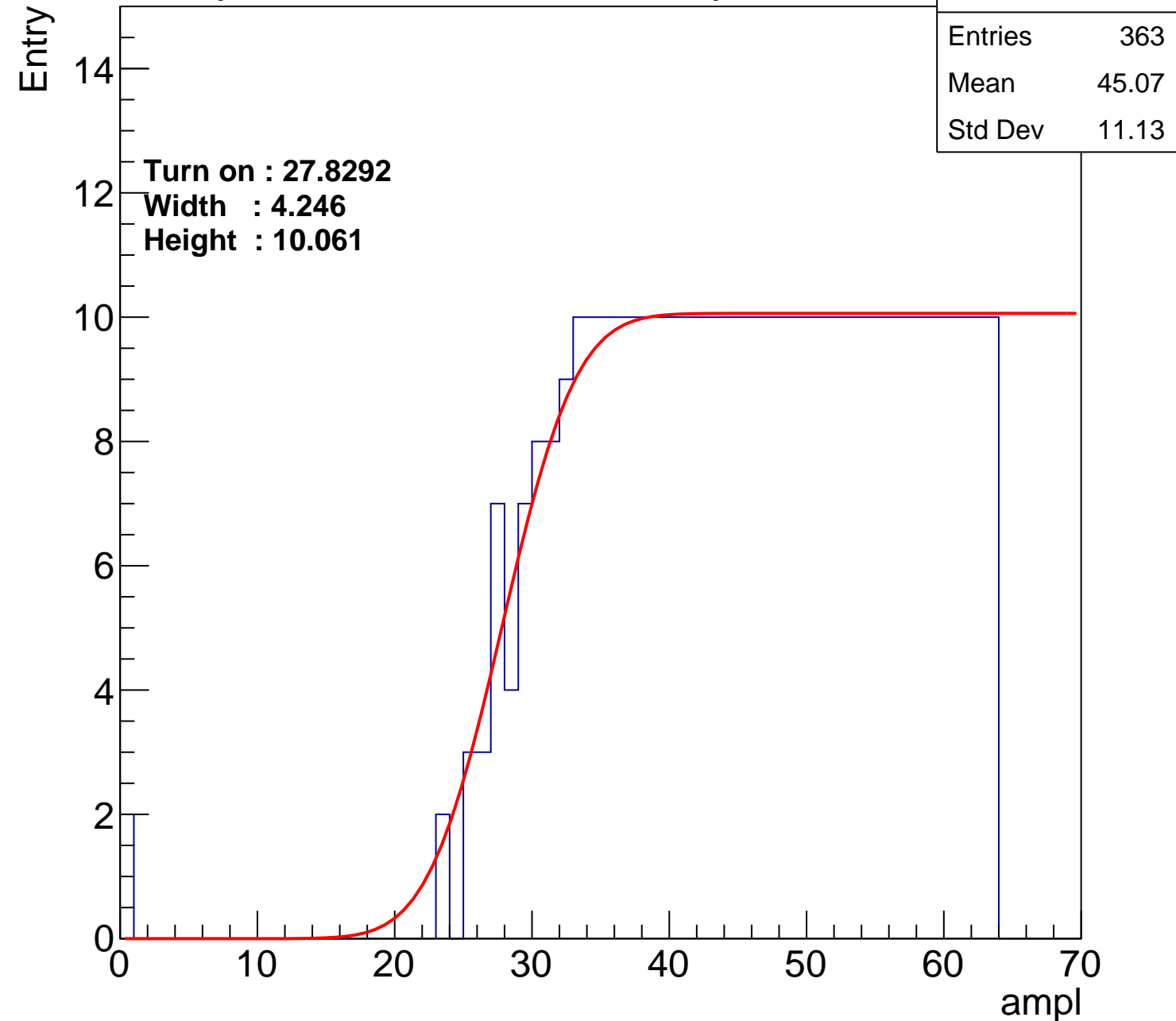
Width : 4.246

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch9

calib_packv5_042523_0143.root, FC#10, port B3

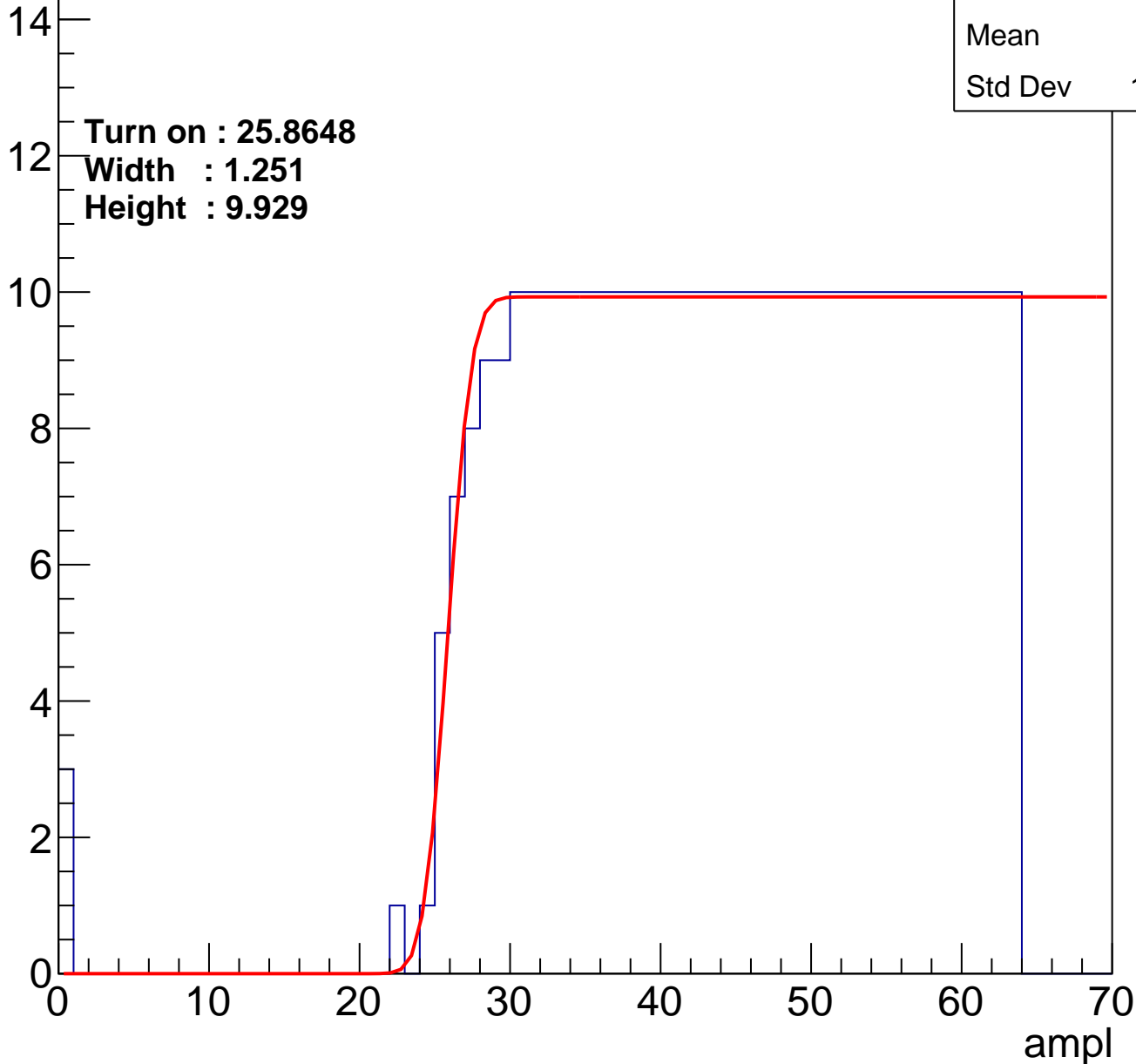
Entries	383
Mean	44.1
Std Dev	11.68

Turn on : 25.8648

Width : 1.251

Height : 9.929

Entry



B1L002S, U8-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.51
Std Dev	12.04

Turn on : 24.9443

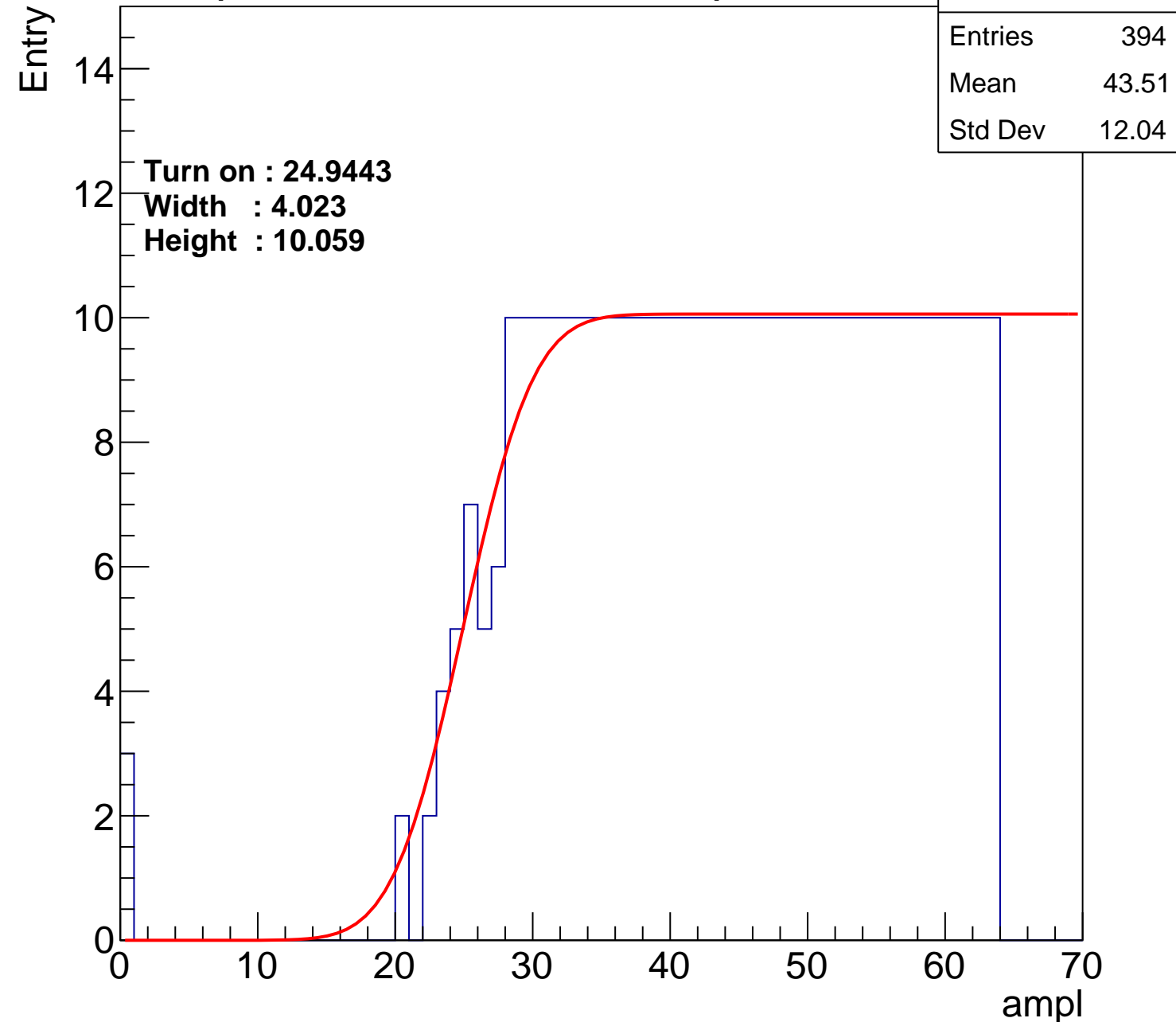
Width : 4.023

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.11

Turn on : 27.1811

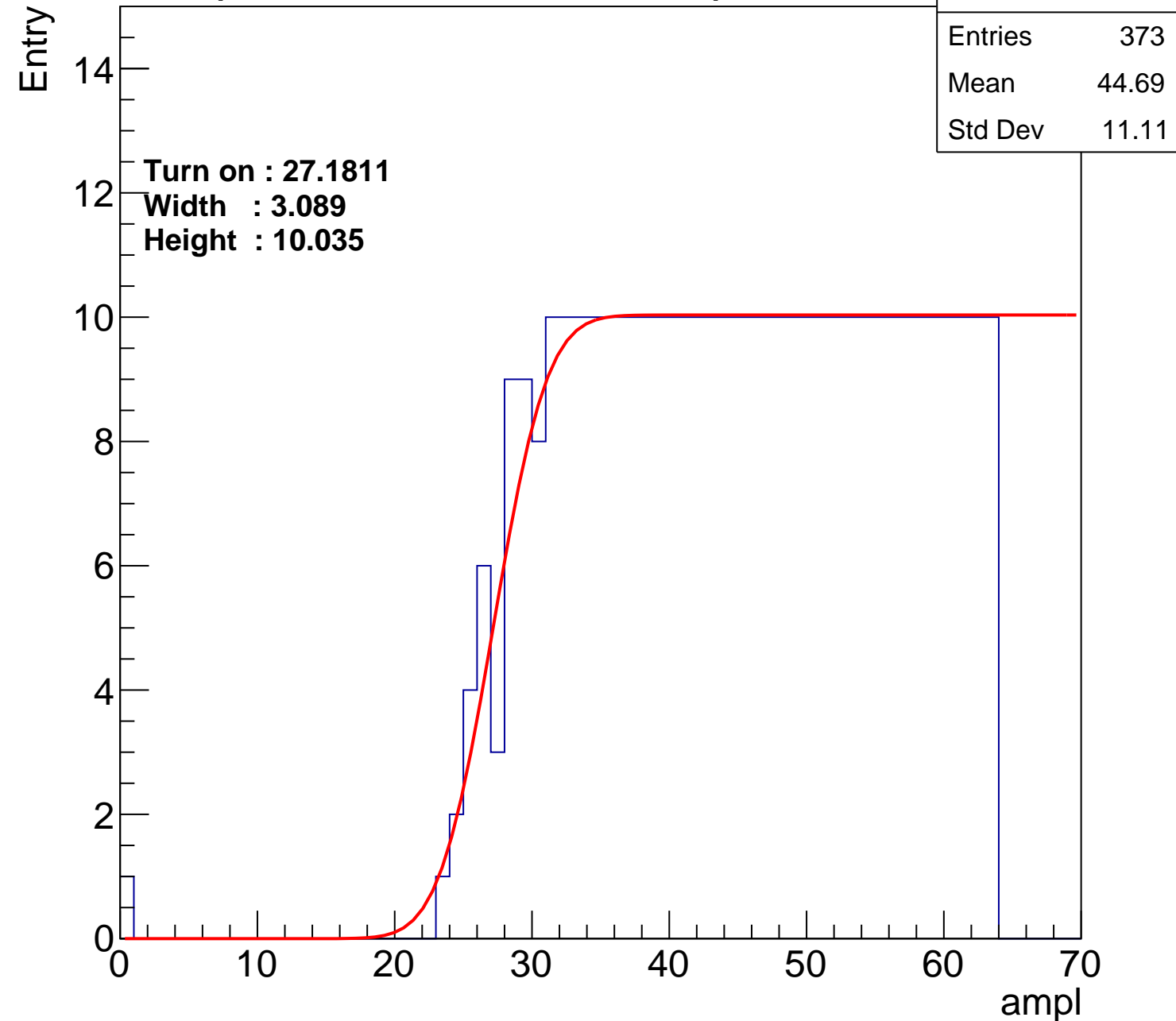
Width : 3.089

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.14
Std Dev	11.93

Turn on : 23.8088

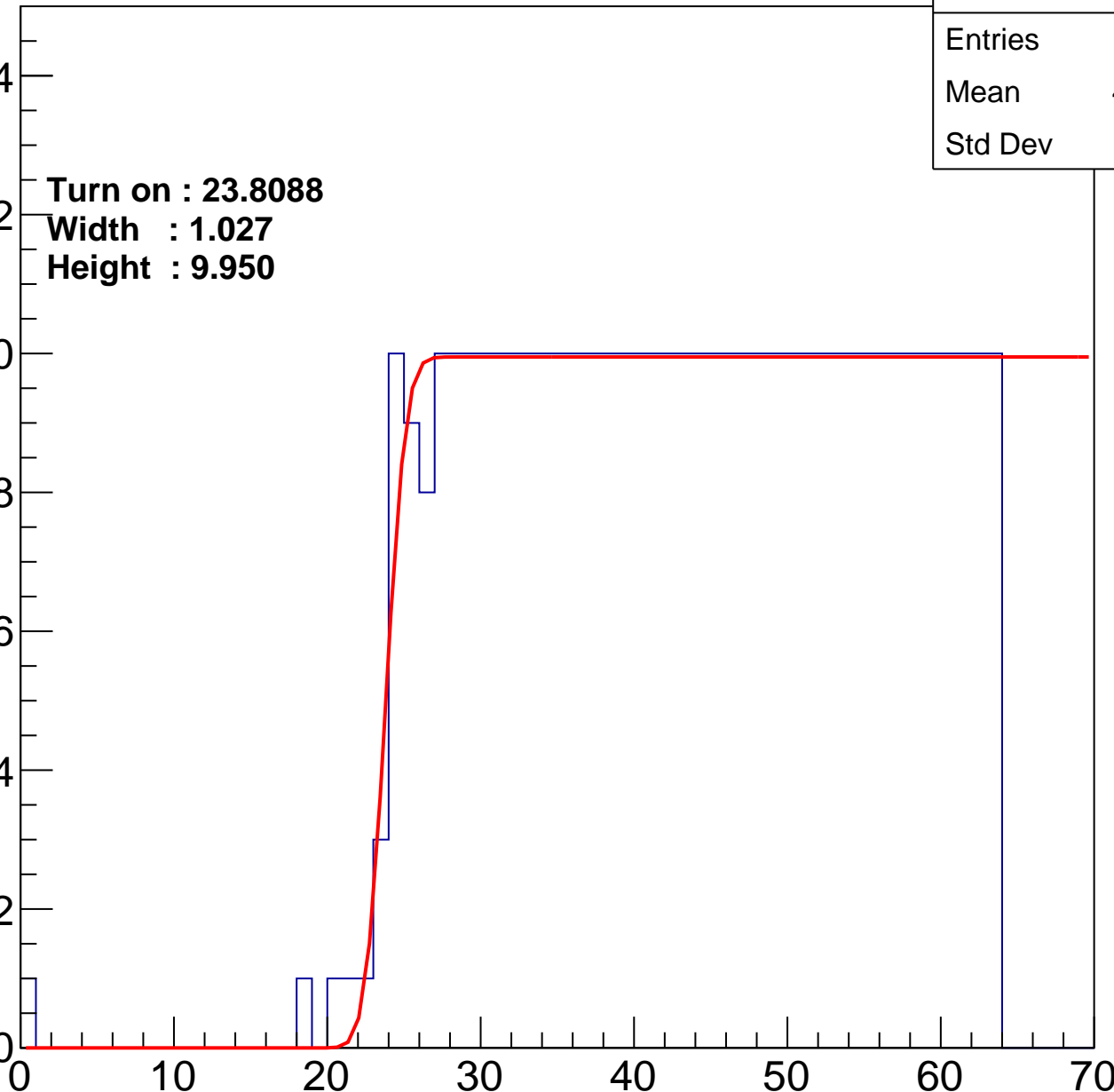
Width : 1.027

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.94
Std Dev	11.55

Turn on : 25.3623

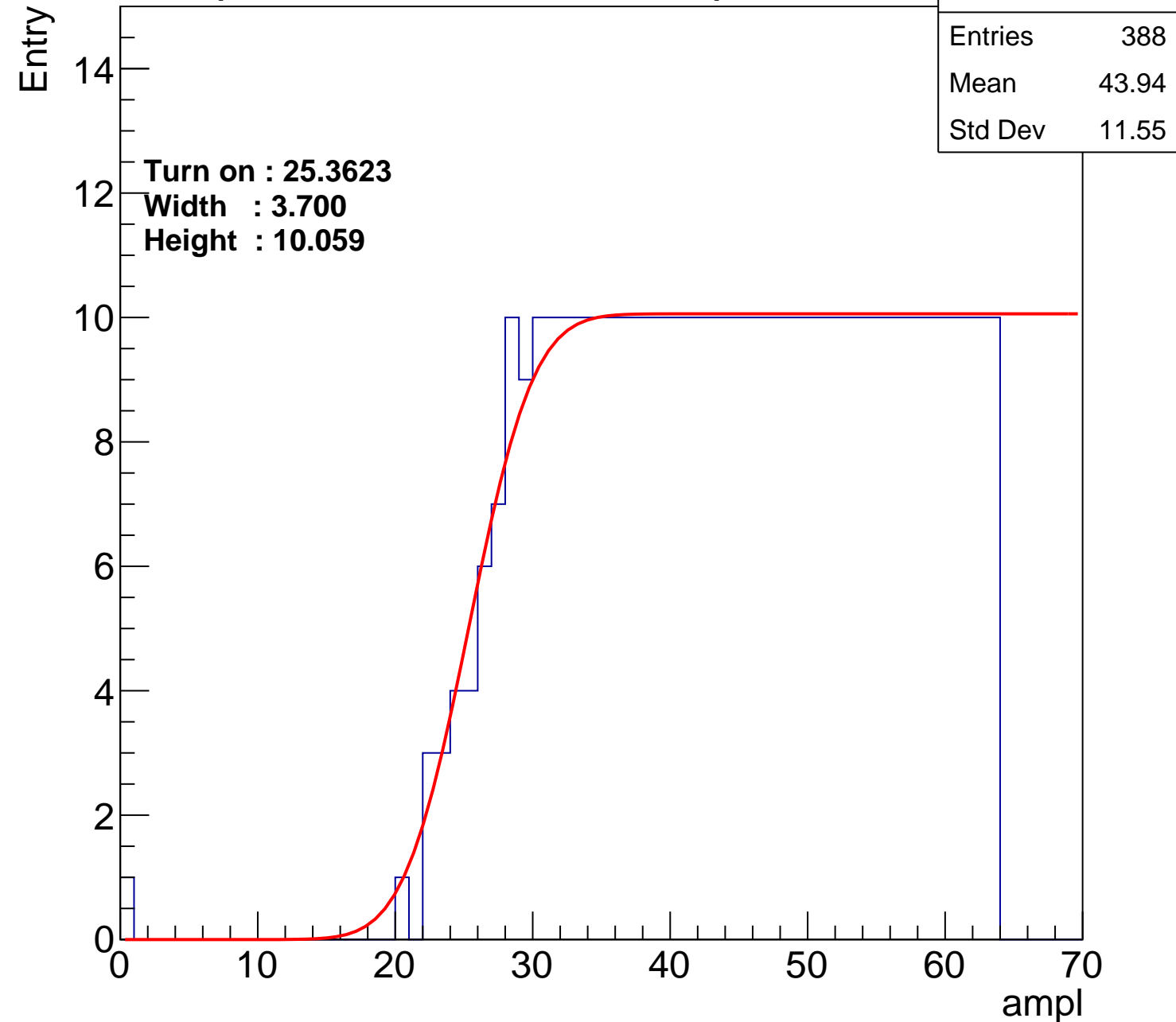
Width : 3.700

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch14

calib_packv5_042523_0143.root, FC#10, port B3

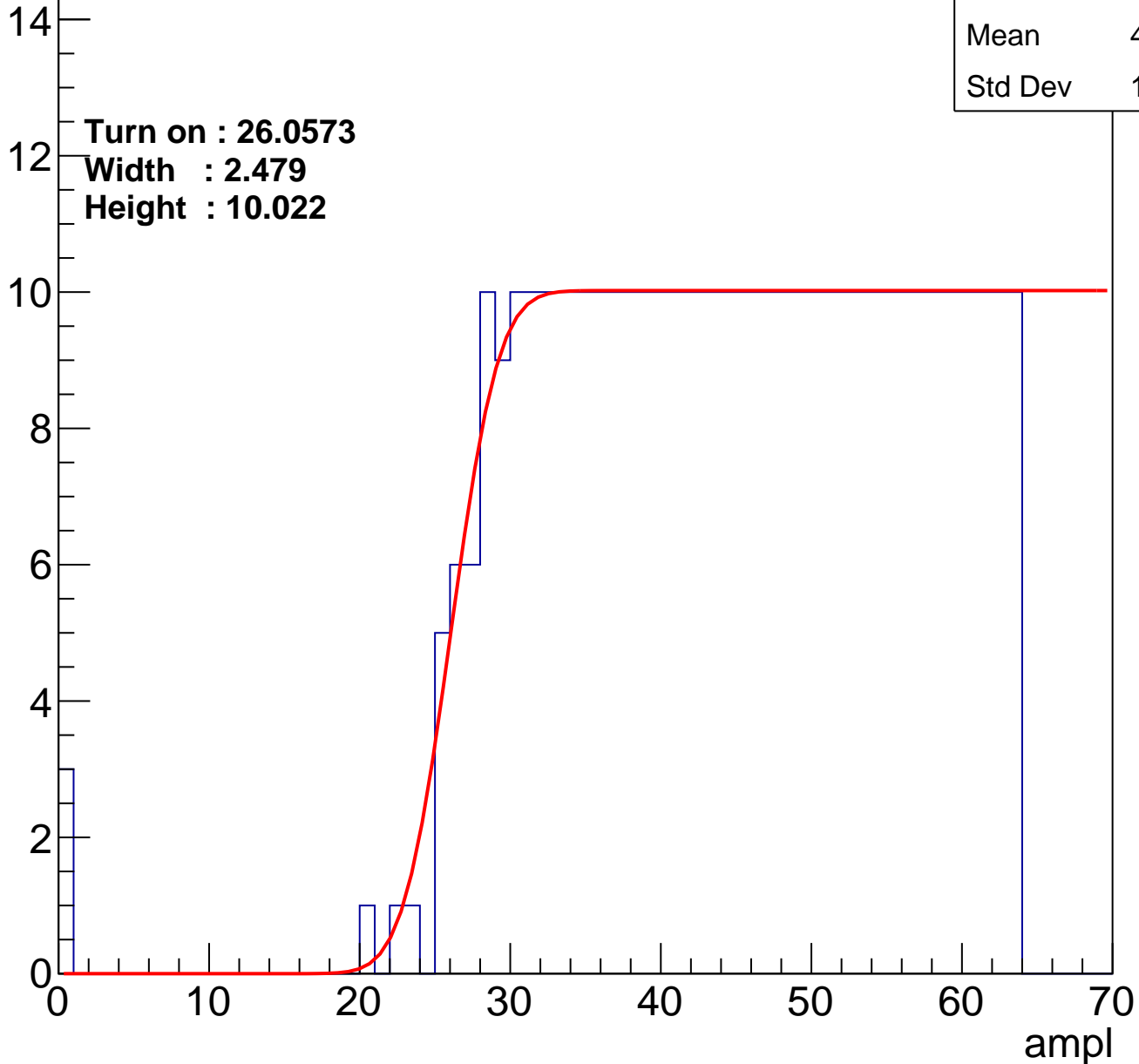
Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 26.0573

Width : 2.479

Height : 10.022

Entry



B1L002S, U8-ch15

calib_packv5_042523_0143.root, FC#10, port B3

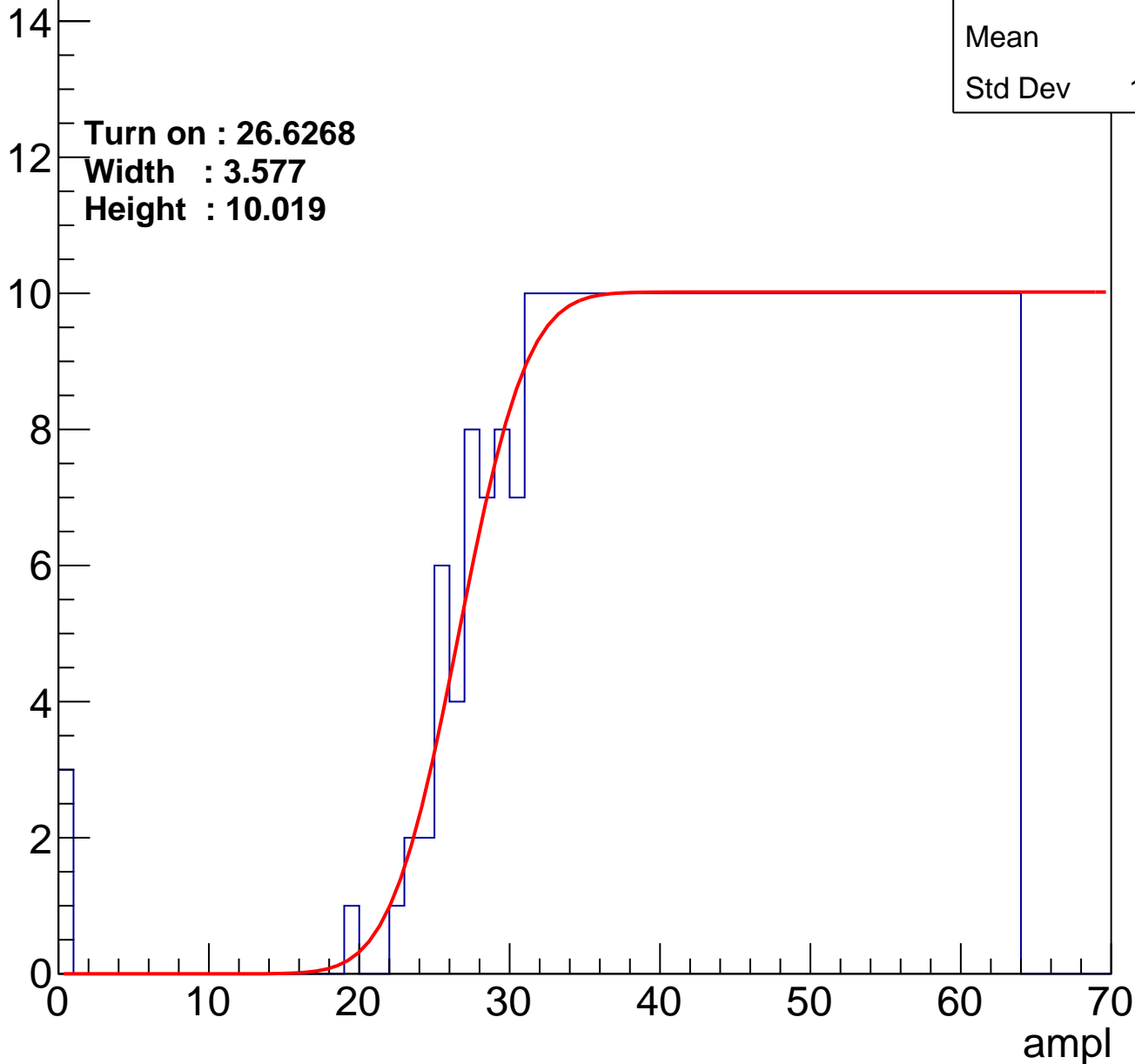
Entries	379
Mean	44.2
Std Dev	11.74

Turn on : 26.6268

Width : 3.577

Height : 10.019

Entry



B1L002S, U8-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.85

Turn on : 25.7933

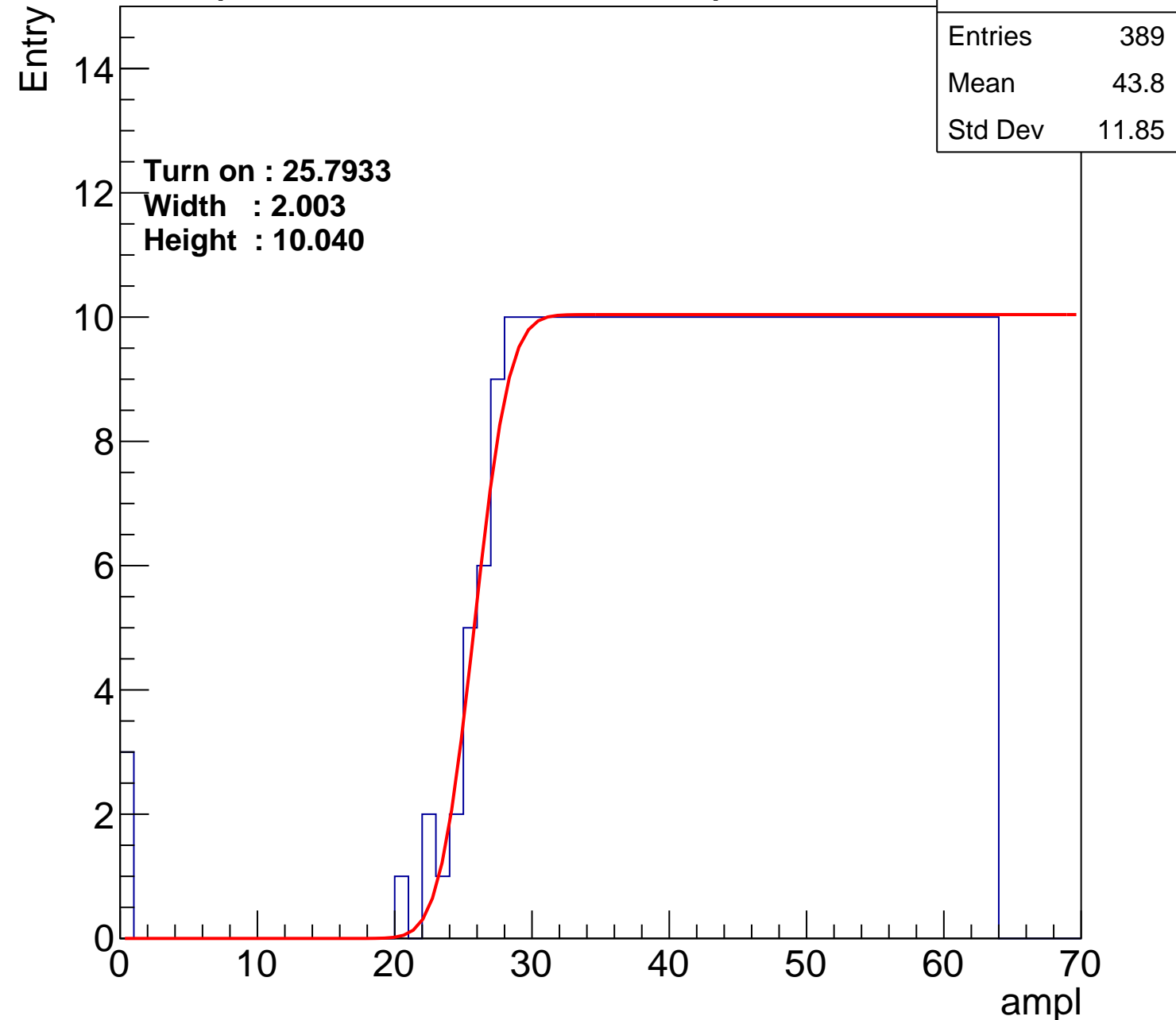
Width : 2.003

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.03
Std Dev	12.27

Turn on : 23.8786

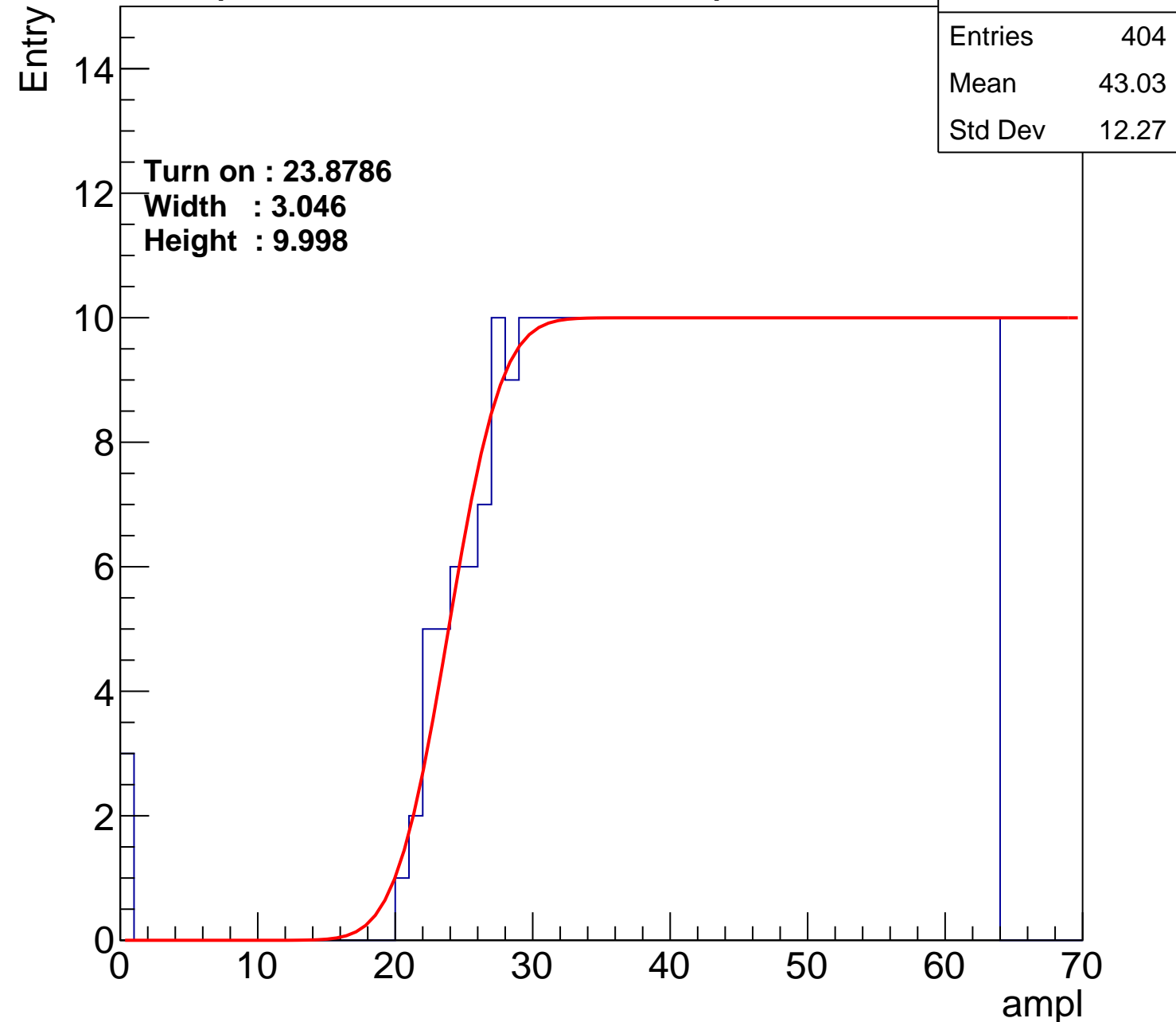
Width : 3.046

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.9142

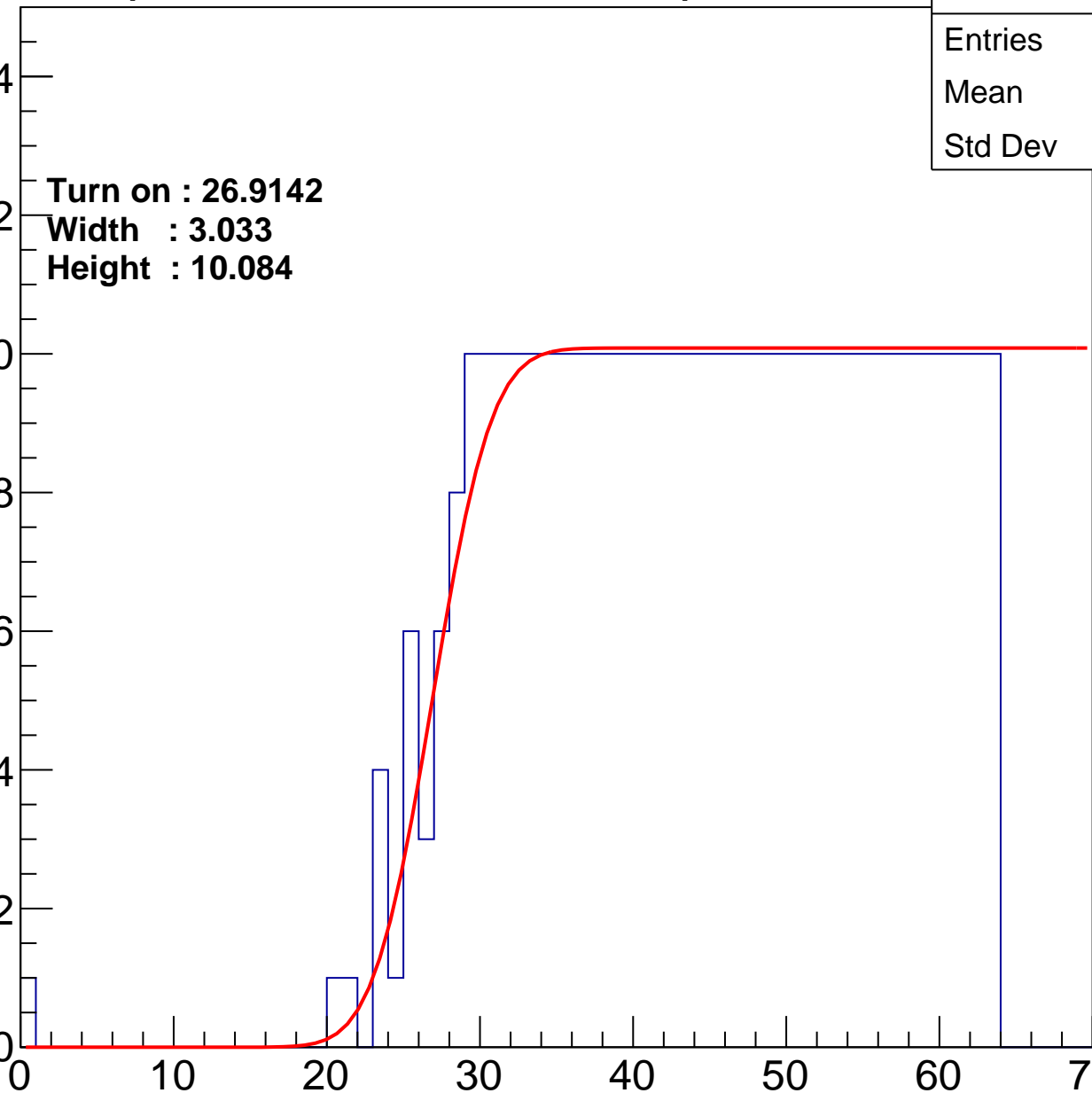
Width : 3.033

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.26
Std Dev	12.13

Turn on : 24.7533

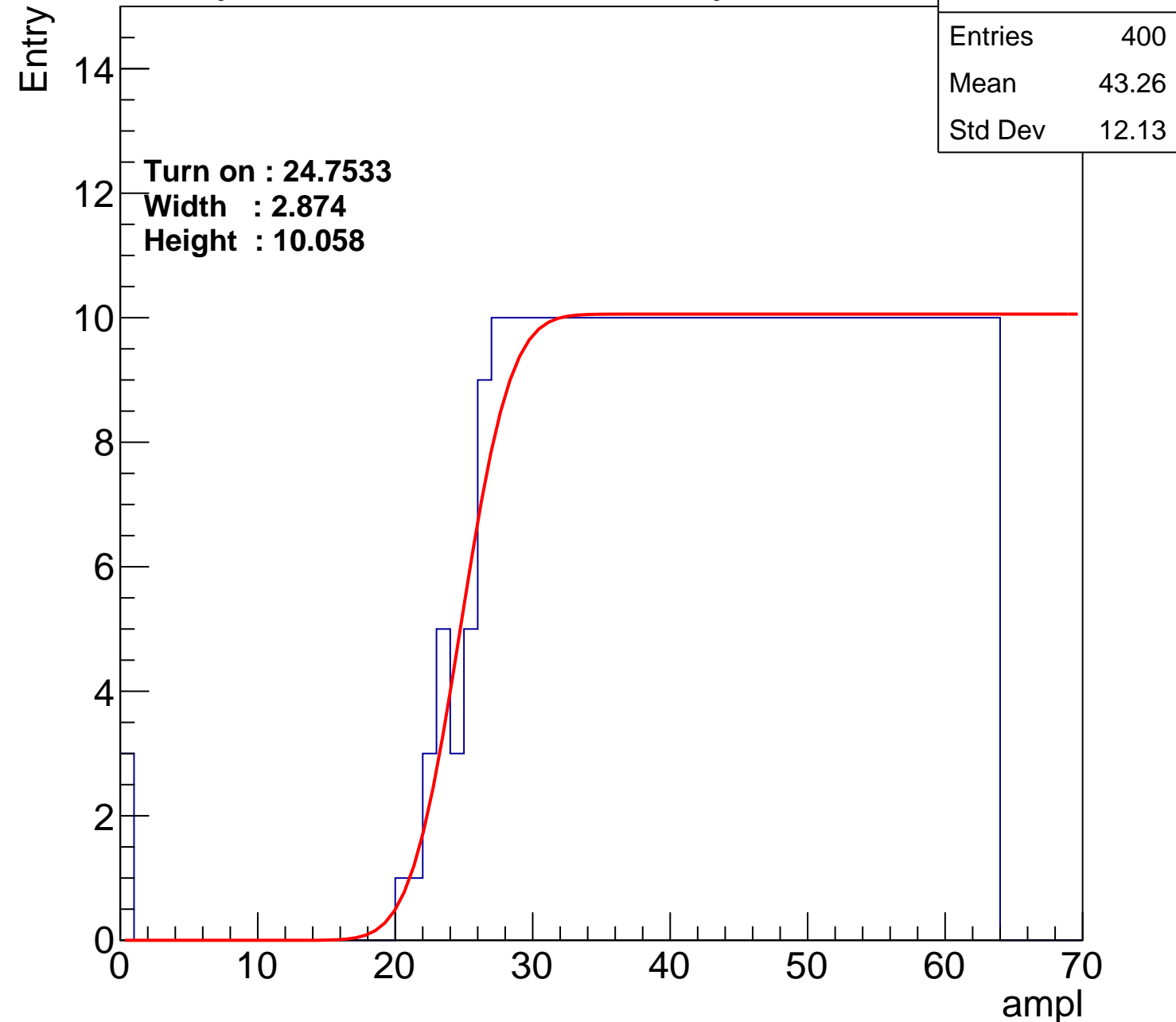
Width : 2.874

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch20

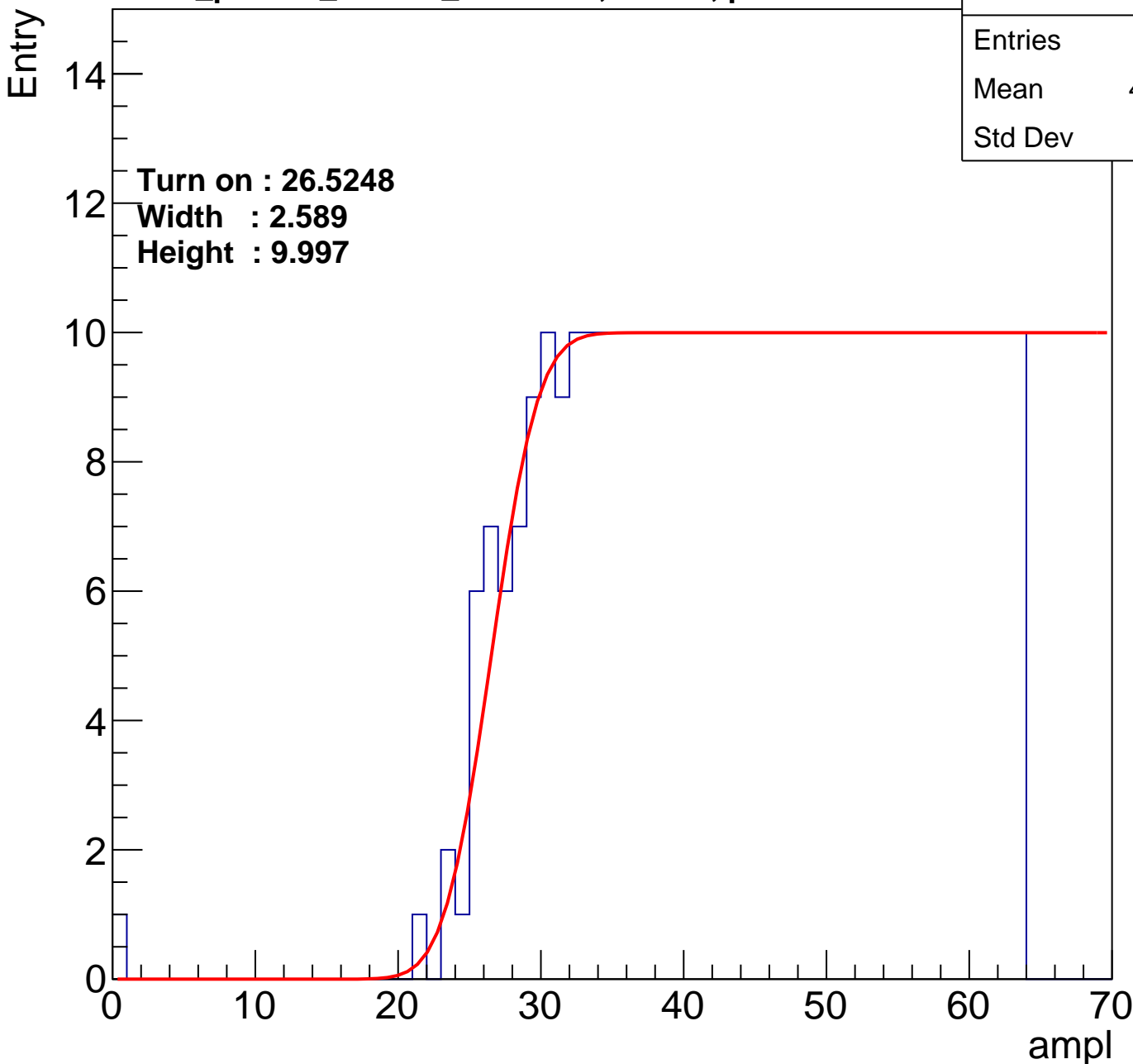
calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 26.5248

Width : 2.589

Height : 9.997



B1L002S, U8-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.88
Std Dev	11.65

Turn on : 25.2746

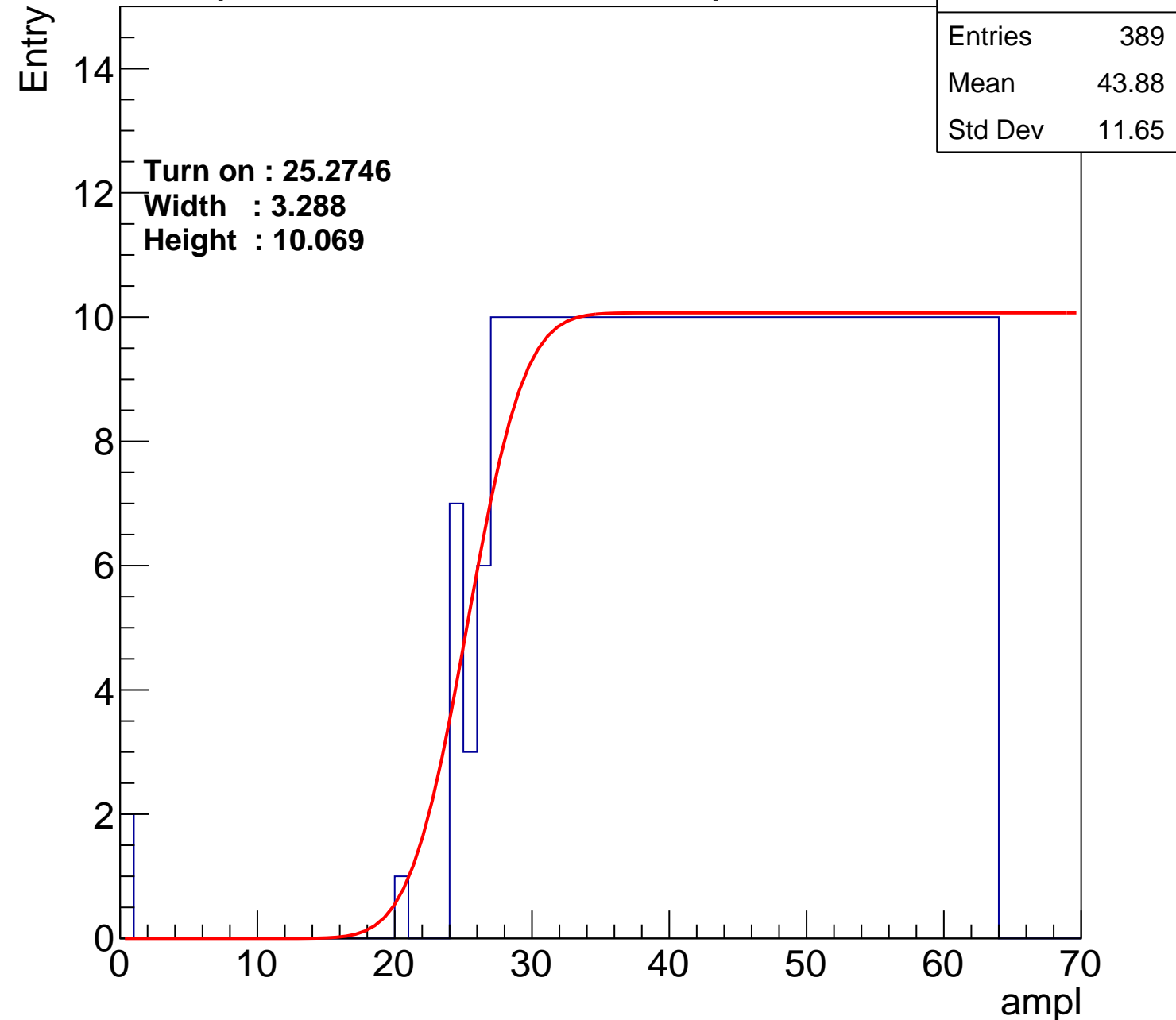
Width : 3.288

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 26.0314

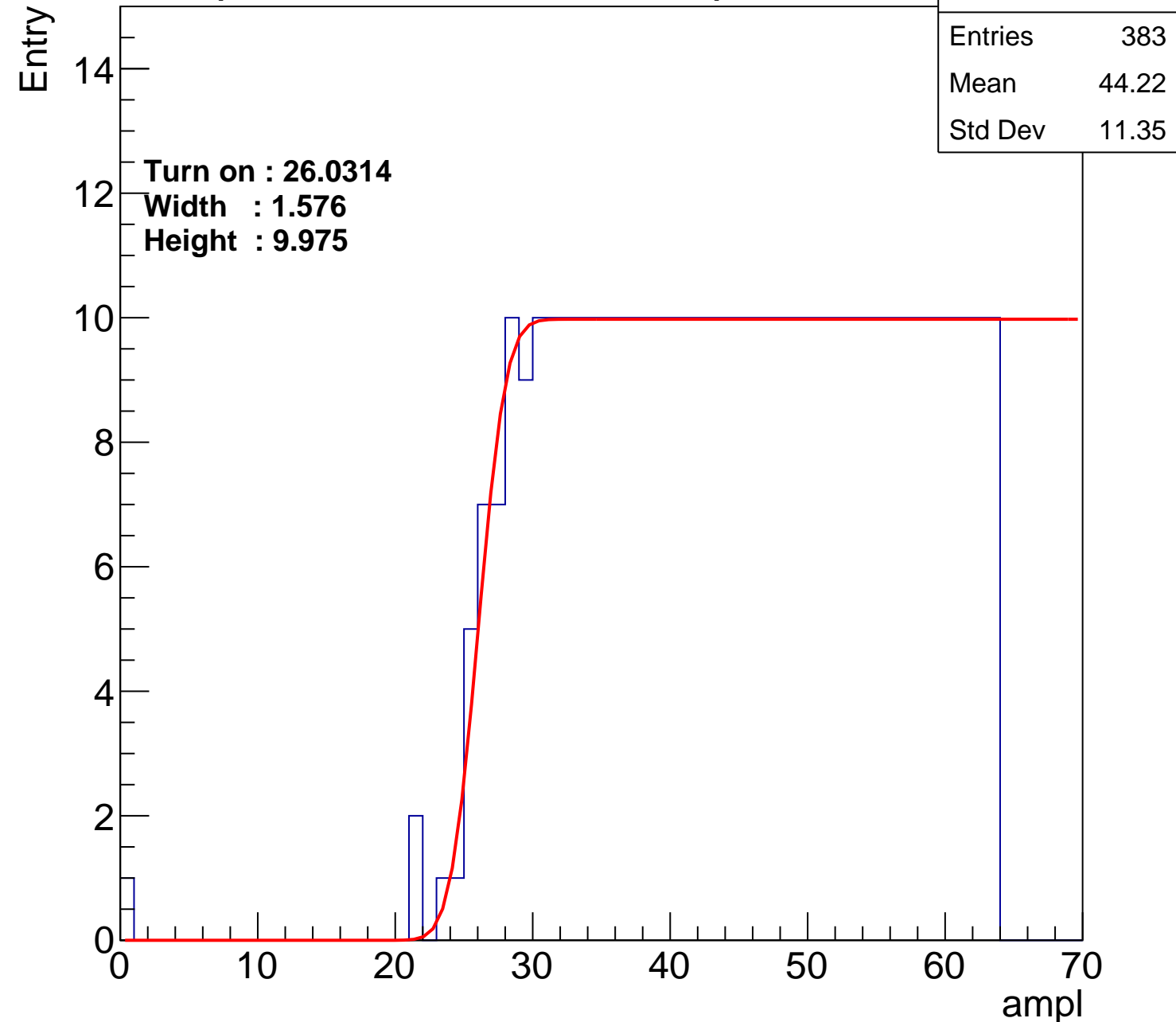
Width : 1.576

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 23.8744

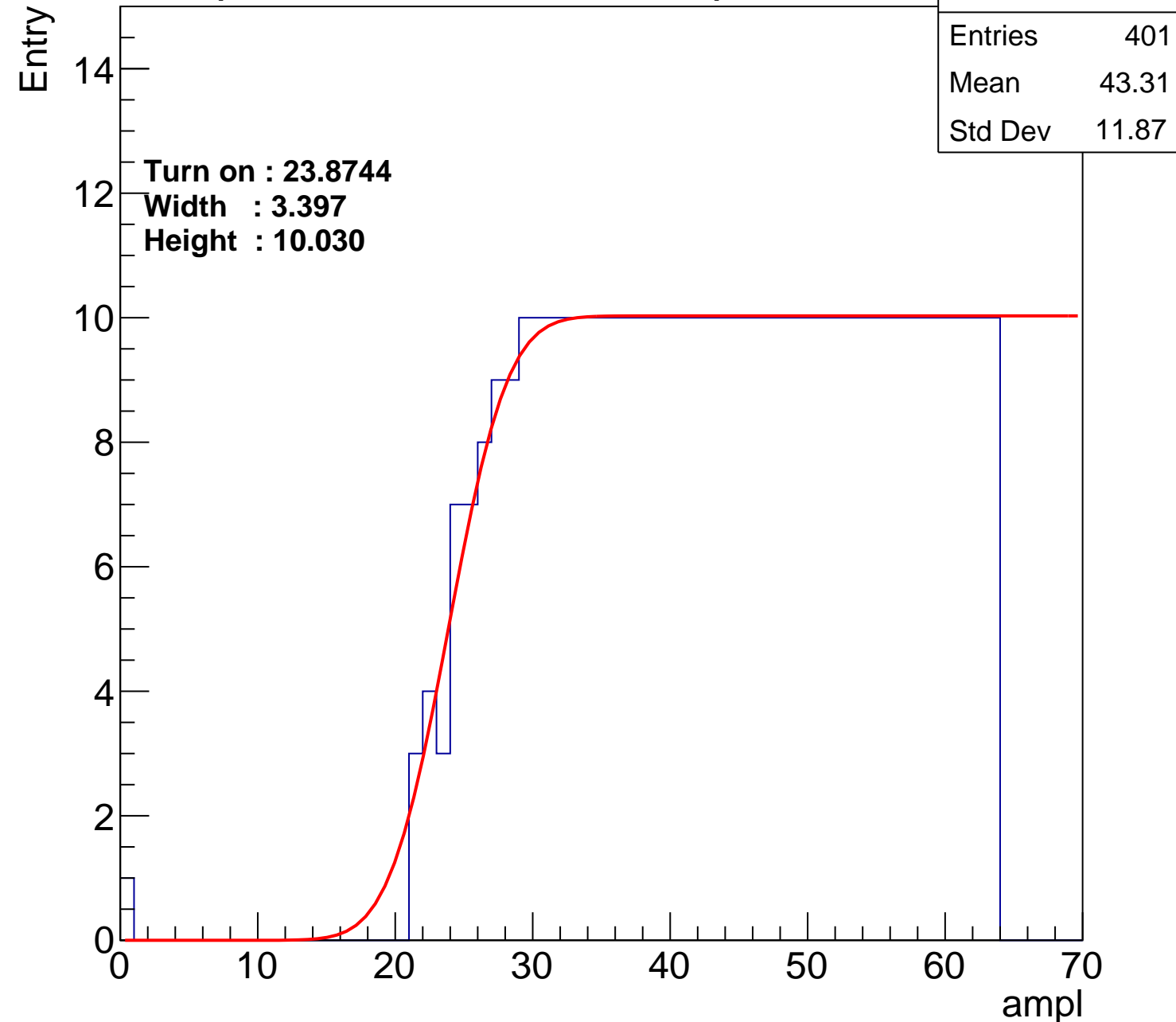
Width : 3.397

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.19
Std Dev	11.41

Turn on : 26.0777

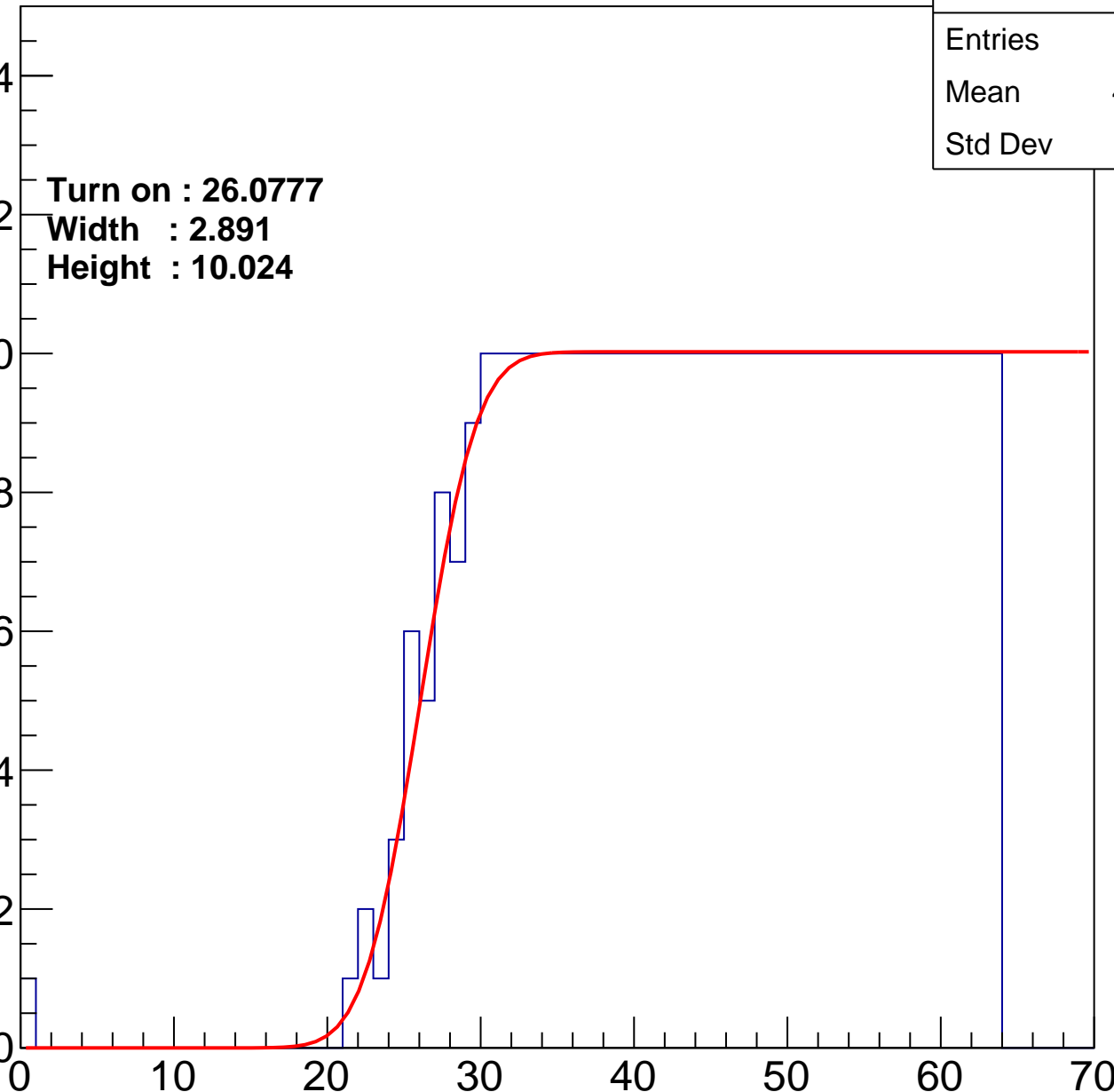
Width : 2.891

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.3421

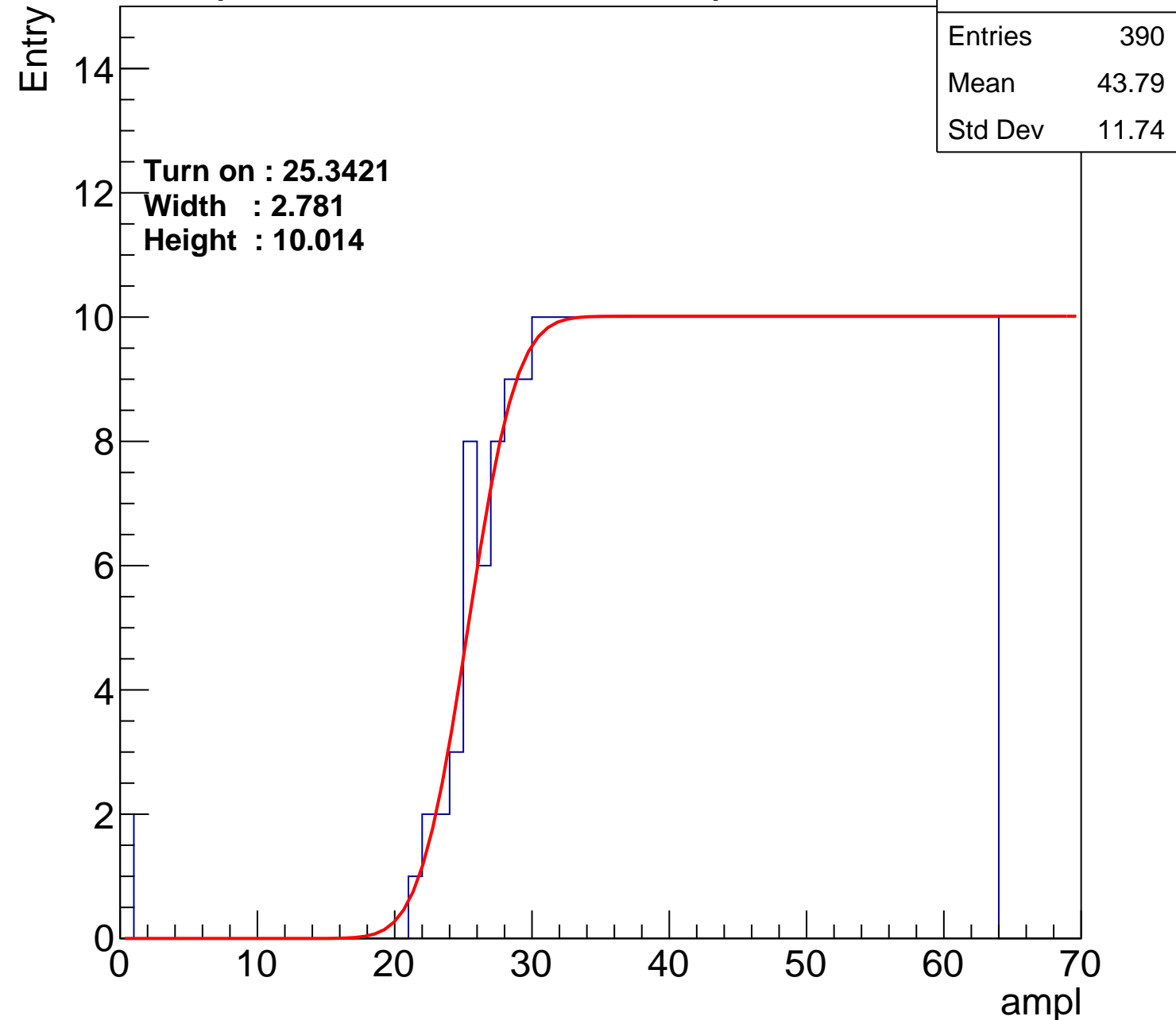
Width : 2.781

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch26

calib_packv5_042523_0143.root, FC#10, port B3

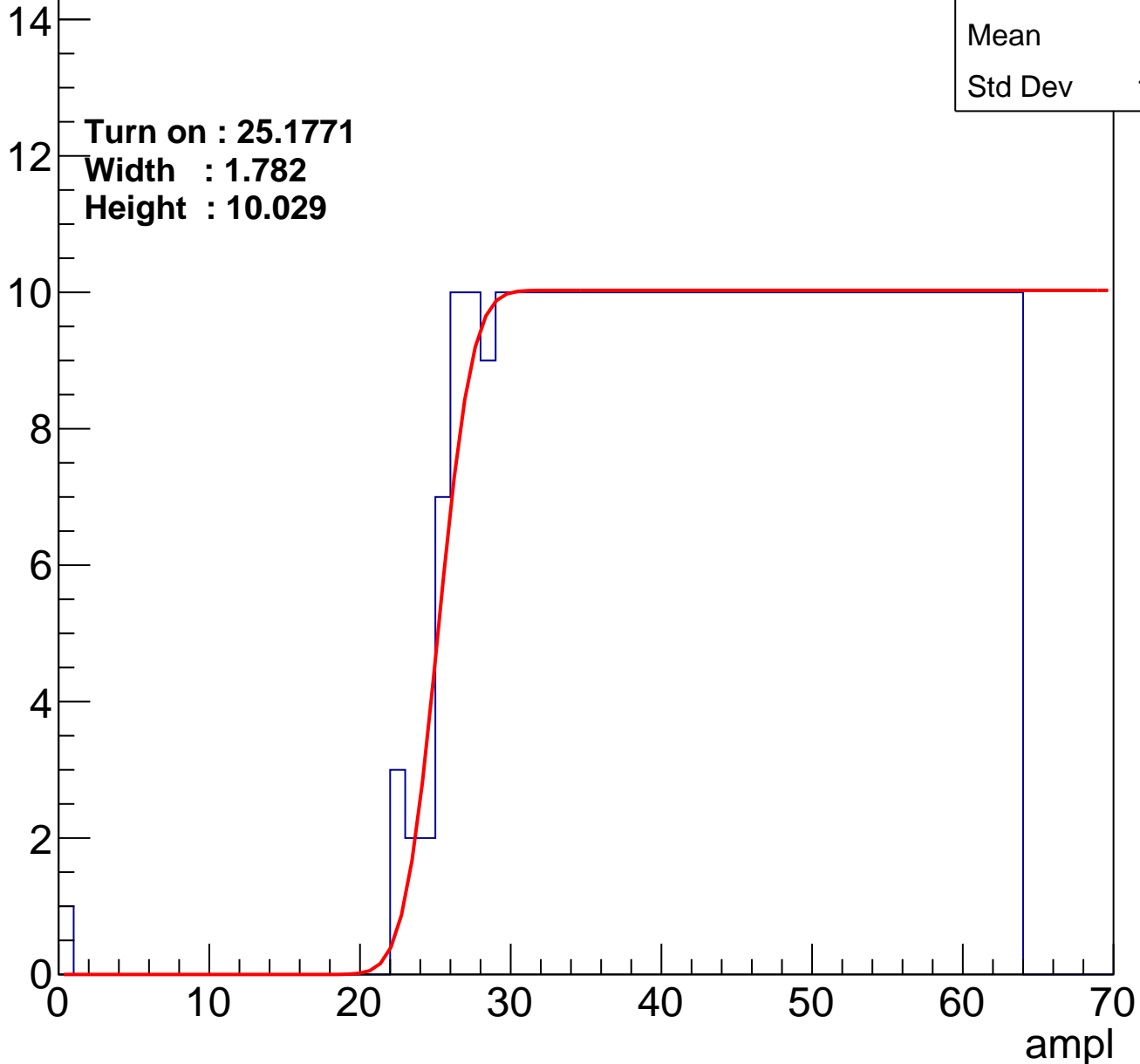
Entries	394
Mean	43.7
Std Dev	11.61

Turn on : 25.1771

Width : 1.782

Height : 10.029

Entry



B1L002S, U8-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.6319

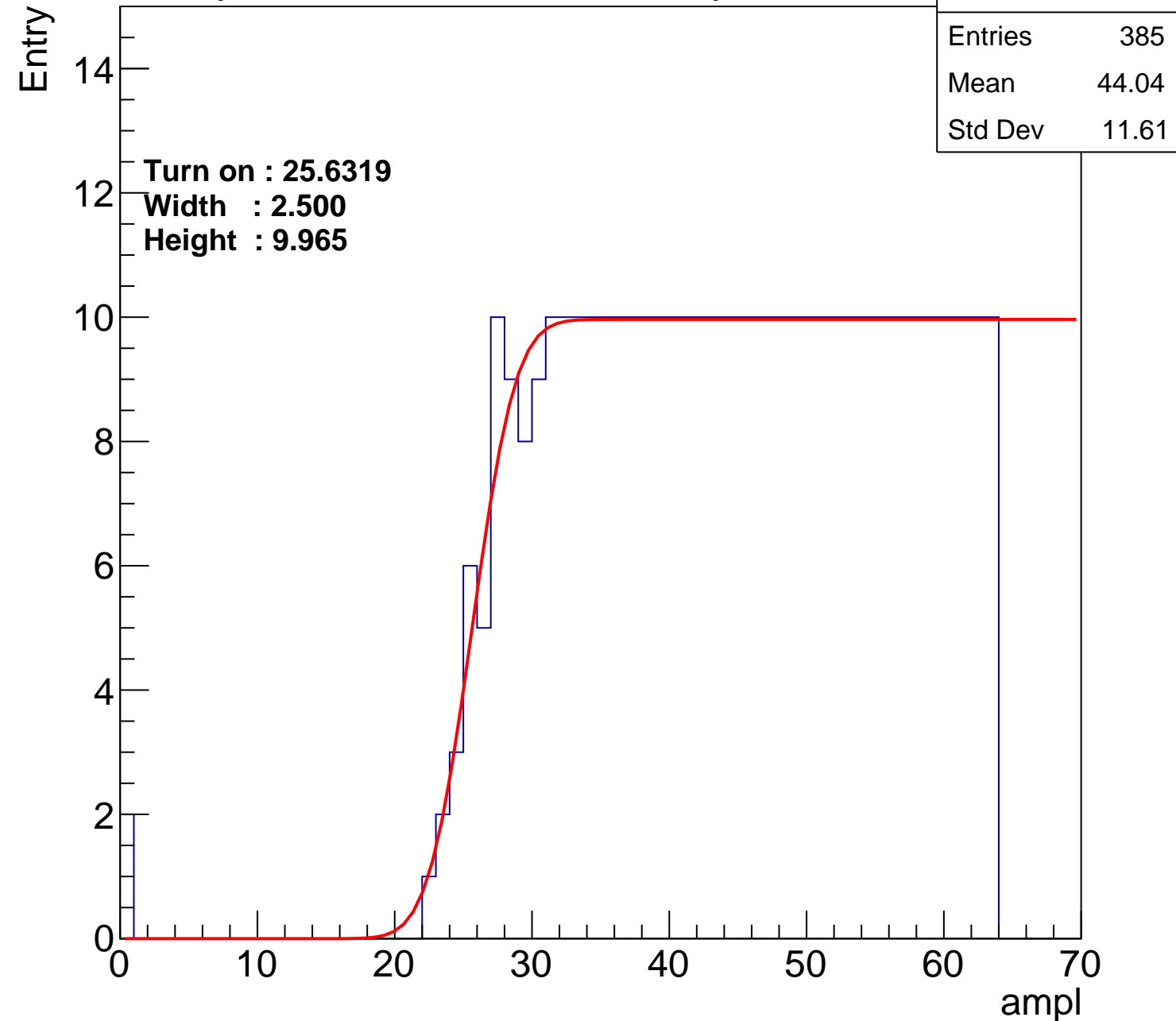
Width : 2.500

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.47
Std Dev	12.05

Turn on : 24.8853

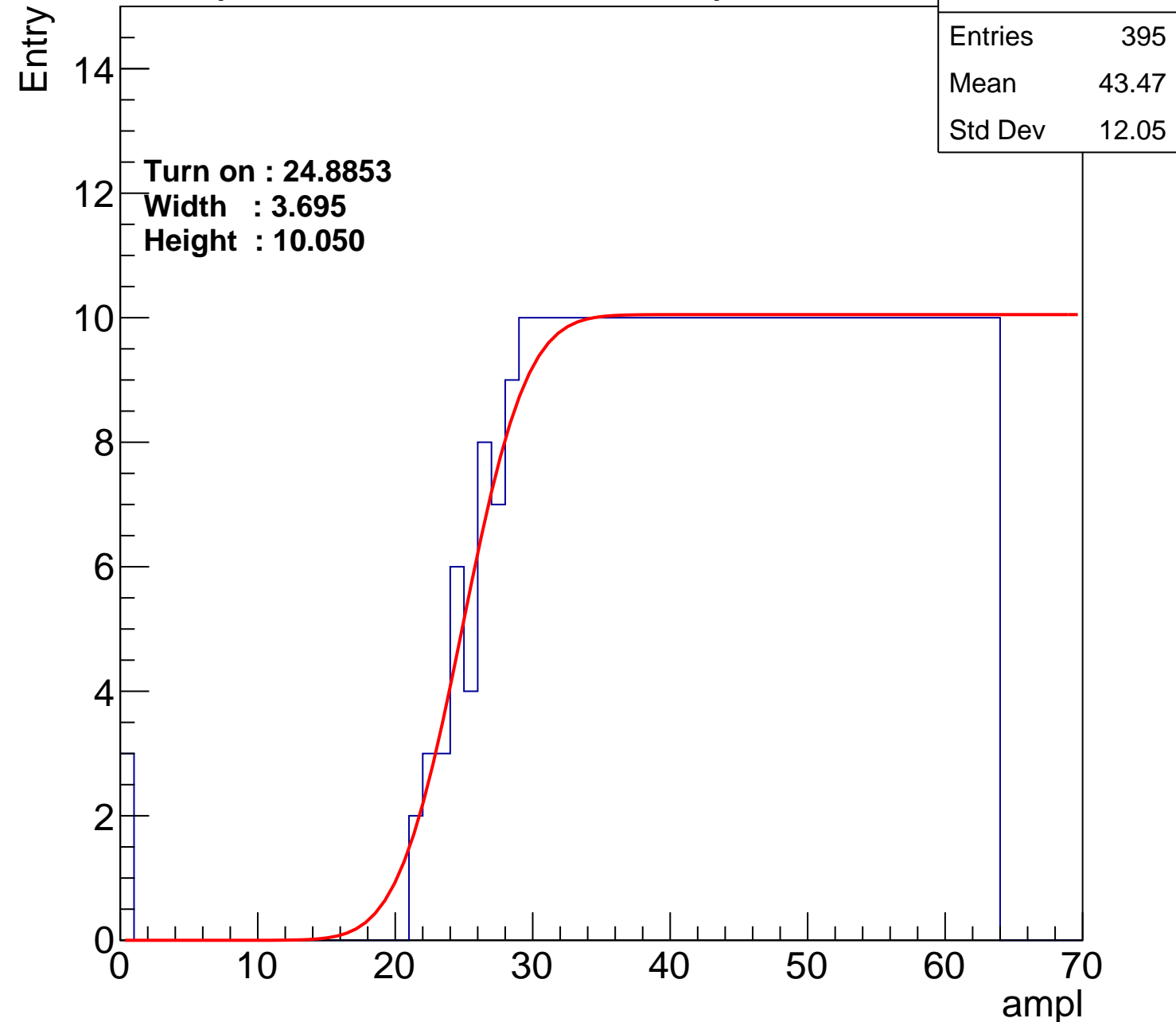
Width : 3.695

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.67
Std Dev	11.66

Turn on : 25.0044

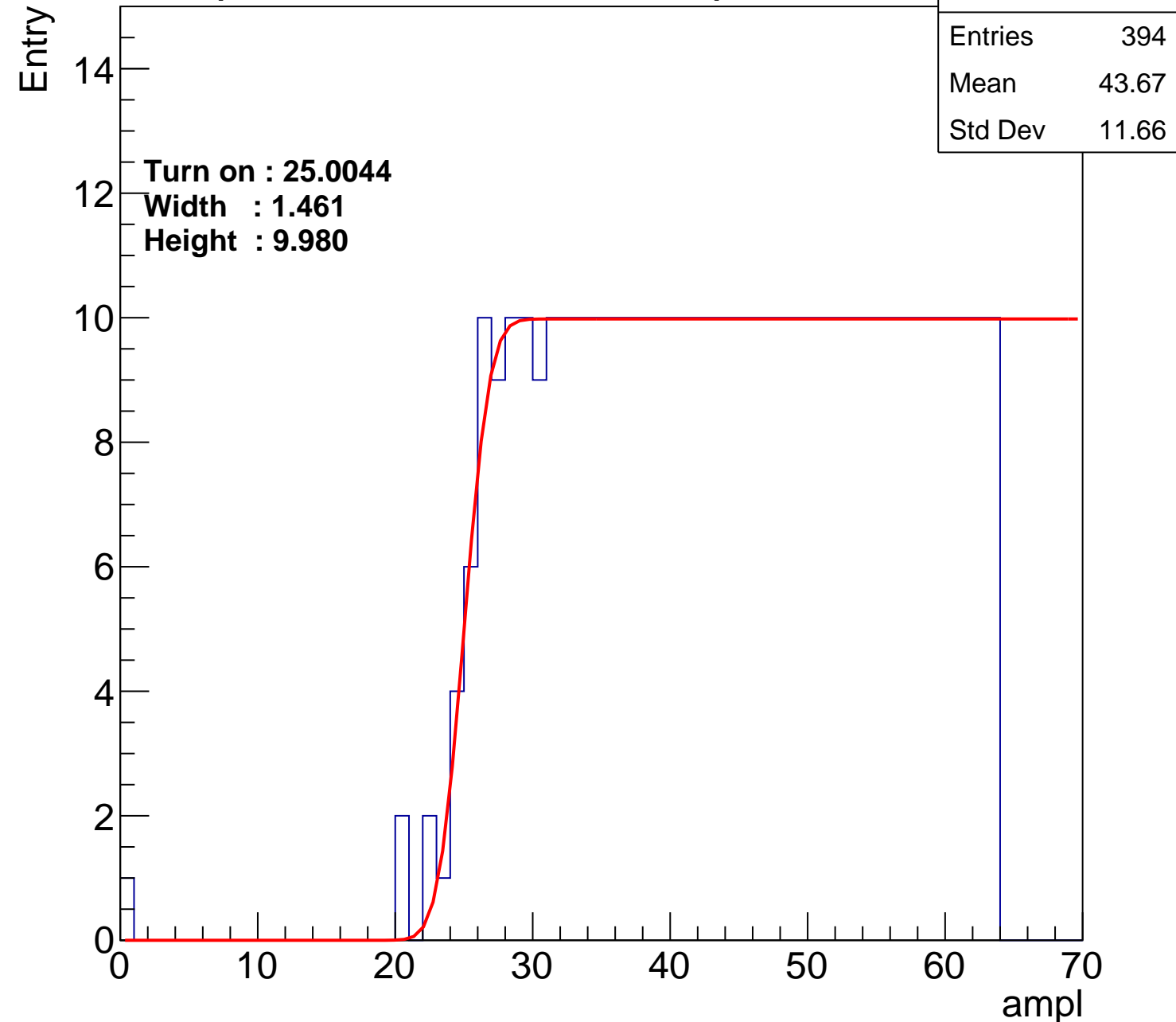
Width : 1.461

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.1244

Width : 3.158

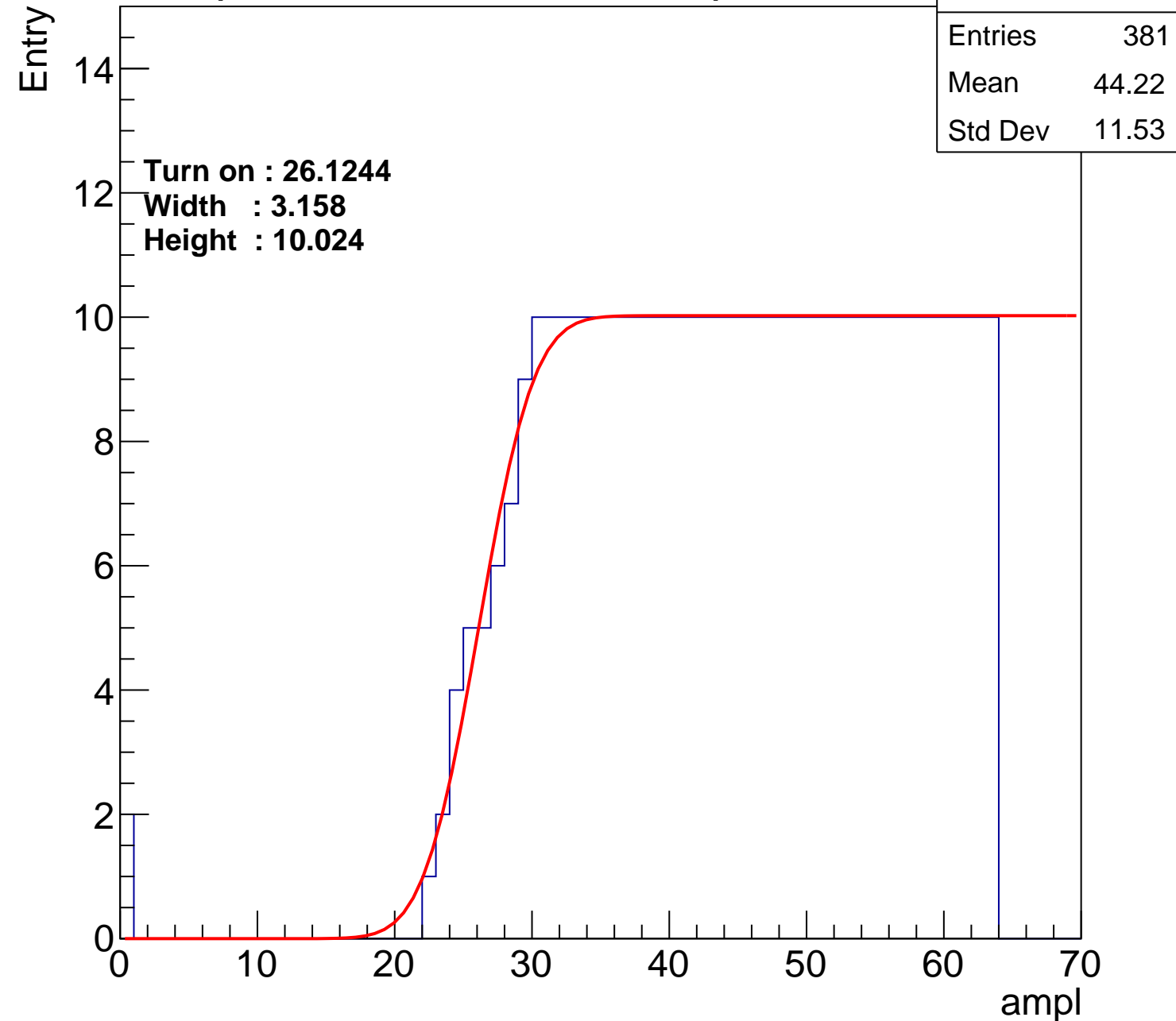
Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U8-ch31

calib_packv5_042523_0143.root, FC#10, port B3

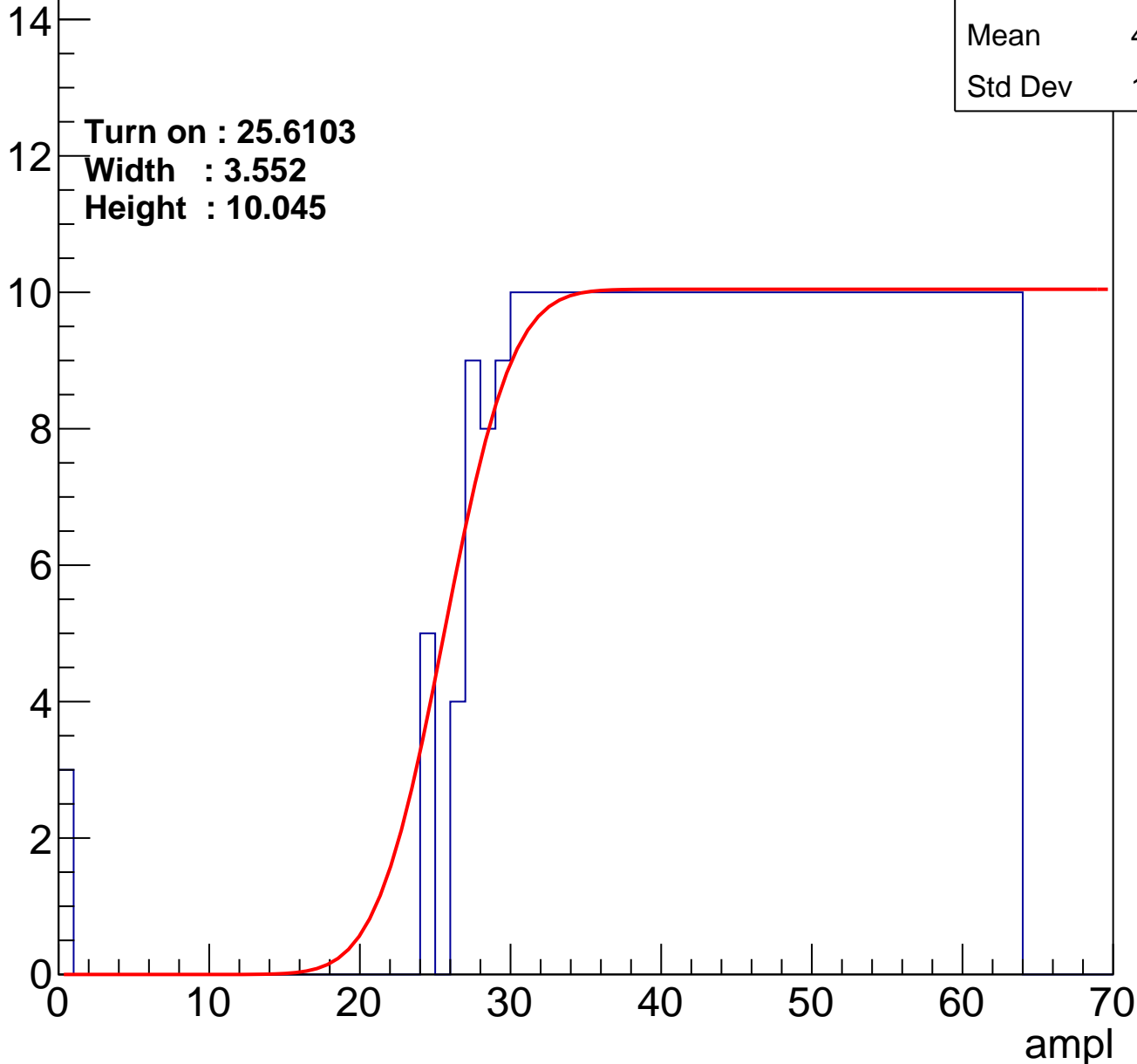
Entries	378
Mean	44.34
Std Dev	11.57

Turn on : 25.6103

Width : 3.552

Height : 10.045

Entry



B1L002S, U8-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.65
Std Dev	11.68

Turn on : 24.7194

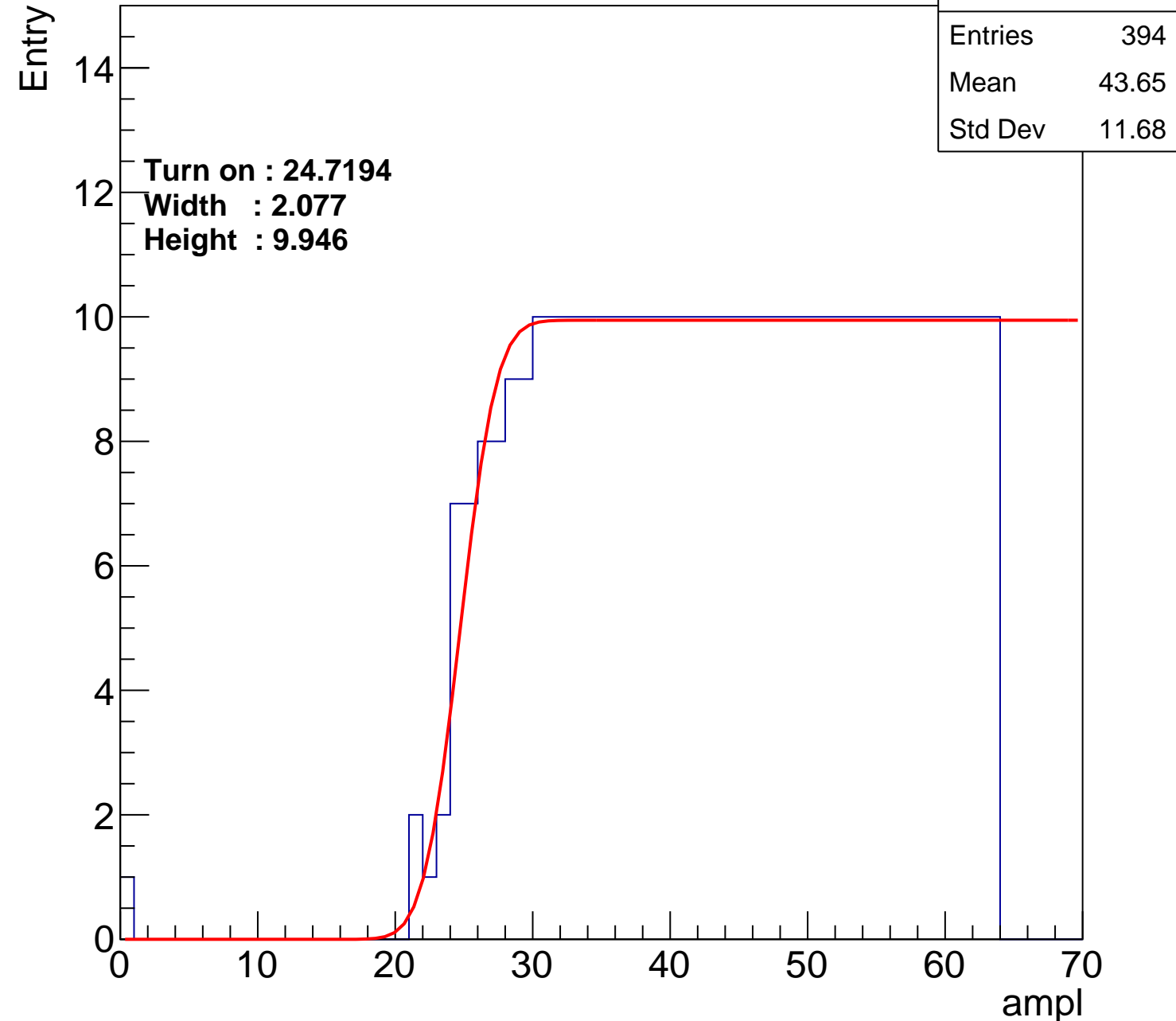
Width : 2.077

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.84
Std Dev	11.59

Turn on : 25.1784

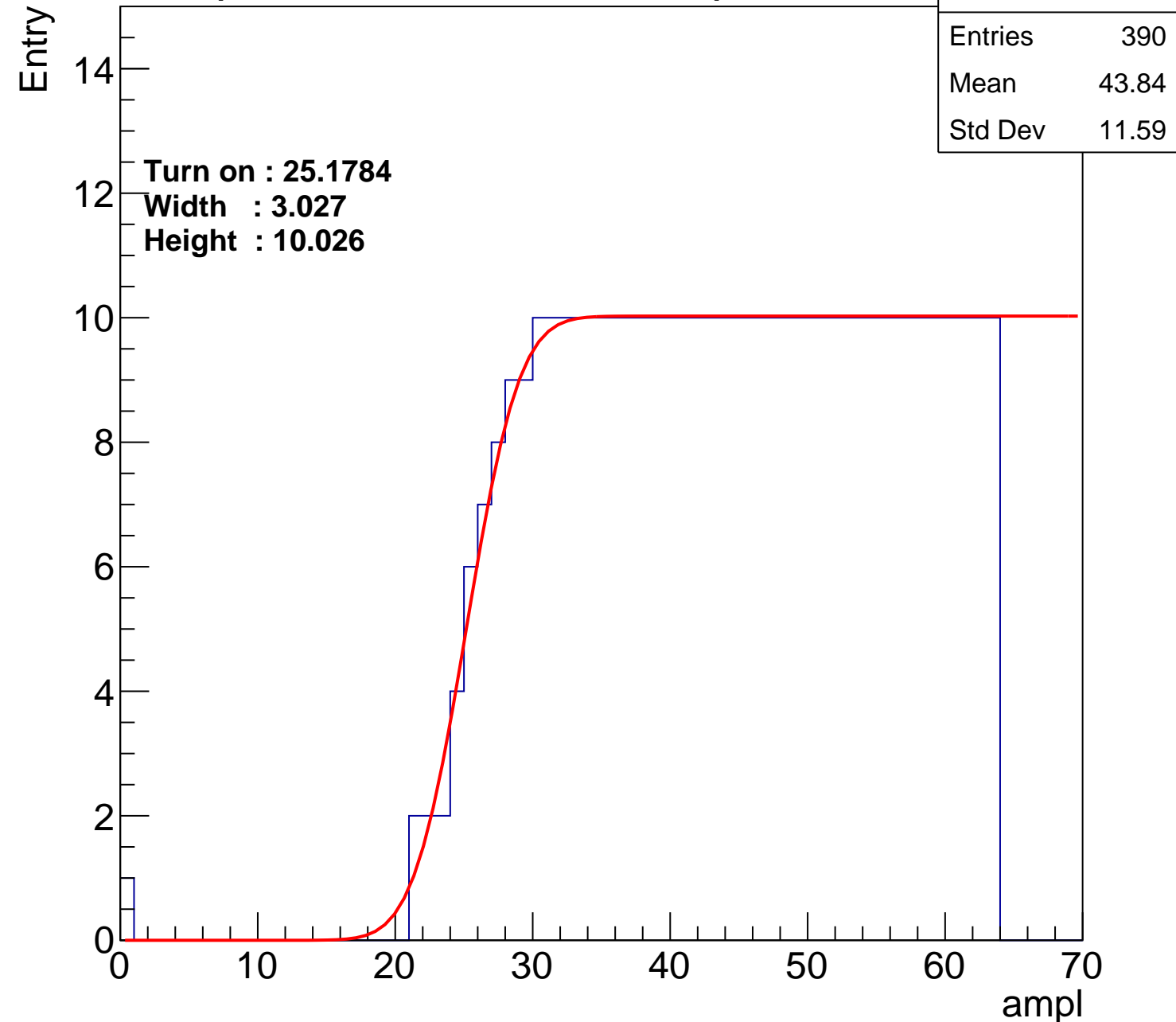
Width : 3.027

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.5737

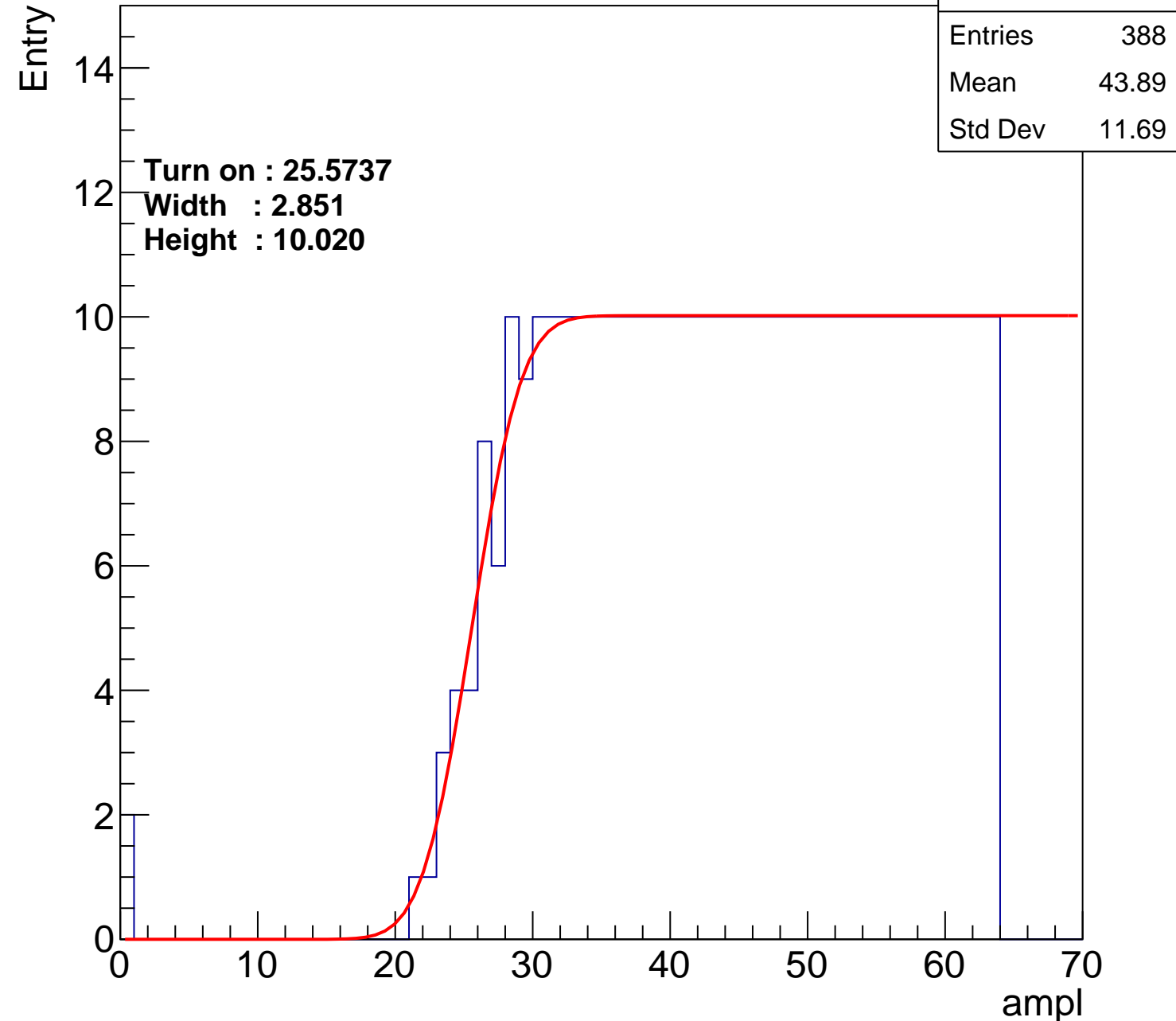
Width : 2.851

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 27.1432

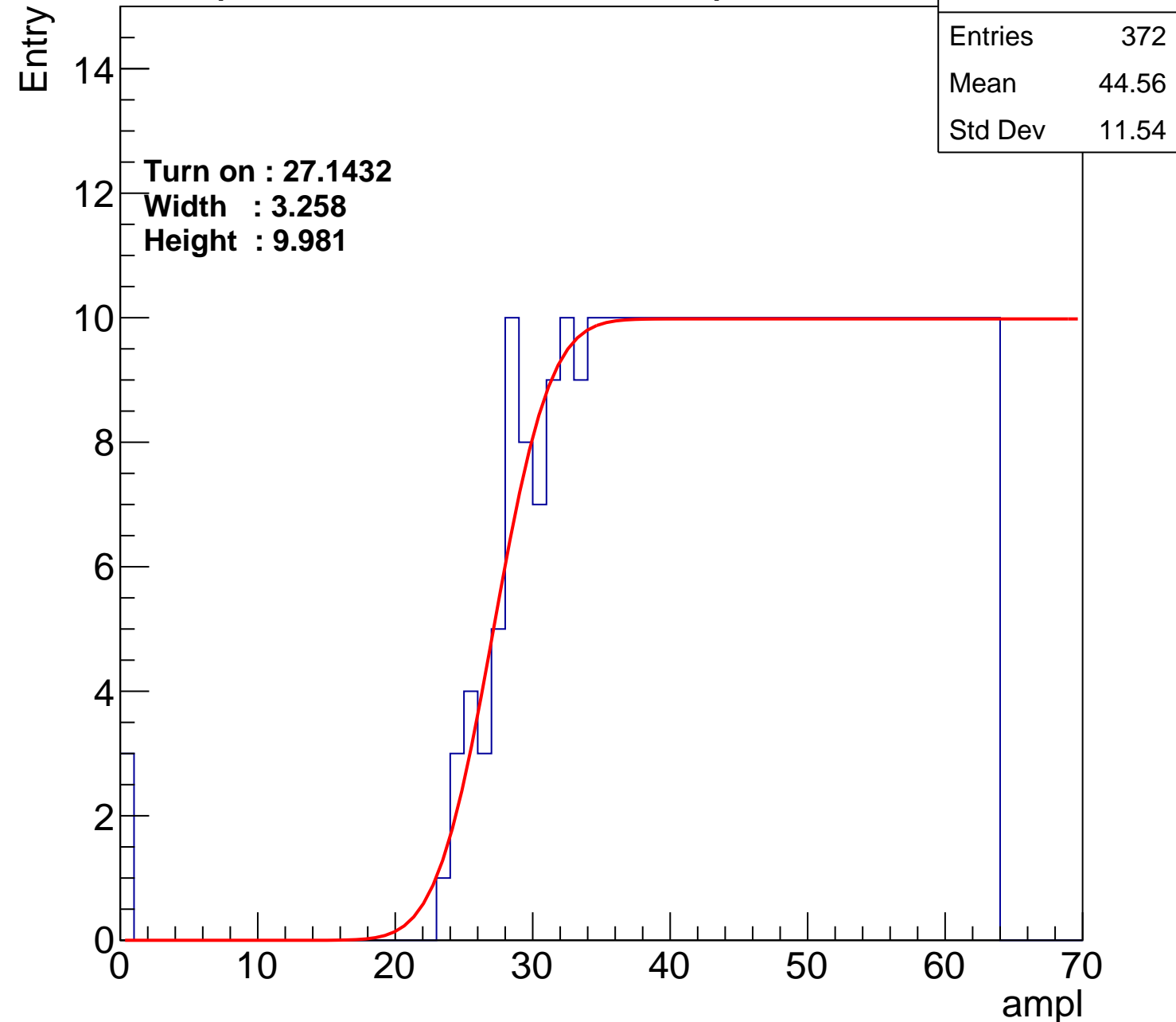
Width : 3.258

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.26
Std Dev	12.12

Turn on : 24.3066

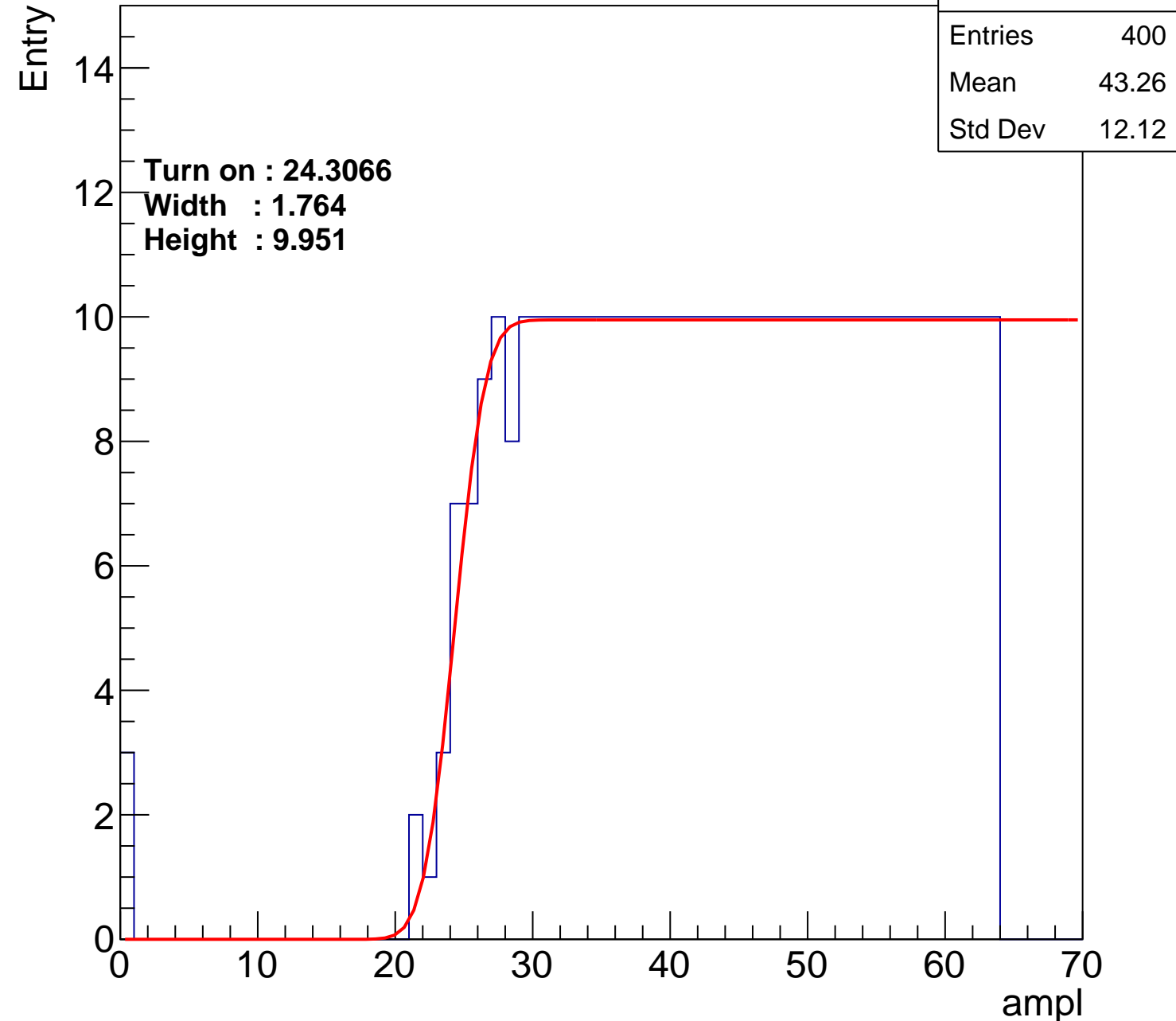
Width : 1.764

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.96
Std Dev	10.94

Turn on : 27.6038

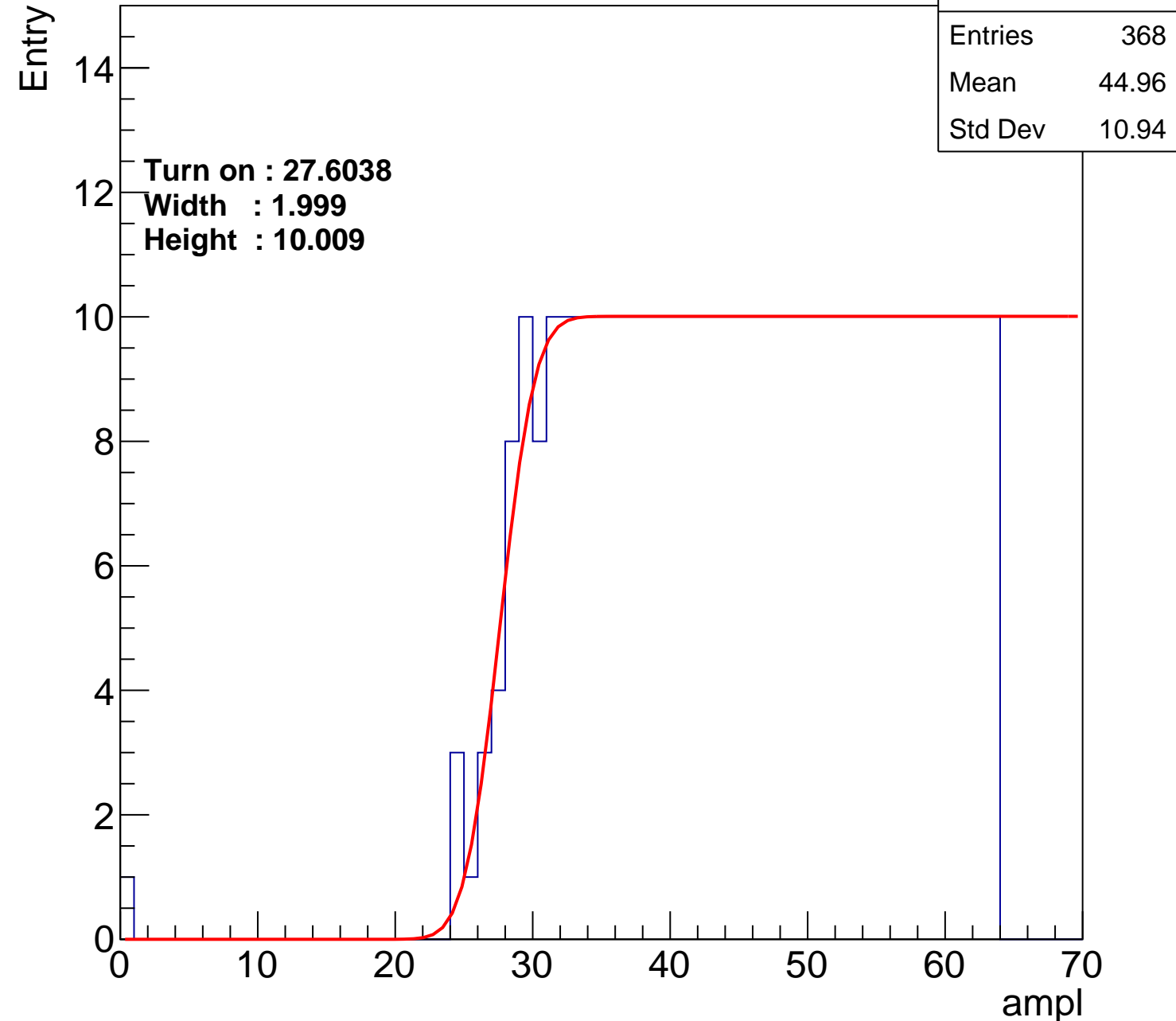
Width : 1.999

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 26.4253

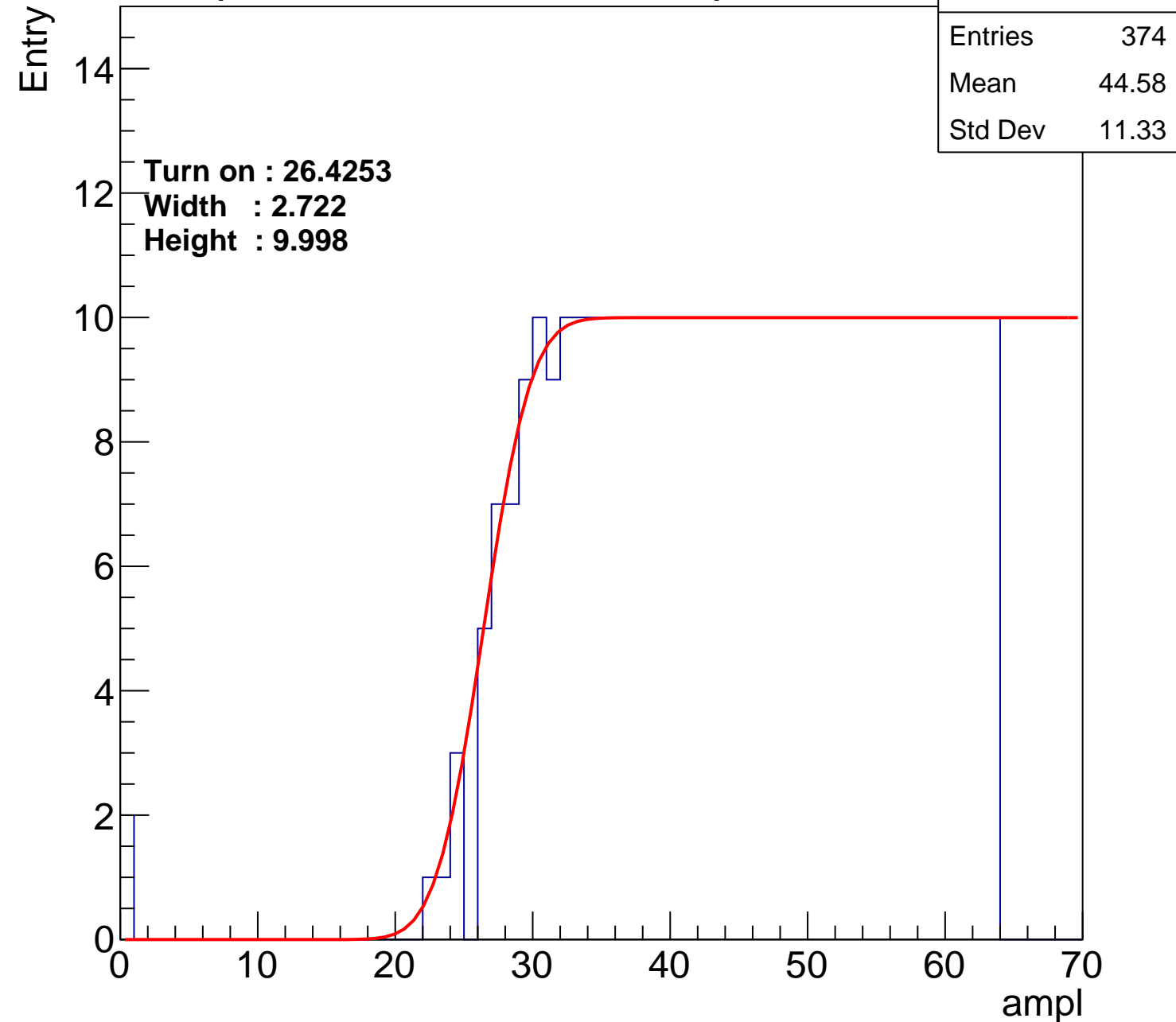
Width : 2.722

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch39

calib_packv5_042523_0143.root, FC#10, port B3

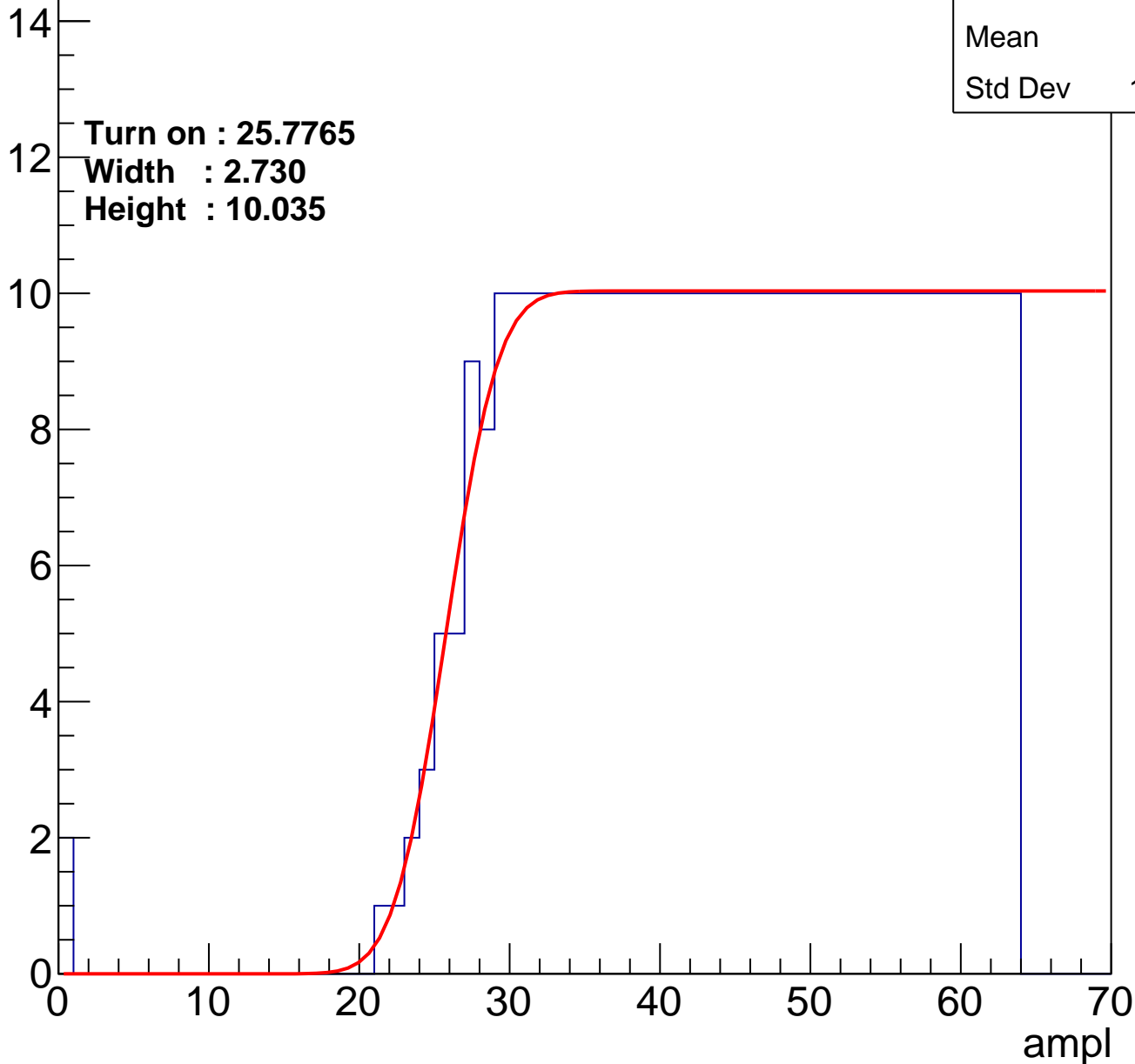
Entry

Entries	386
Mean	44
Std Dev	11.62

Turn on : 25.7765

Width : 2.730

Height : 10.035



B1L002S, U8-ch40

calib_packv5_042523_0143.root, FC#10, port B3

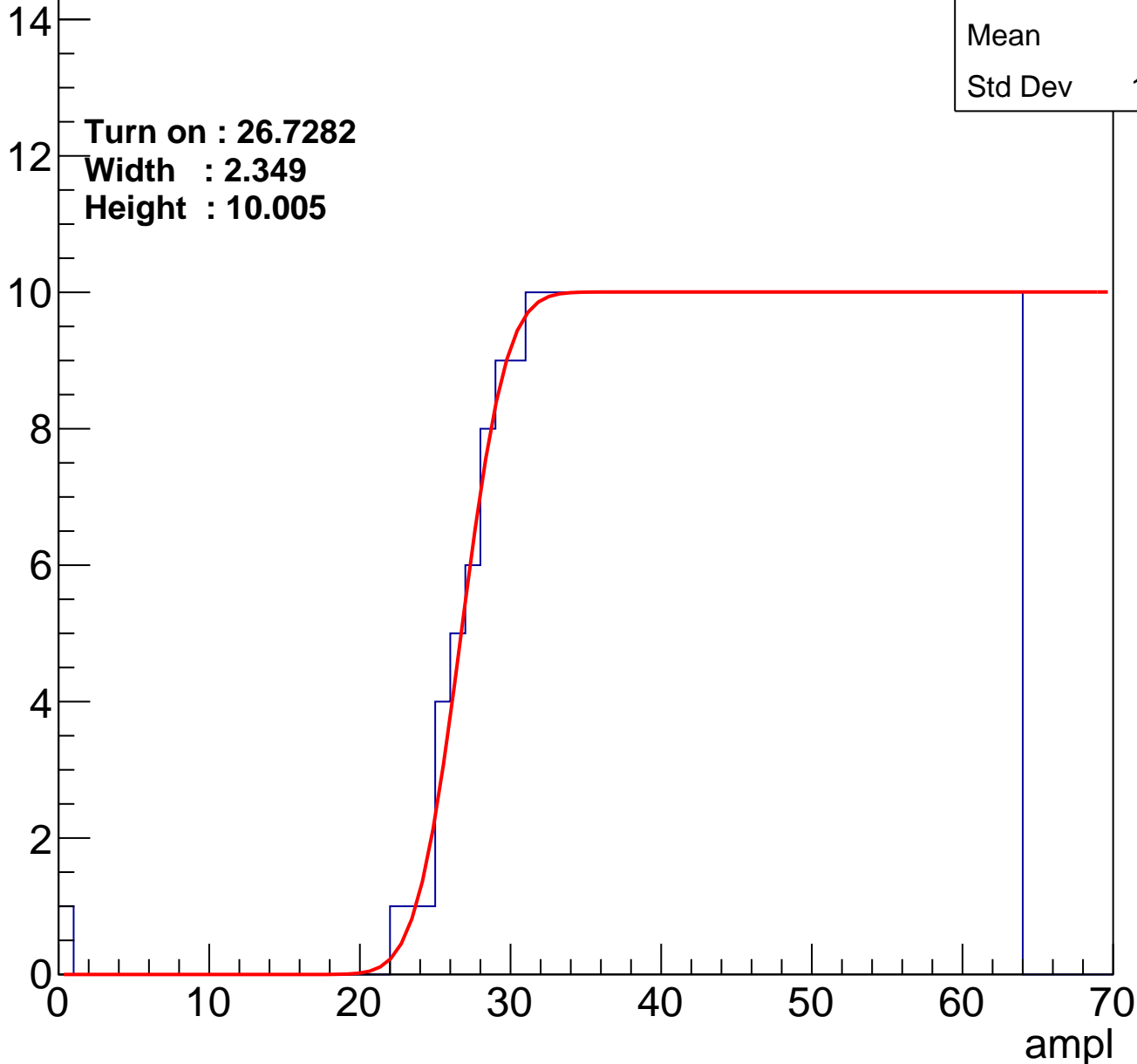
Entries	375
Mean	44.6
Std Dev	11.15

Turn on : 26.7282

Width : 2.349

Height : 10.005

Entry



B1L002S, U8-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	361
Mean	45.3
Std Dev	10.78

Turn on : 28.3773

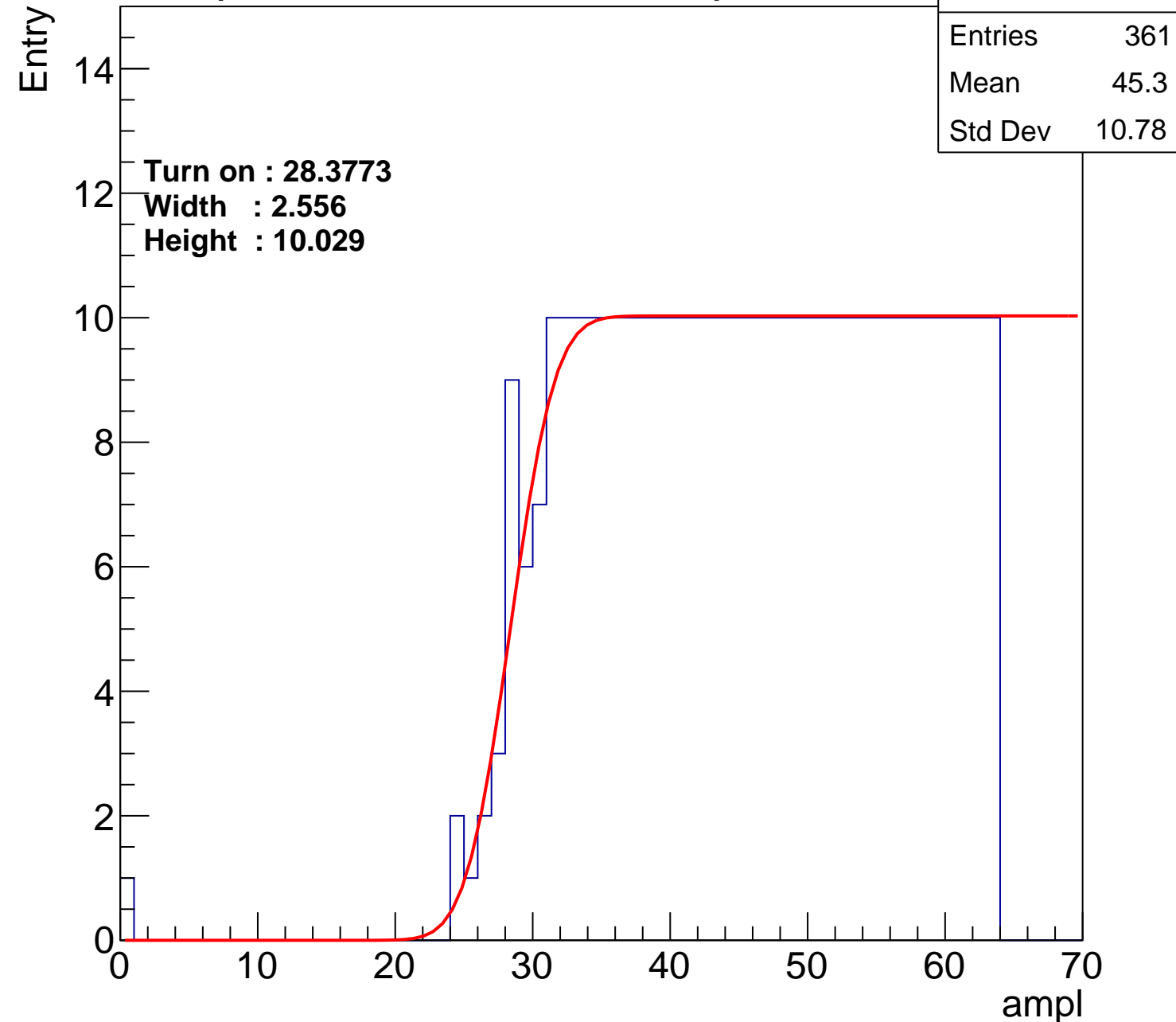
Width : 2.556

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.62
Std Dev	11.7

Turn on : 24.8715

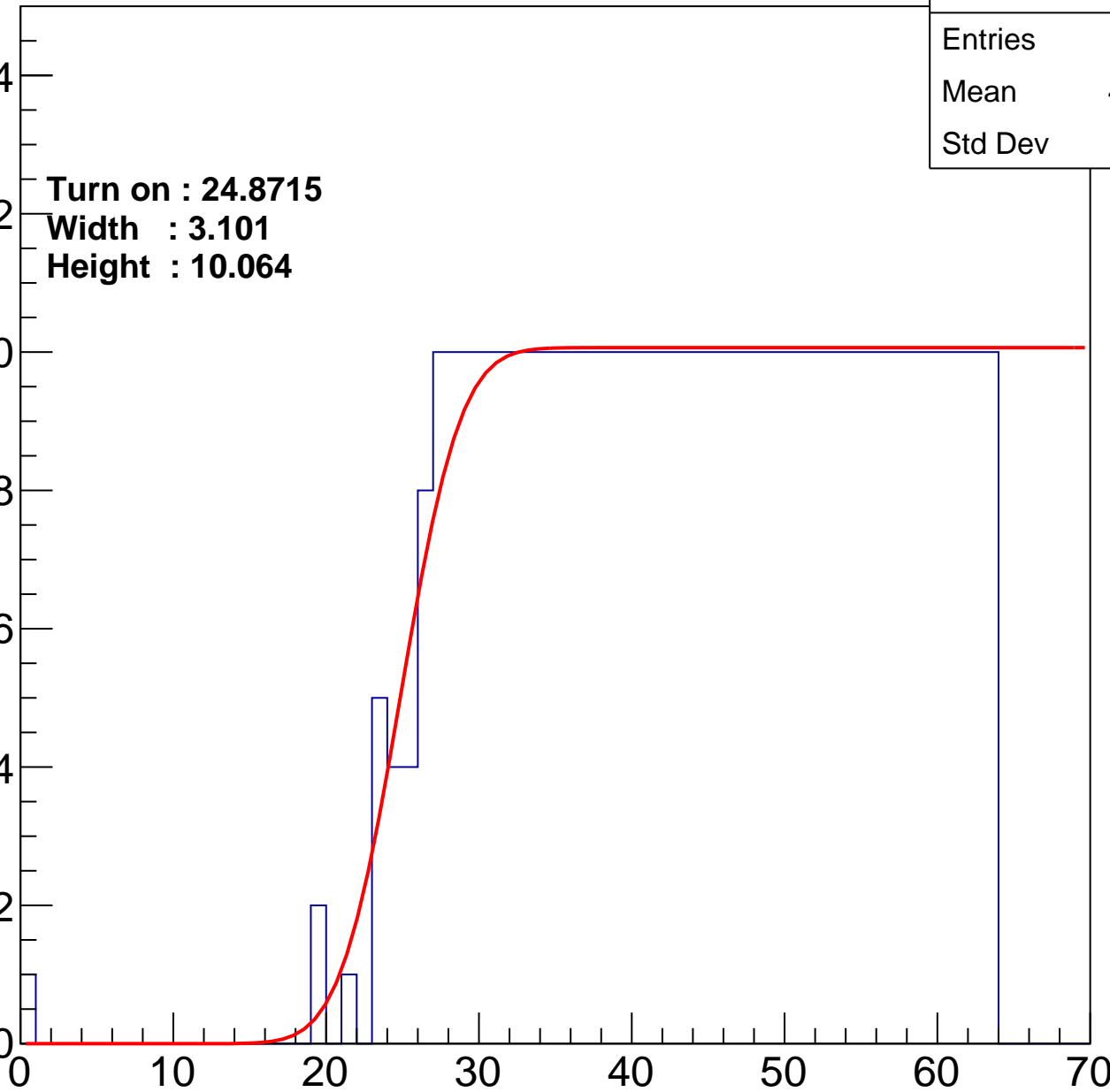
Width : 3.101

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.3275

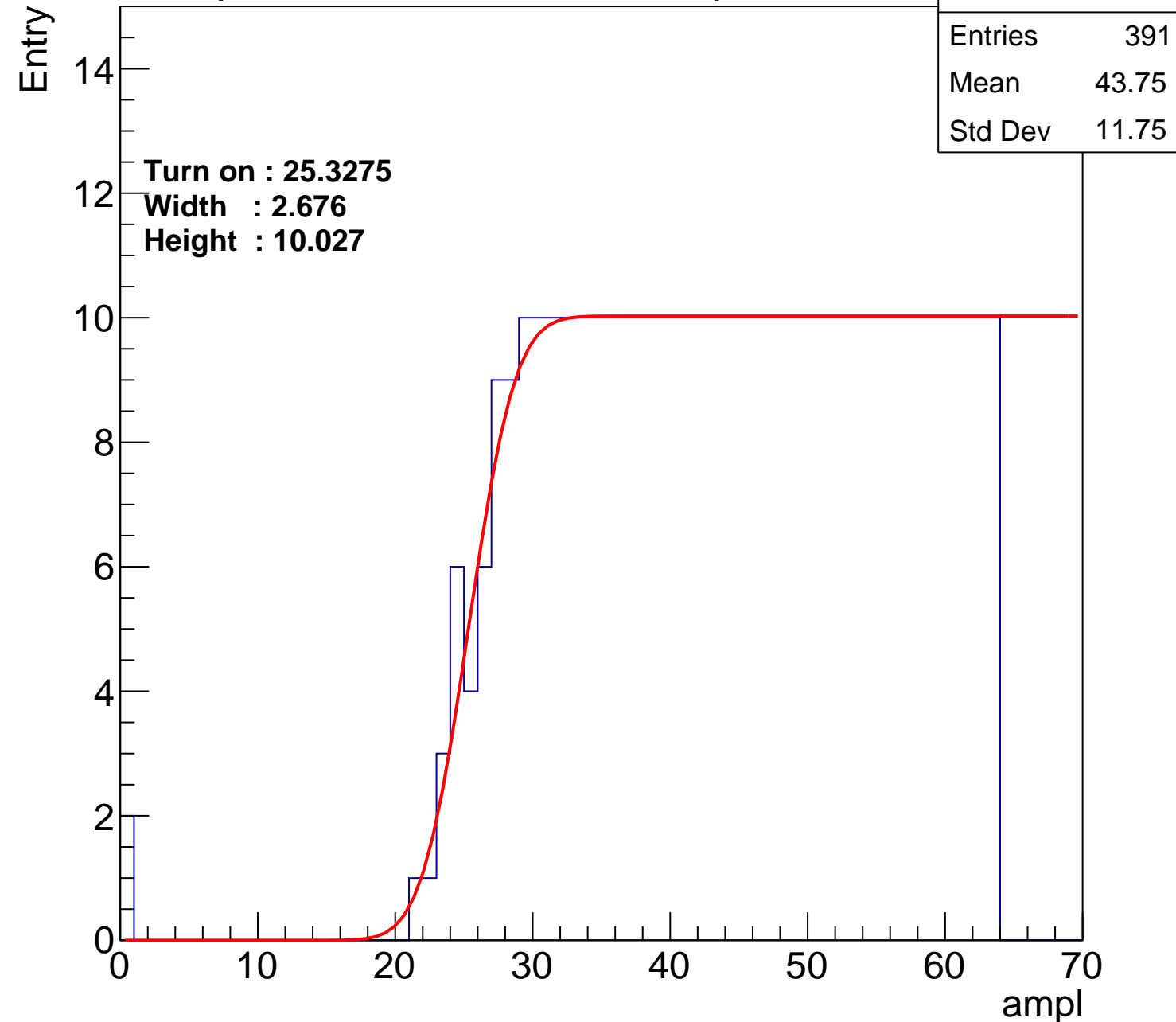
Width : 2.676

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch44

calib_packv5_042523_0143.root, FC#10, port B3

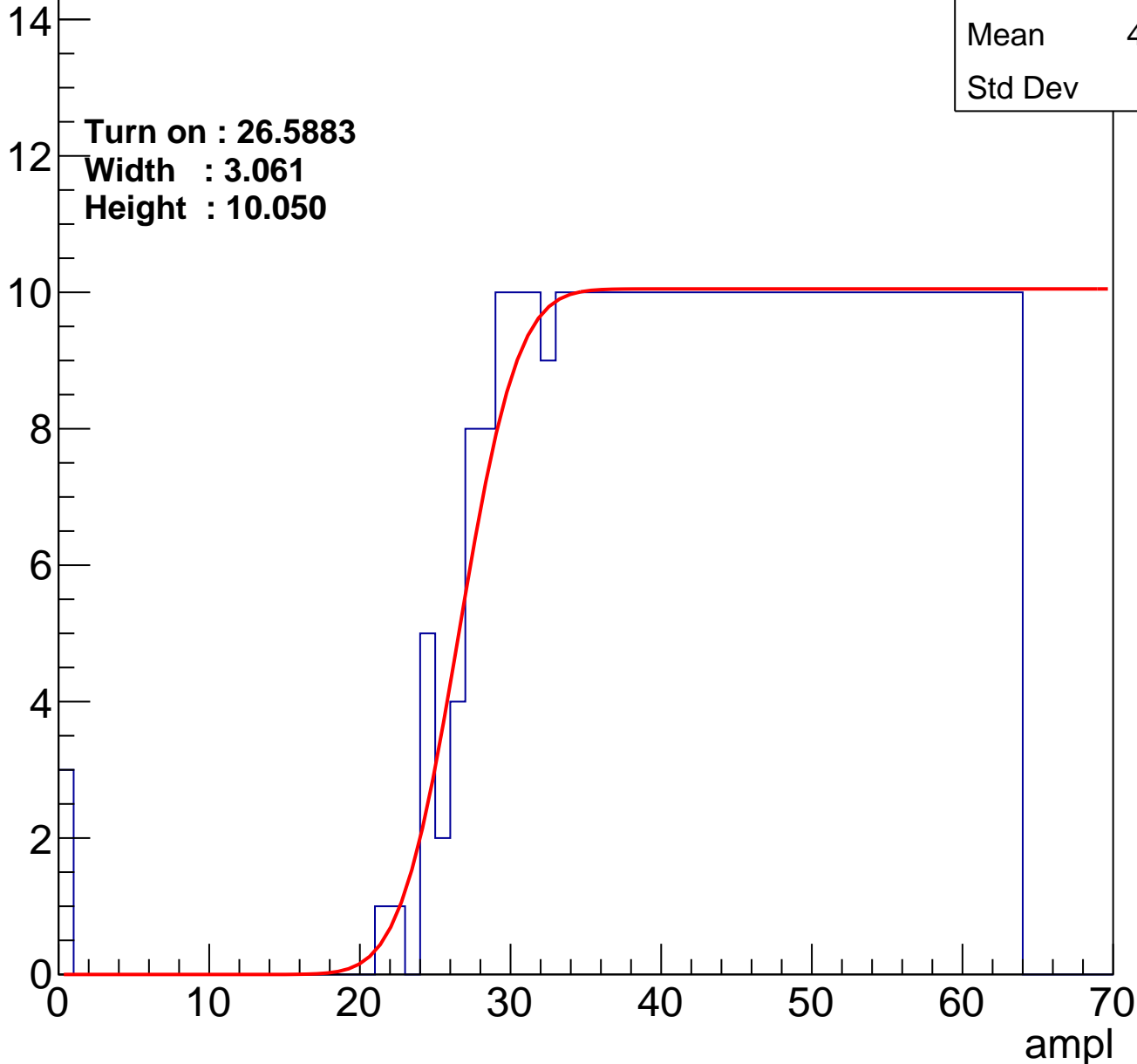
Entry

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.5883

Width : 3.061

Height : 10.050



B1L002S, U8-ch45

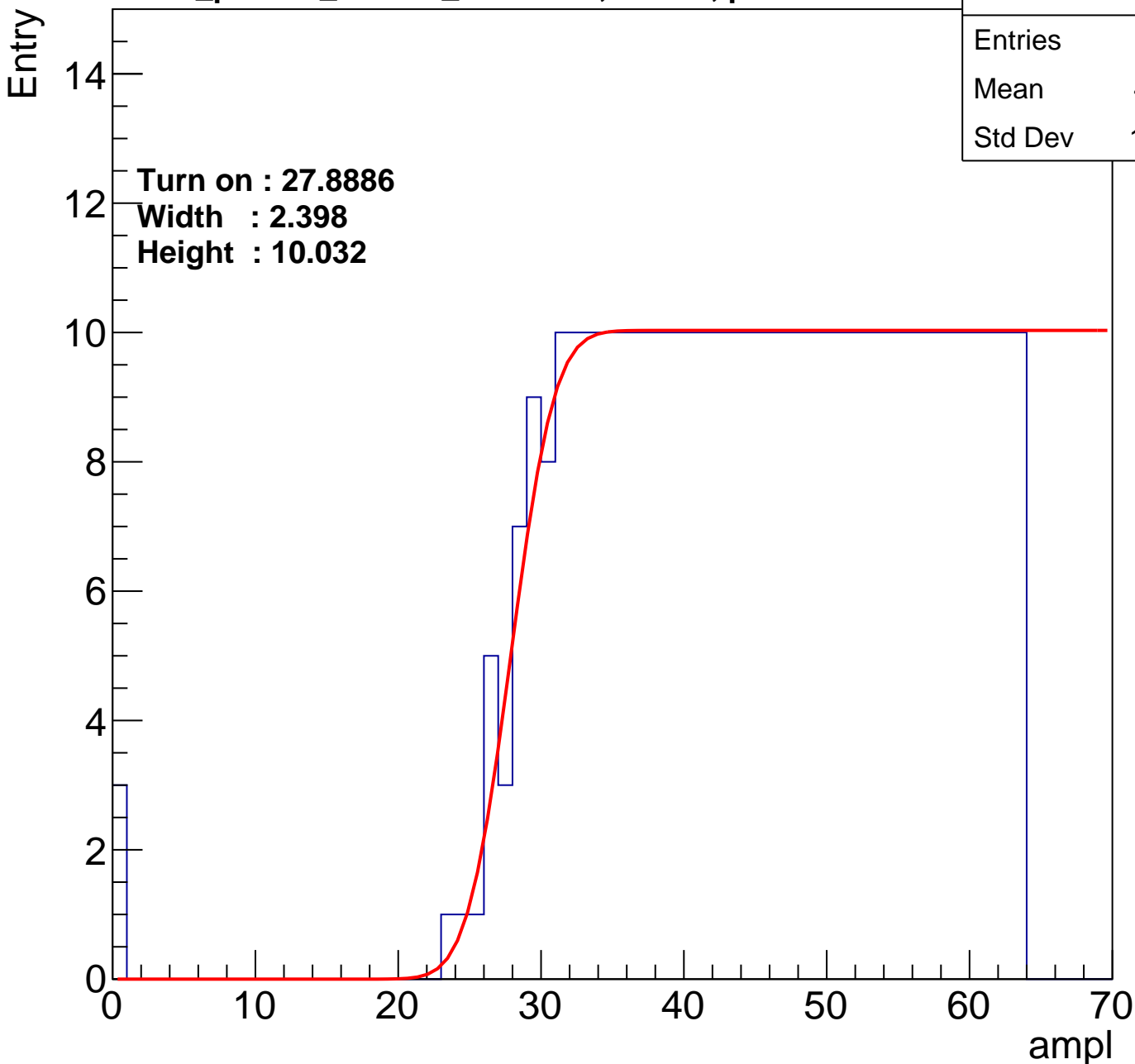
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 27.8886

Width : 2.398

Height : 10.032

Entries	368
Mean	44.81
Std Dev	11.36



B1L002S, U8-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.03
Std Dev	11.44

Turn on : 25.3539

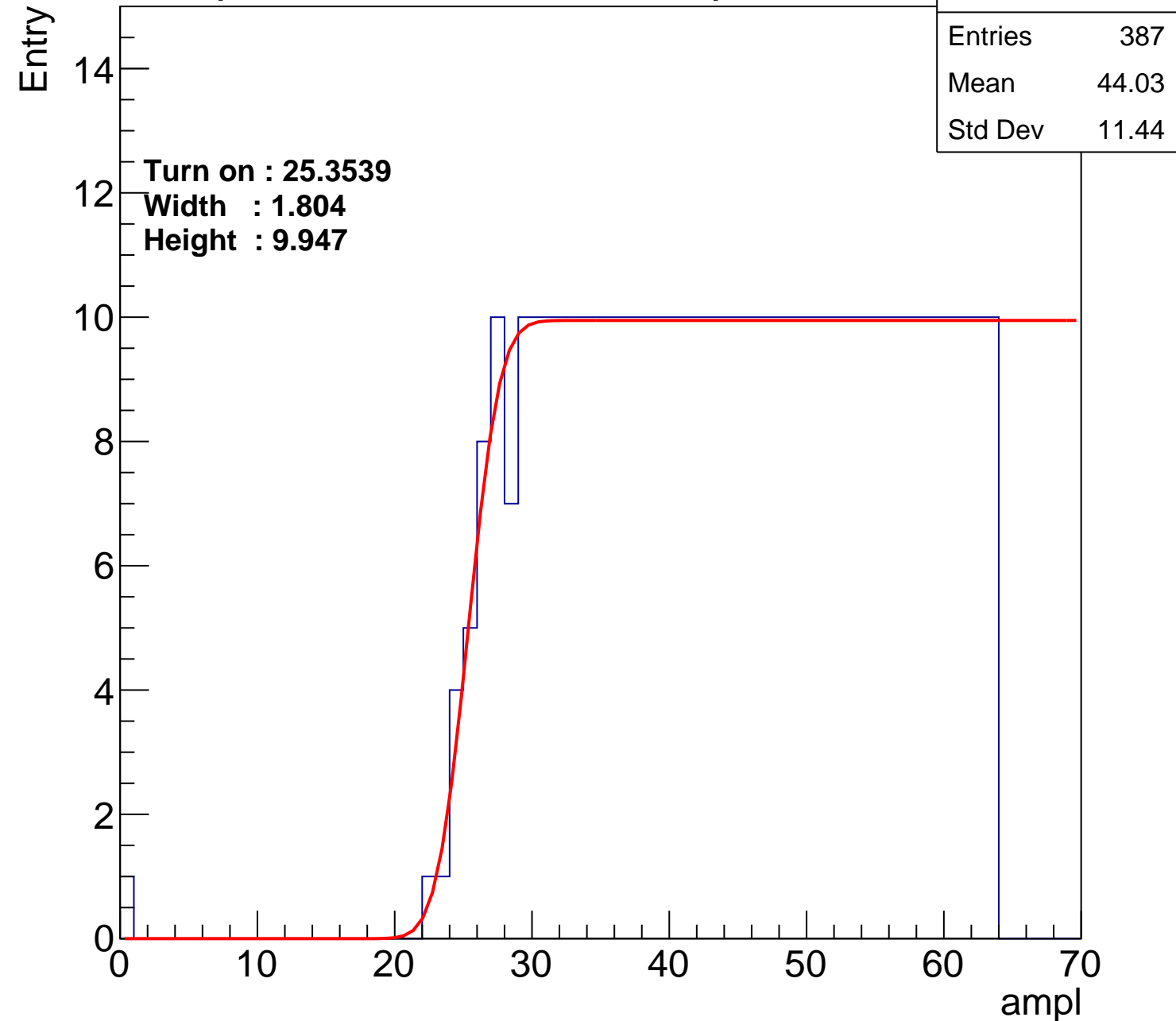
Width : 1.804

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.76

Turn on : 25.3723

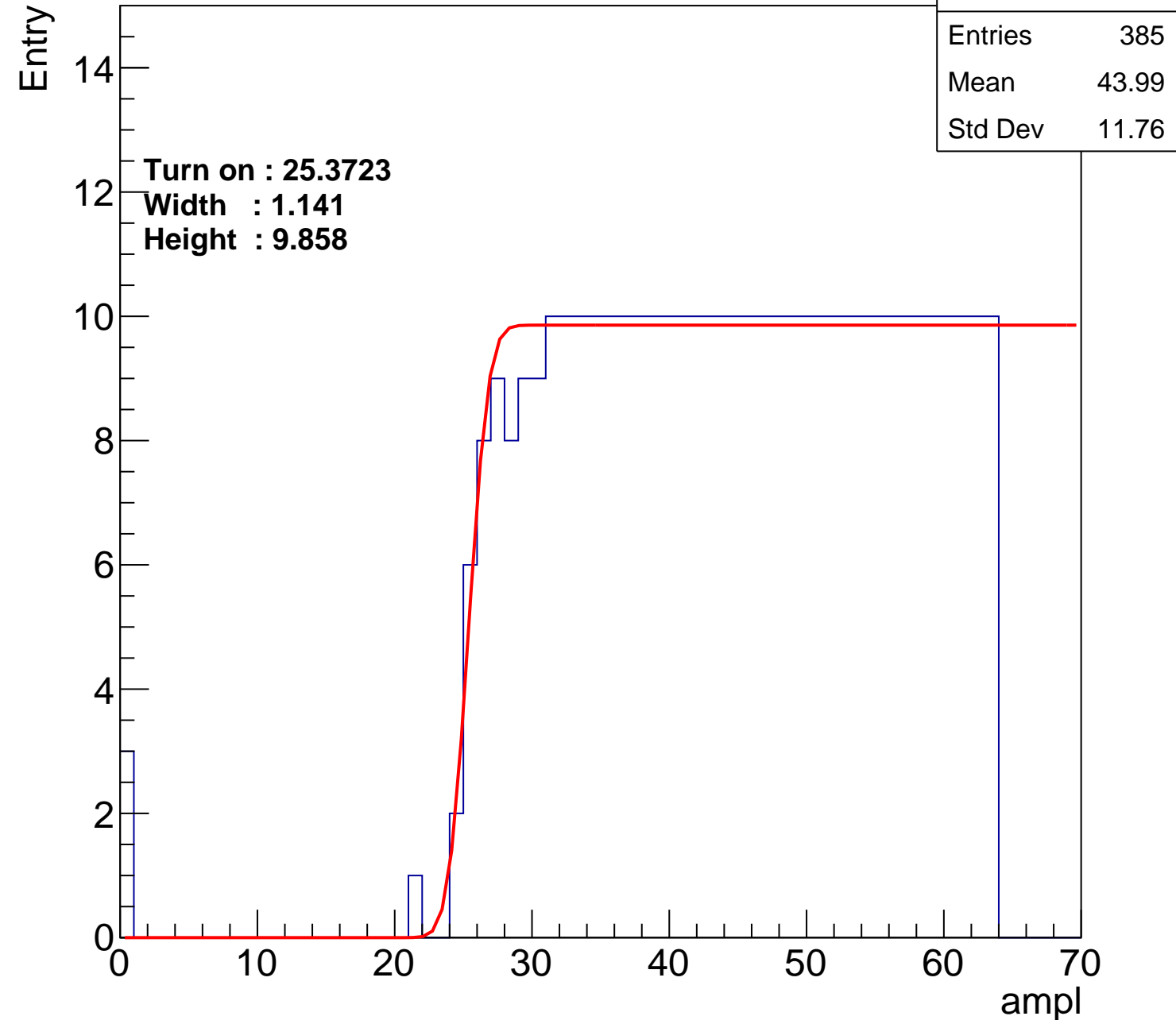
Width : 1.141

Height : 9.858

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.56
Std Dev	11.84

Turn on : 24.8839

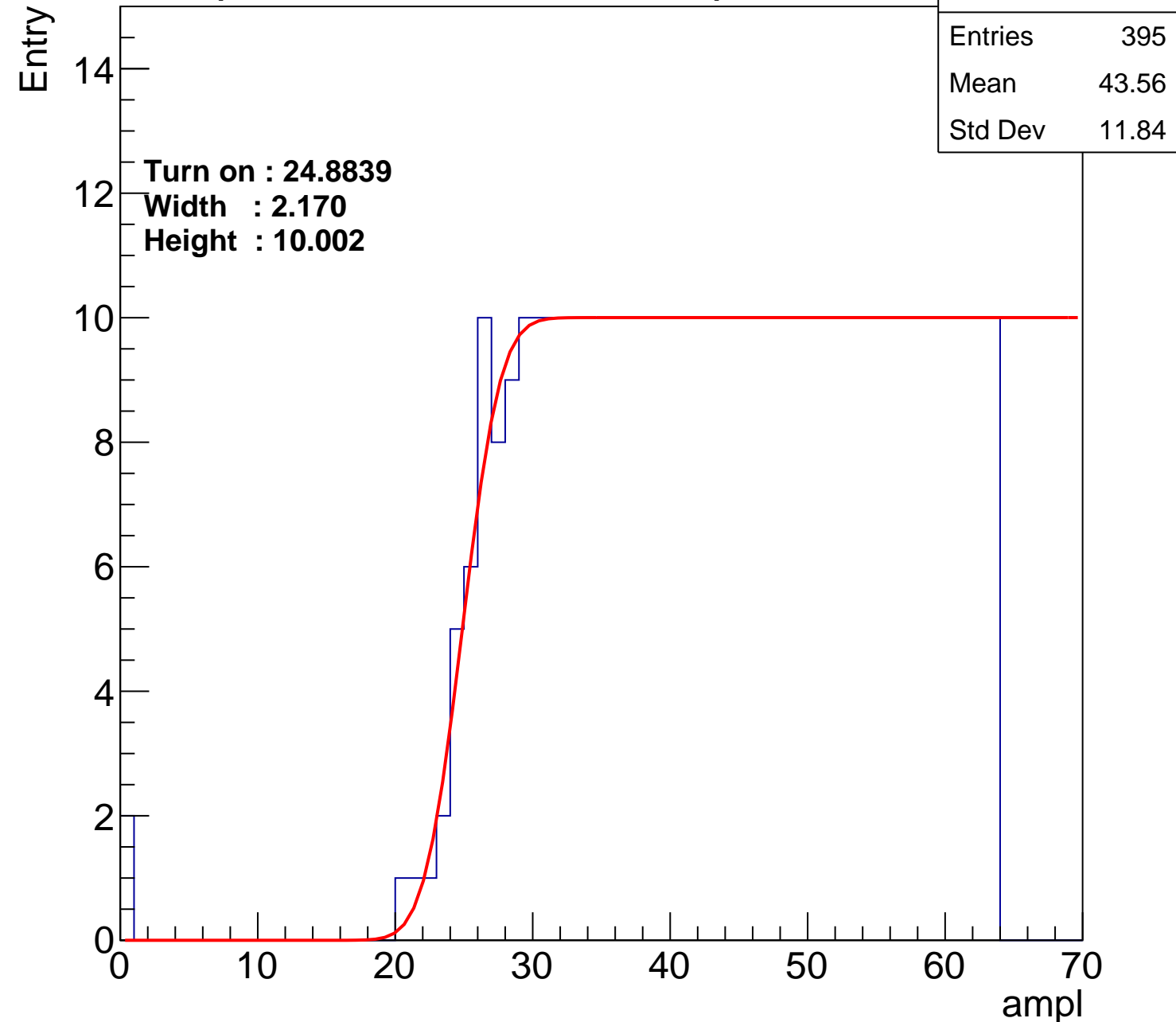
Width : 2.170

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch49

calib_packv5_042523_0143.root, FC#10, port B3

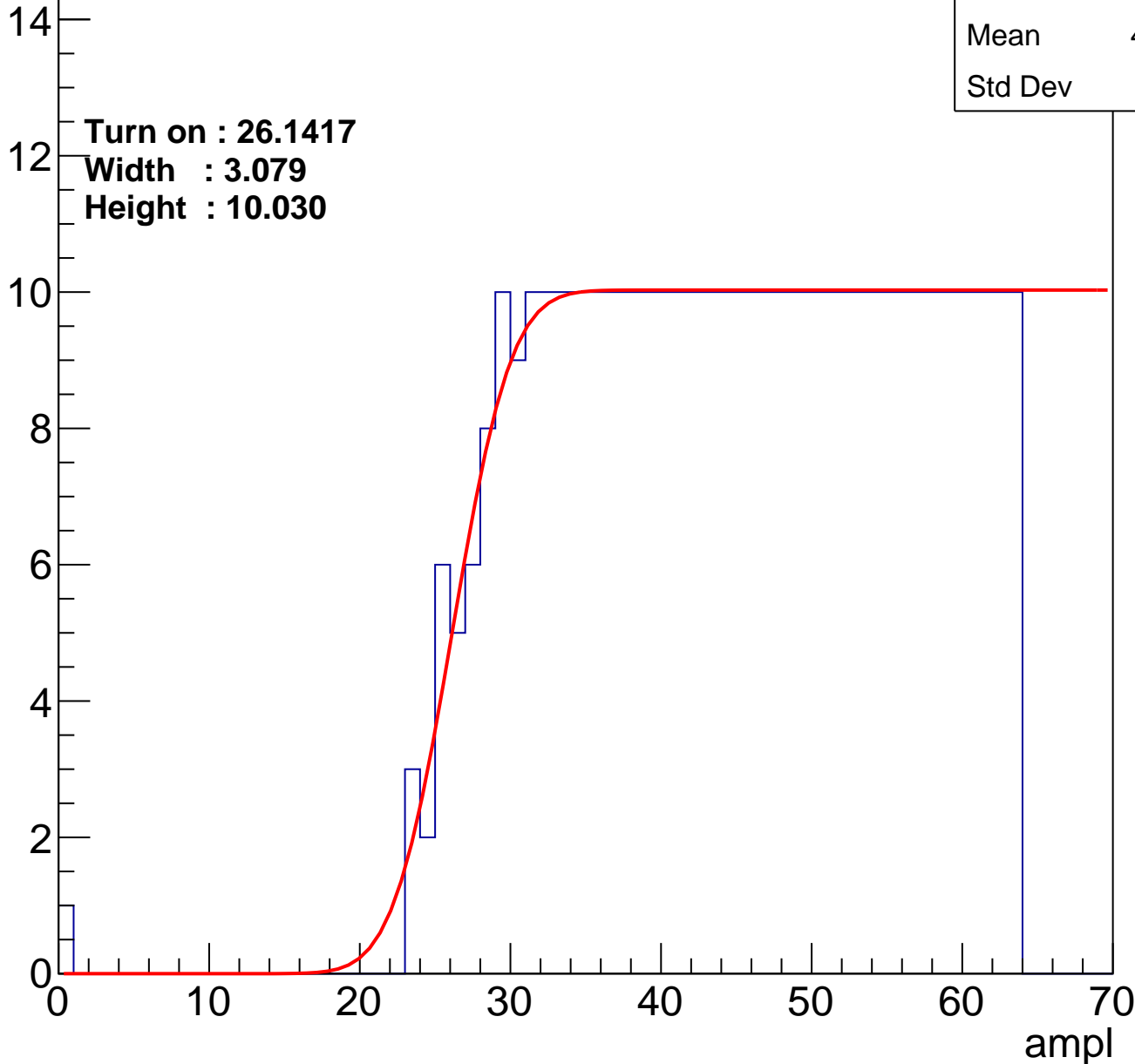
Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.1417

Width : 3.079

Height : 10.030

Entry



B1L002S, U8-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.9245

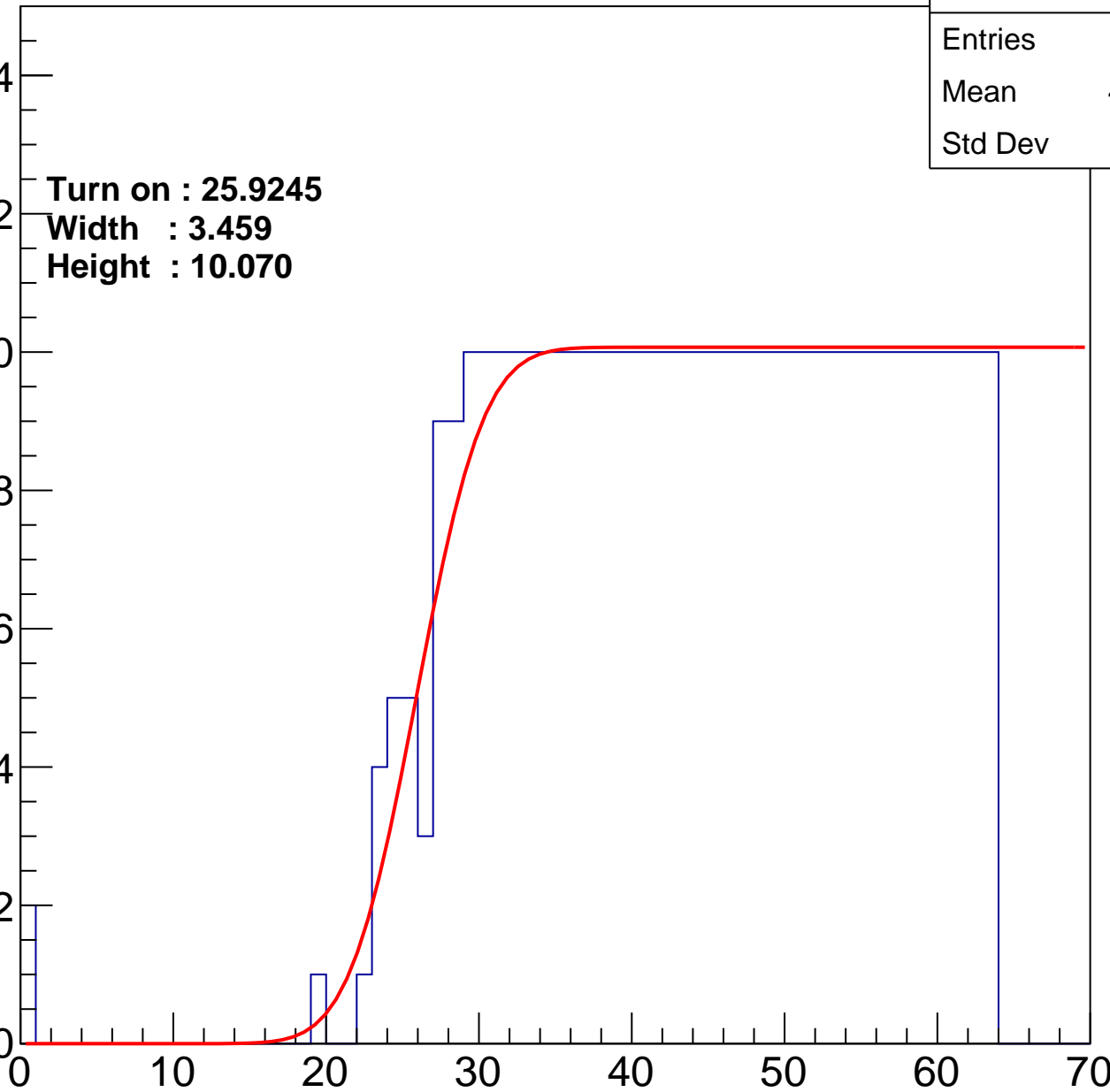
Width : 3.459

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.27
Std Dev	11.44

Turn on : 26.2125

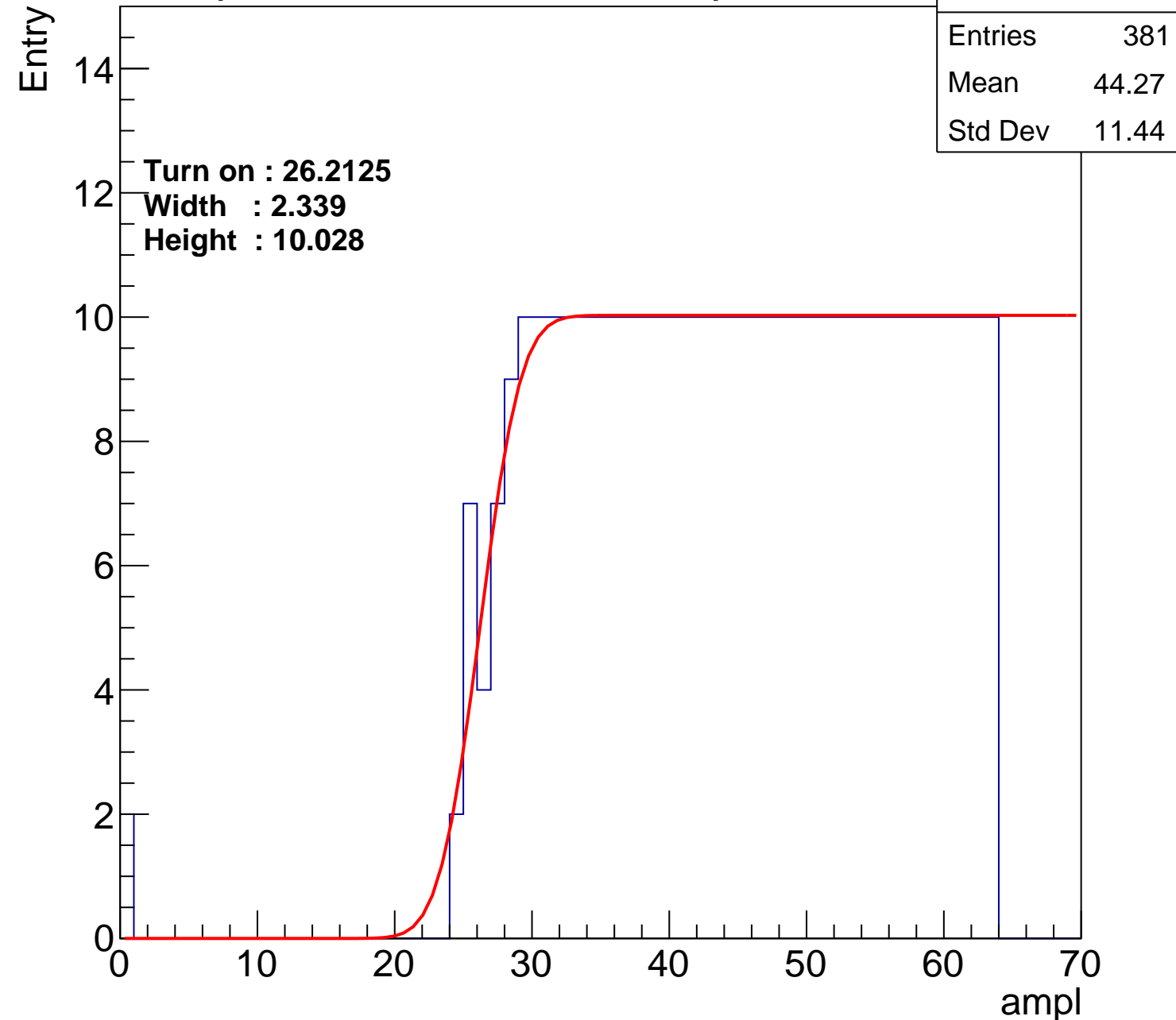
Width : 2.339

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.66
Std Dev	11.78

Turn on : 24.7419

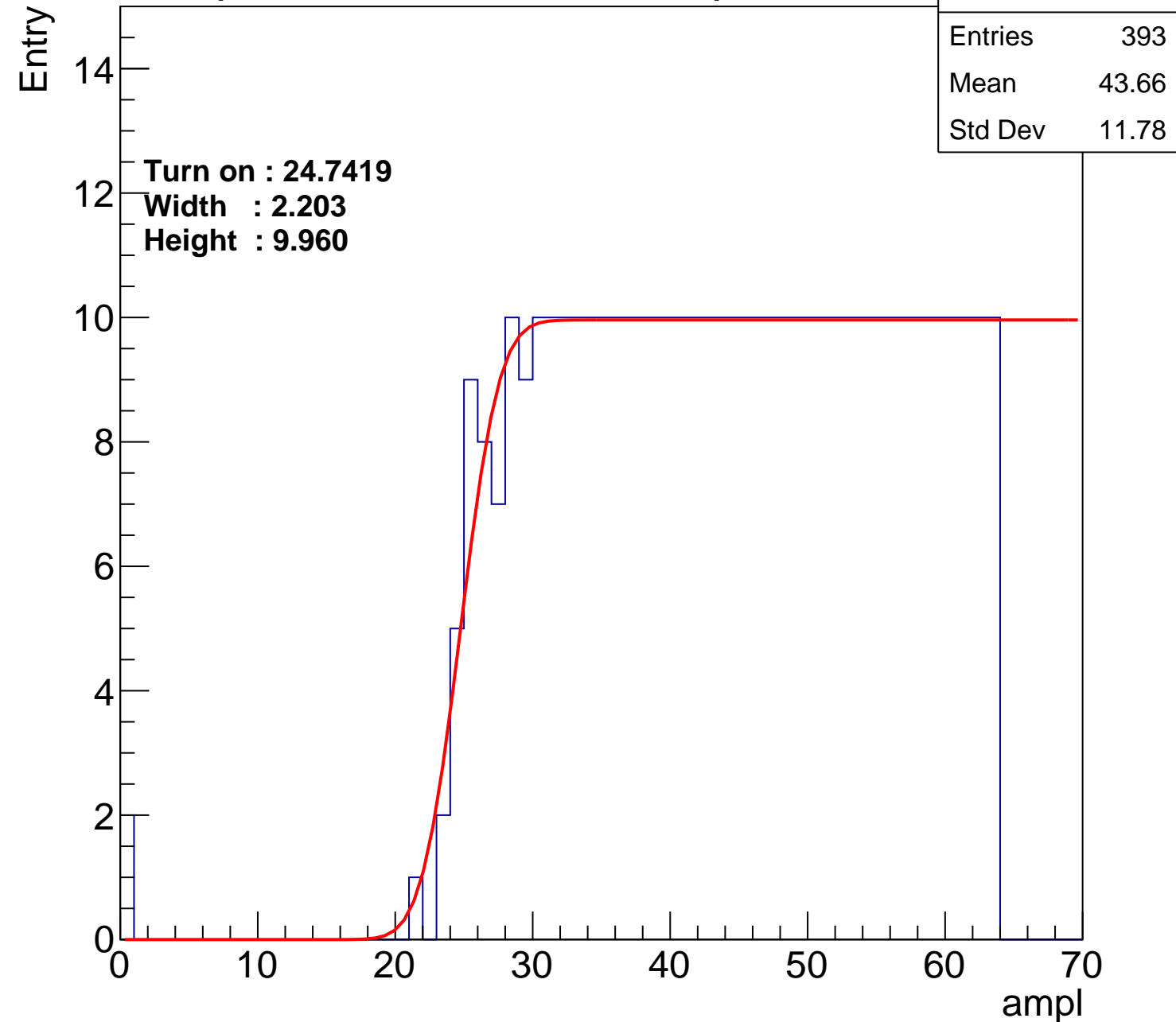
Width : 2.203

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch53

calib_packv5_042523_0143.root, FC#10, port B3

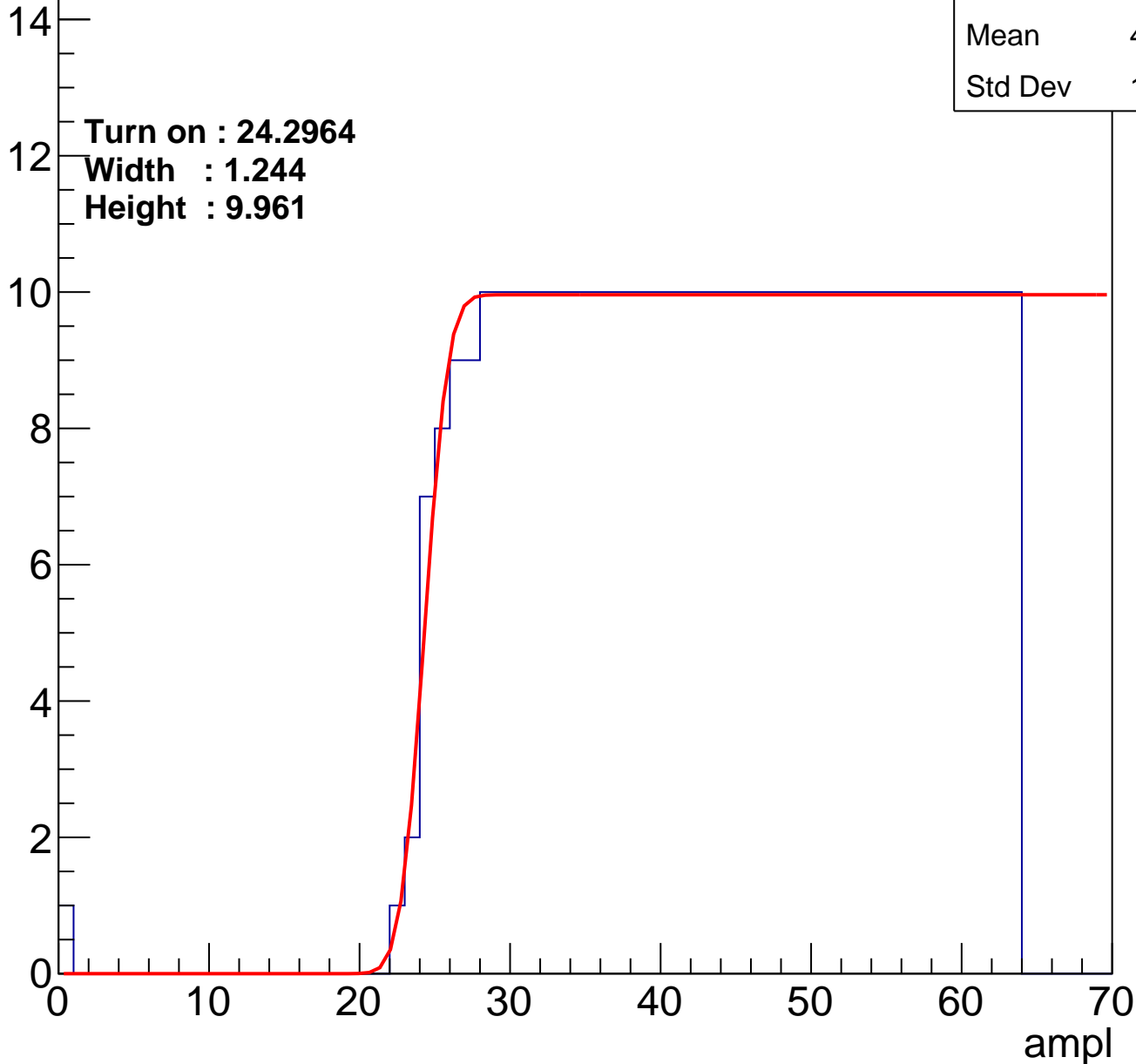
Entry

Entries	397
Mean	43.56
Std Dev	11.67

Turn on : 24.2964

Width : 1.244

Height : 9.961



B1L002S, U8-ch54

calib_packv5_042523_0143.root, FC#10, port B3

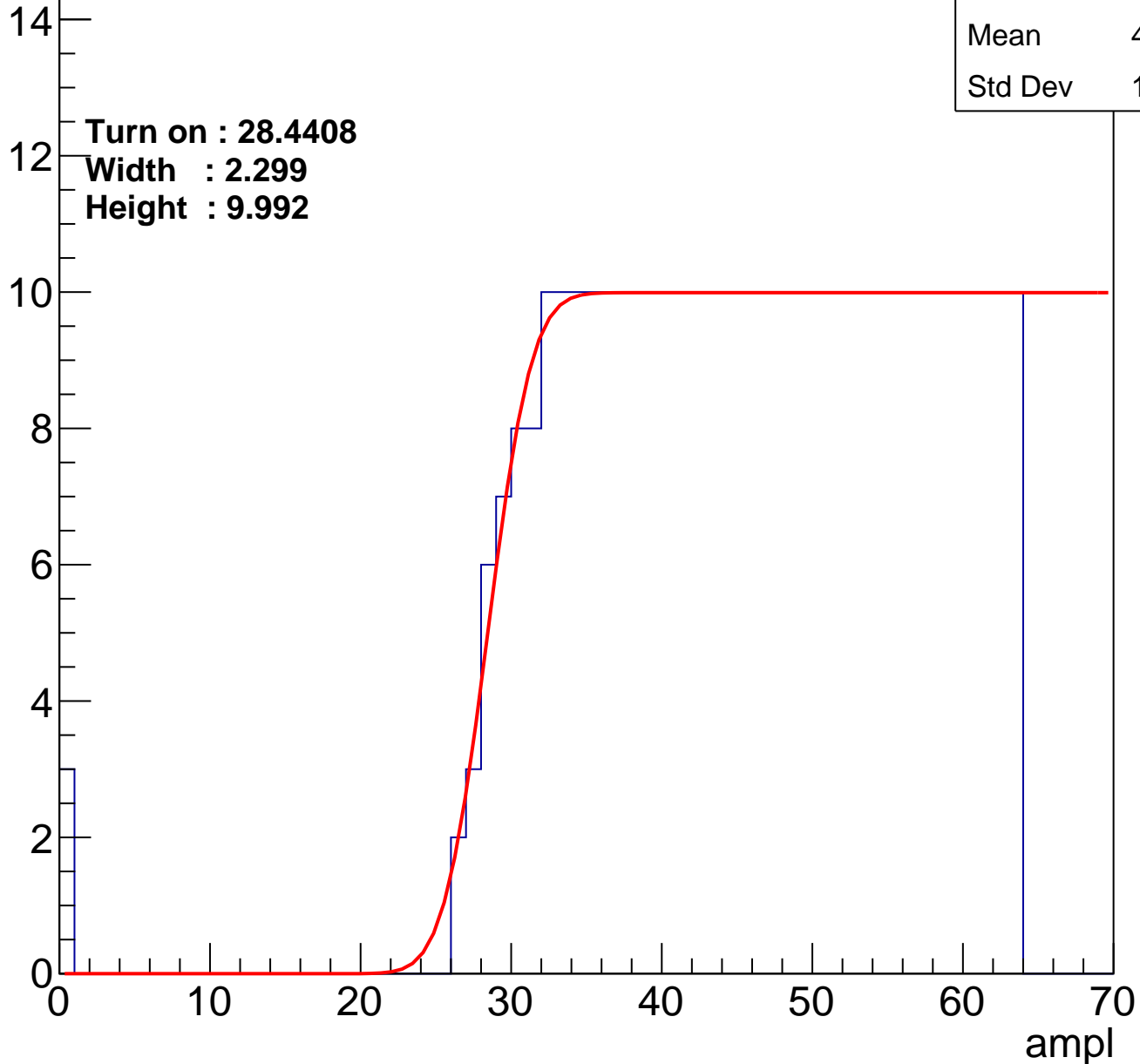
Entries	357
Mean	45.36
Std Dev	11.09

Turn on : 28.4408

Width : 2.299

Height : 9.992

Entry



B1L002S, U8-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.96
Std Dev	11.64

Turn on : 25.5526

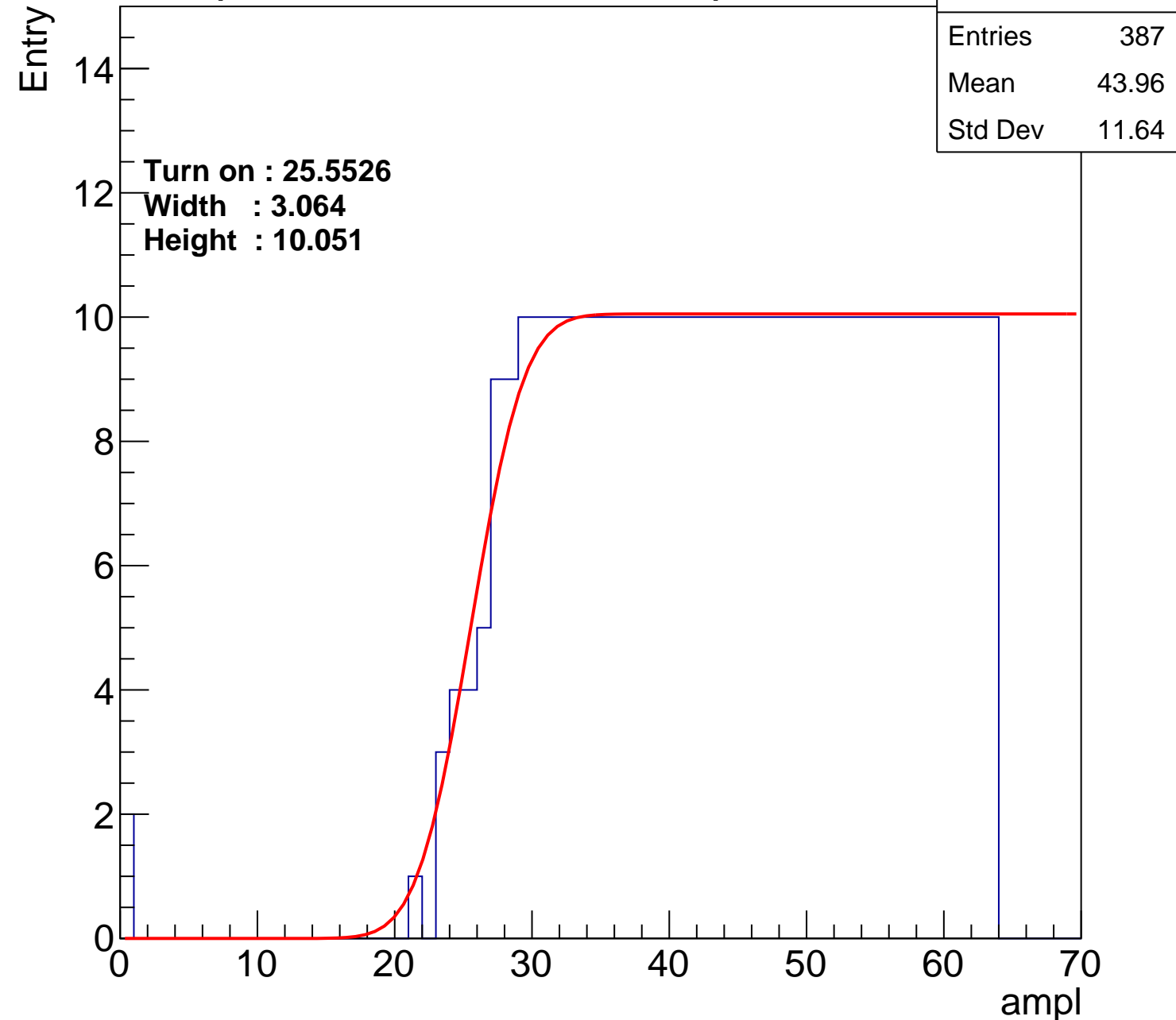
Width : 3.064

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.08
Std Dev	12.47

Turn on : 24.6359

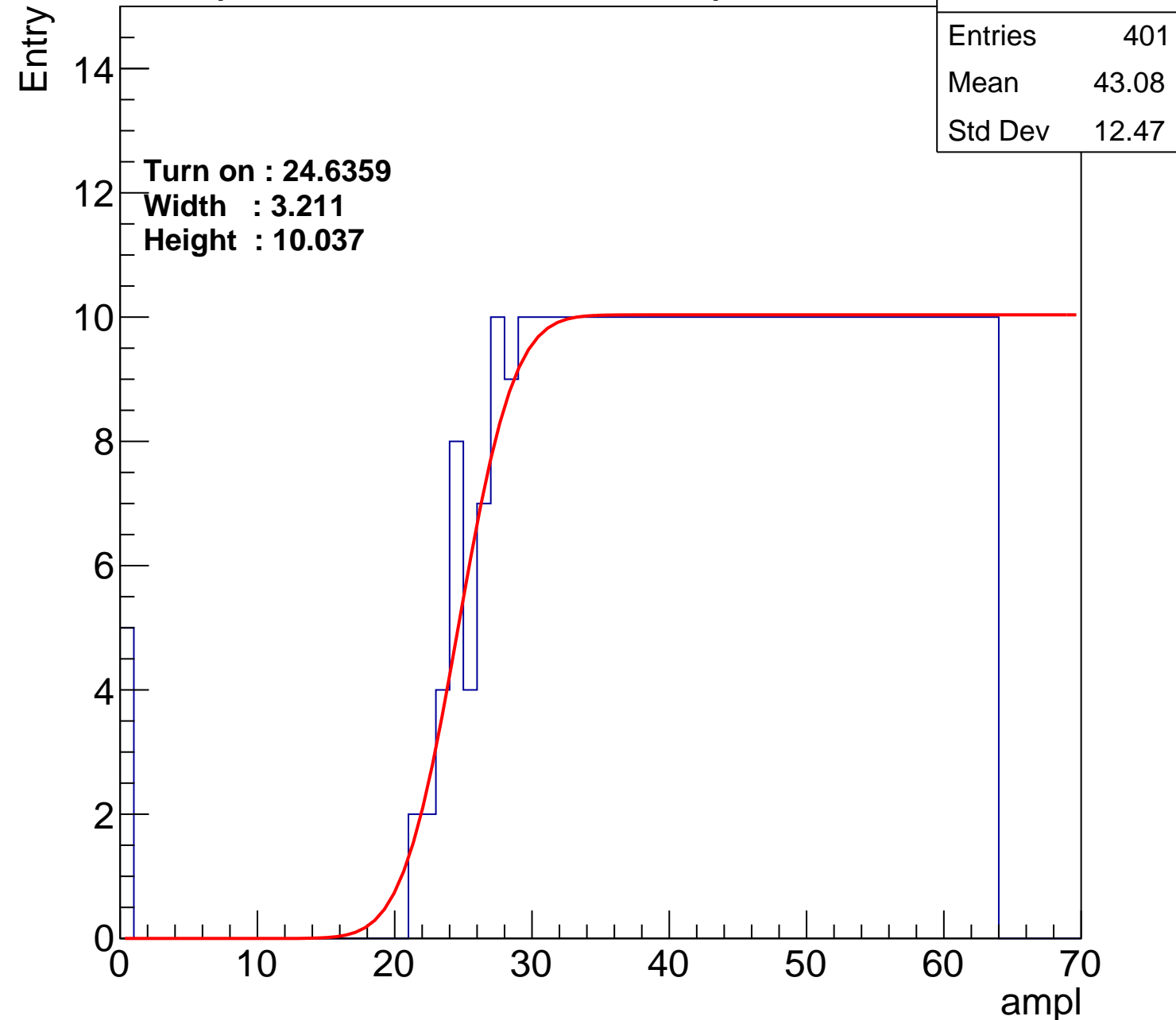
Width : 3.211

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.45
Std Dev	11.91

Turn on : 24.8201

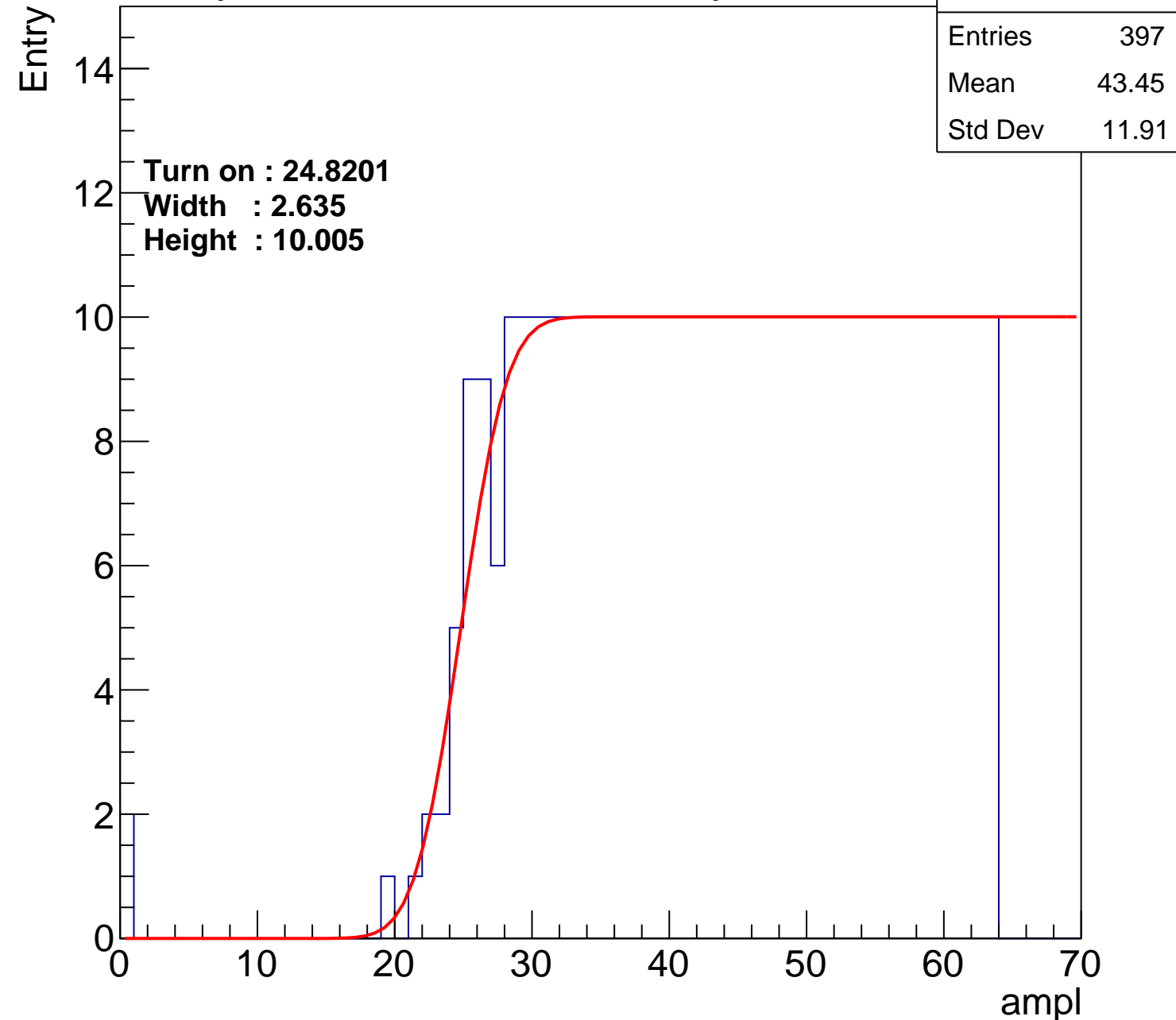
Width : 2.635

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.17
Std Dev	12.05

Turn on : 23.9958

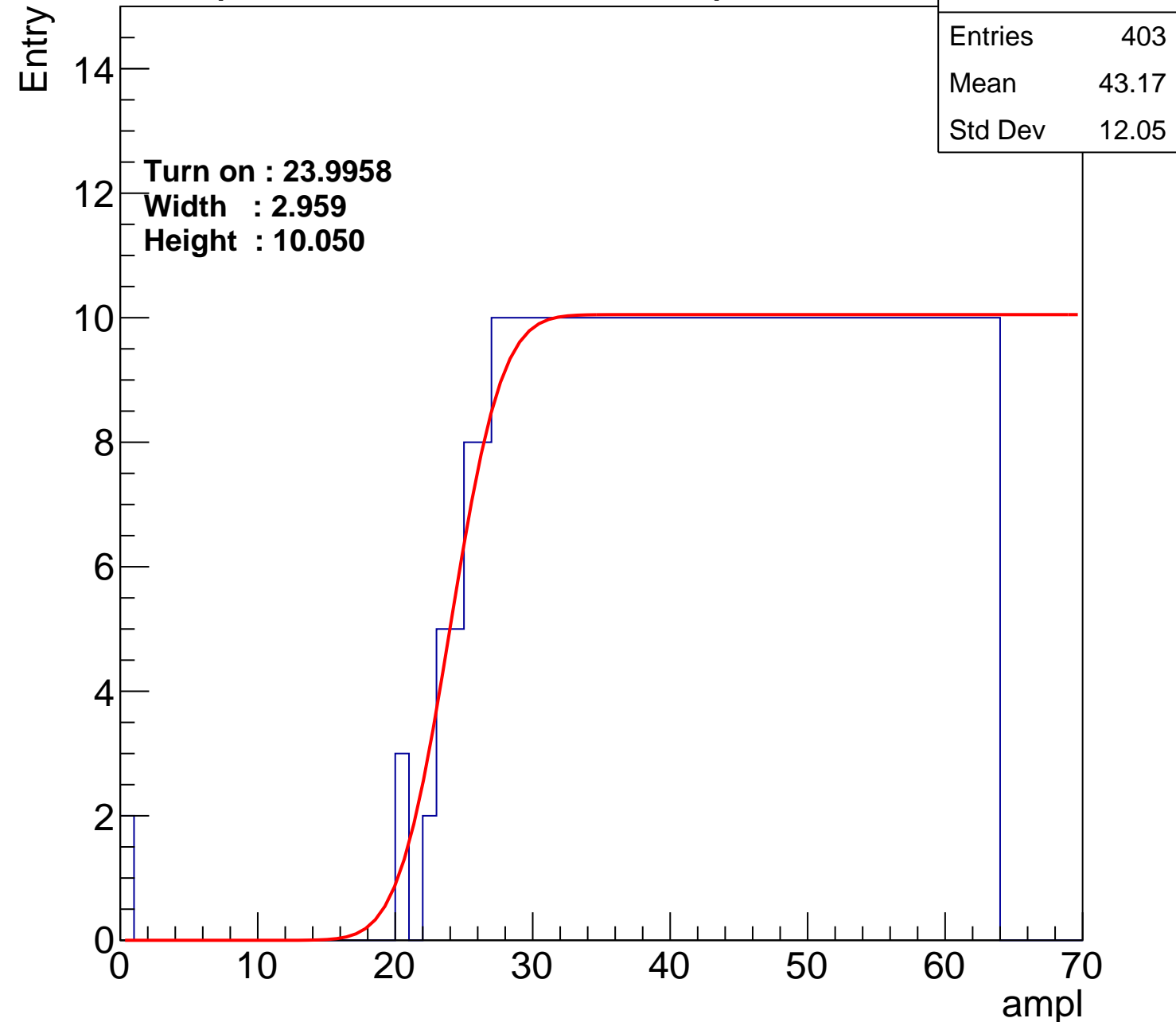
Width : 2.959

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.25
Std Dev	11.86

Turn on : 23.8965

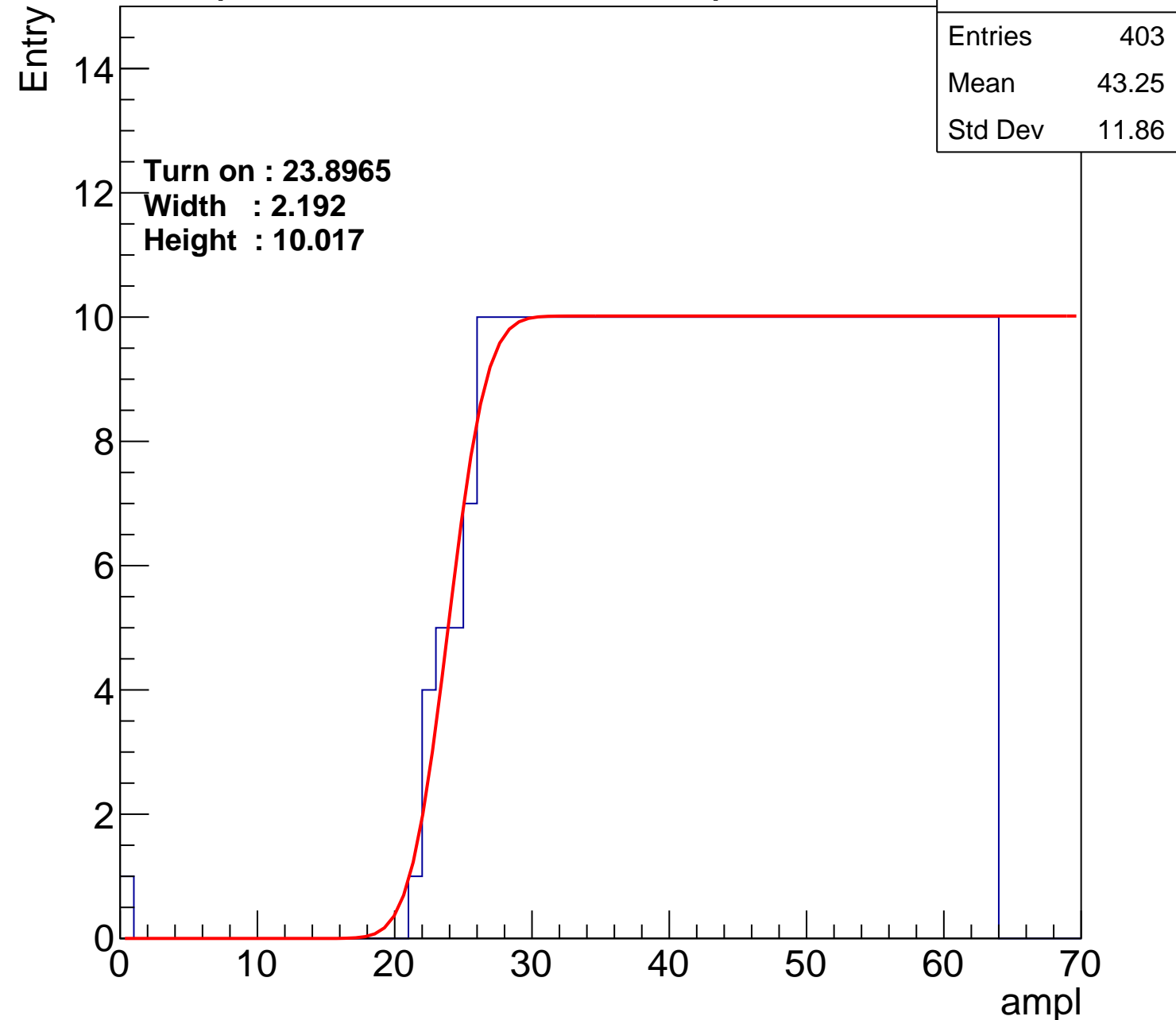
Width : 2.192

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.8122

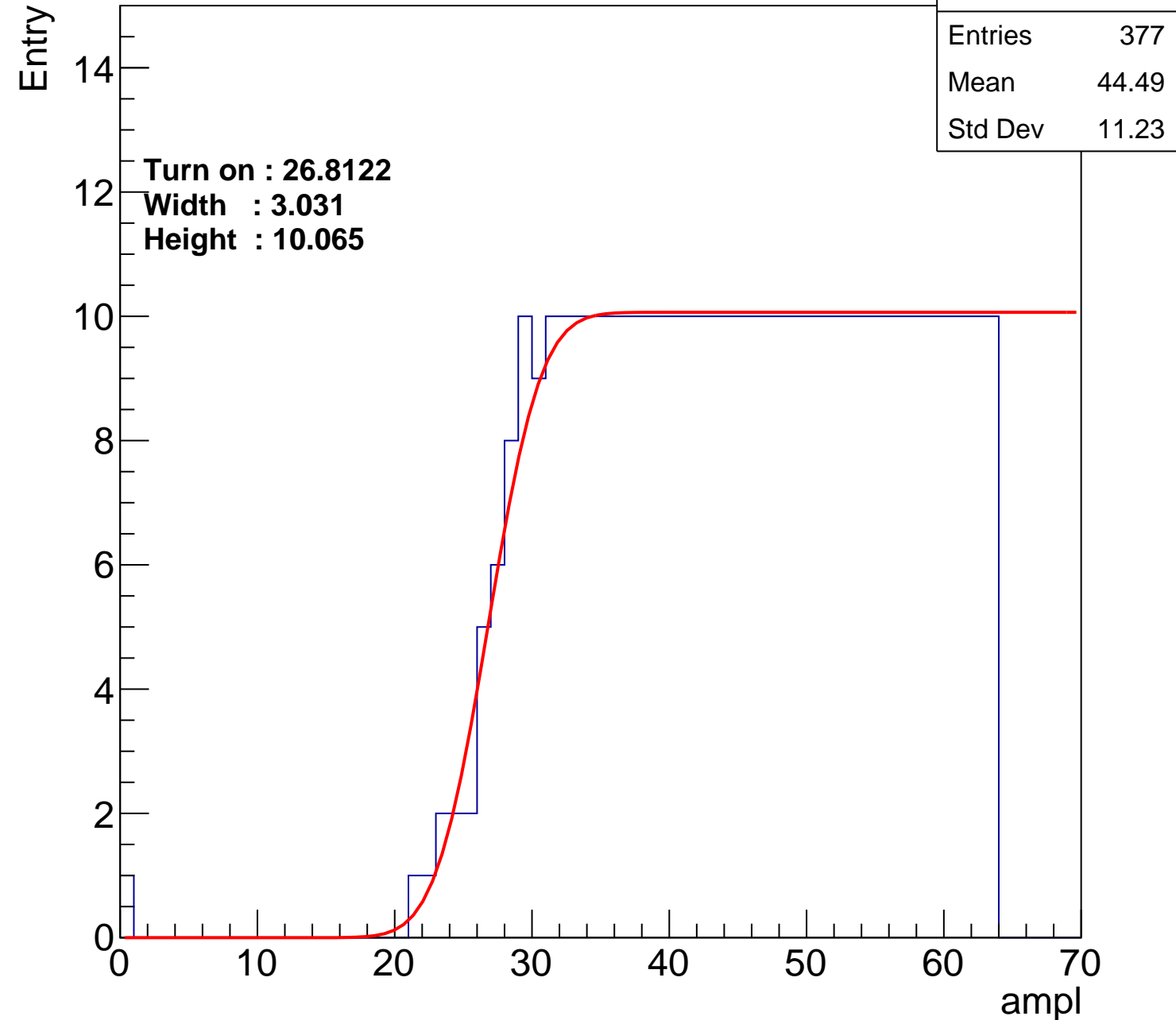
Width : 3.031

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch61

calib_packv5_042523_0143.root, FC#10, port B3

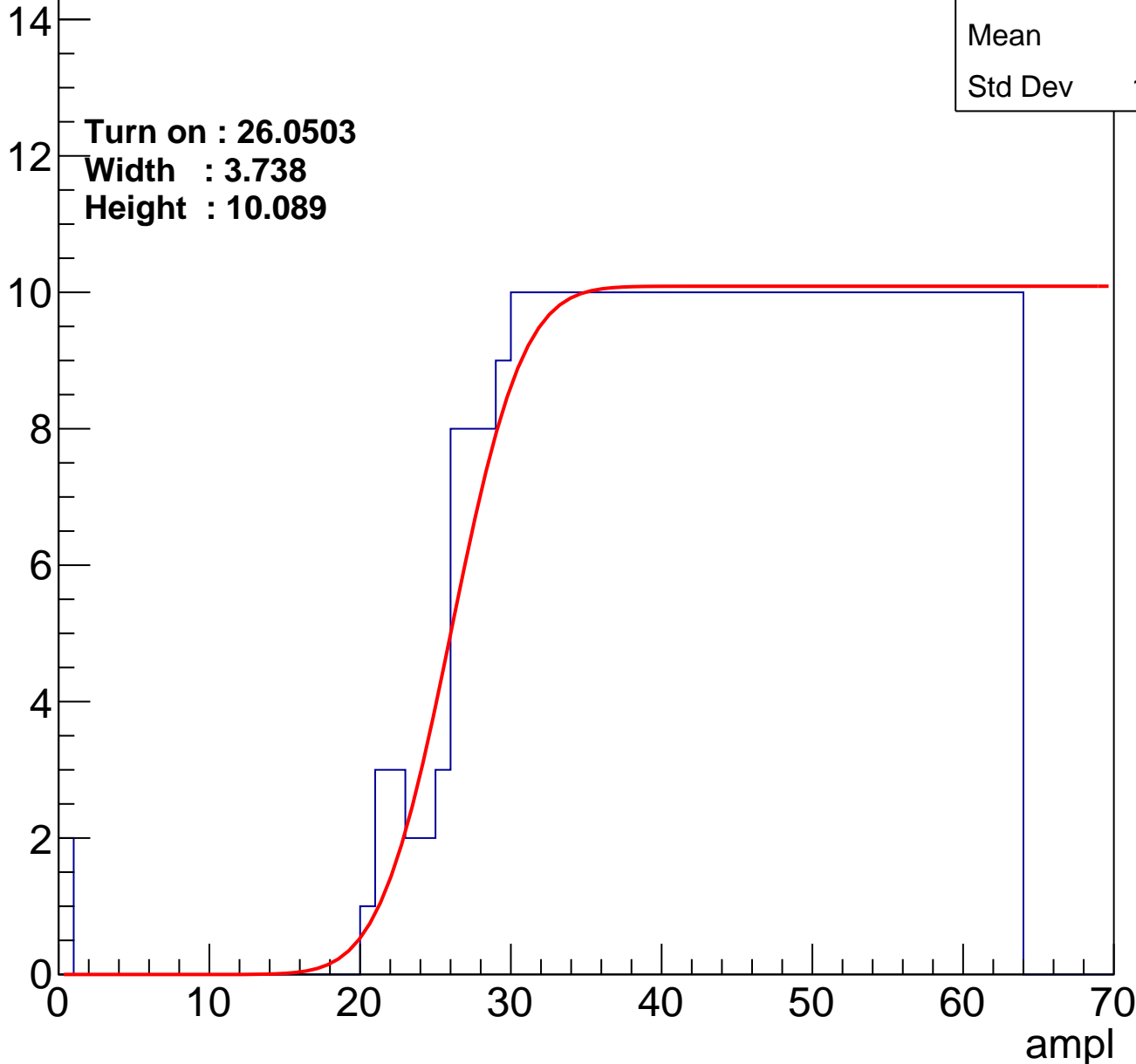
Entries	389
Mean	43.8
Std Dev	11.79

Turn on : 26.0503

Width : 3.738

Height : 10.089

Entry



B1L002S, U8-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.47
Std Dev	11.21

Turn on : 26.1498

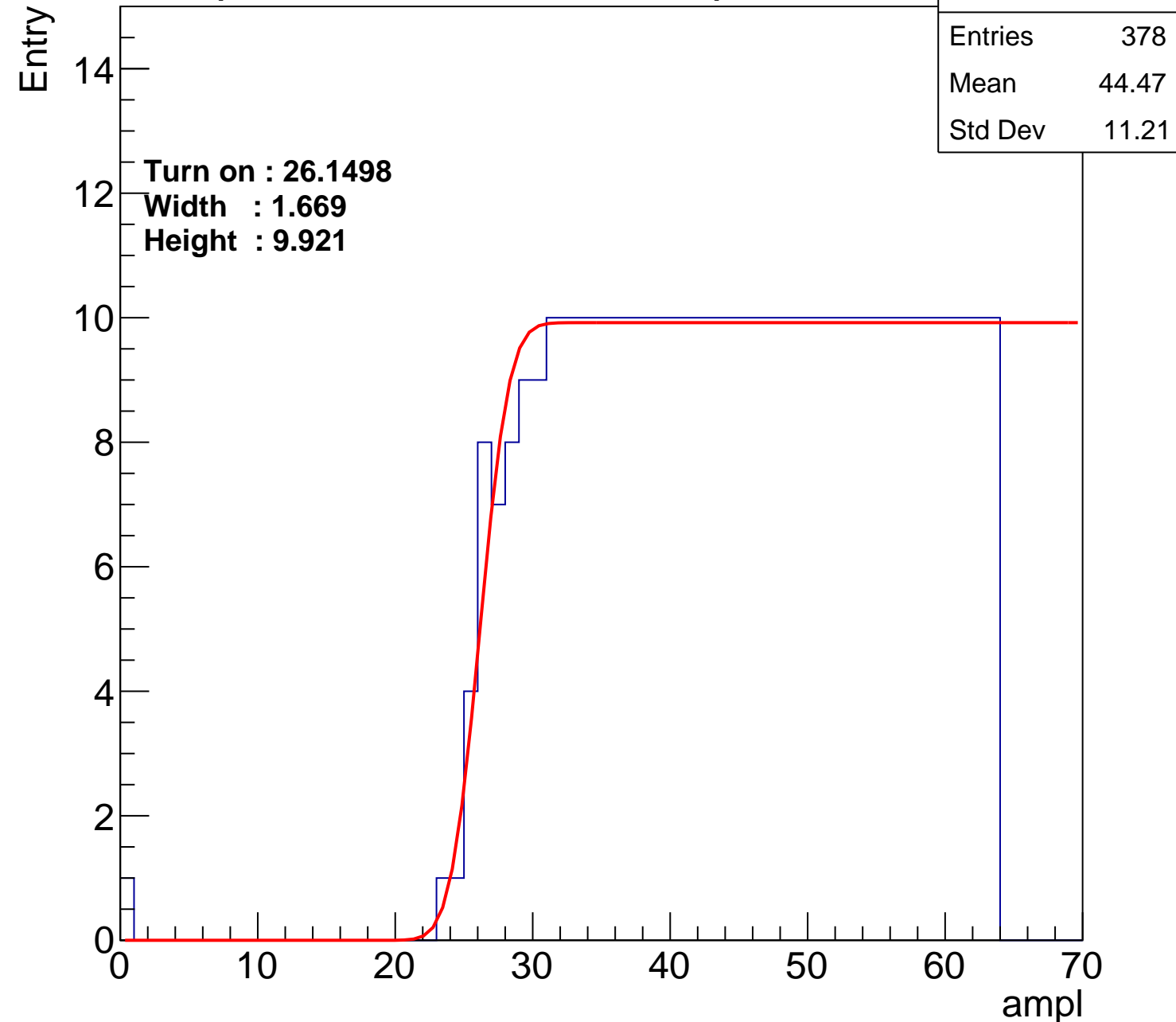
Width : 1.669

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 25.3857

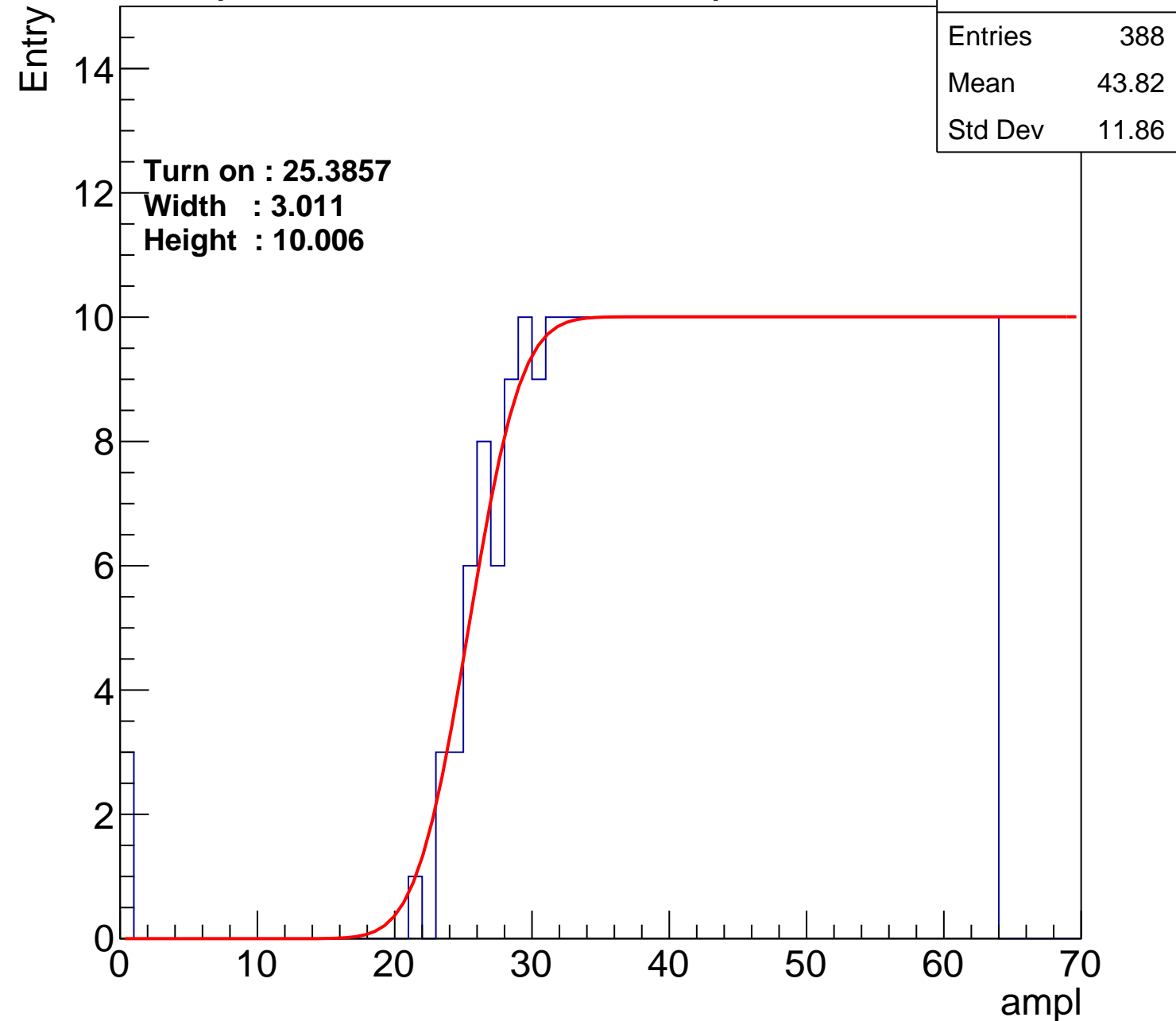
Width : 3.011

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.43
Std Dev	11.9

Turn on : 24.8041

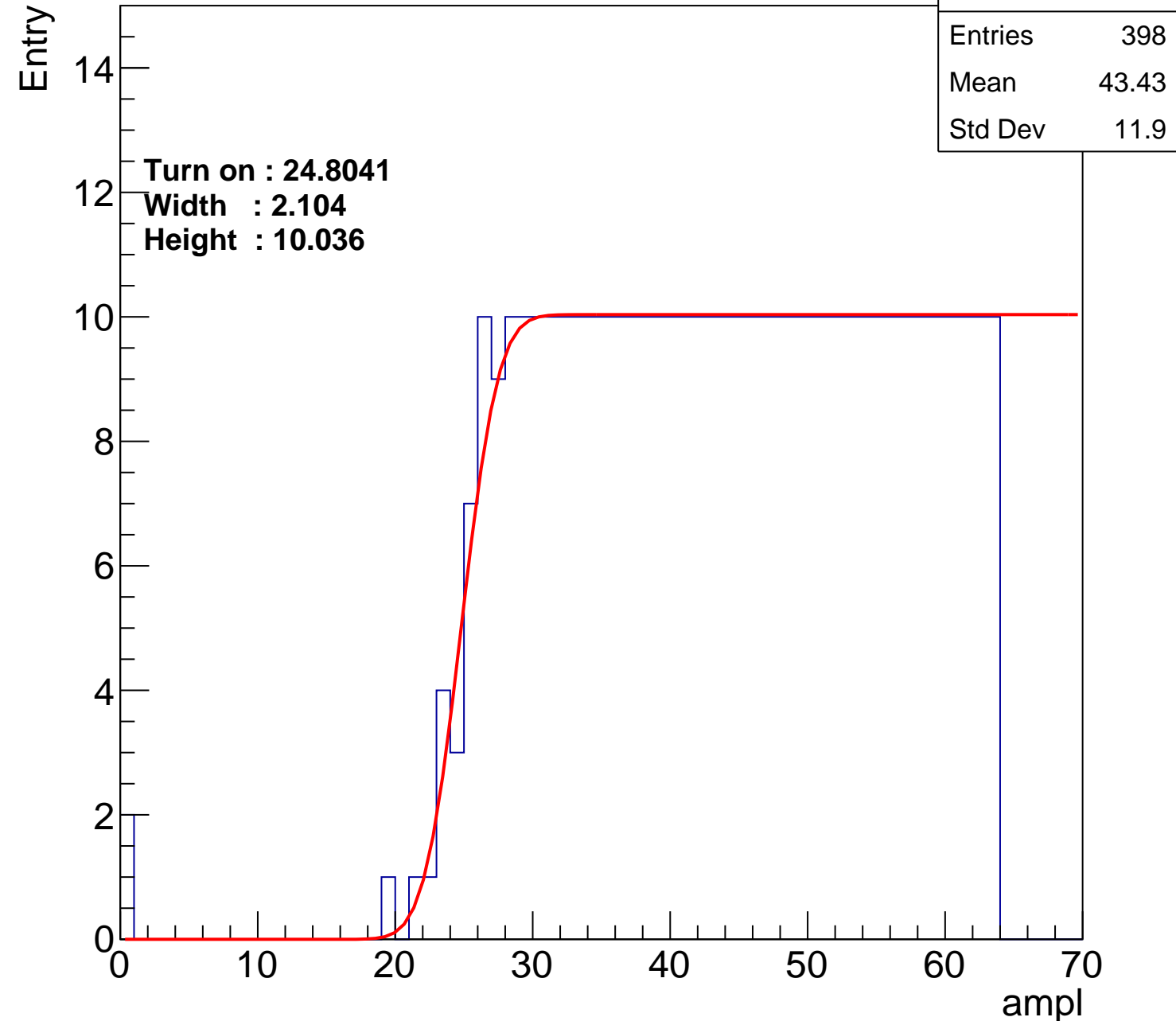
Width : 2.104

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.5
Std Dev	11.2

Turn on : 26.2249

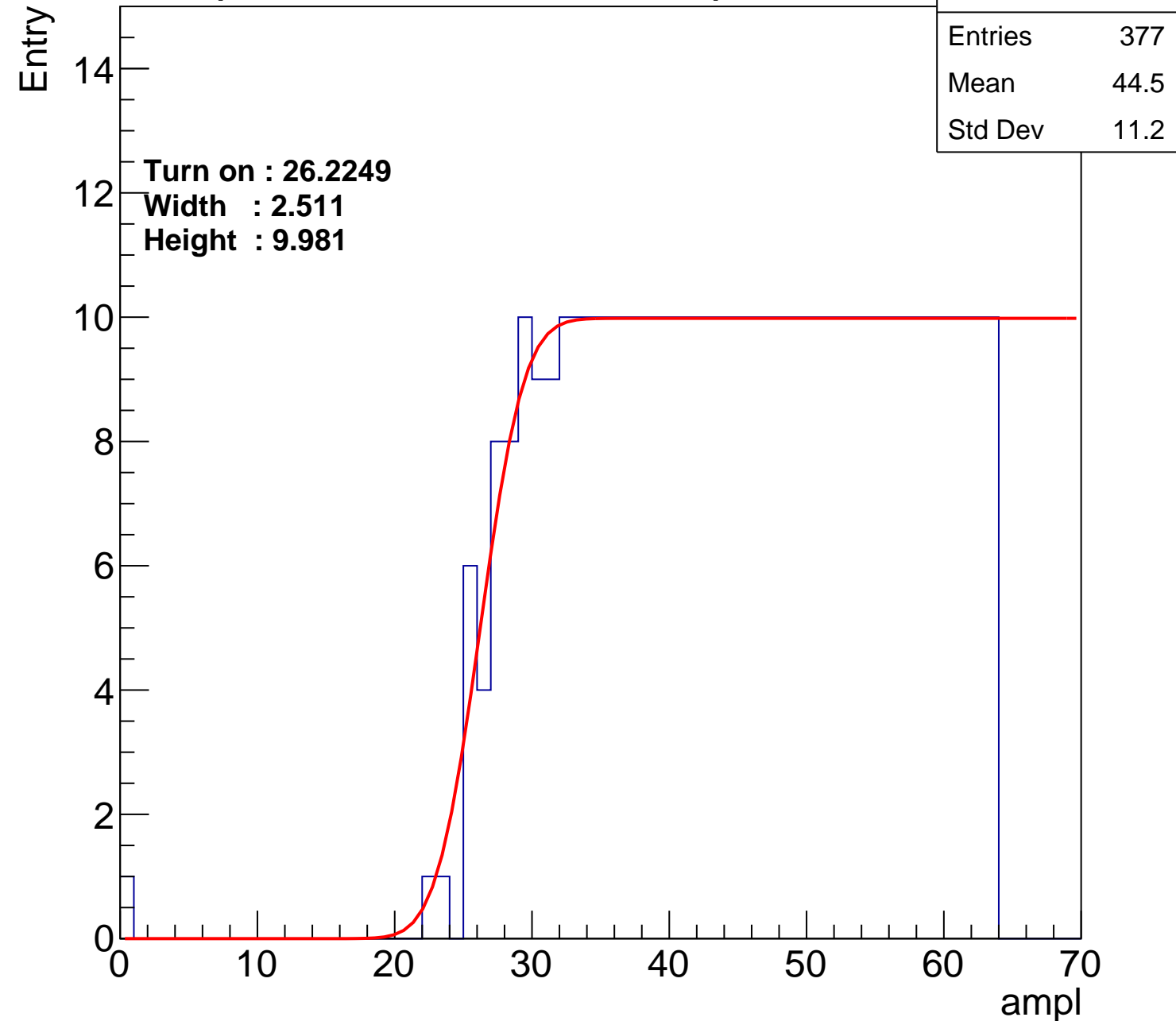
Width : 2.511

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.81
Std Dev	11.89

Turn on : 25.6704

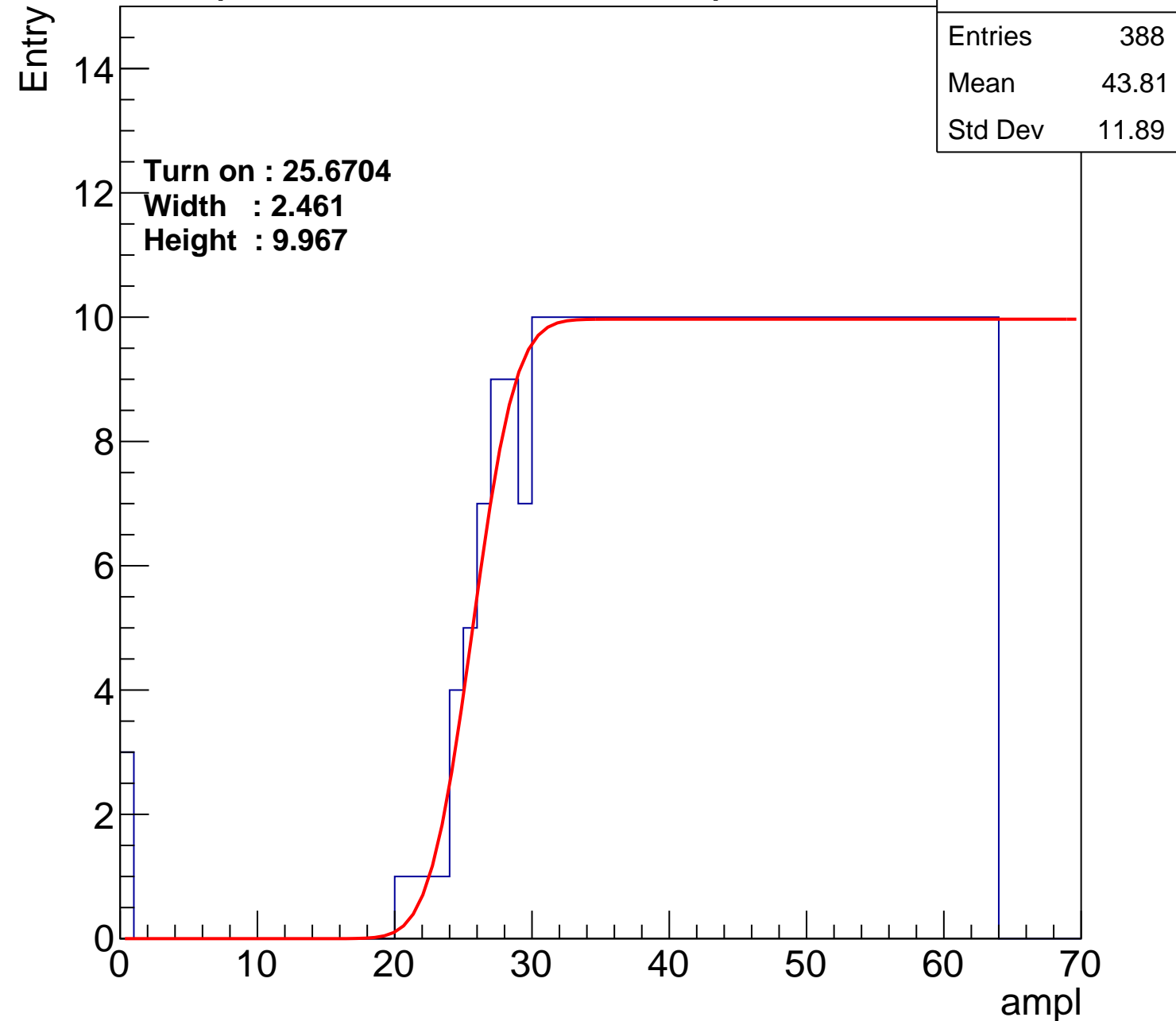
Width : 2.461

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.9815

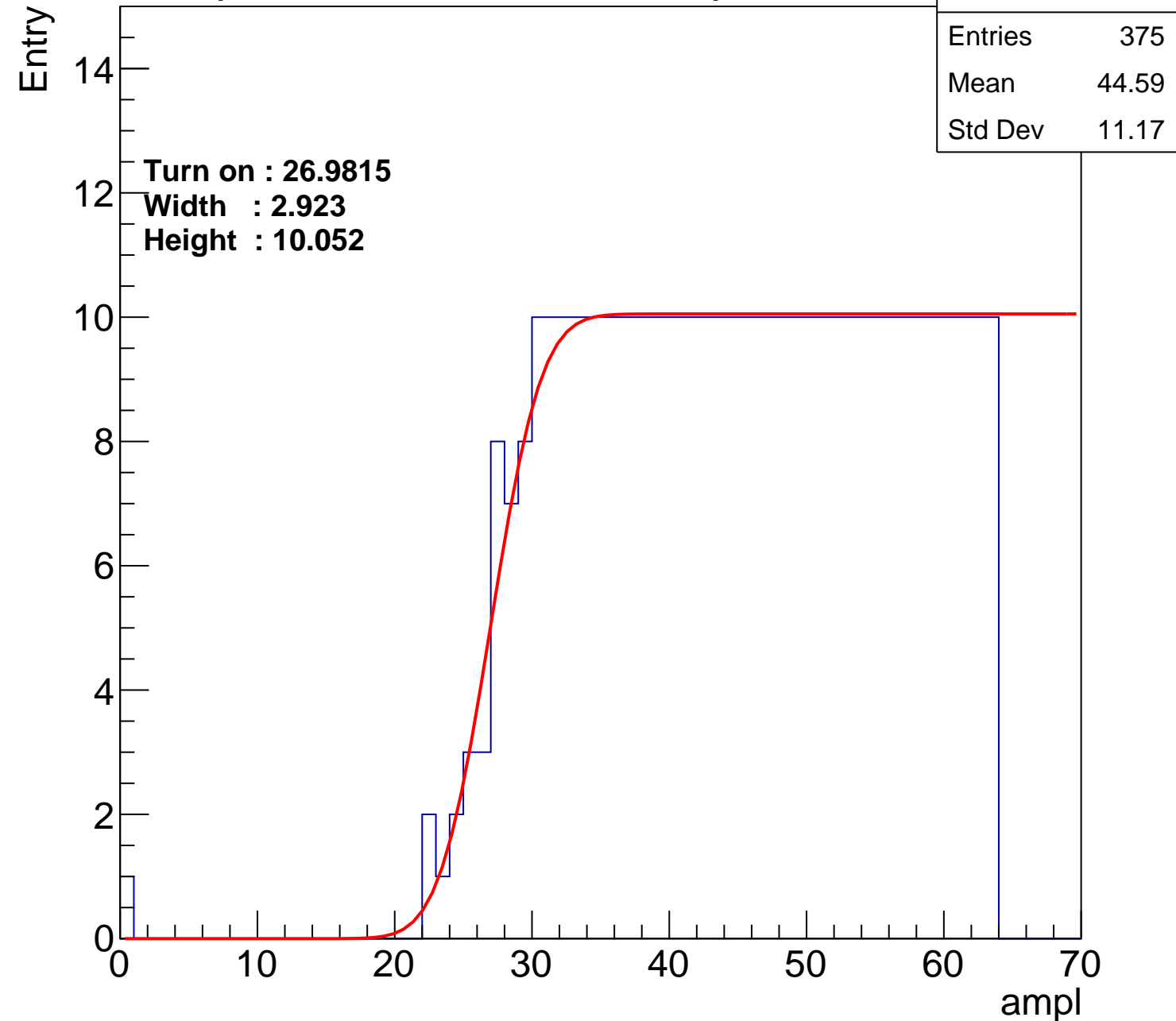
Width : 2.923

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.73
Std Dev	11.39

Turn on : 27.5432

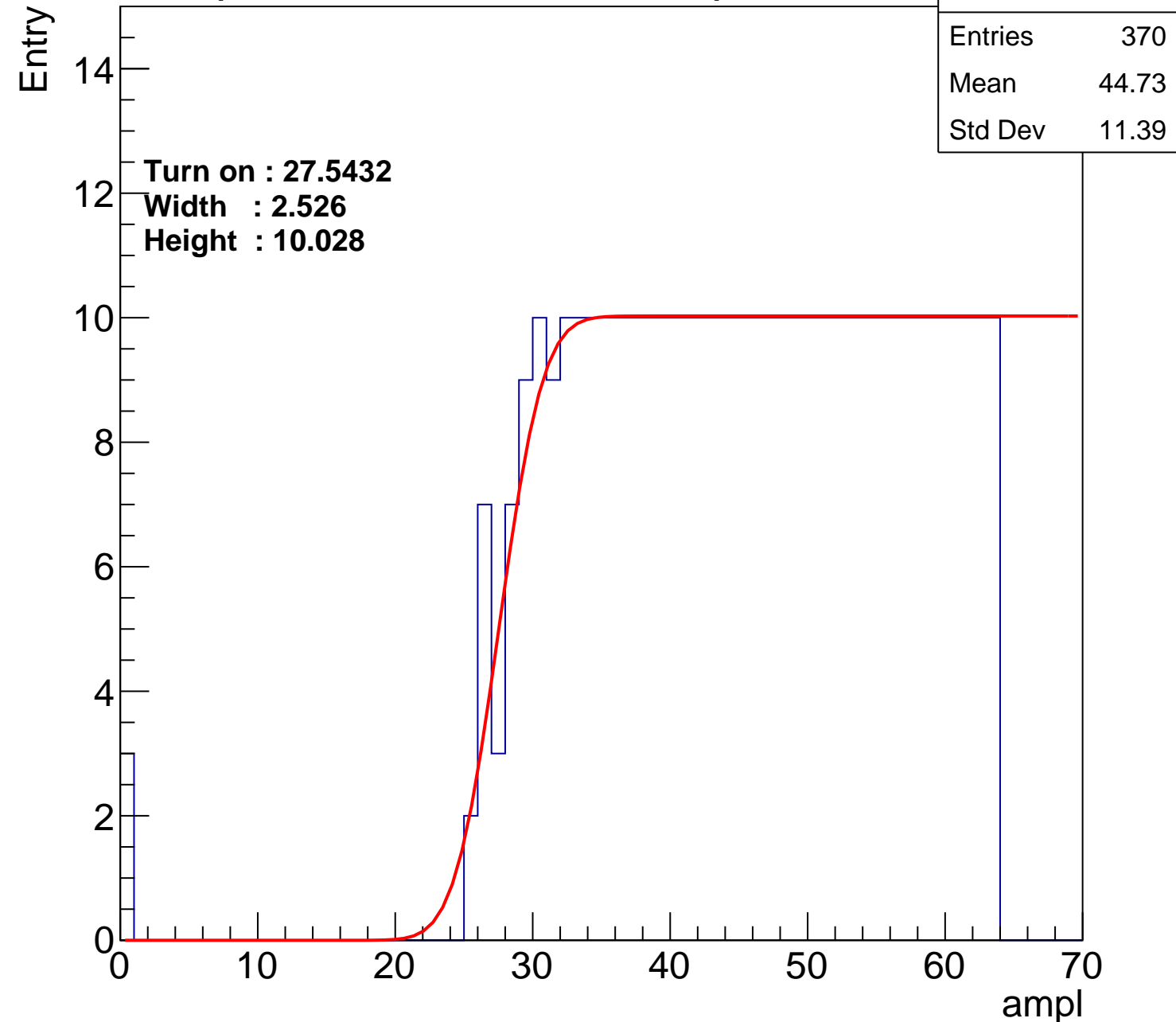
Width : 2.526

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.73
Std Dev	11.72

Turn on : 24.9769

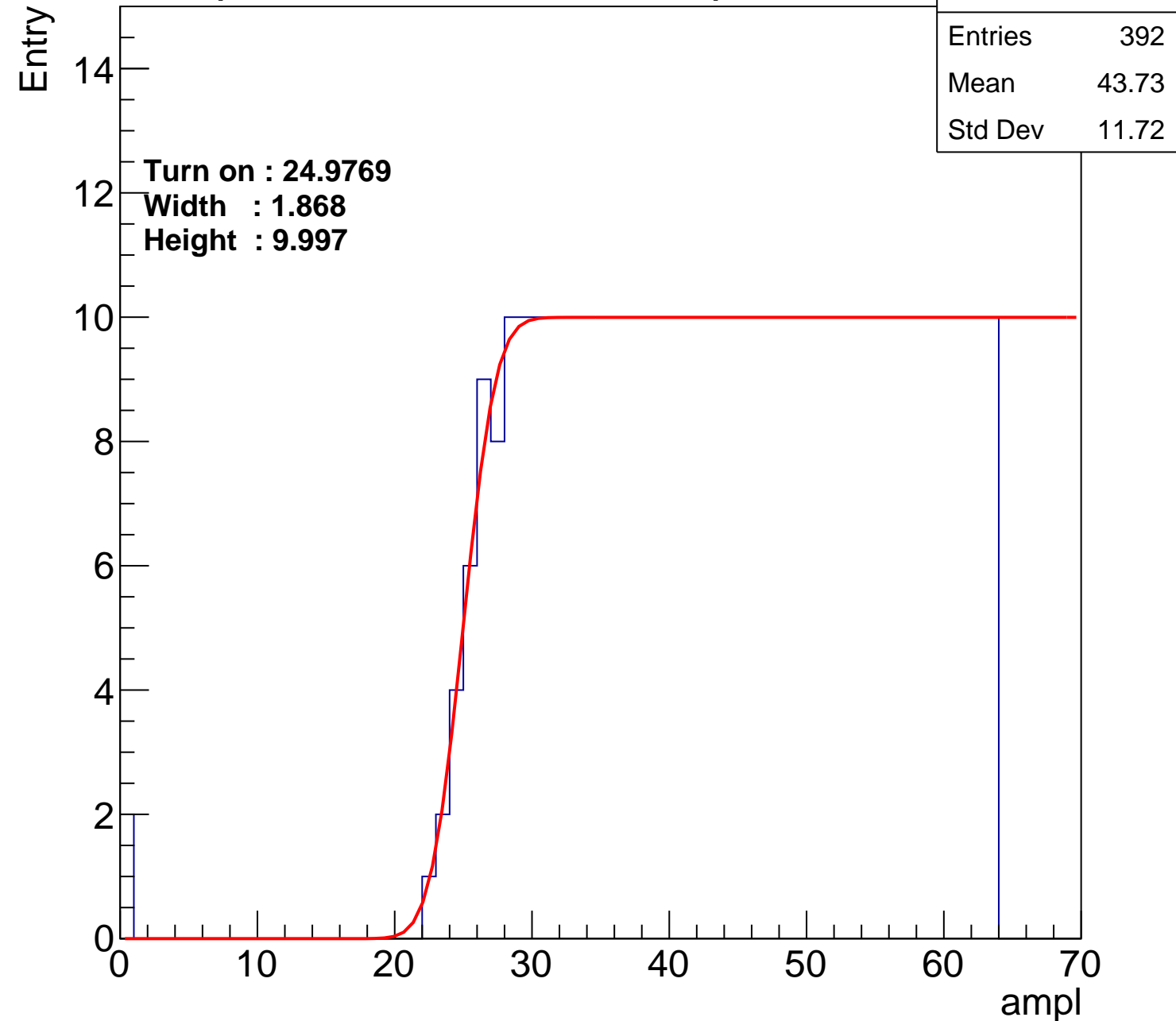
Width : 1.868

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 25.1203

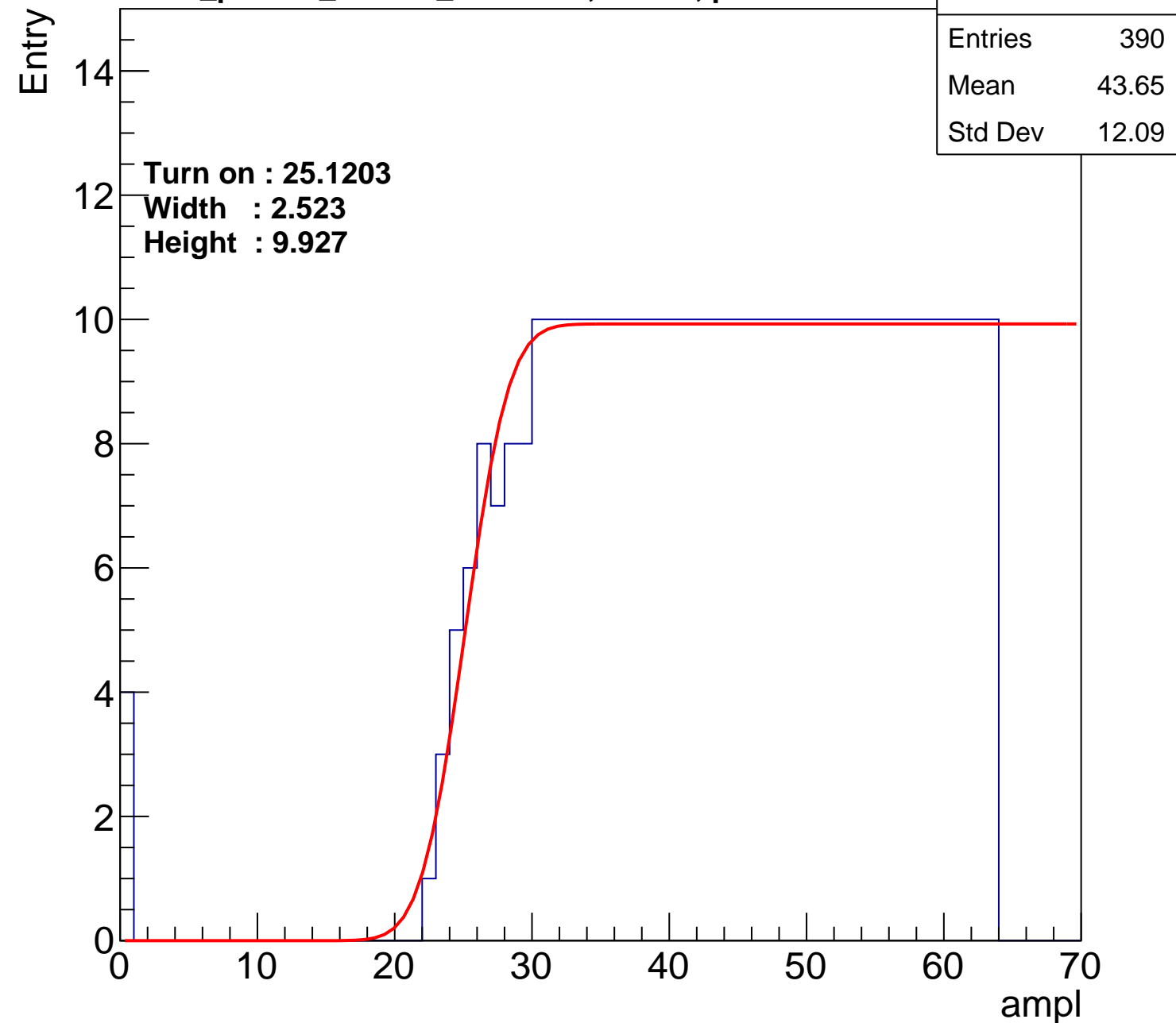
Width : 2.523

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 25.6509

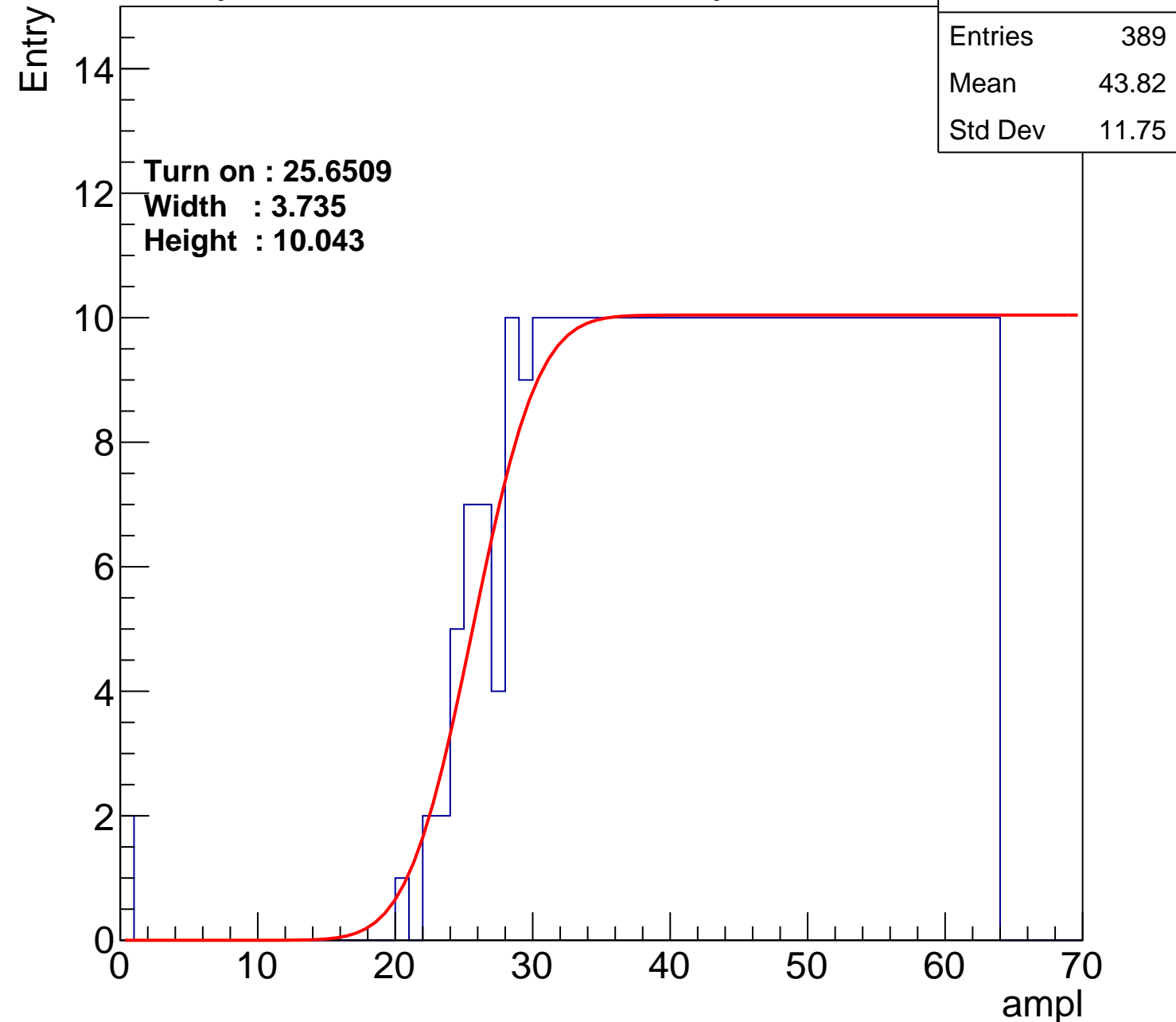
Width : 3.735

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.25
Std Dev	12.02

Turn on : 24.3968

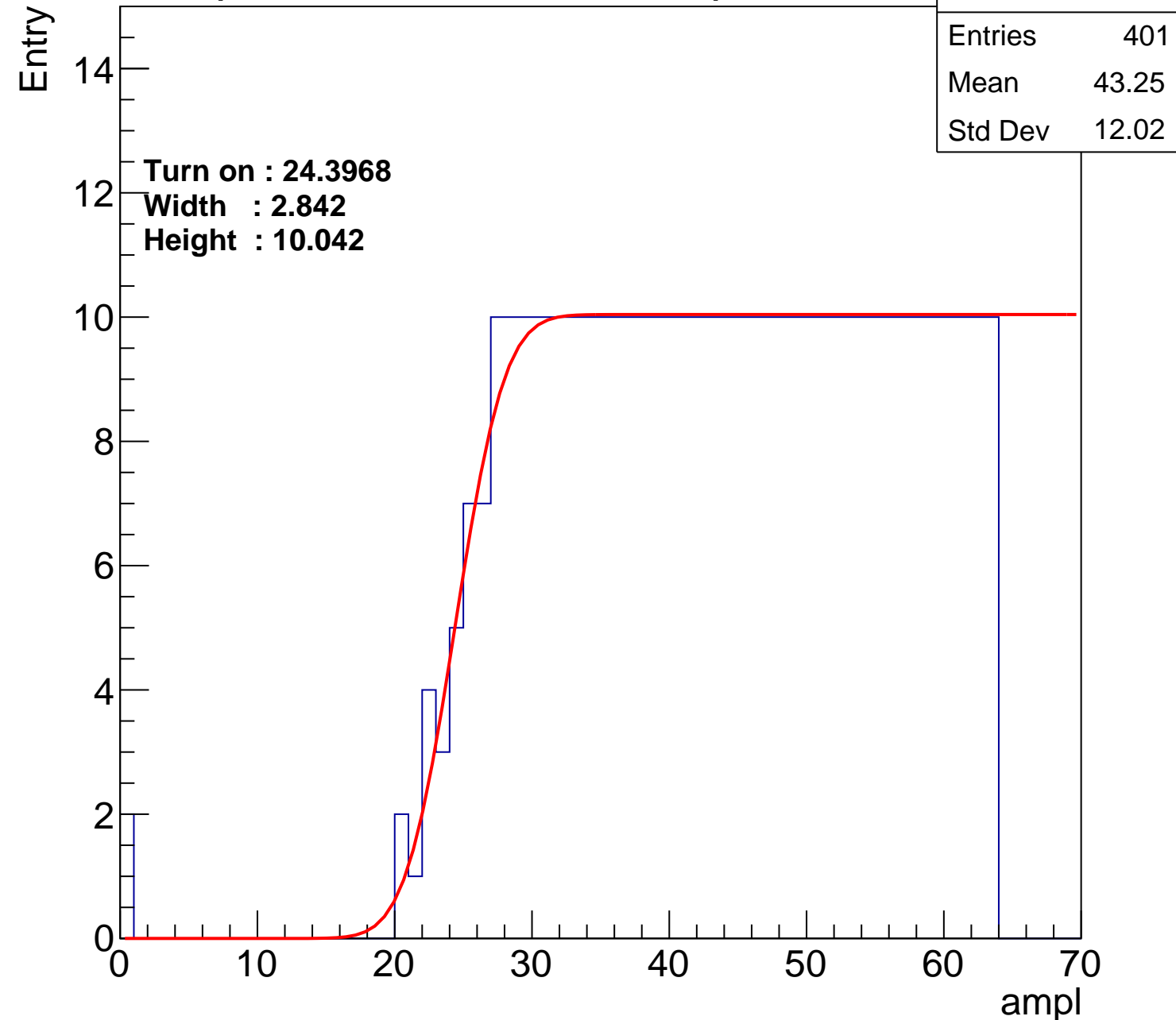
Width : 2.842

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch73

calib_packv5_042523_0143.root, FC#10, port B3

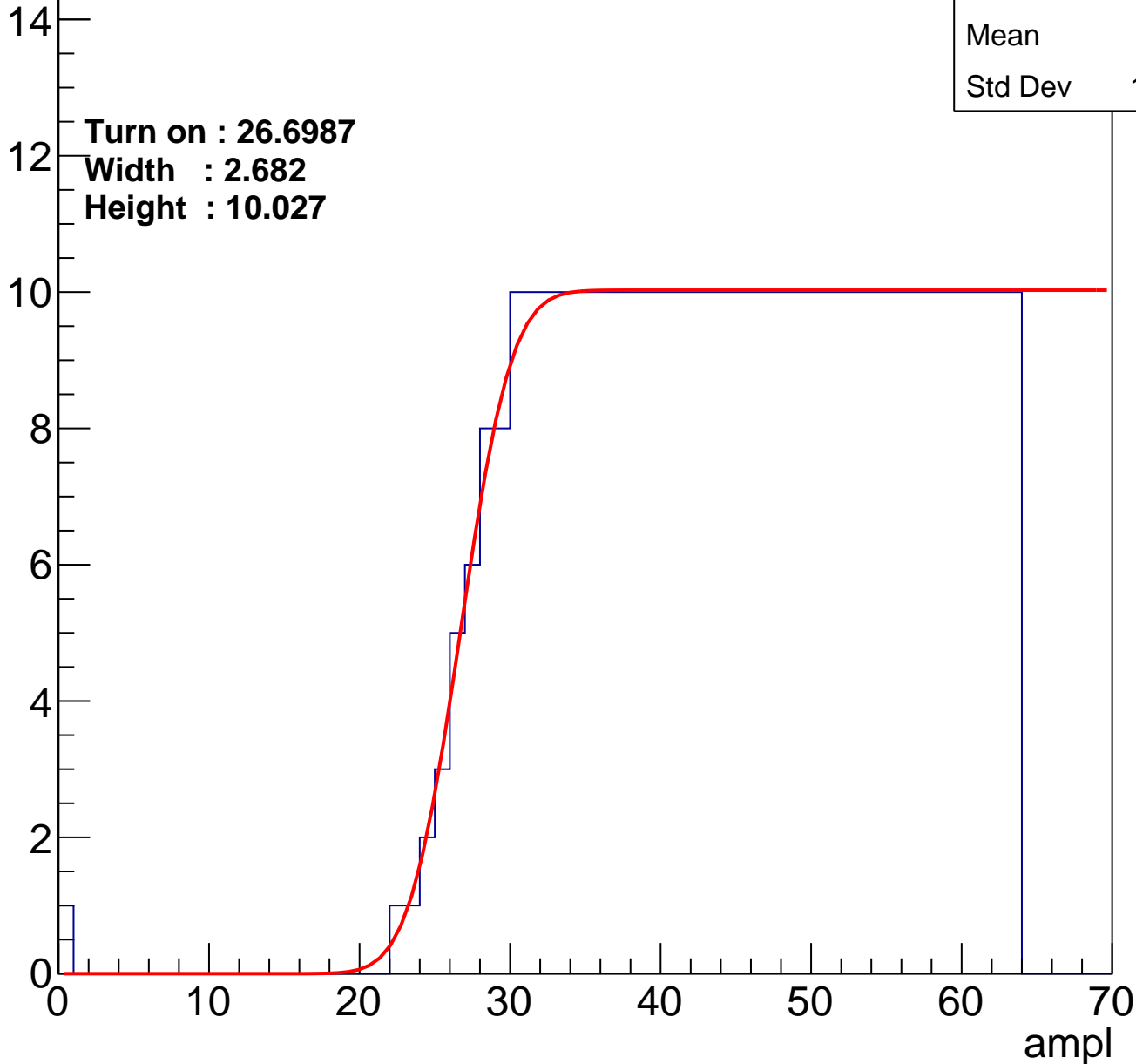
Entry

Entries	375
Mean	44.6
Std Dev	11.15

Turn on : 26.6987

Width : 2.682

Height : 10.027



B1L002S, U8-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.95
Std Dev	11.52

Turn on : 25.3533

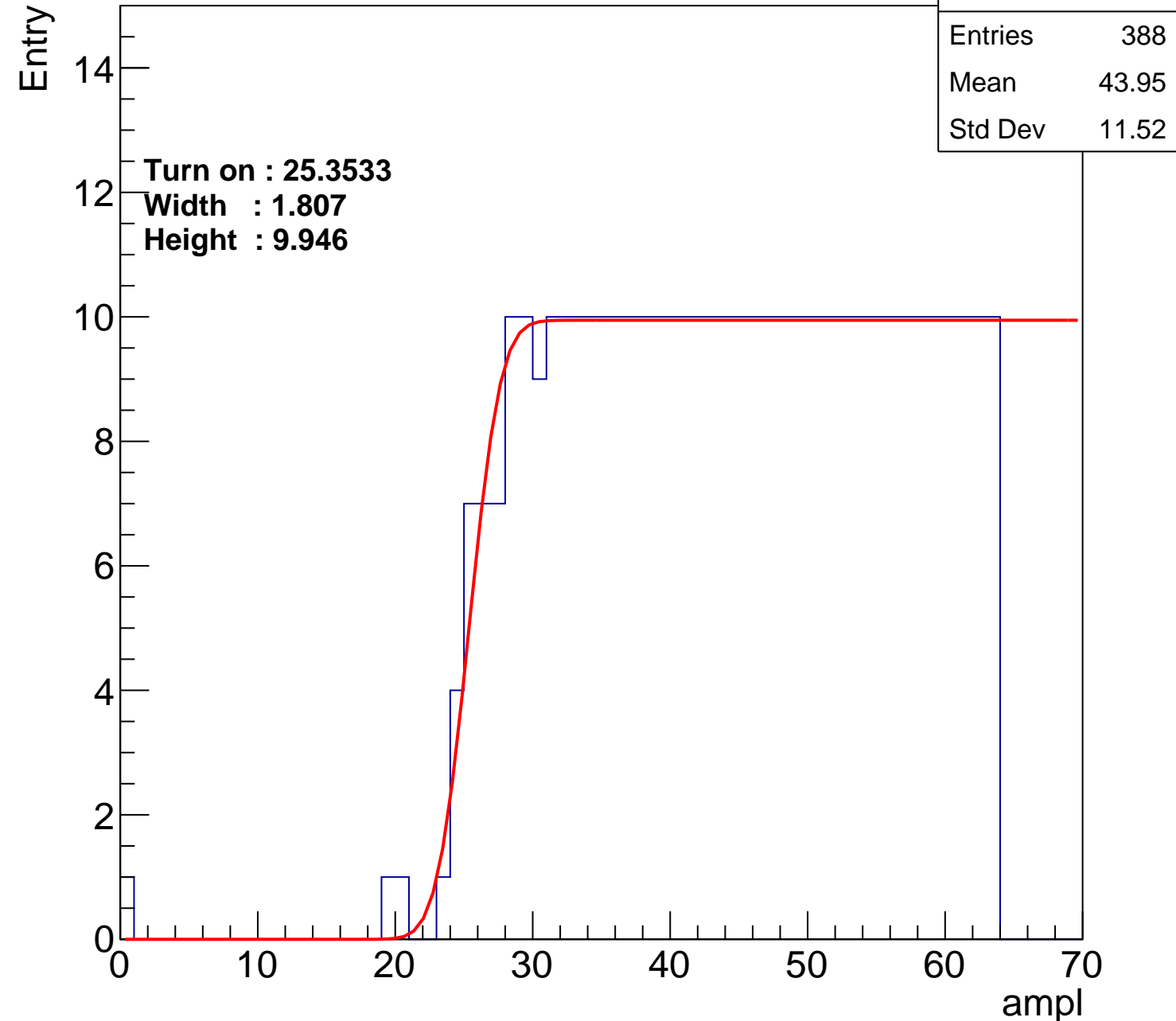
Width : 1.807

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.92
Std Dev	11.69

Turn on : 25.8129

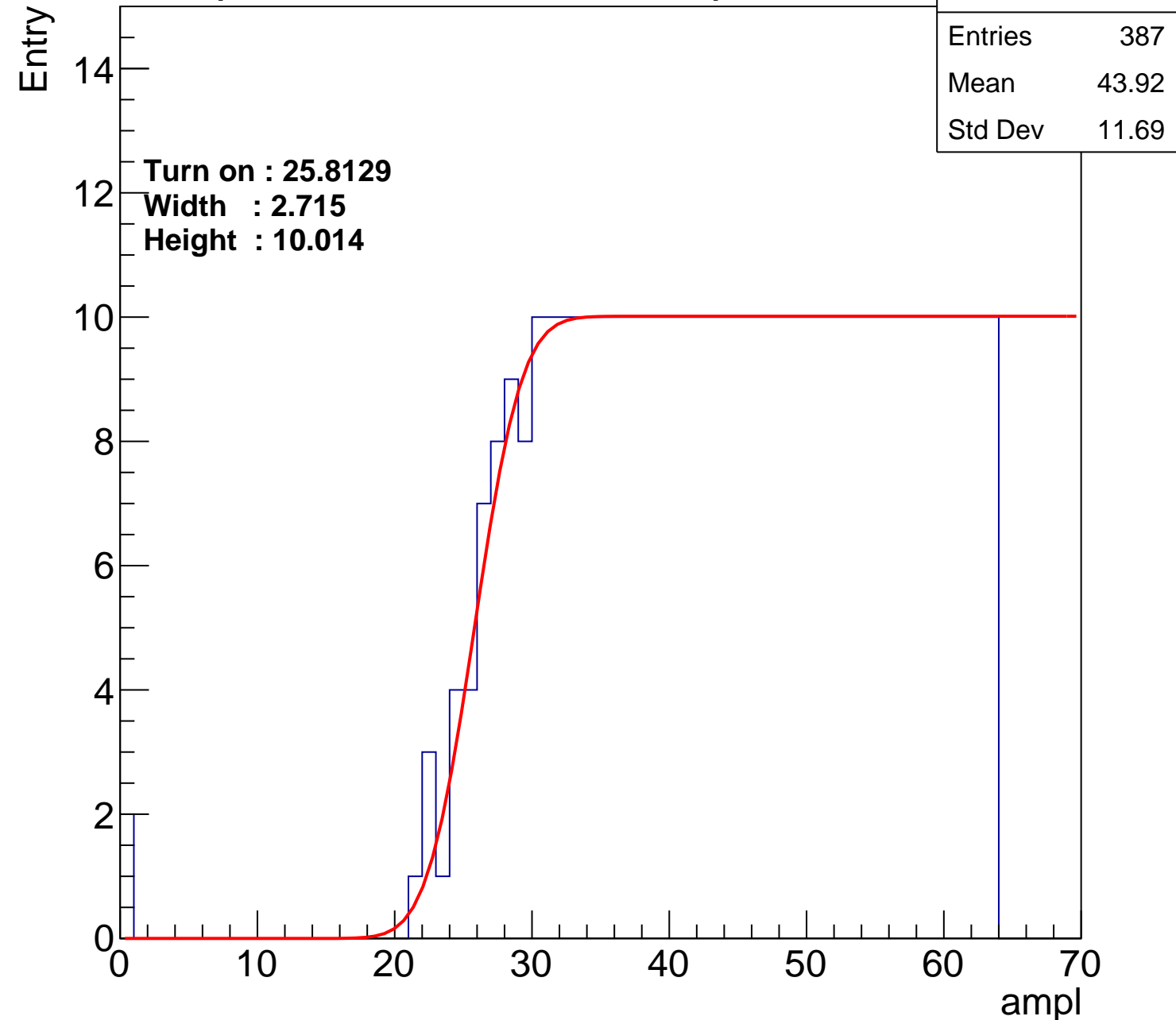
Width : 2.715

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.26
Std Dev	11.46

Turn on : 26.1404

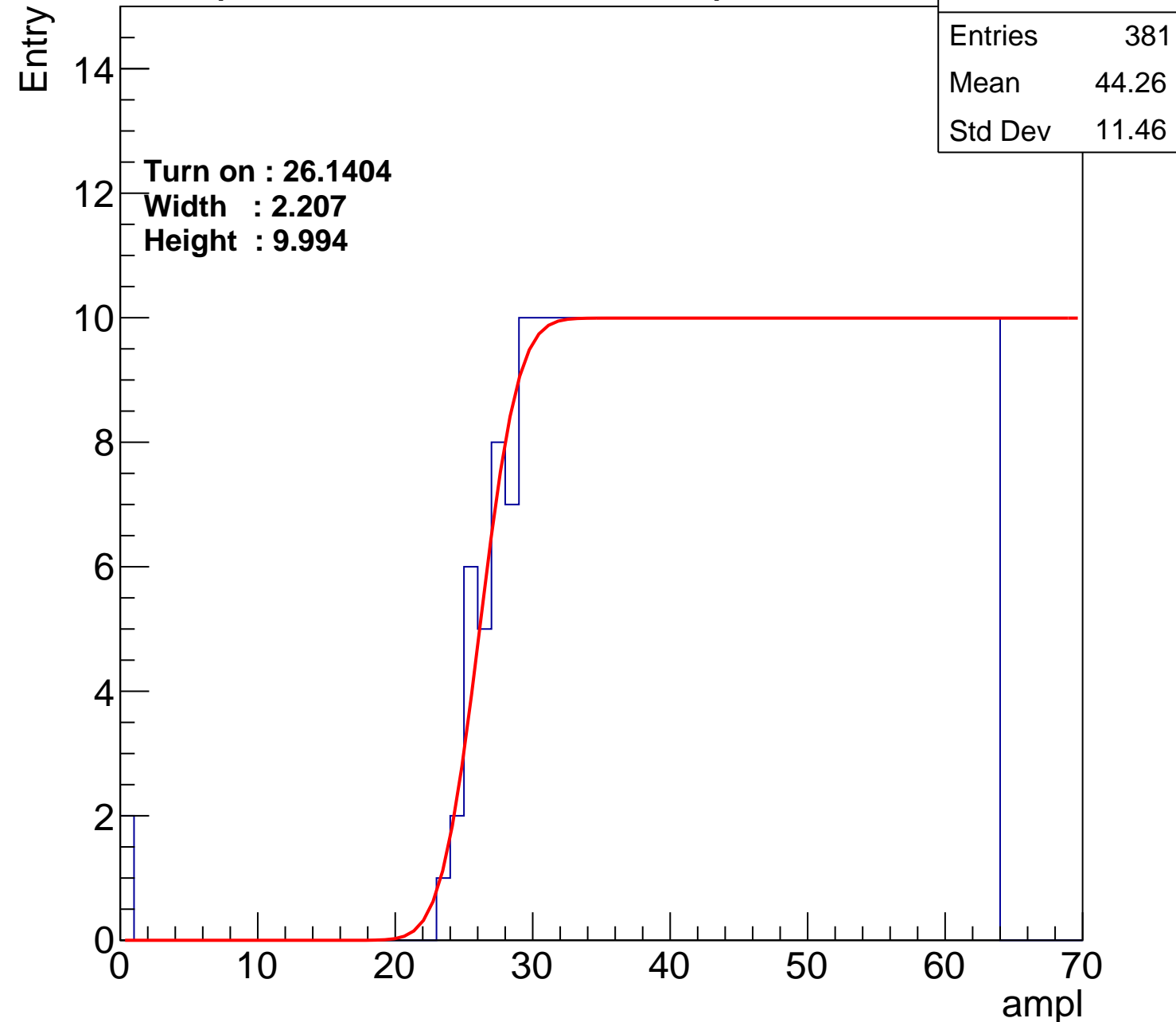
Width : 2.207

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.2062

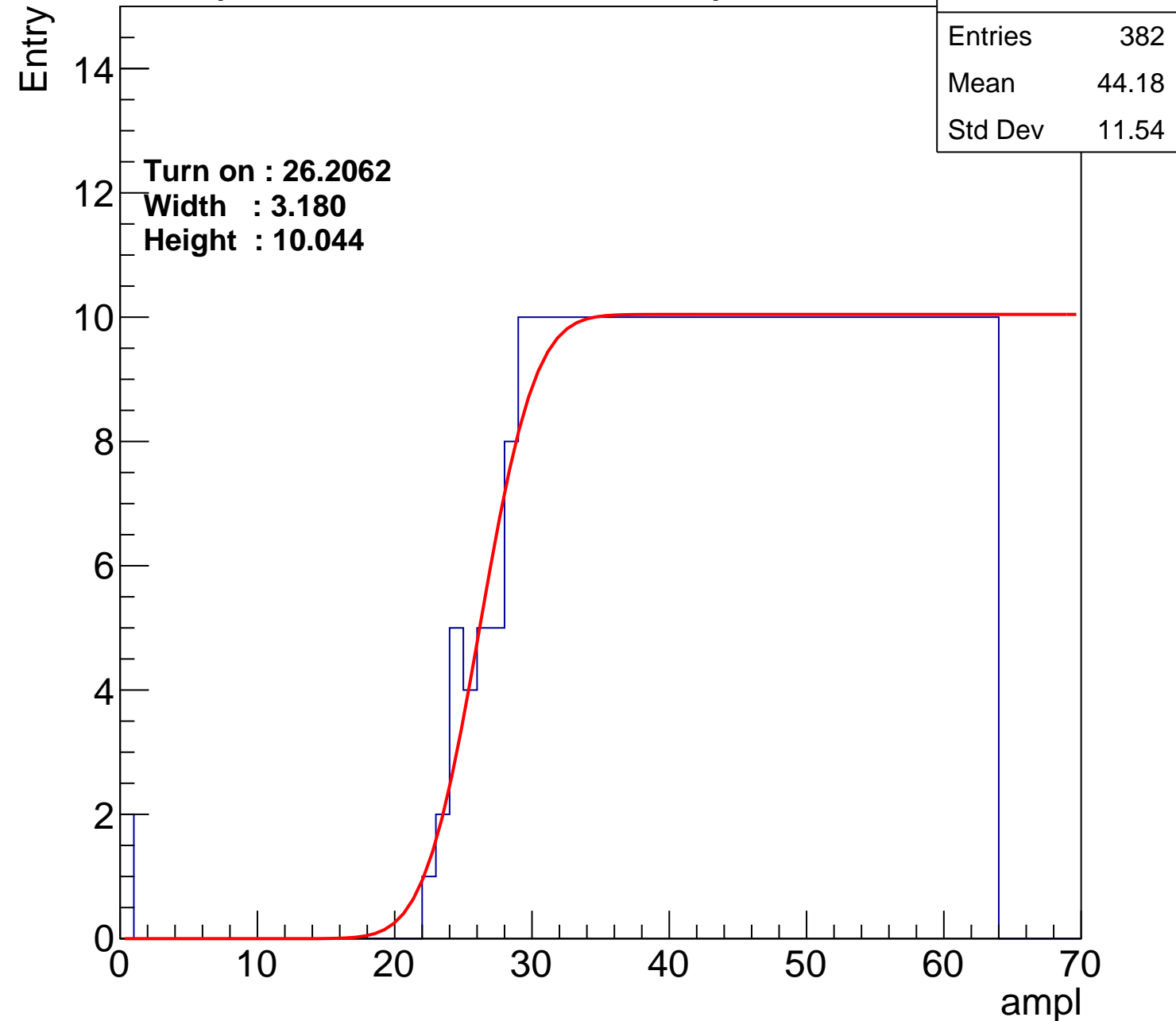
Width : 3.180

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.72

Turn on : 26.0486

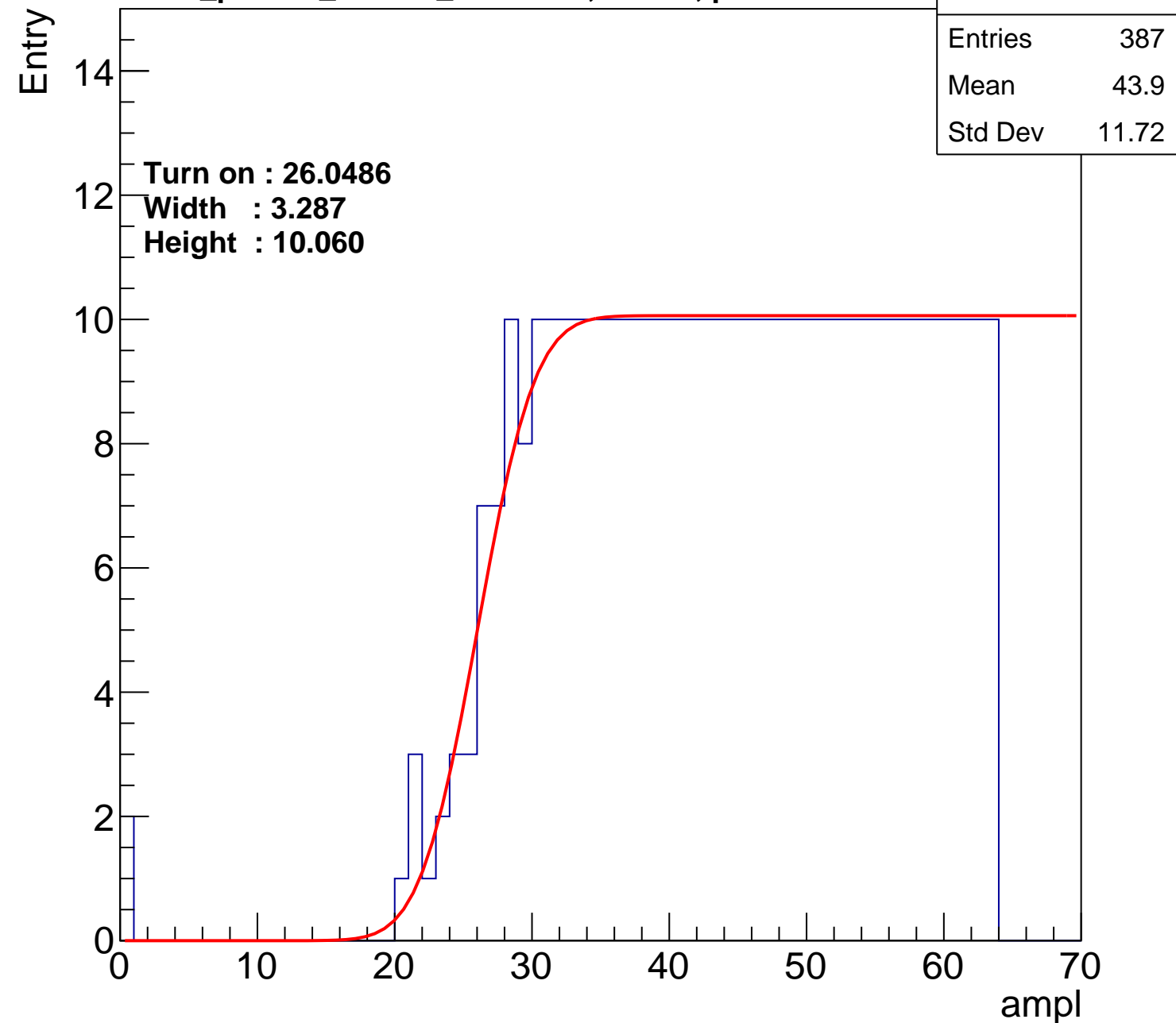
Width : 3.287

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch79

calib_packv5_042523_0143.root, FC#10, port B3

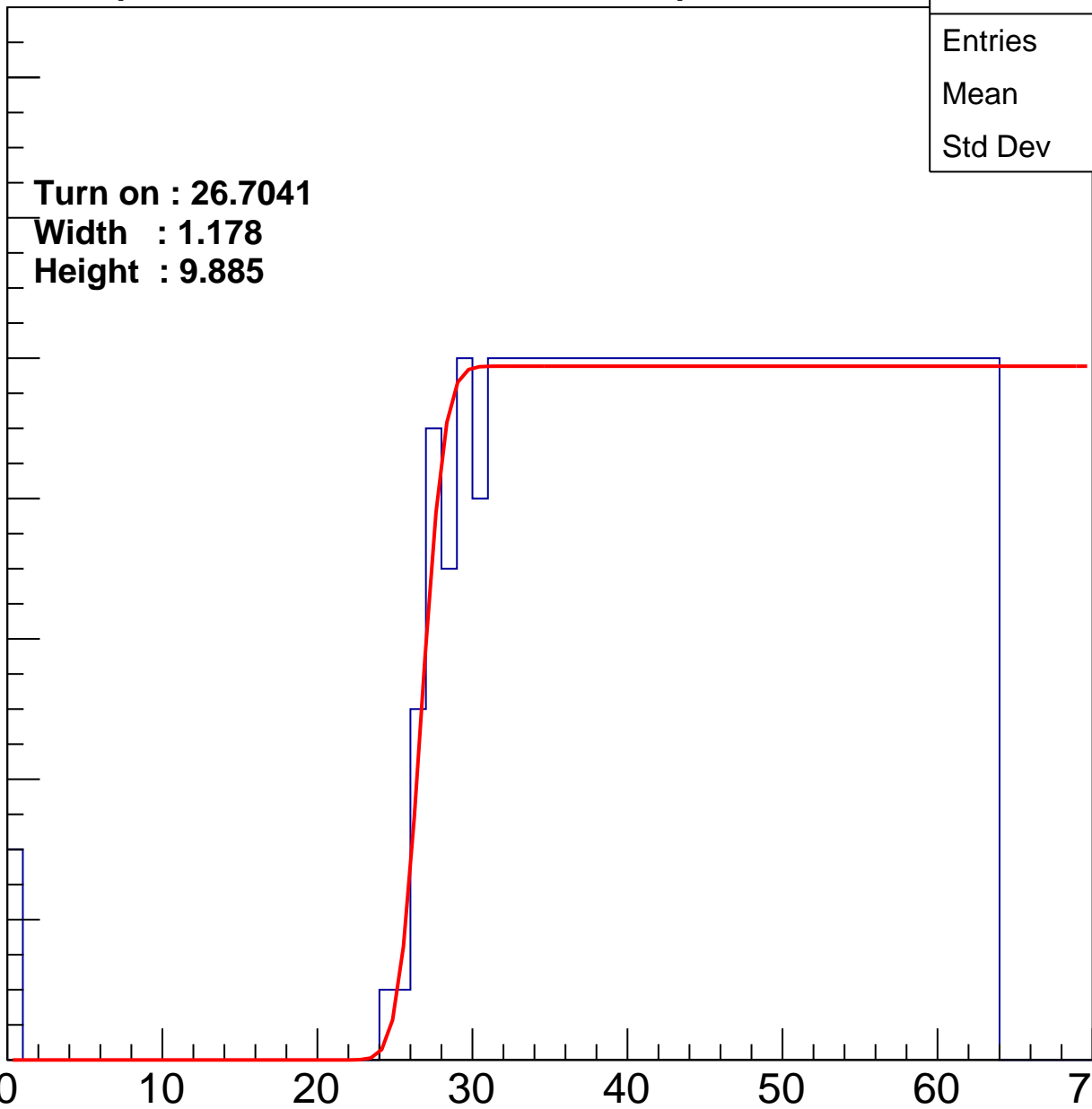
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7041
Width : 1.178
Height : 9.885

Entries	374
Mean	44.54
Std Dev	11.47

ampl



B1L002S, U8-ch80

calib_packv5_042523_0143.root, FC#10, port B3

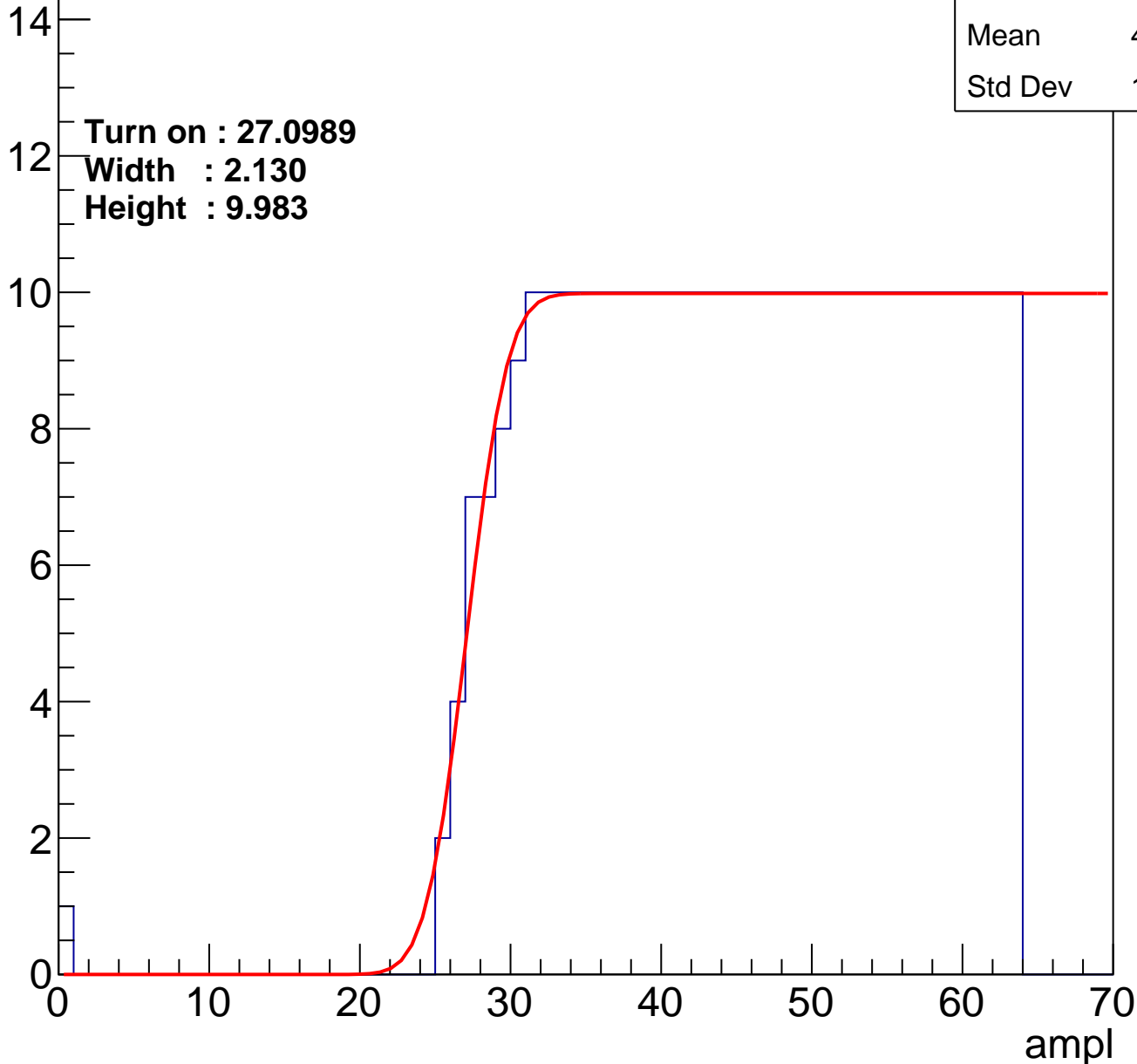
Entry

Entries	368
Mean	44.98
Std Dev	10.92

Turn on : 27.0989

Width : 2.130

Height : 9.983



B1L002S, U8-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.02
Std Dev	11.95

Turn on : 26.5101

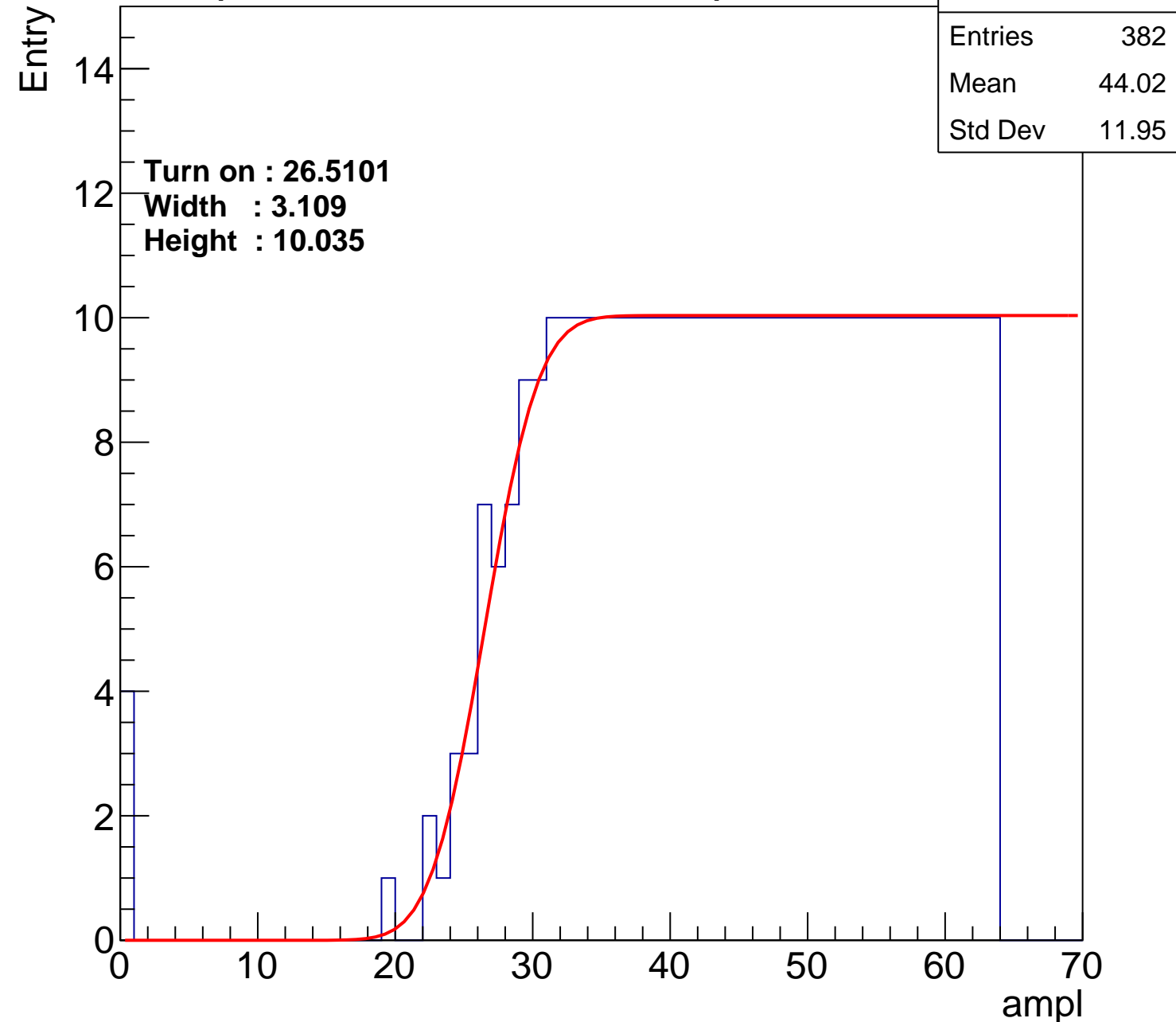
Width : 3.109

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.71
Std Dev	11.64

Turn on : 24.5703

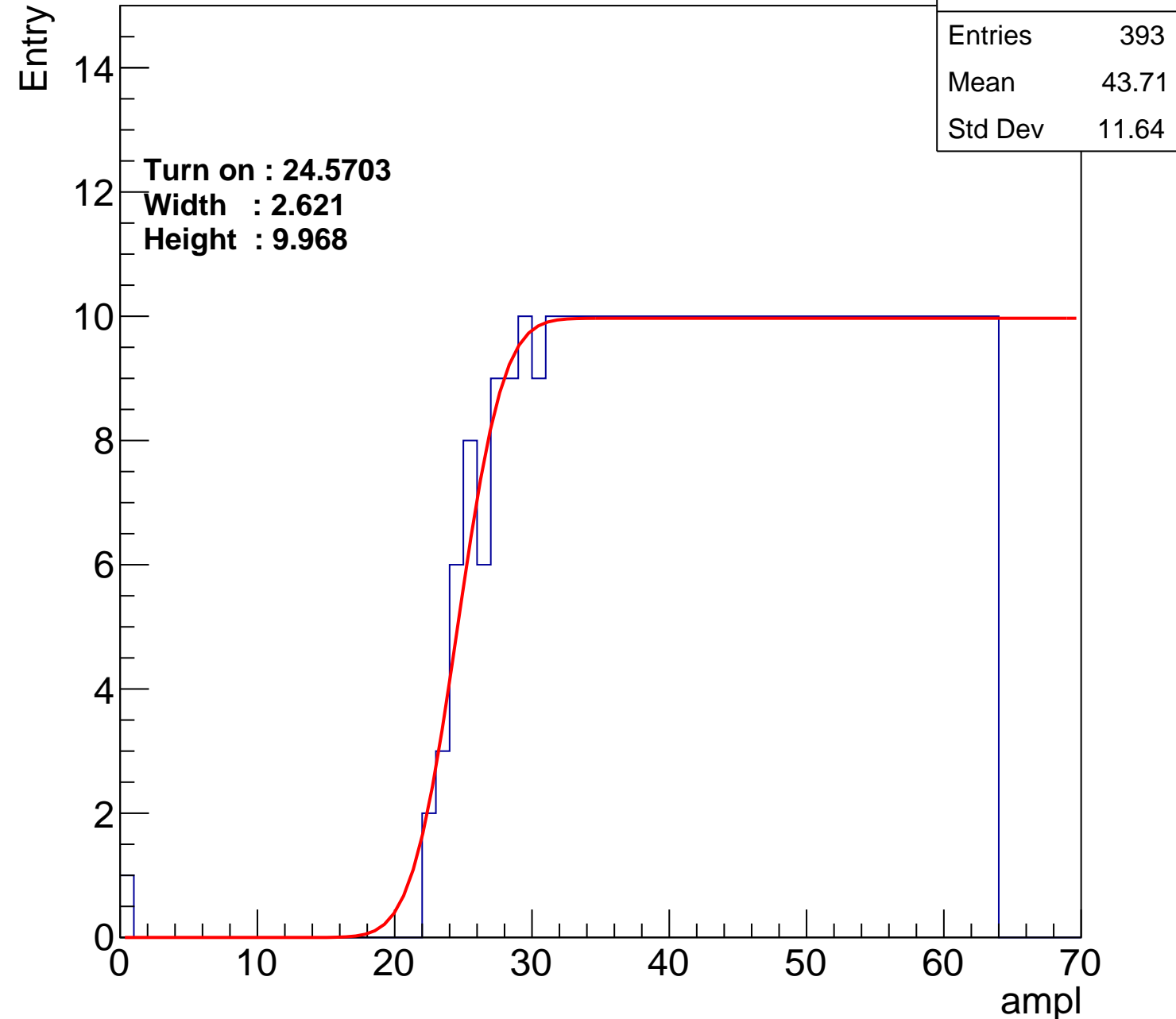
Width : 2.621

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.9583

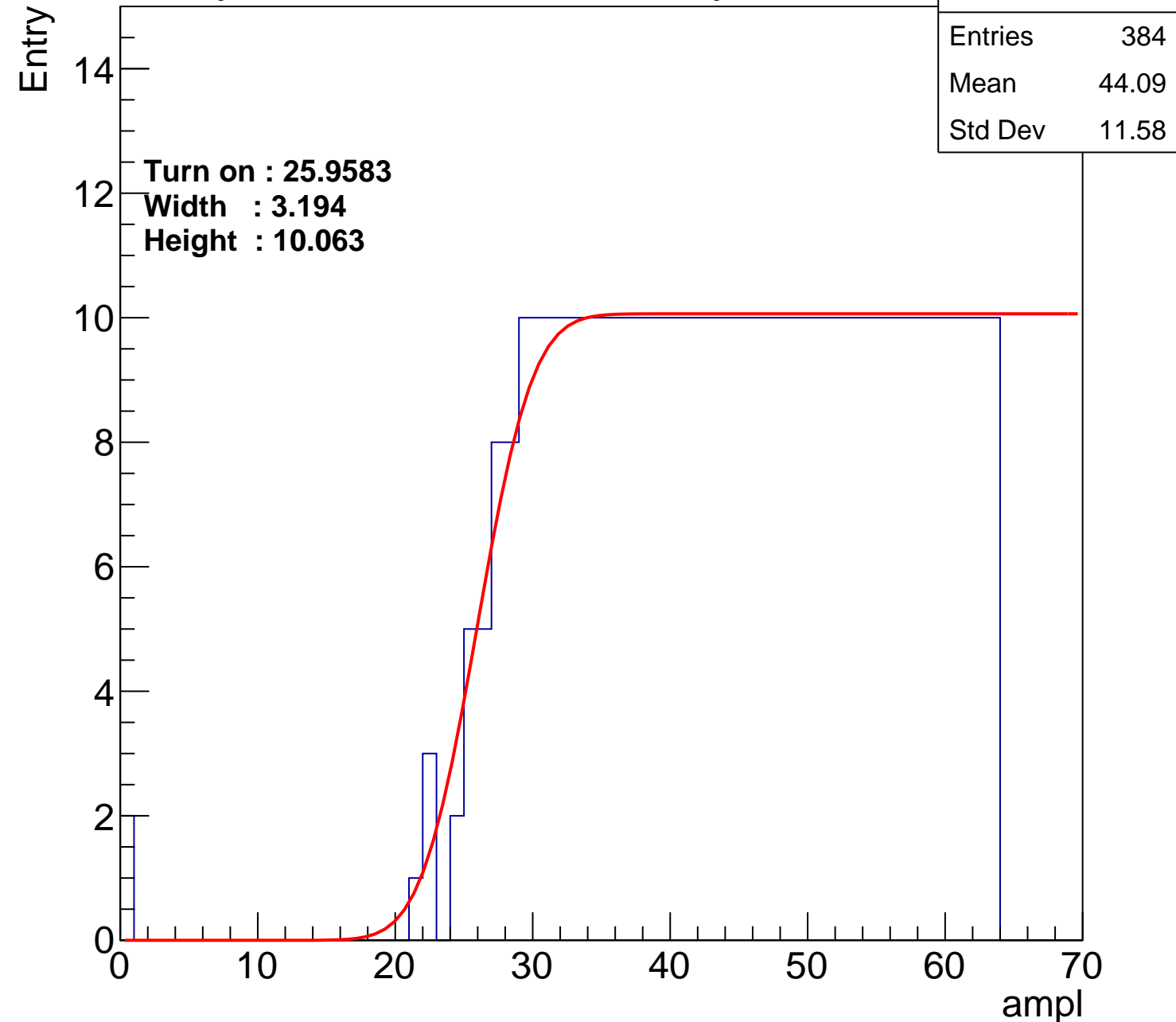
Width : 3.194

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch84

calib_packv5_042523_0143.root, FC#10, port B3

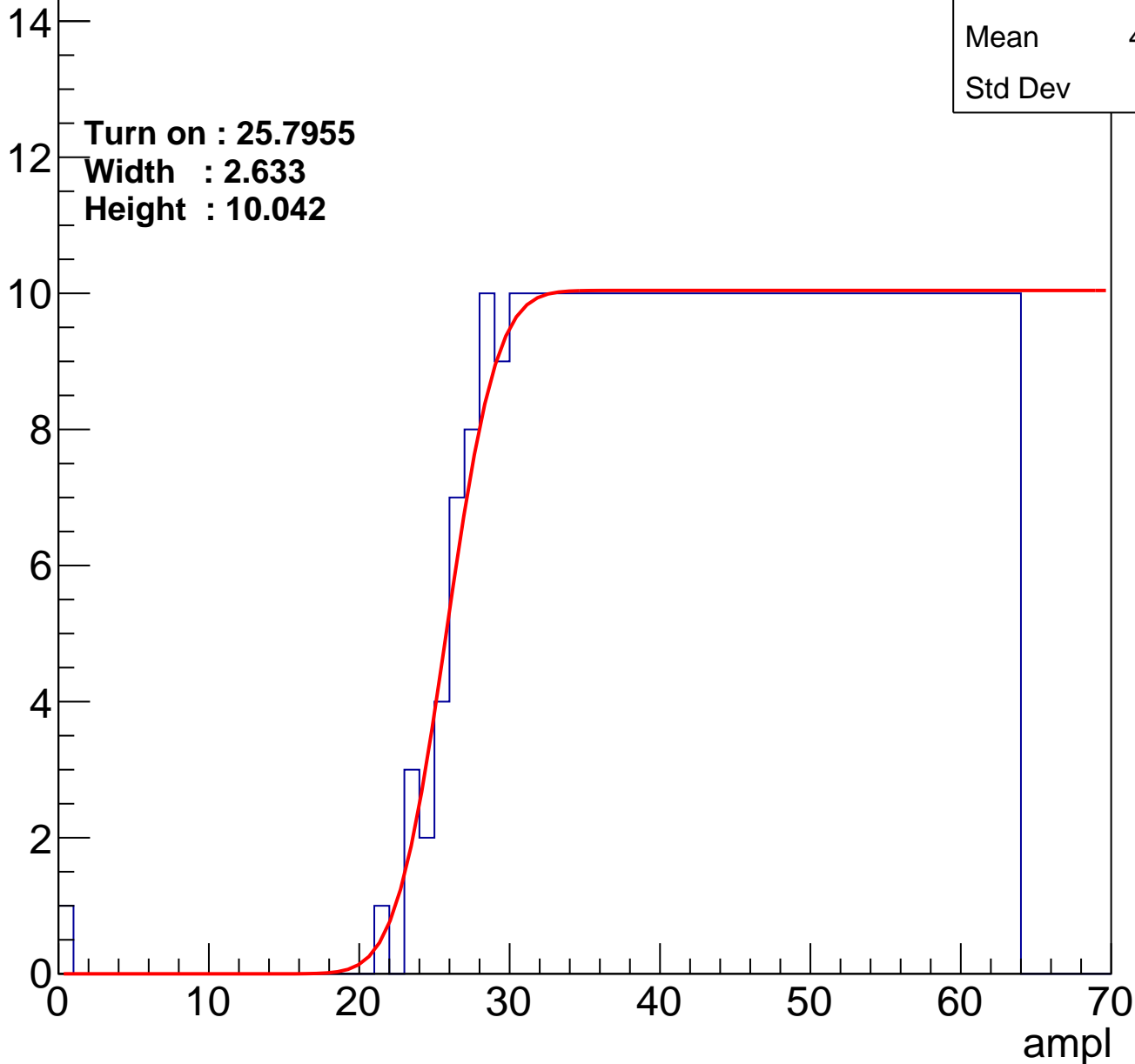
Entry

Entries	385
Mean	44.12
Std Dev	11.4

Turn on : 25.7955

Width : 2.633

Height : 10.042



B1L002S, U8-ch85

calib_packv5_042523_0143.root, FC#10, port B3

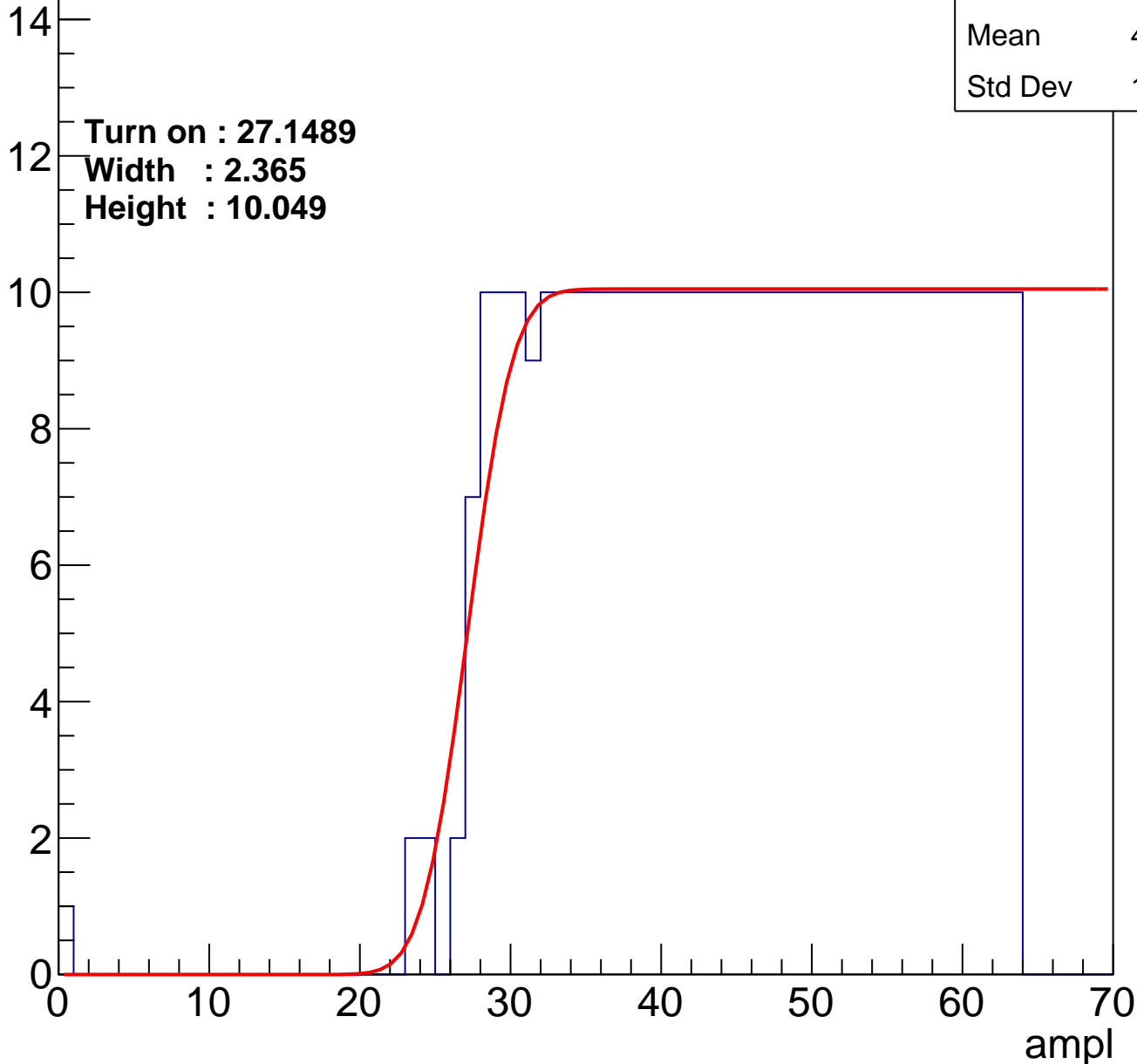
Entry

Entries	373
Mean	44.73
Std Dev	11.05

Turn on : 27.1489

Width : 2.365

Height : 10.049



B1L002S, U8-ch86

calib_packv5_042523_0143.root, FC#10, port B3

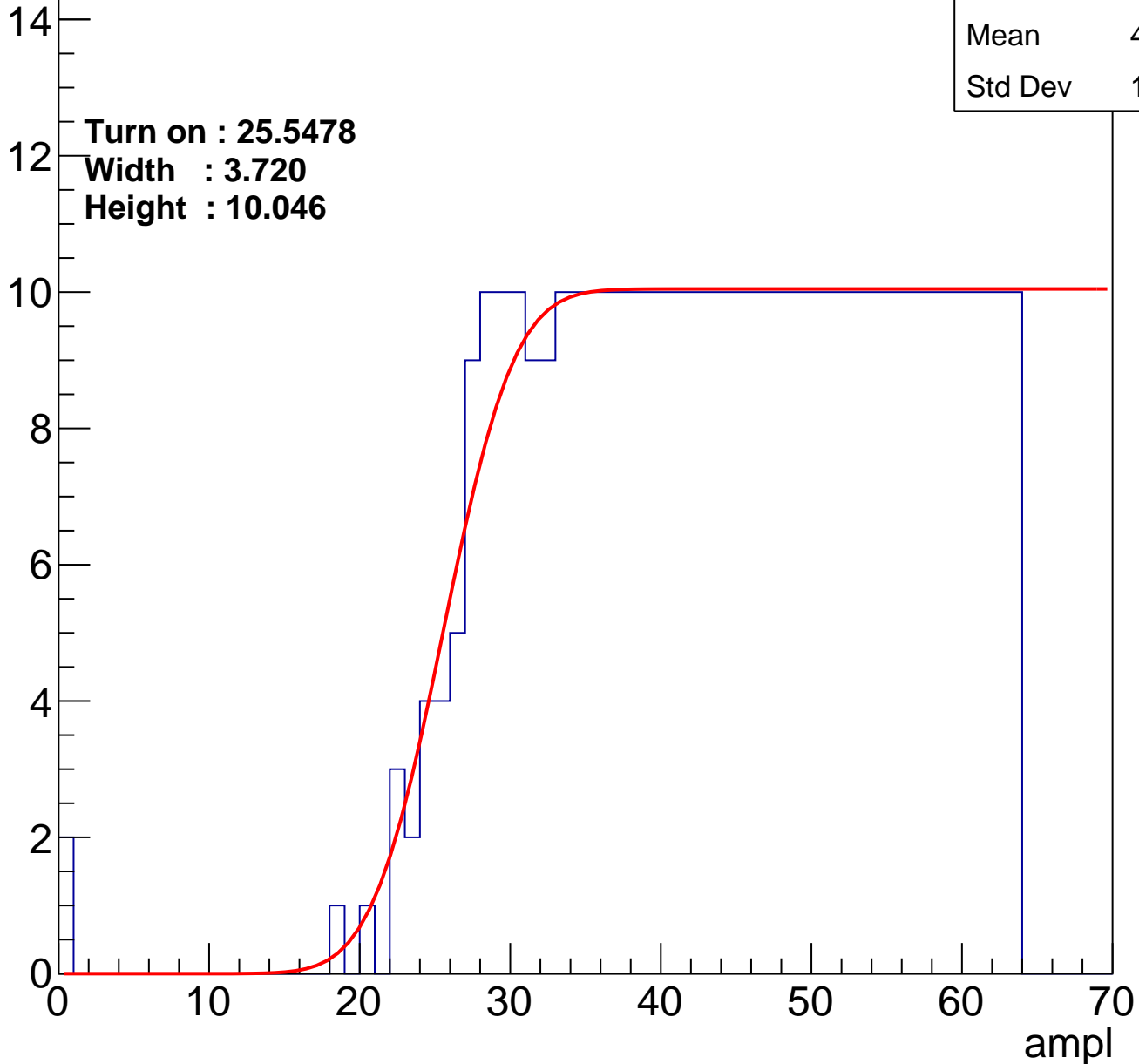
Entries	389
Mean	43.79
Std Dev	11.79

Turn on : 25.5478

Width : 3.720

Height : 10.046

Entry



B1L002S, U8-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.4356

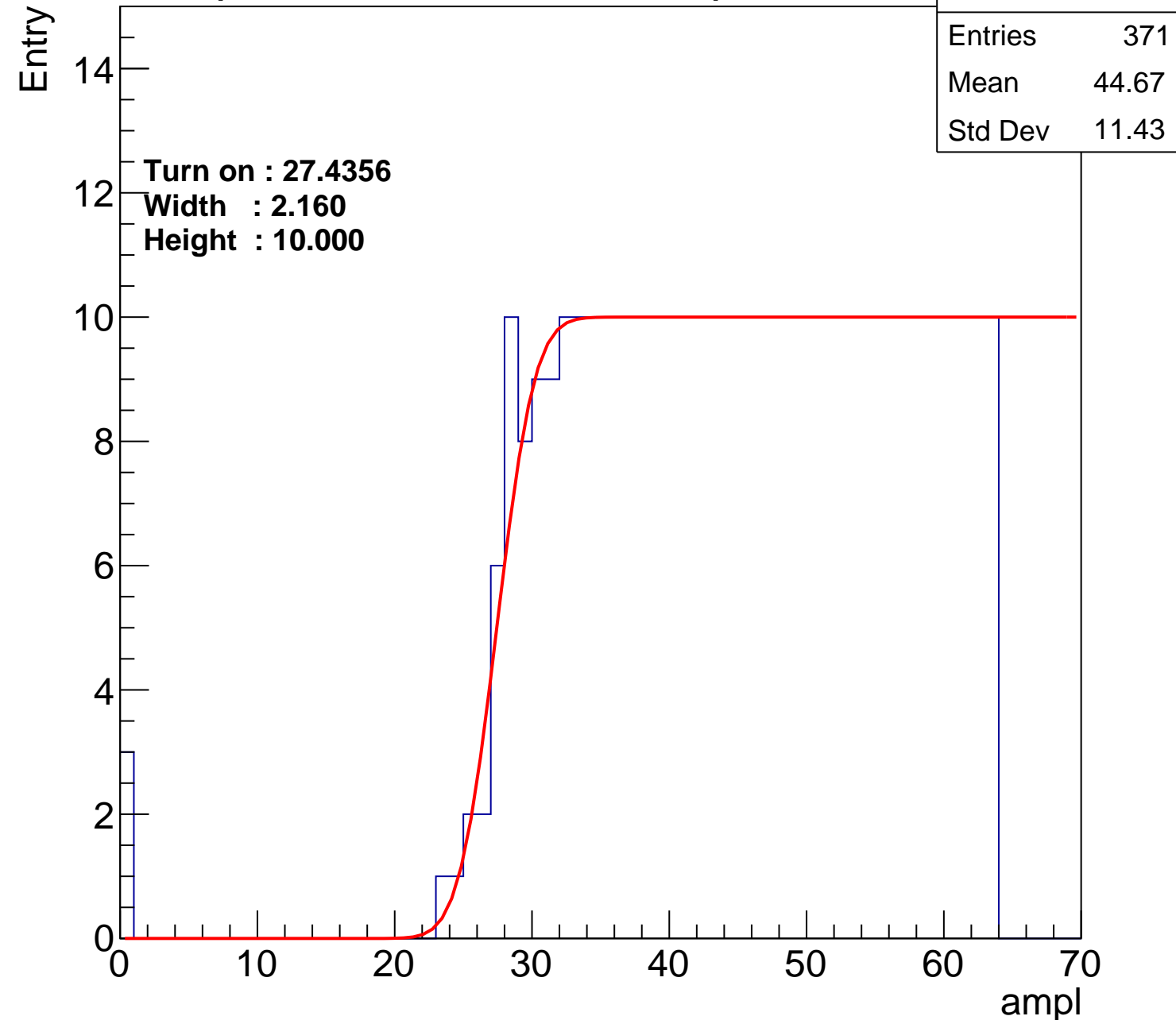
Width : 2.160

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.81
Std Dev	11.71

Turn on : 25.2349

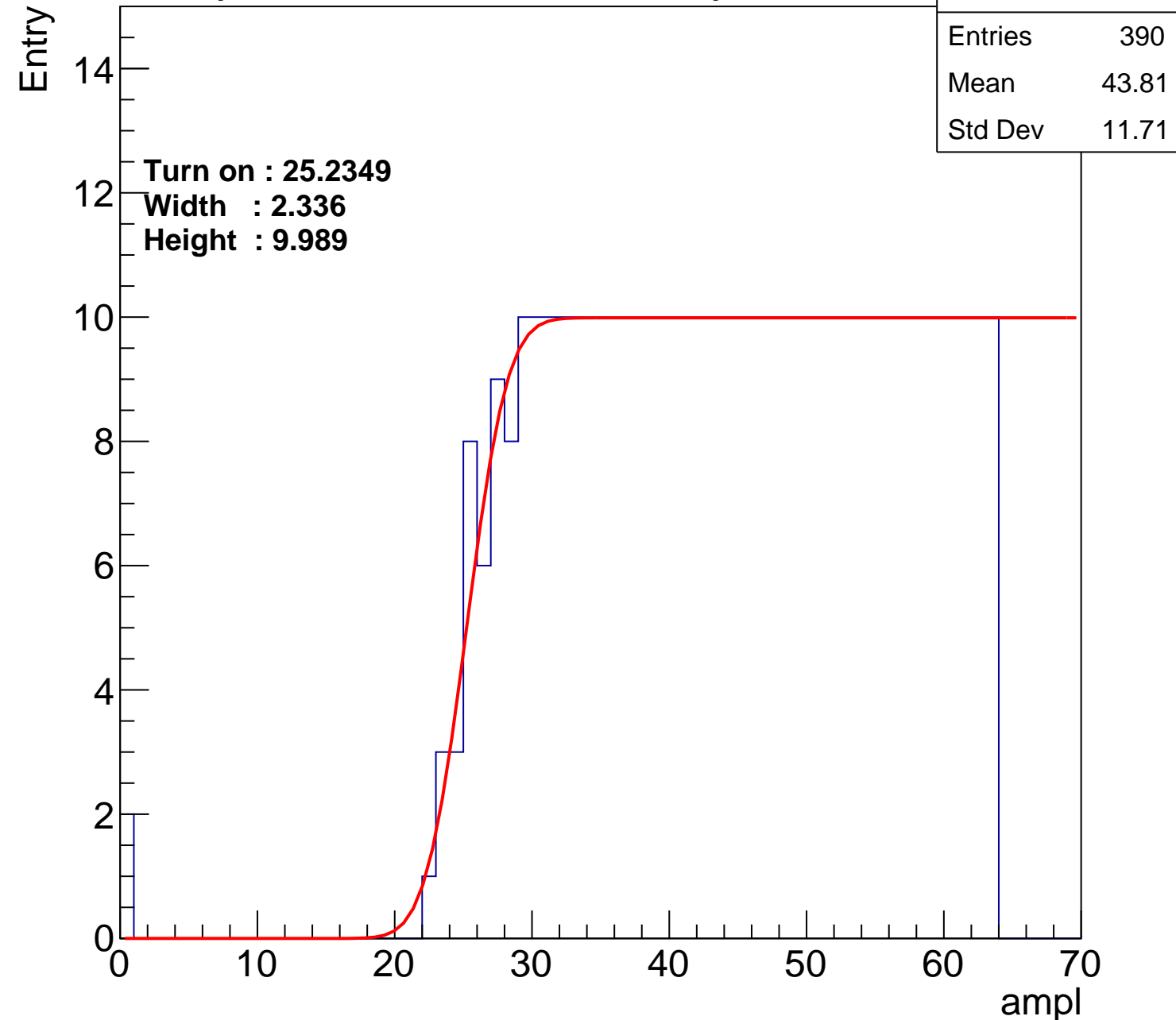
Width : 2.336

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.59
Std Dev	11.97

Turn on : 25.3074

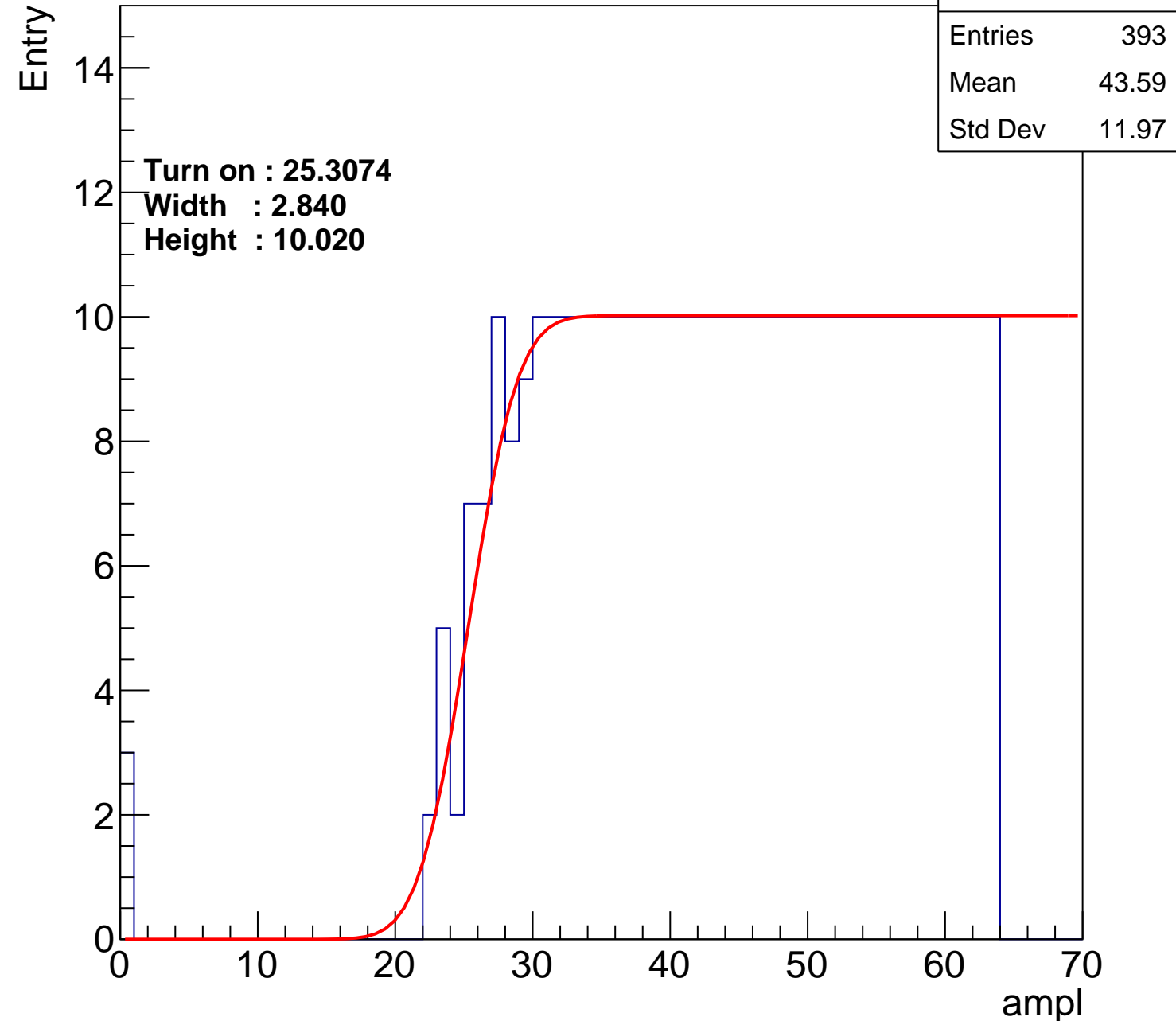
Width : 2.840

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch90

calib_packv5_042523_0143.root, FC#10, port B3

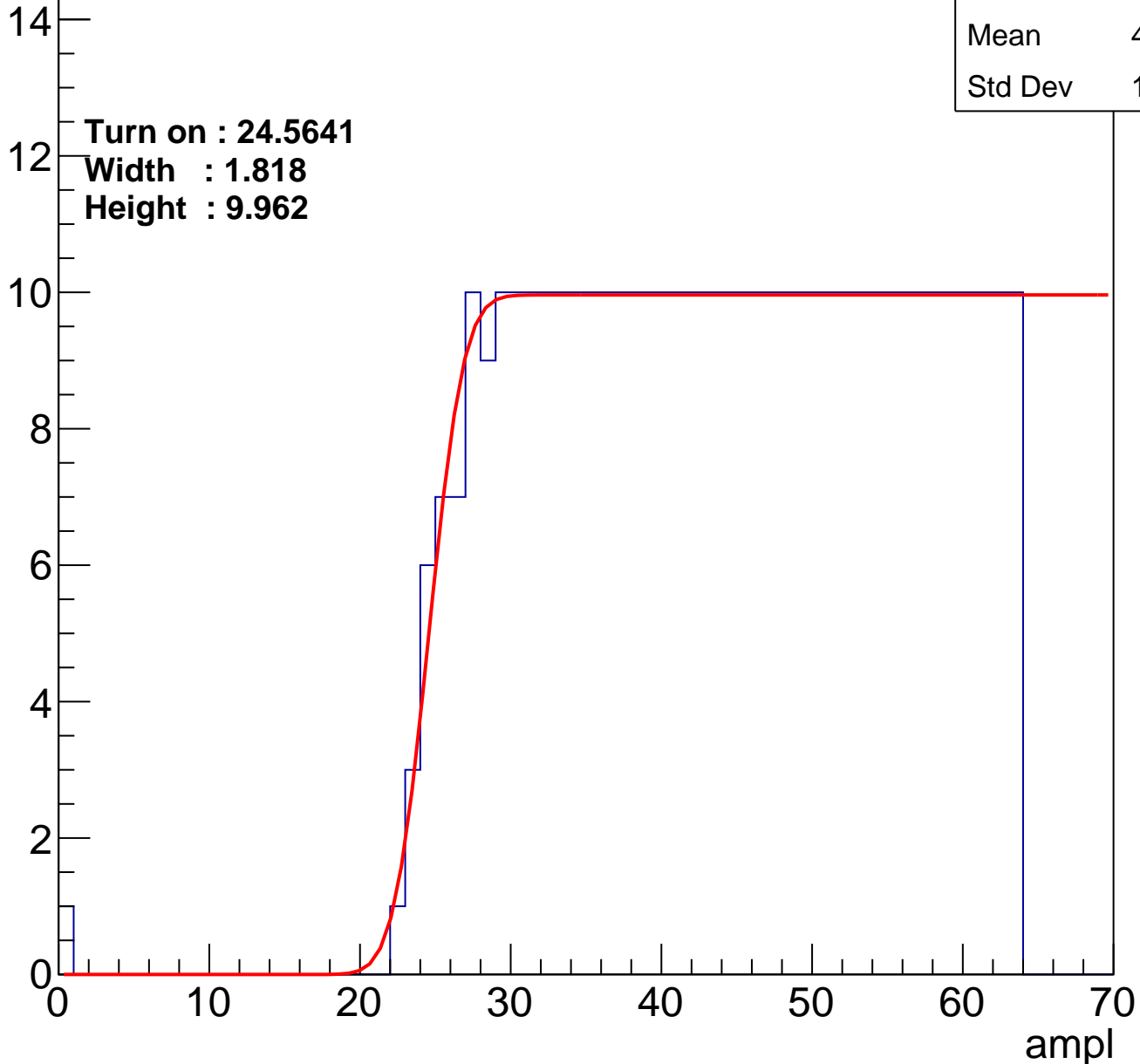
Entries	394
Mean	43.69
Std Dev	11.62

Turn on : 24.5641

Width : 1.818

Height : 9.962

Entry



B1L002S, U8-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 26.0953

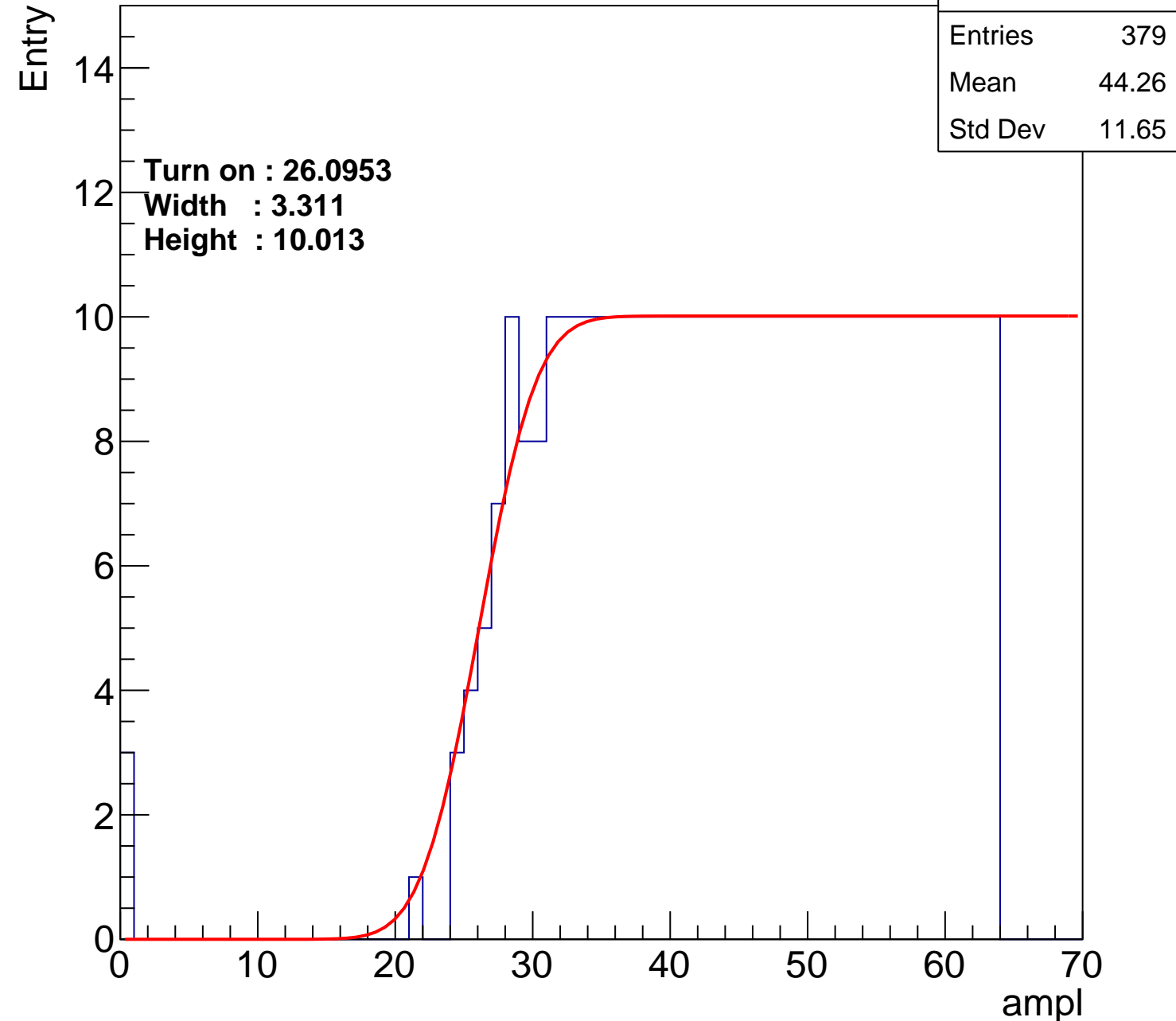
Width : 3.311

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.65
Std Dev	11.14

Turn on : 26.9334

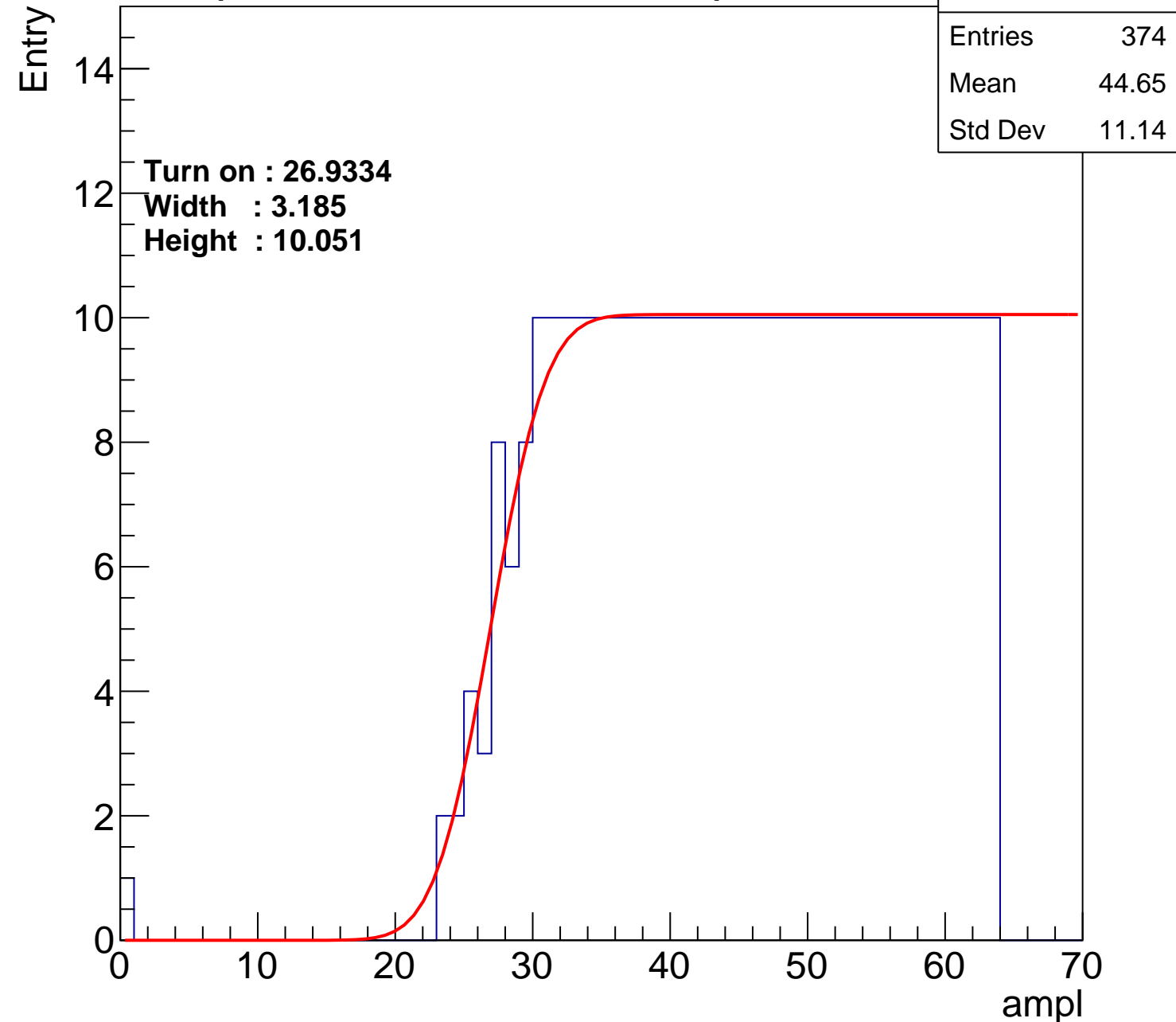
Width : 3.185

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	419
Mean	42.36
Std Dev	12.56

Turn on : 22.8832

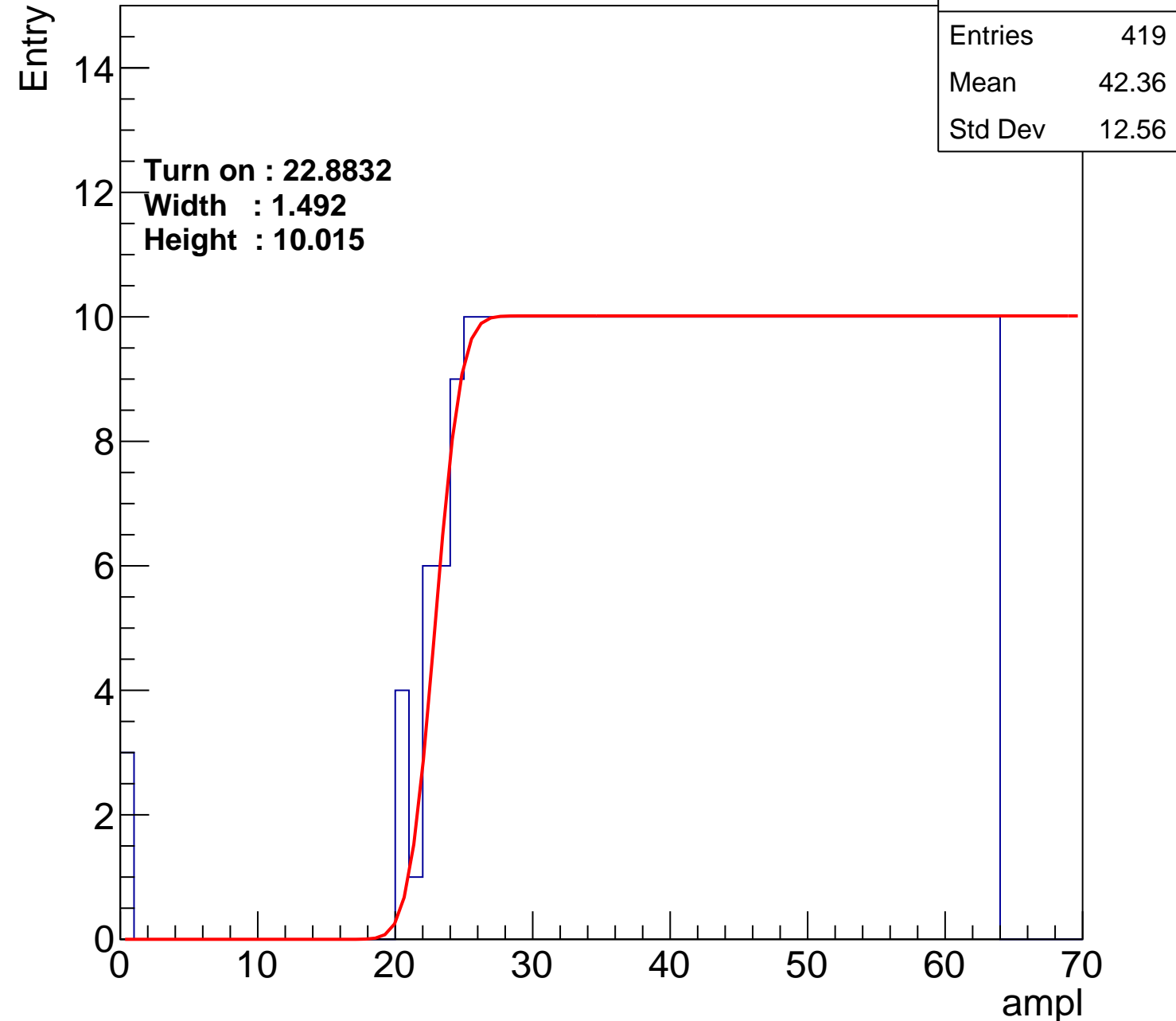
Width : 1.492

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch94

calib_packv5_042523_0143.root, FC#10, port B3

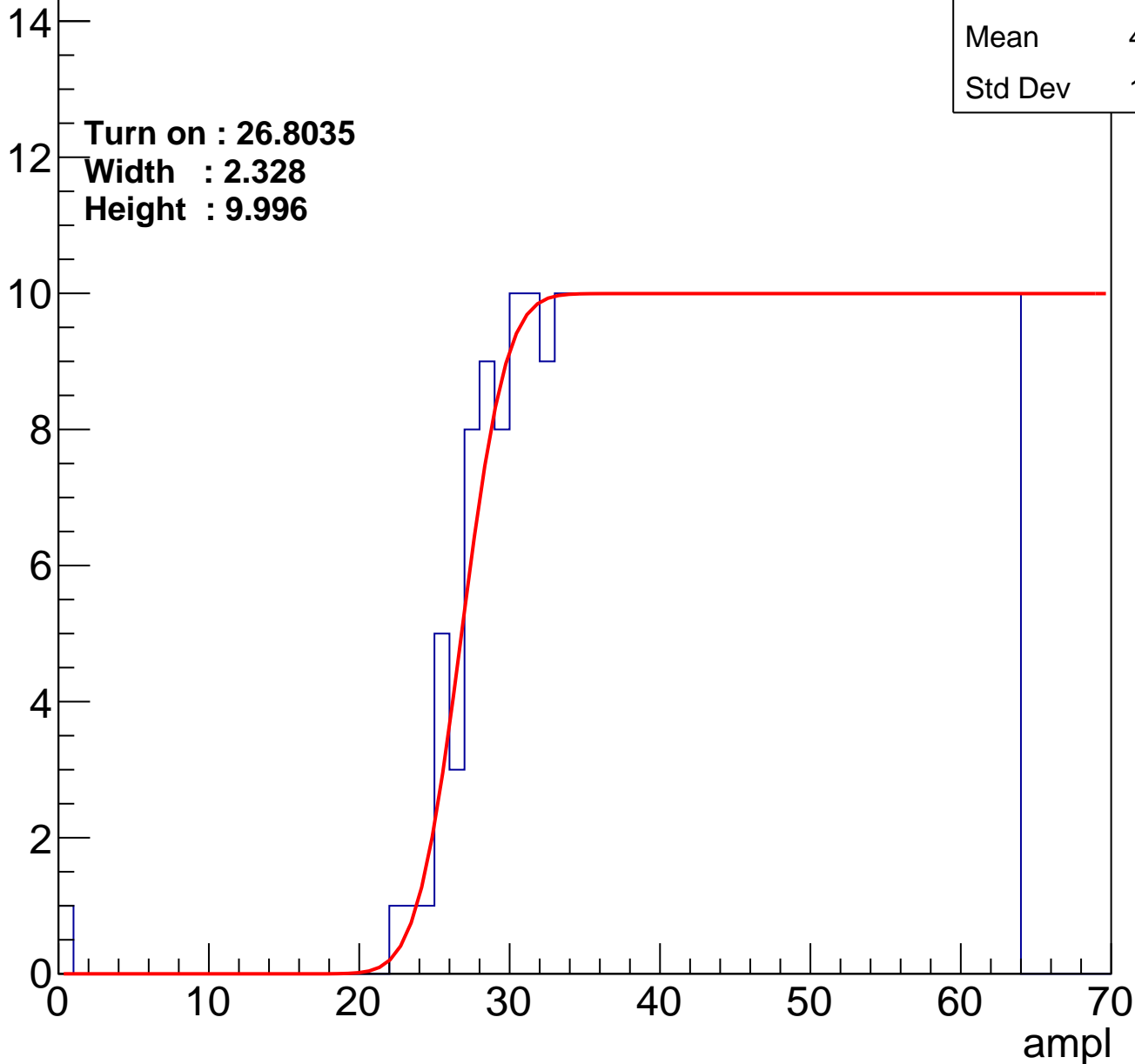
Entry

Entries	376
Mean	44.55
Std Dev	11.19

Turn on : 26.8035

Width : 2.328

Height : 9.996



B1L002S, U8-ch95

calib_packv5_042523_0143.root, FC#10, port B3

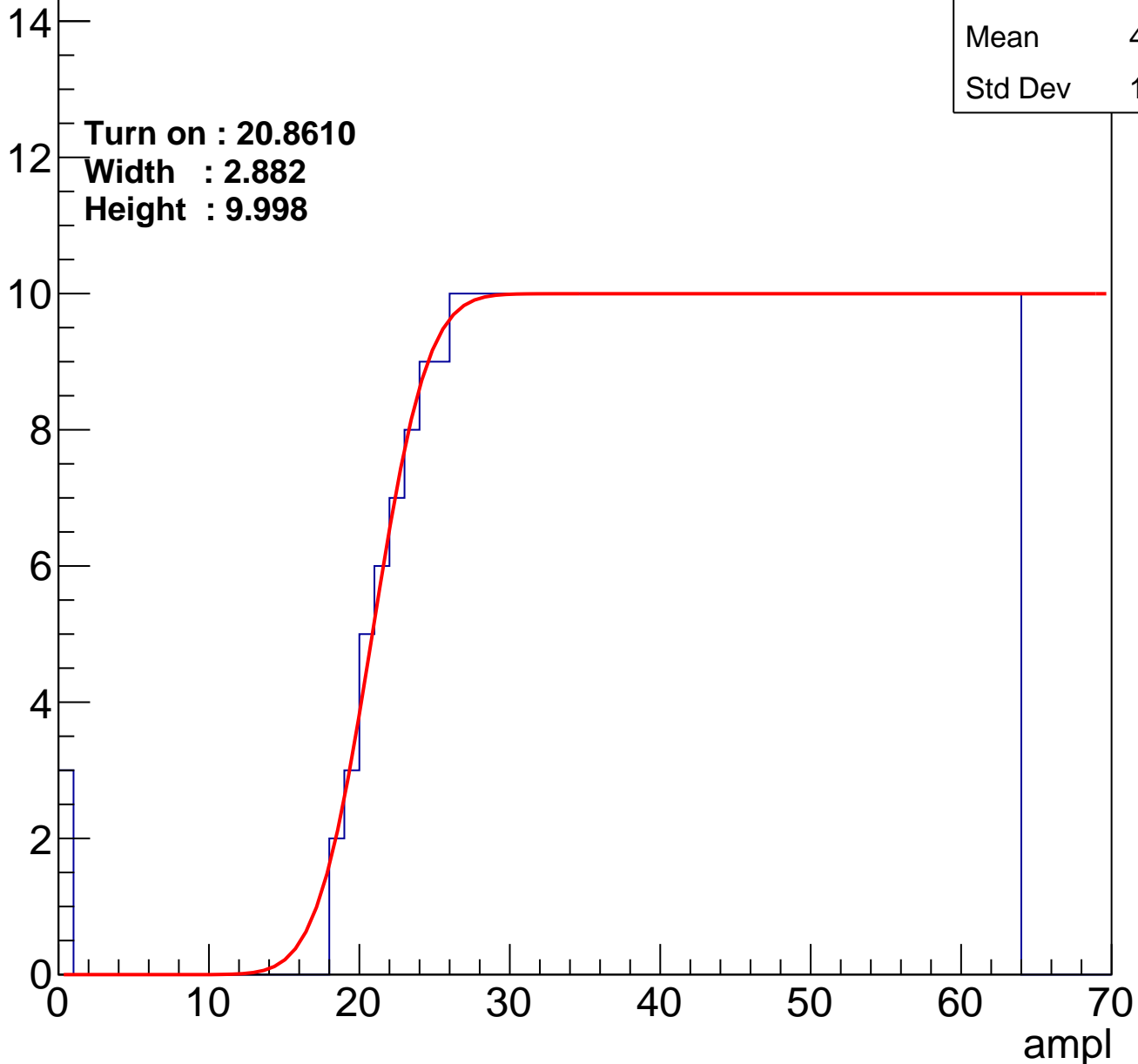
Entries	432
Mean	41.69
Std Dev	12.94

Turn on : 20.8610

Width : 2.882

Height : 9.998

Entry



B1L002S, U8-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.48
Std Dev	12.04

Turn on : 25.0518

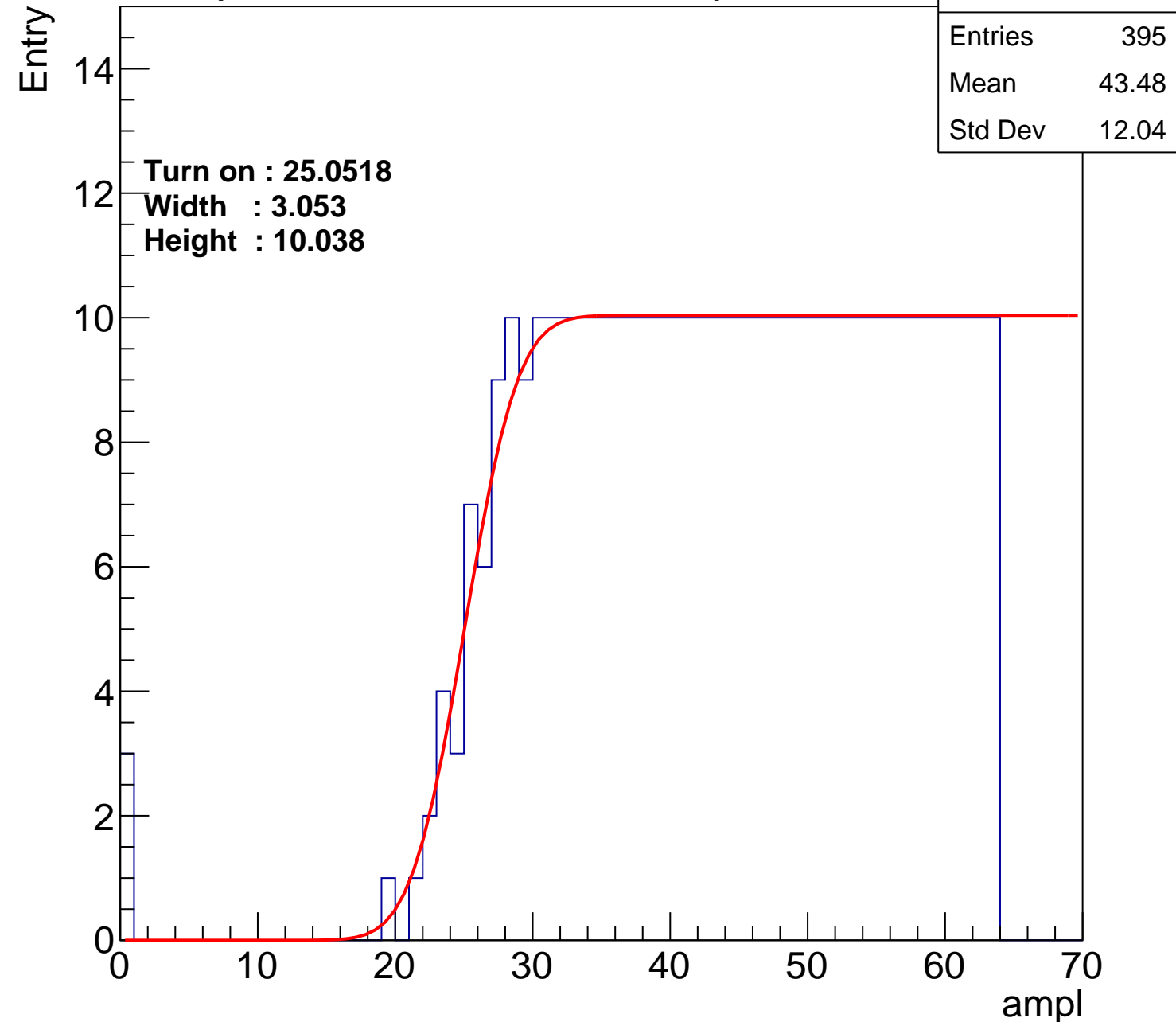
Width : 3.053

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.3155

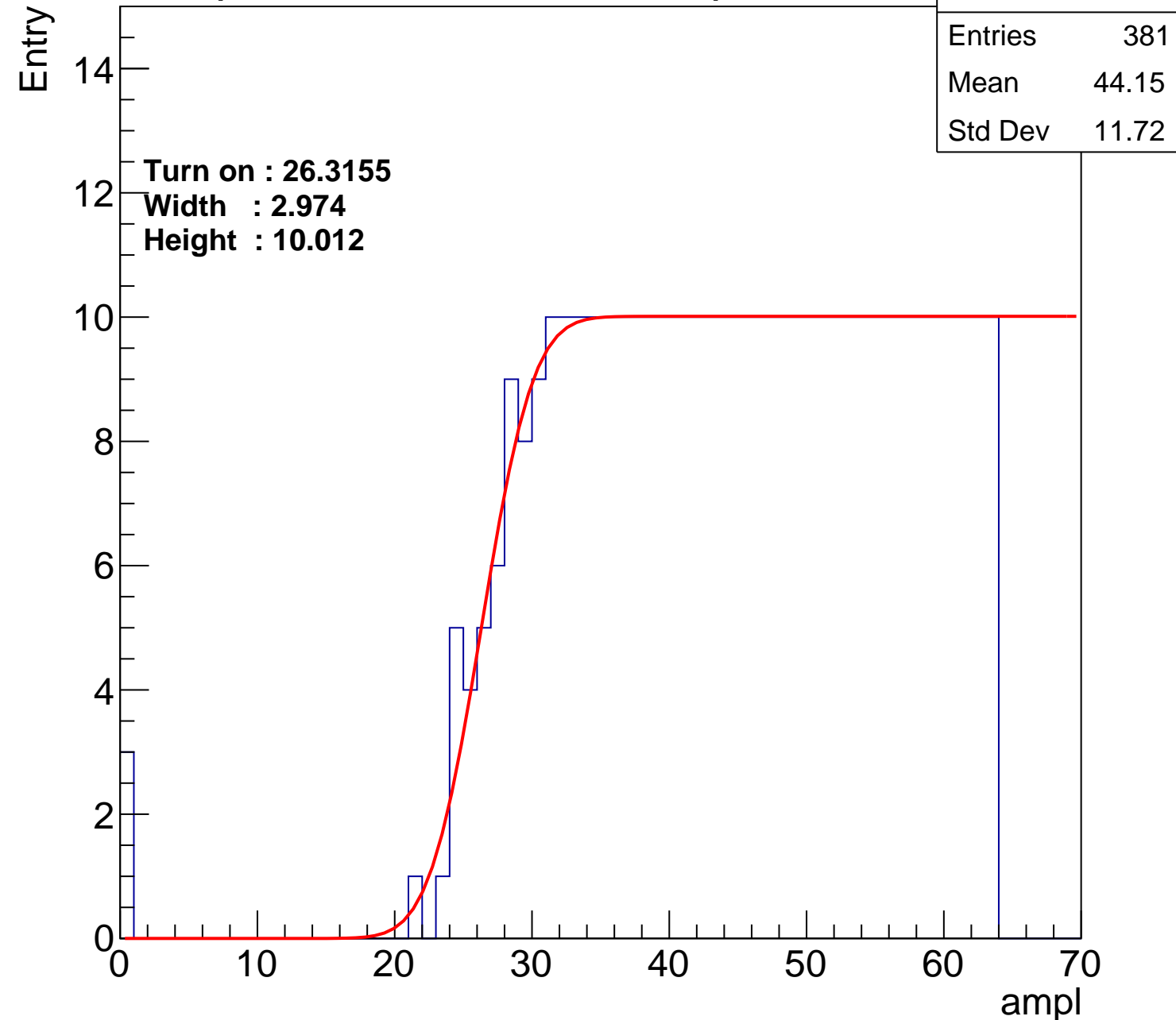
Width : 2.974

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.99
Std Dev	12.02

Turn on : 23.5338

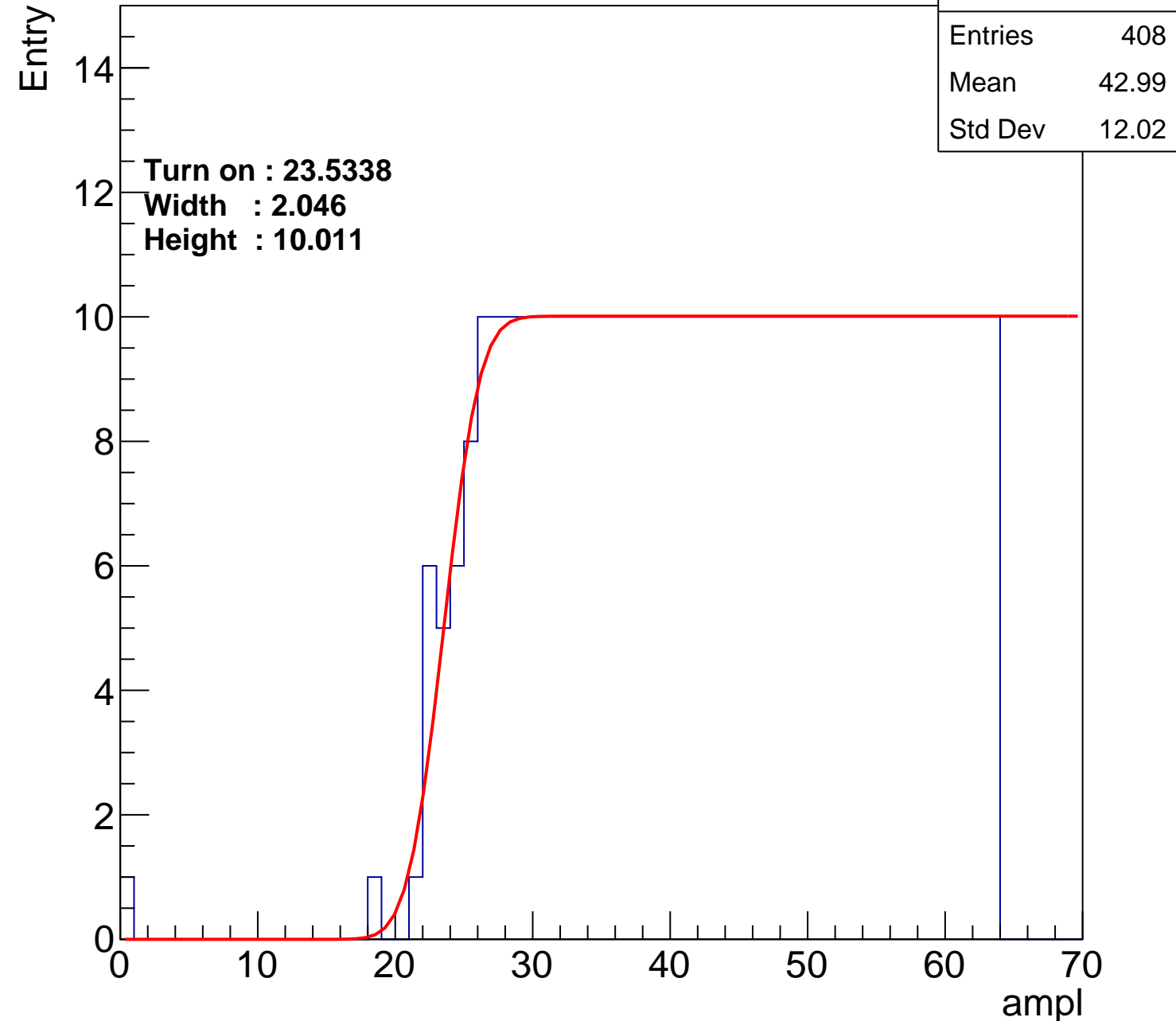
Width : 2.046

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.38
Std Dev	11.48

Turn on : 26.6874

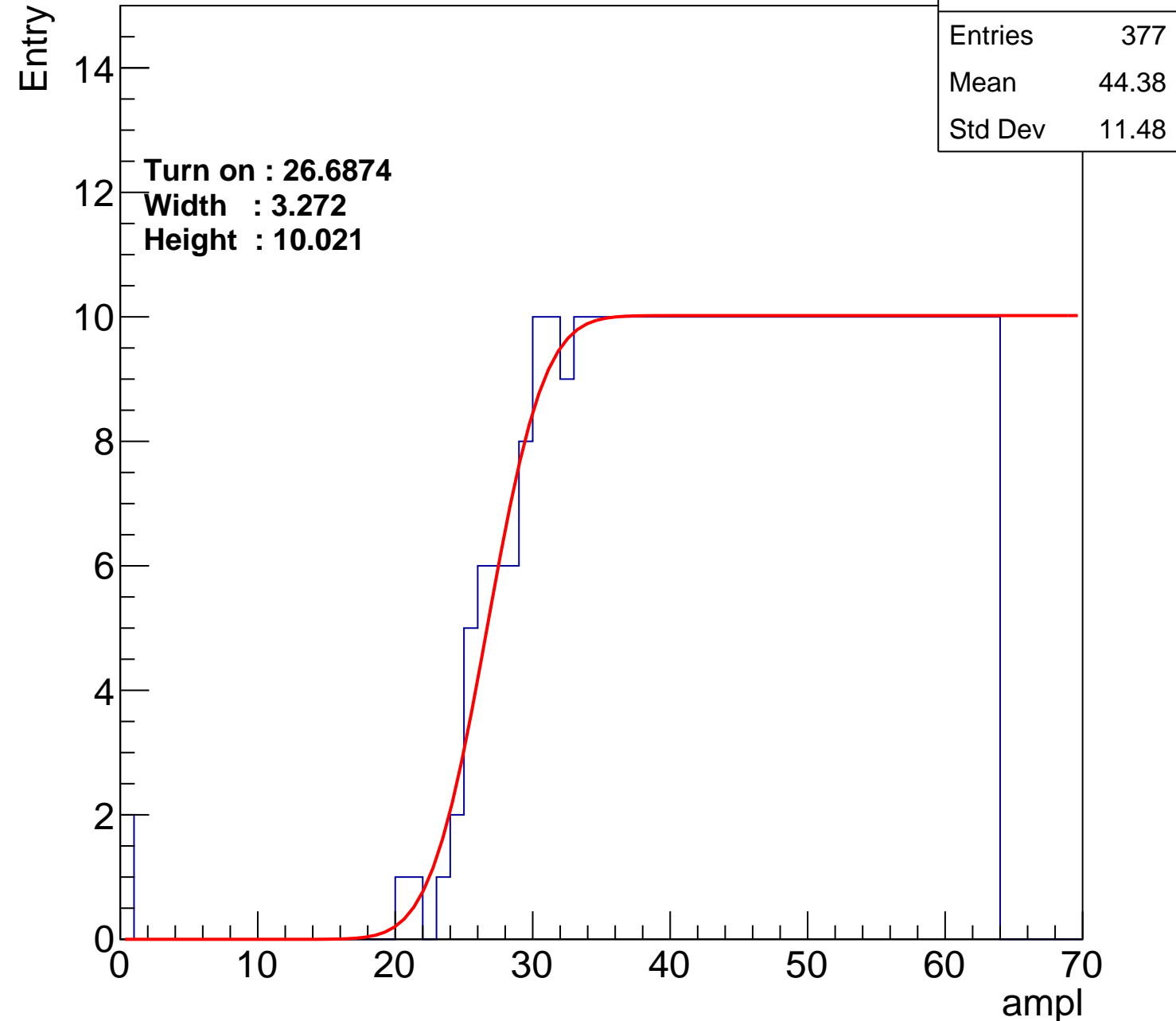
Width : 3.272

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.36

Turn on : 25.9528

Width : 3.090

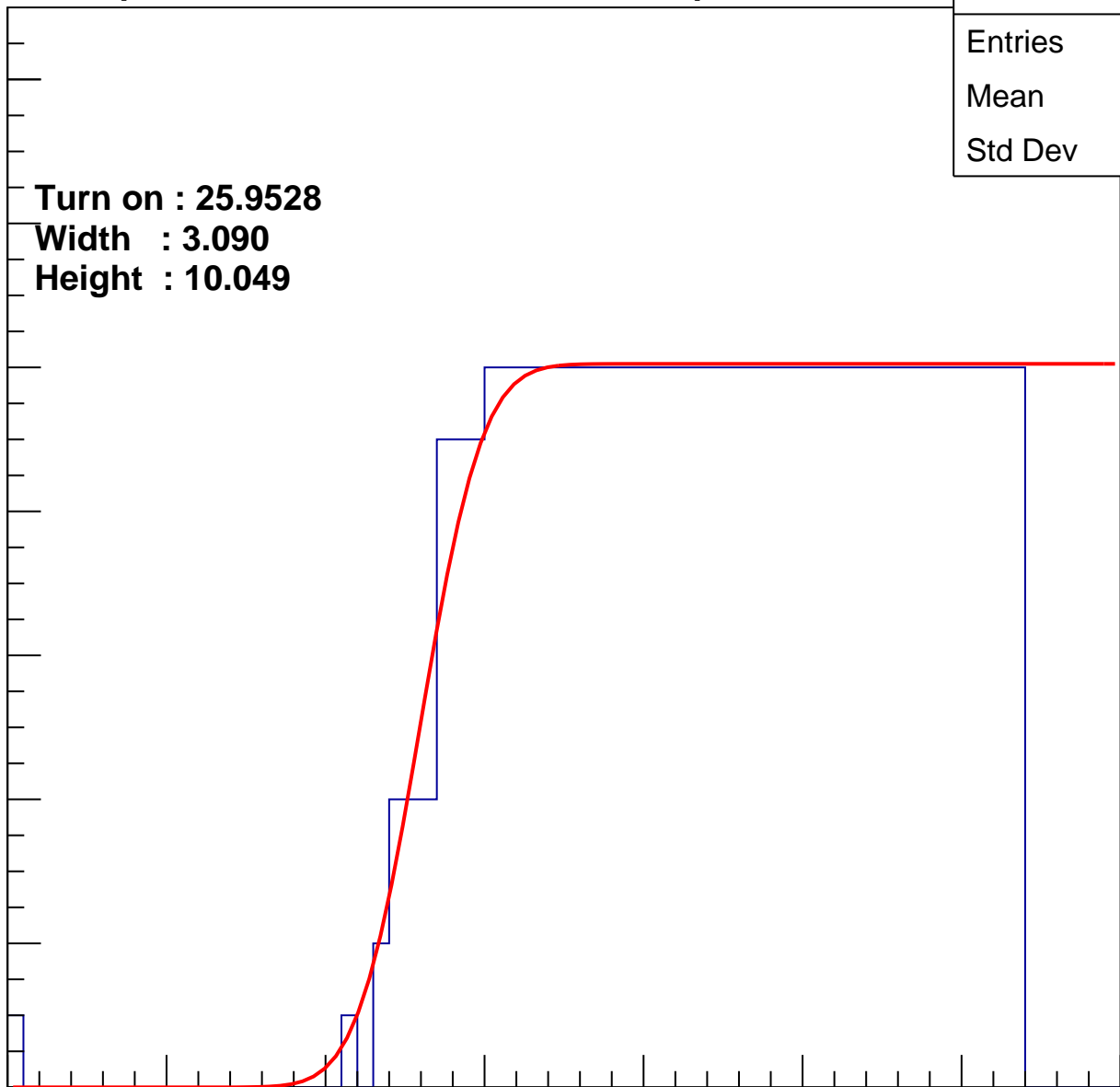
Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U8-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 26.4911

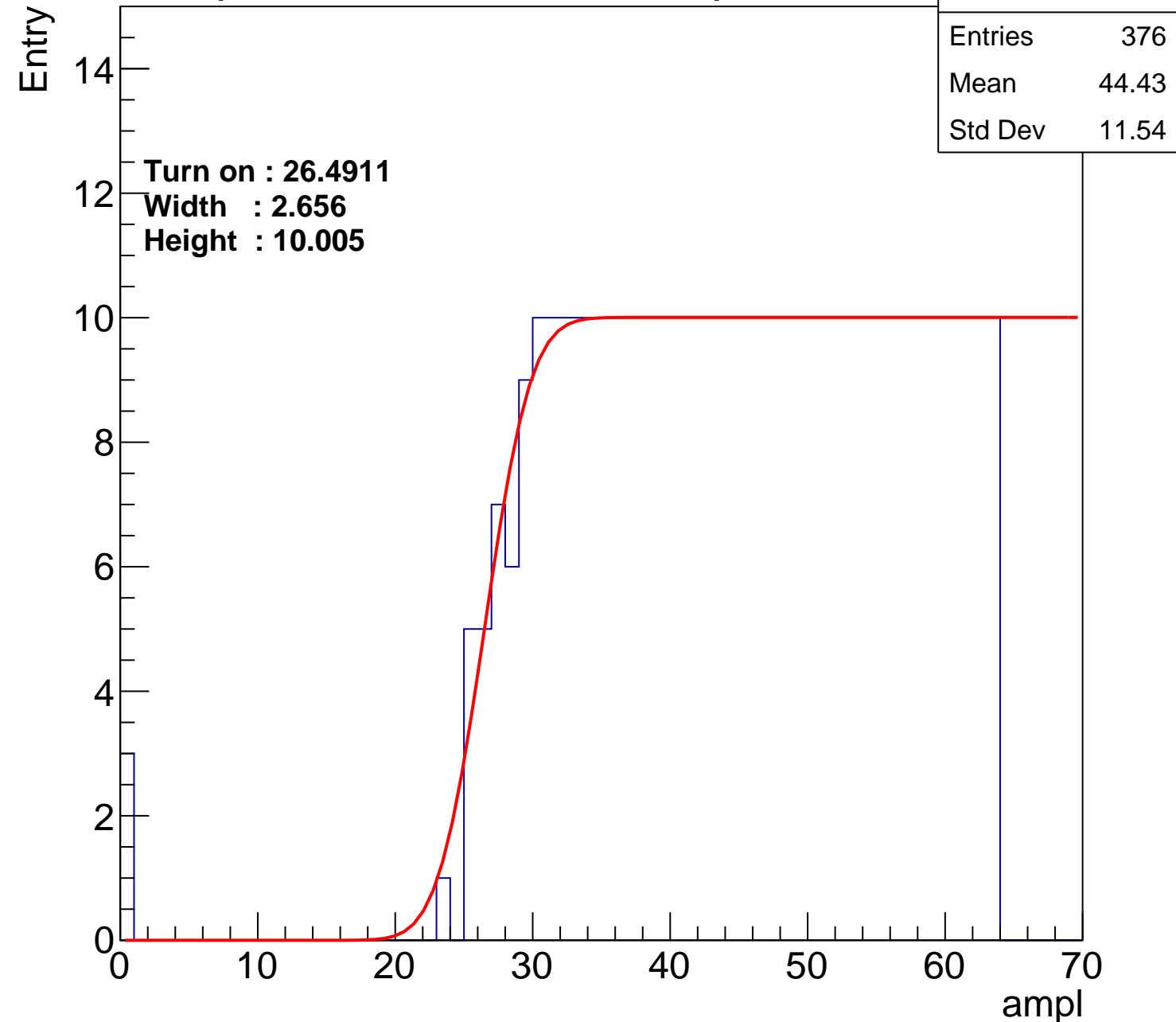
Width : 2.656

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 27.2742

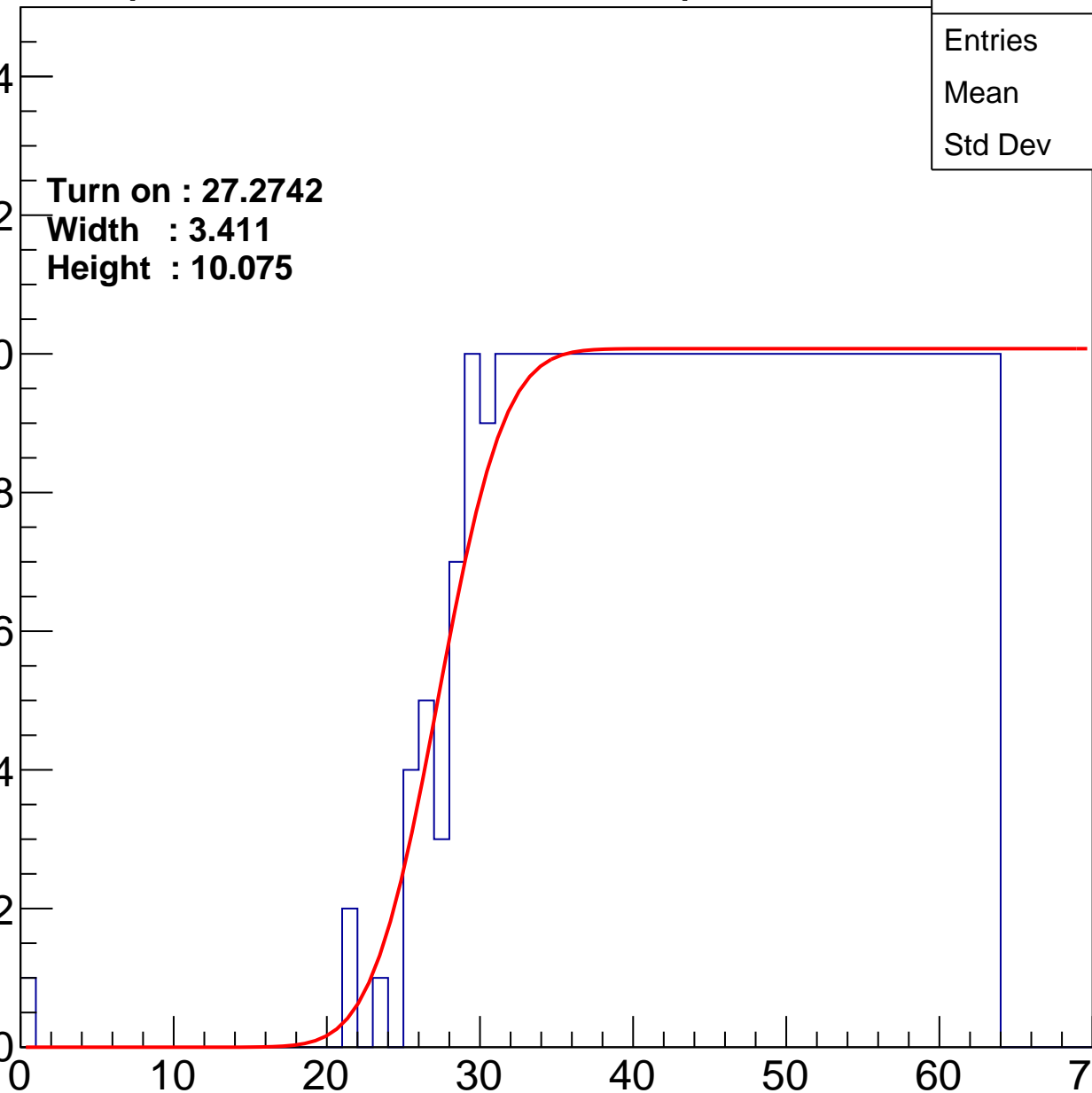
Width : 3.411

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.21
Std Dev	12.14

Turn on : 23.9843

Width : 0.381

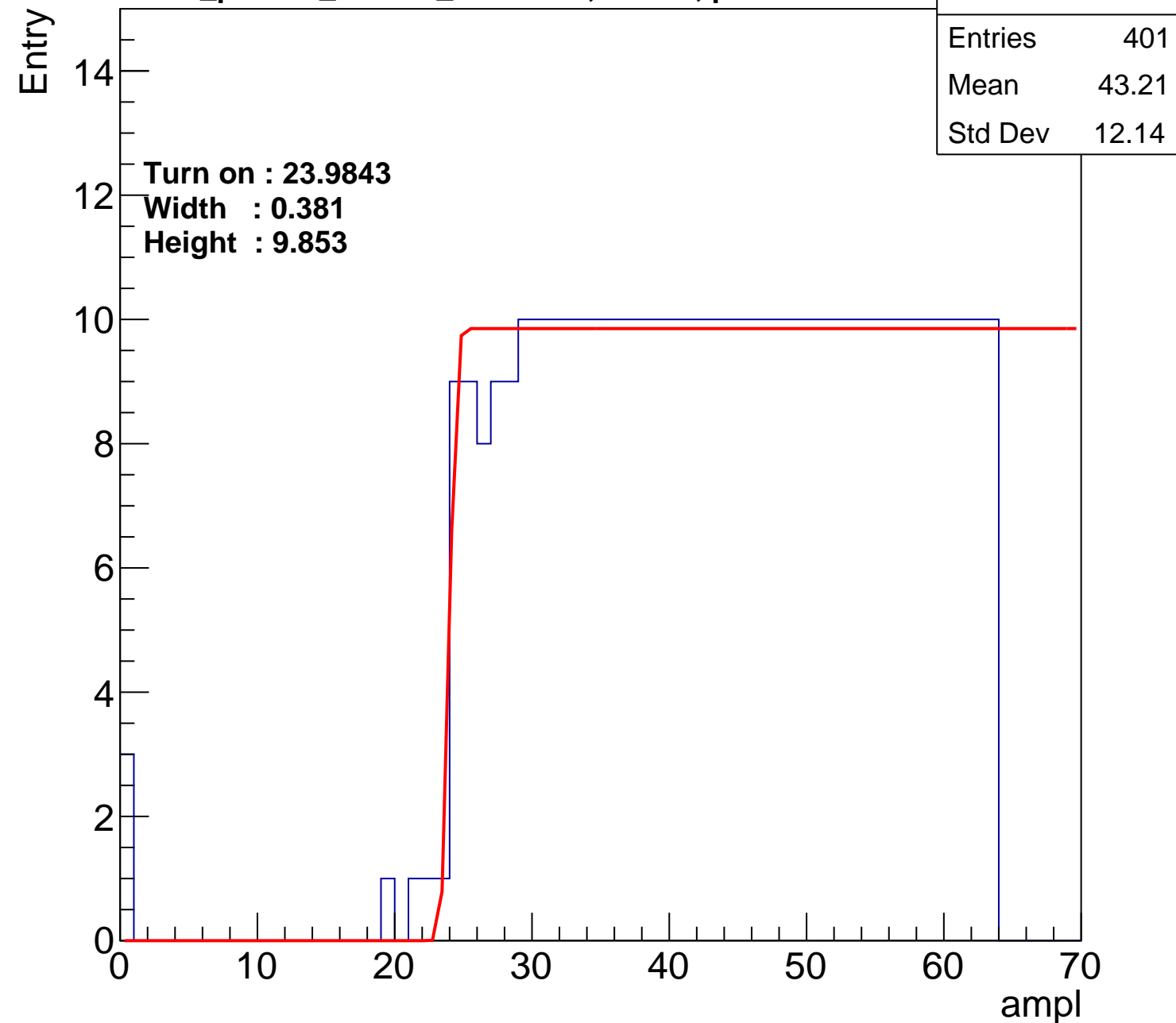
Height : 9.853

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U8-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.72
Std Dev	11.68

Turn on : 25.0504

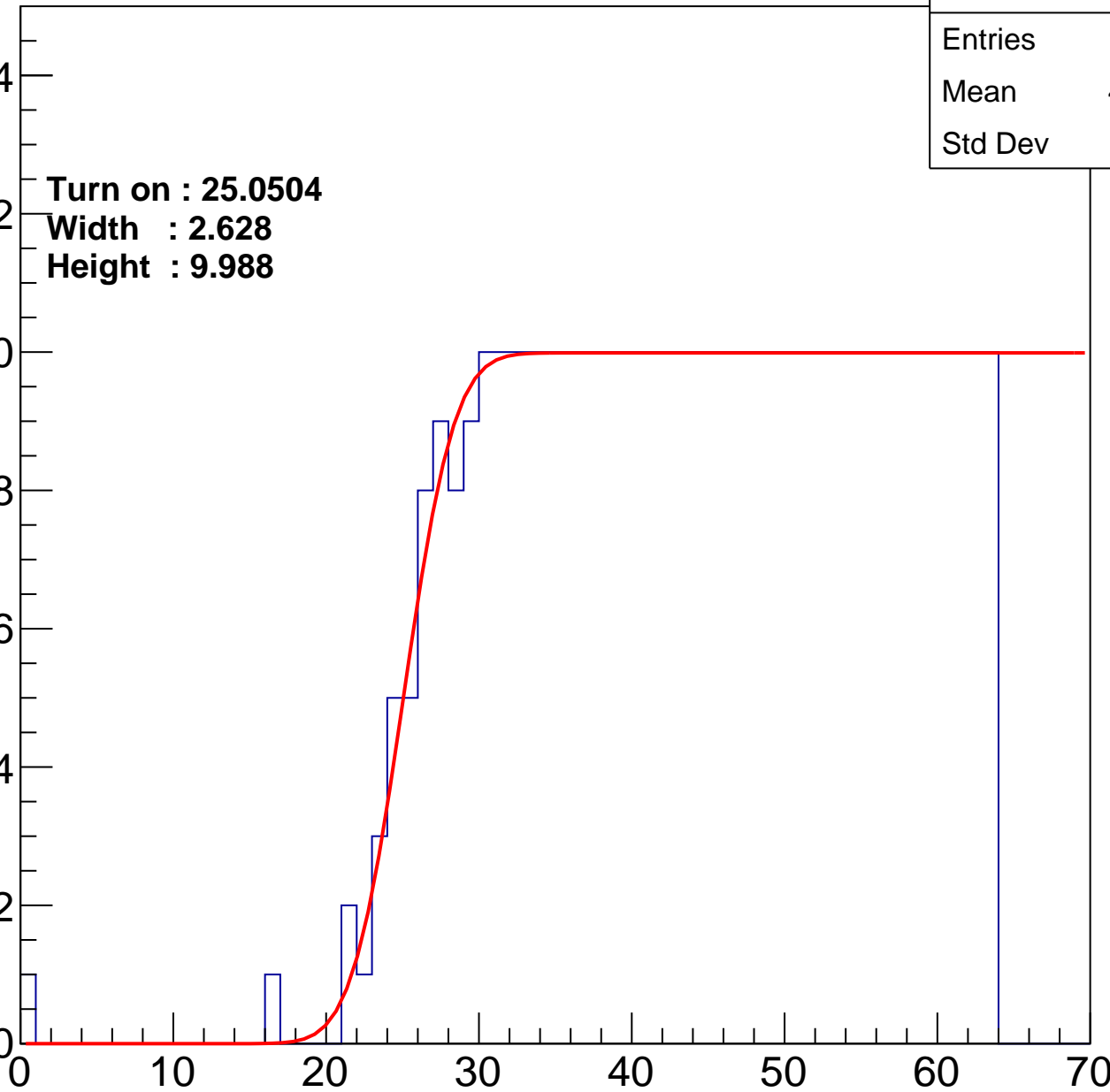
Width : 2.628

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.0513

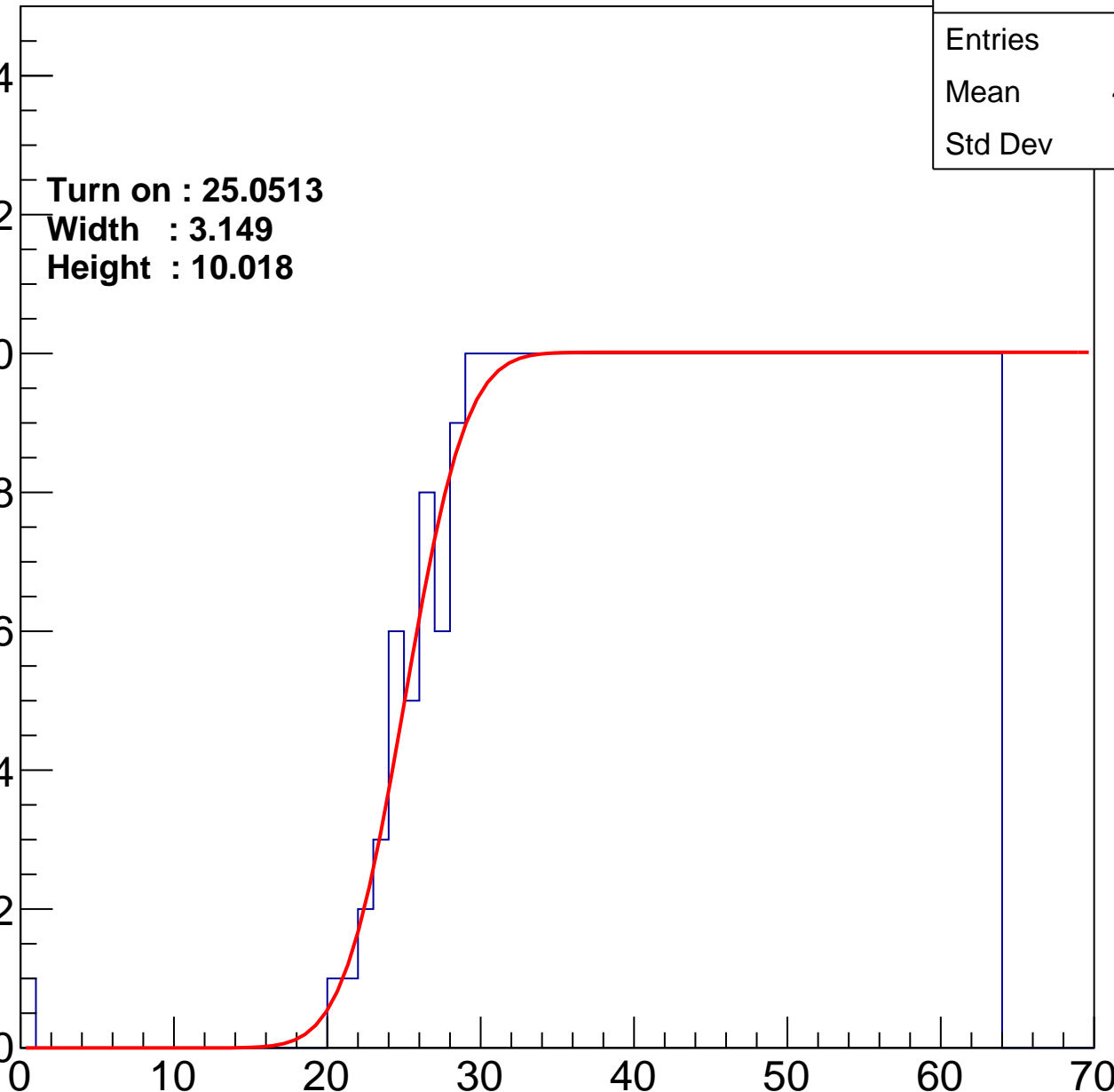
Width : 3.149

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.82
Std Dev	12.46

Turn on : 24.1280

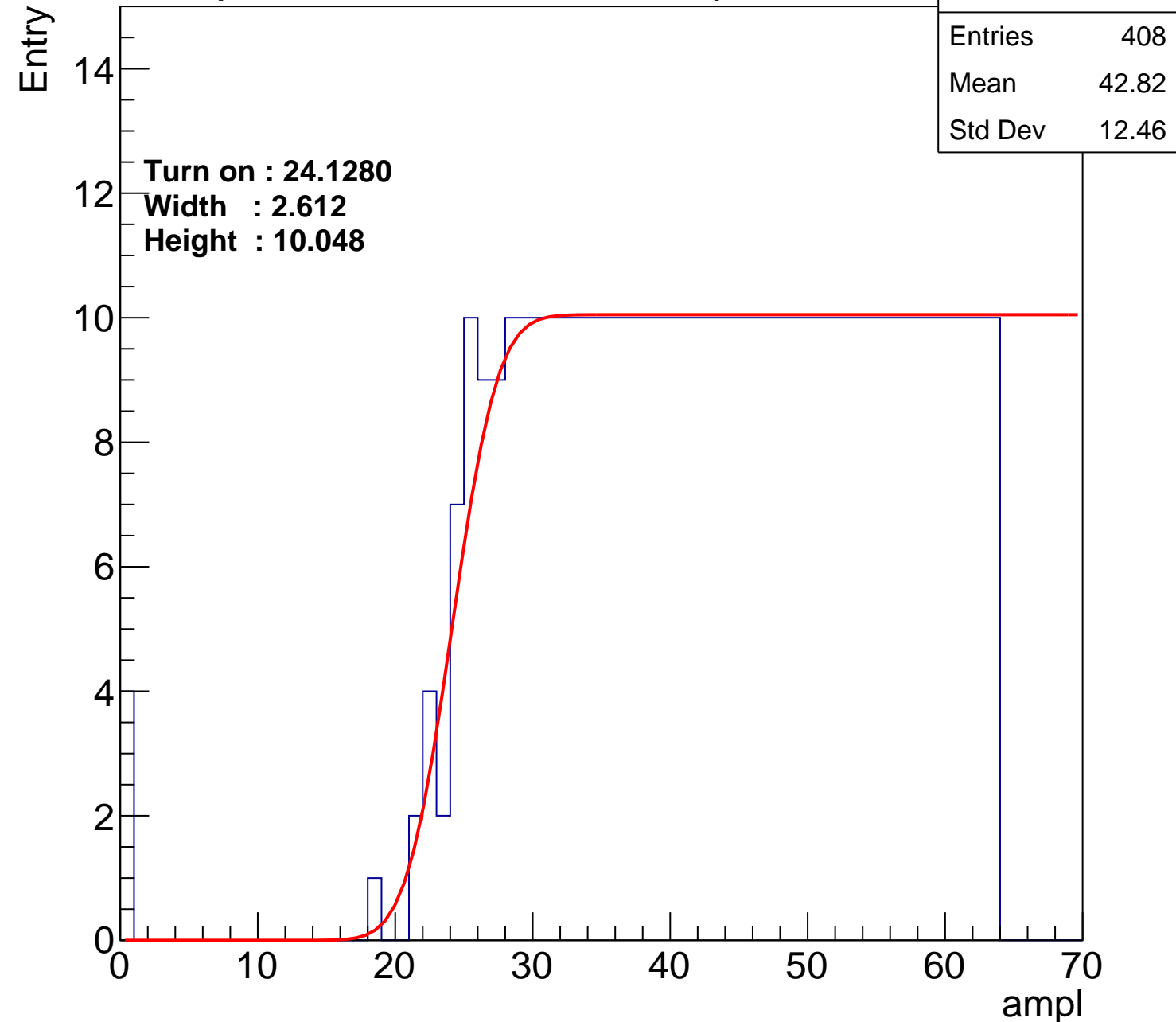
Width : 2.612

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.21
Std Dev	11.35

Turn on : 25.3779

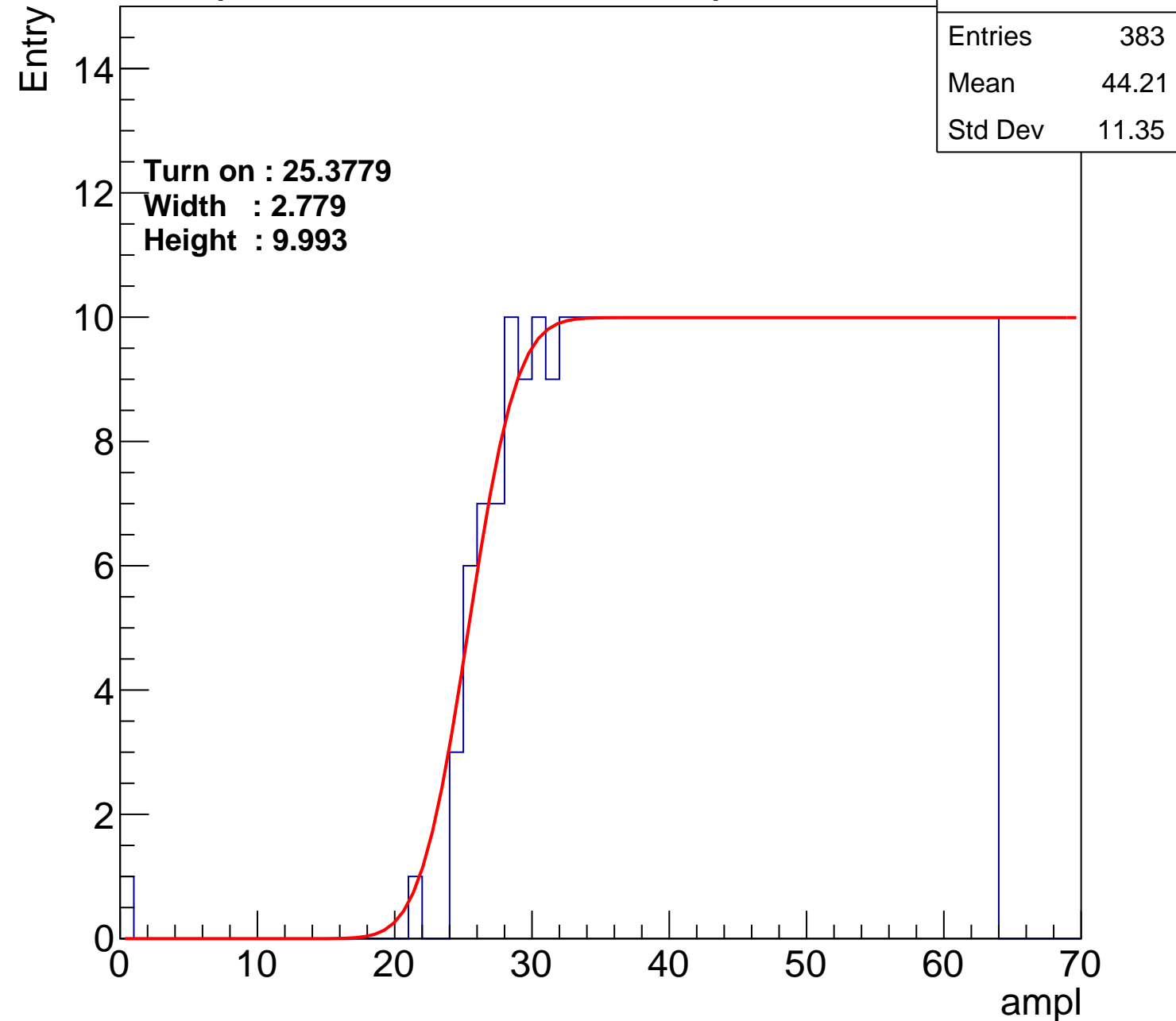
Width : 2.779

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.26
Std Dev	11.39

Turn on : 25.7699

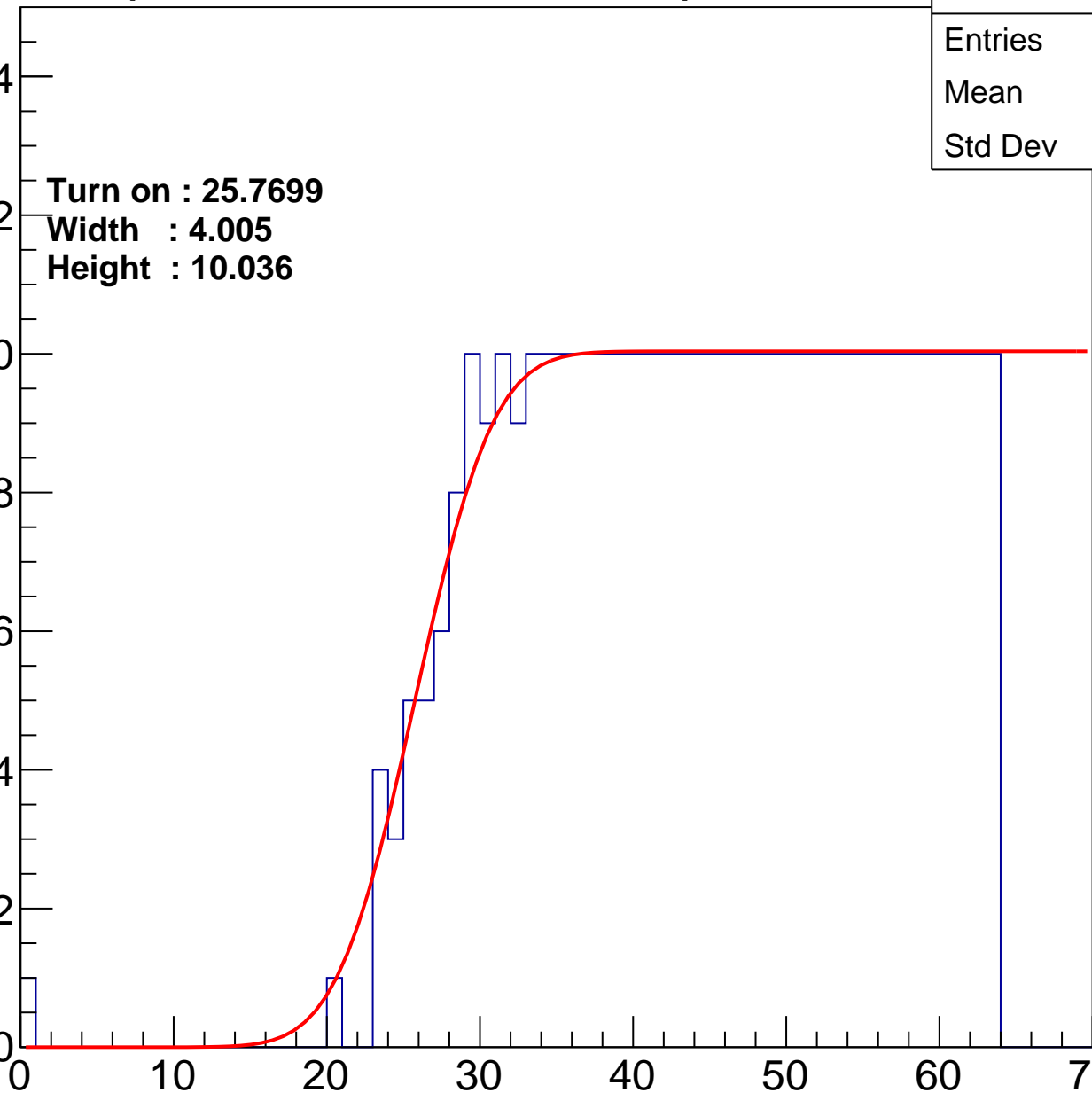
Width : 4.005

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.9
Std Dev	10.99

Turn on : 27.2356

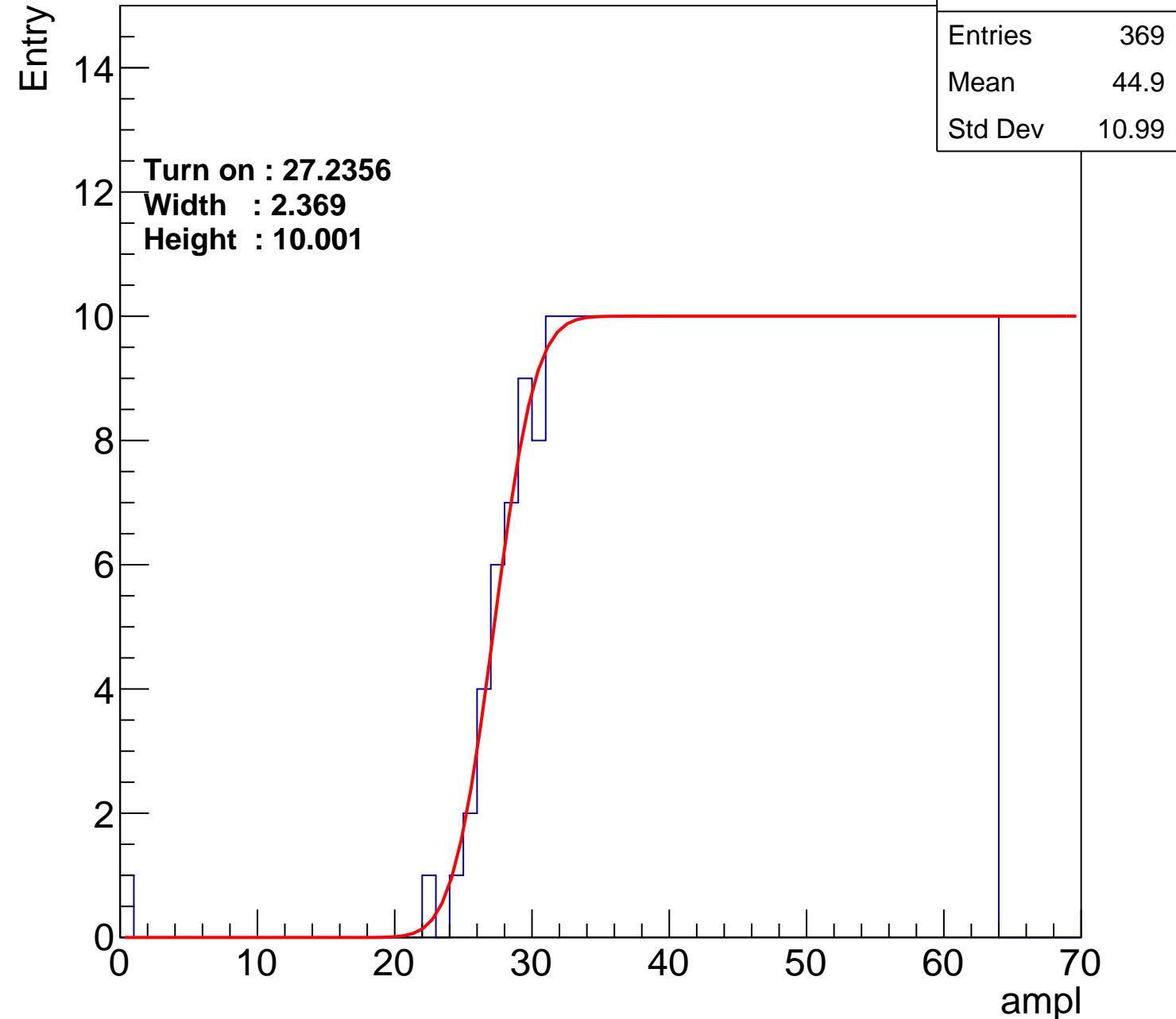
Width : 2.369

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.75
Std Dev	11.07

Turn on : 27.3871

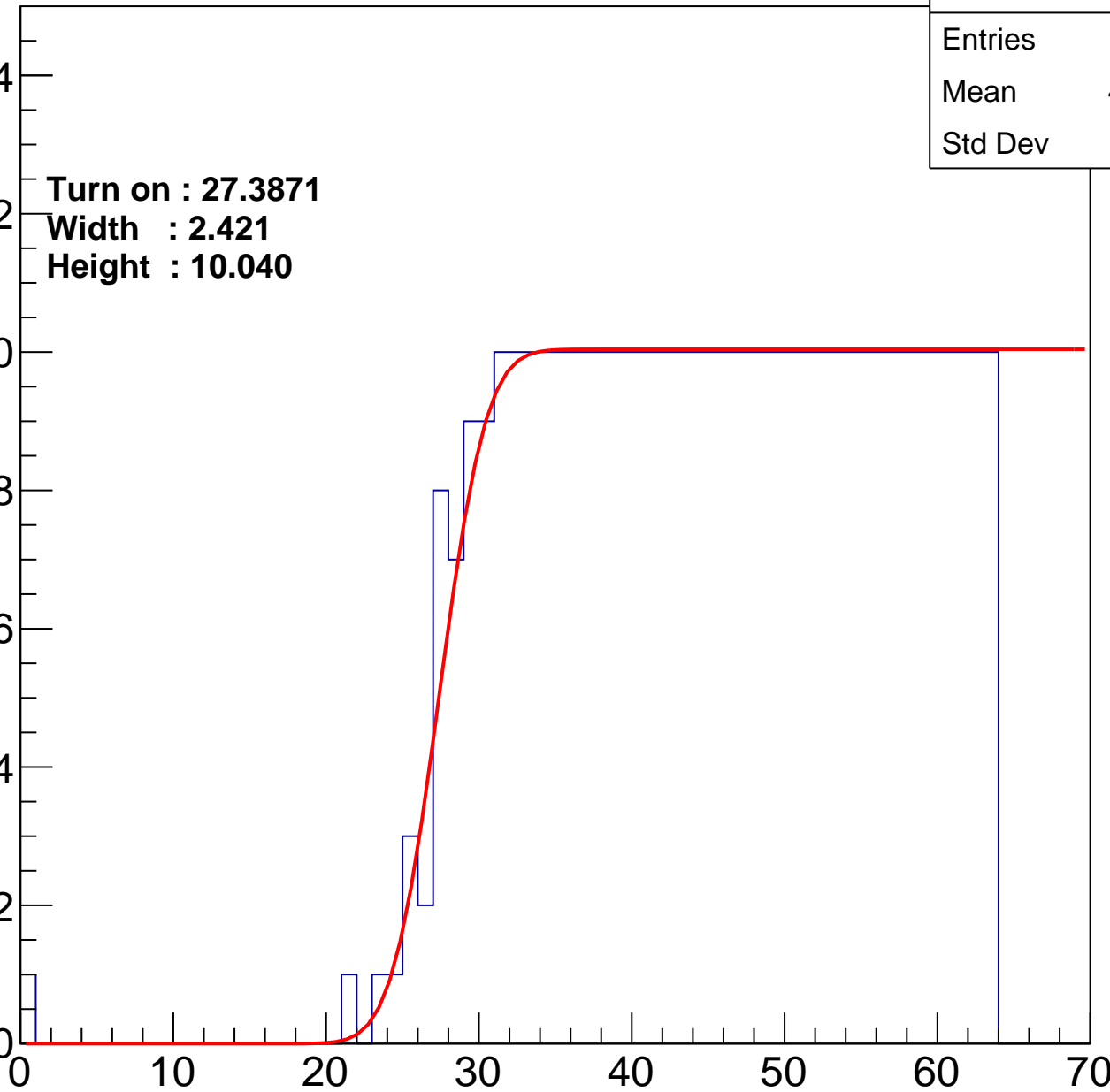
Width : 2.421

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 27.0032

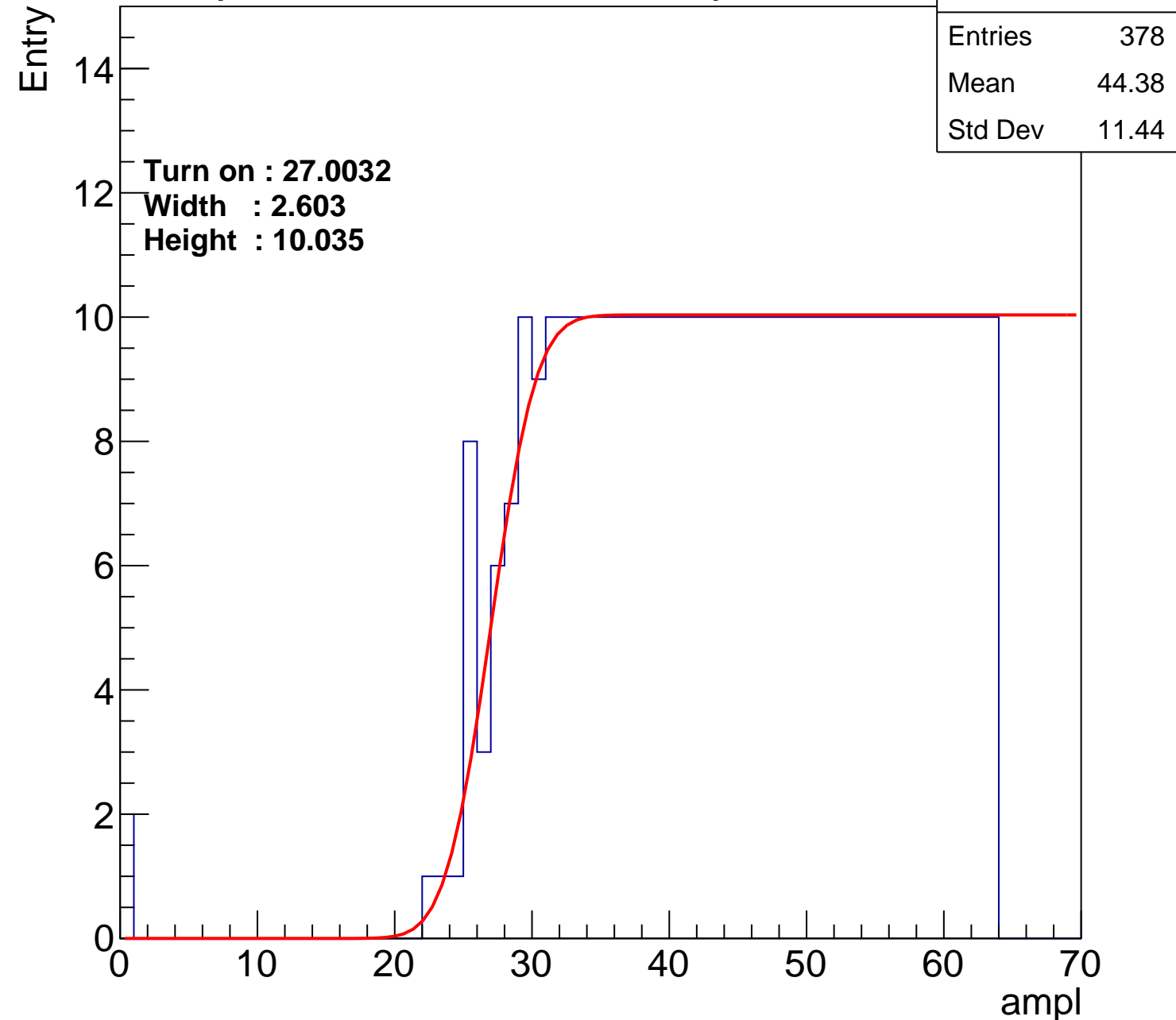
Width : 2.603

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch112

calib_packv5_042523_0143.root, FC#10, port B3

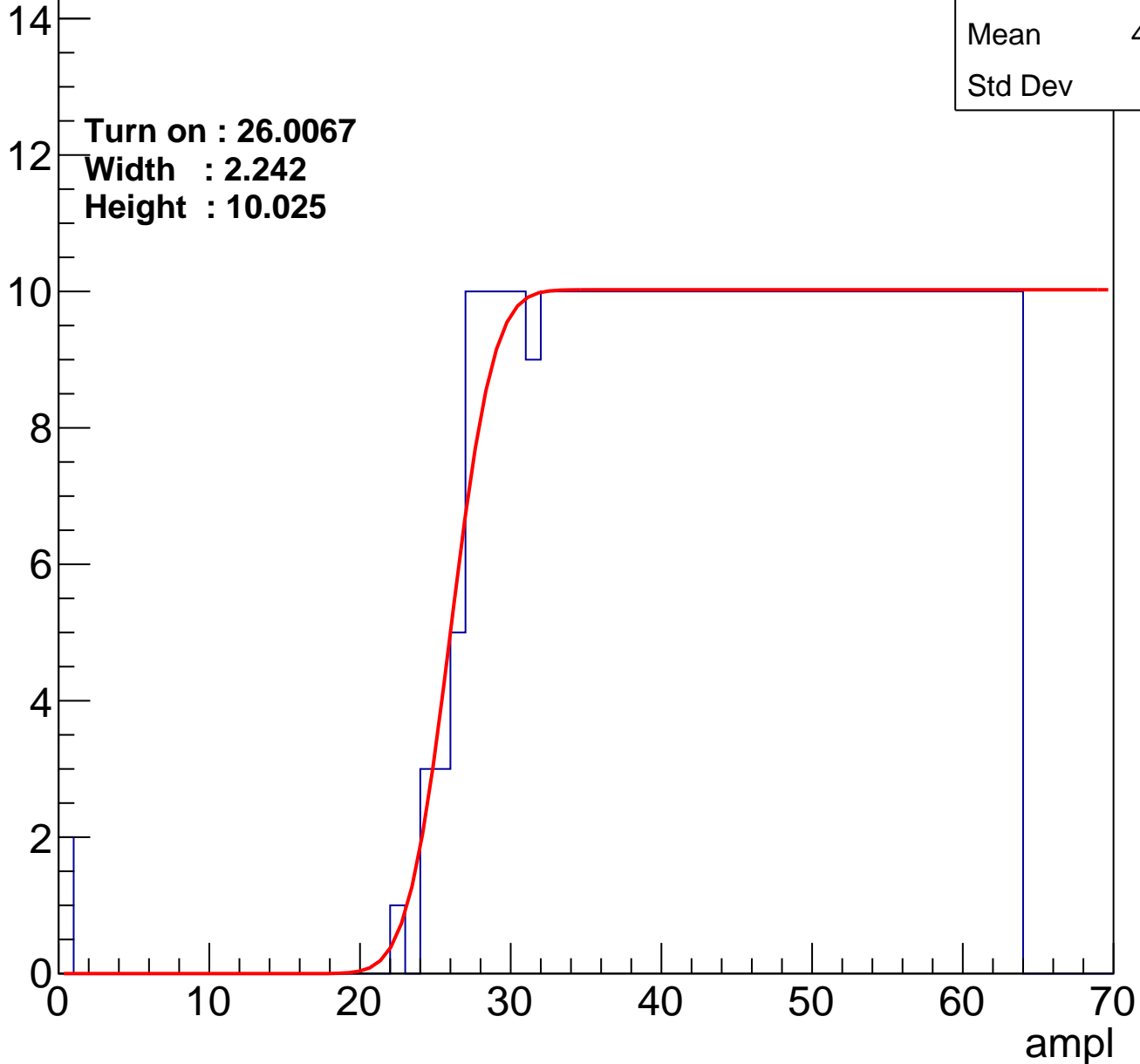
Entries	383
Mean	44.17
Std Dev	11.5

Turn on : 26.0067

Width : 2.242

Height : 10.025

Entry



B1L002S, U8-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.29

Turn on : 25.4134

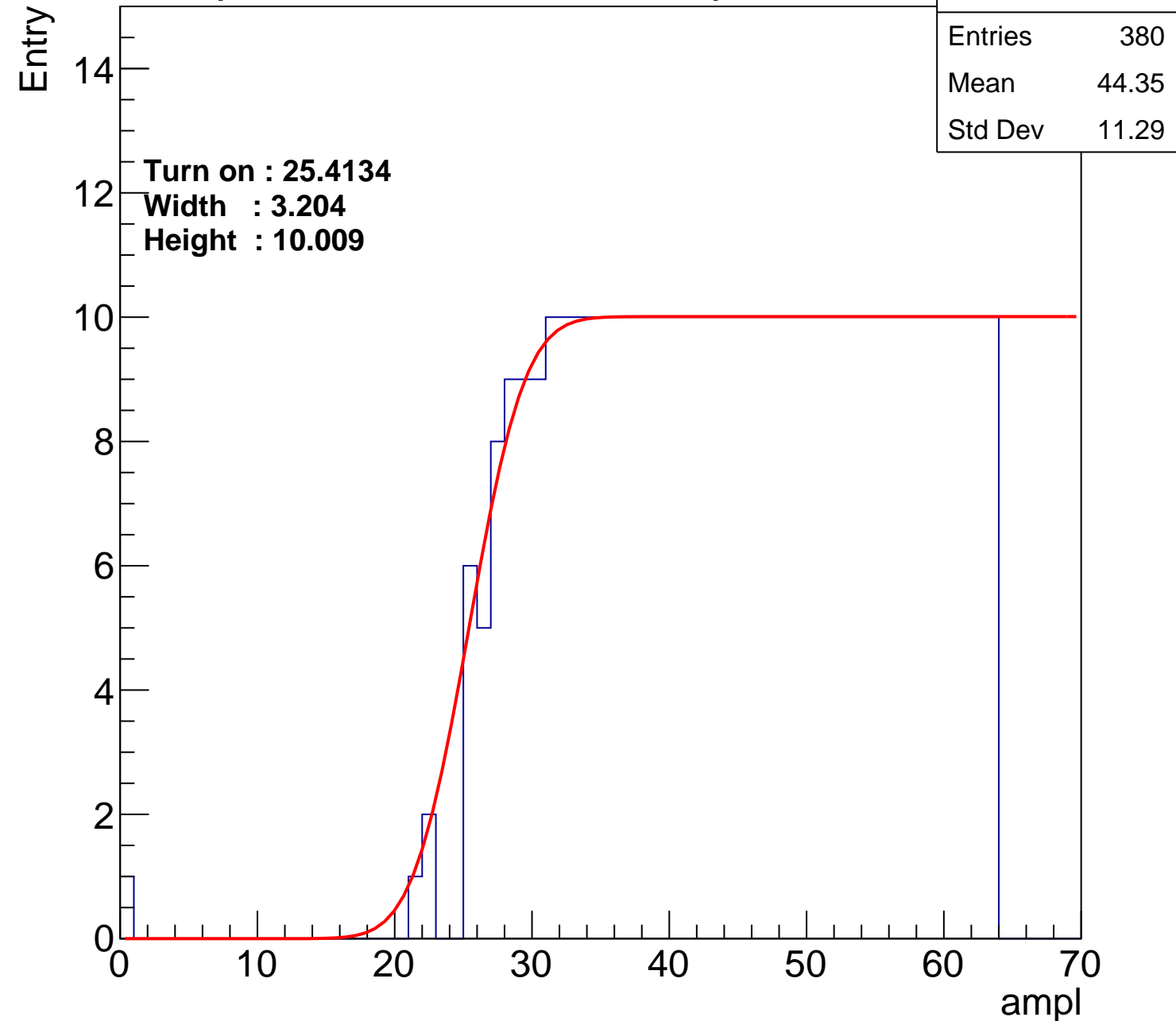
Width : 3.204

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl

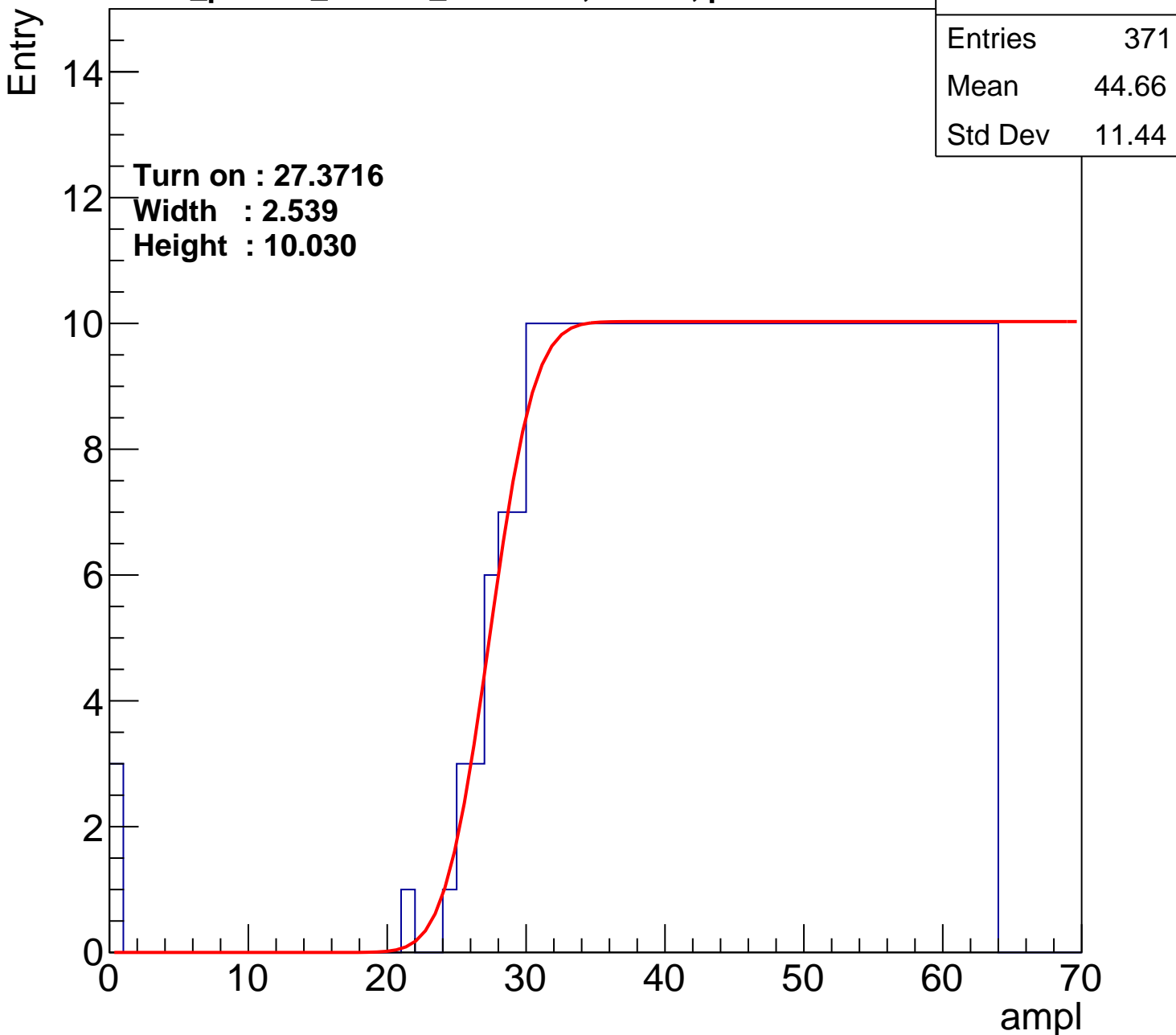


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.66
Std Dev	11.44

Height : 10.030



B1L002S, U8-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.01
Std Dev	11.47

Turn on : 25.4754

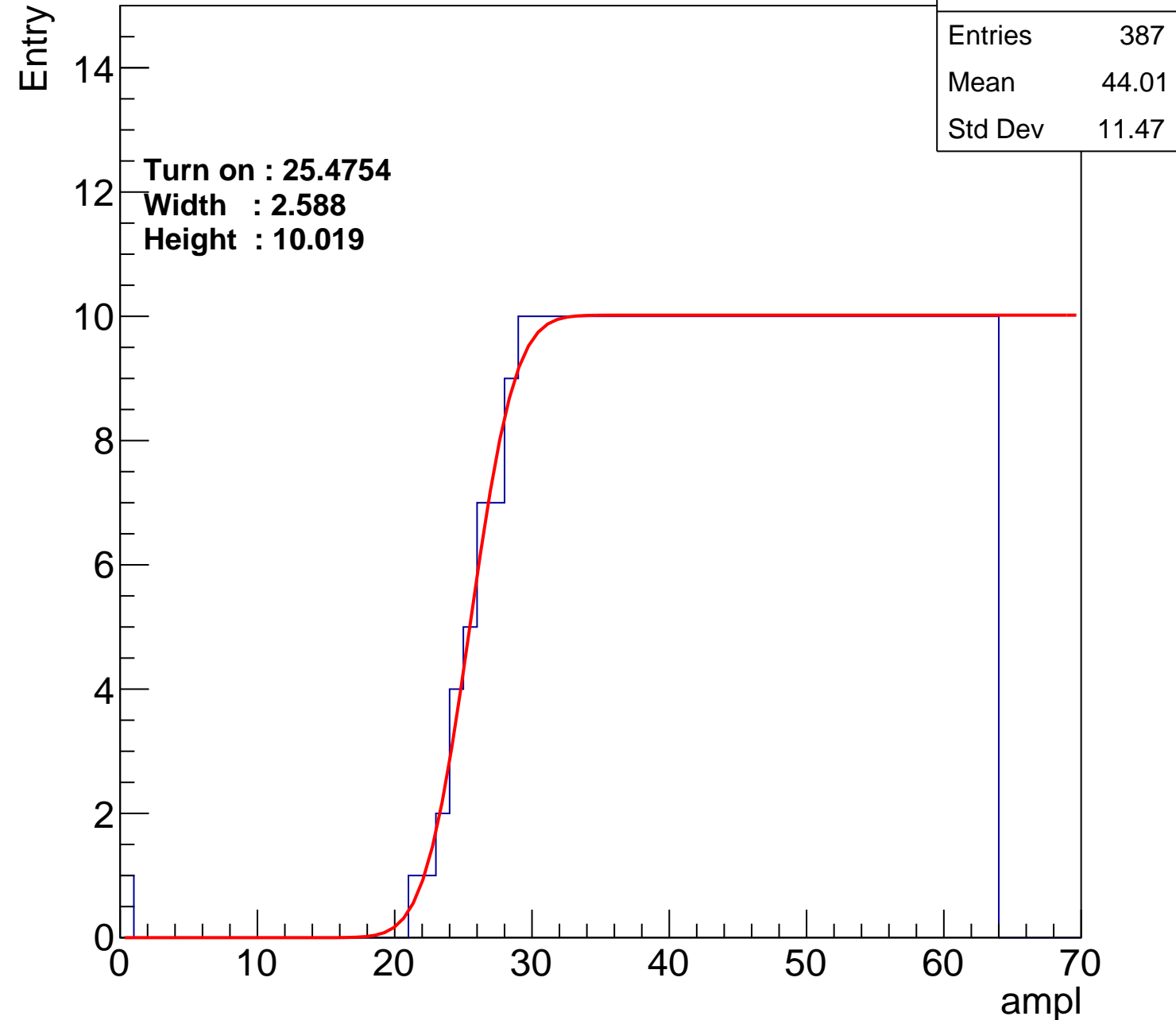
Width : 2.588

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch116

calib_packv5_042523_0143.root, FC#10, port B3

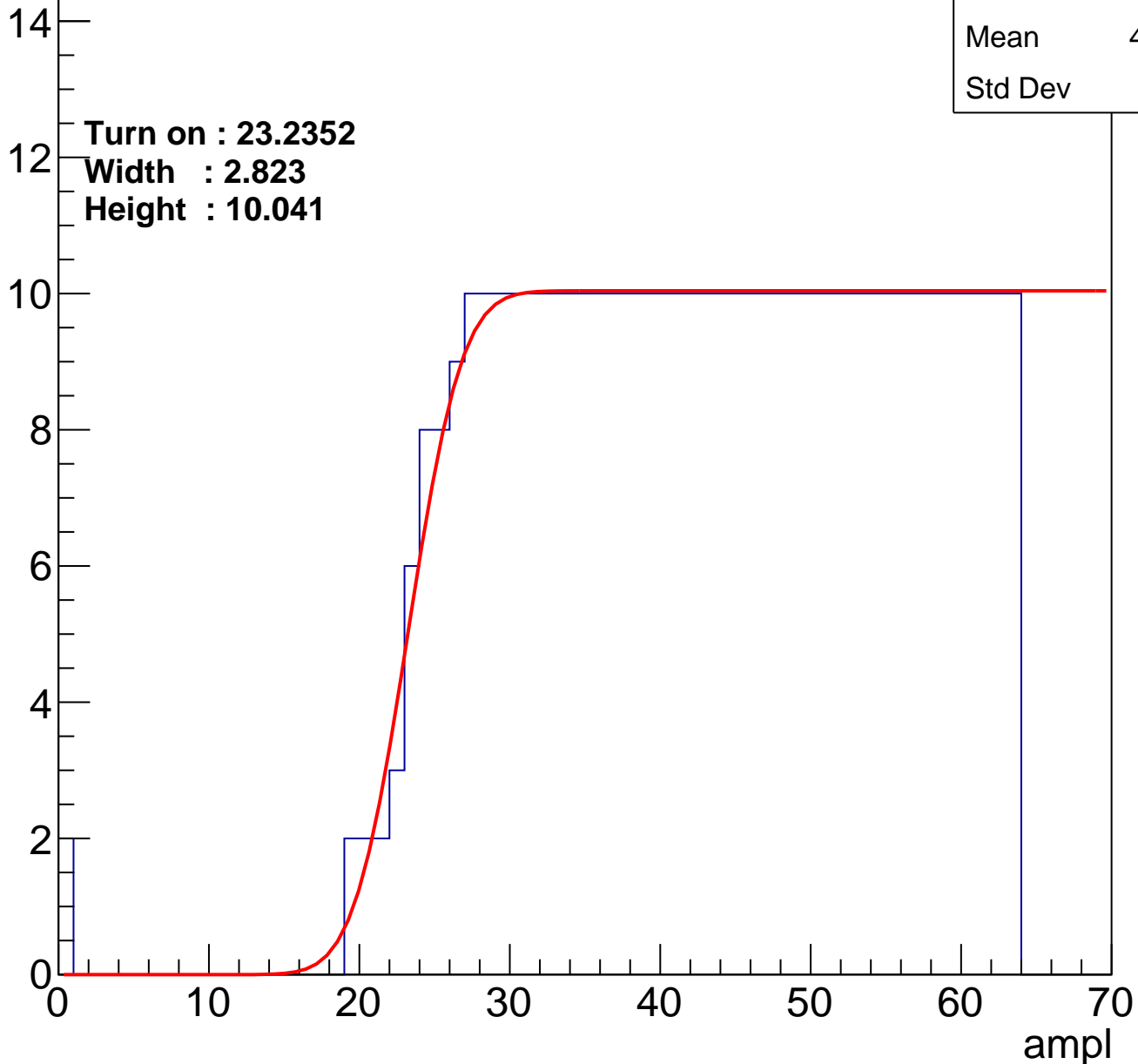
Entries	412
Mean	42.72
Std Dev	12.3

Turn on : 23.2352

Width : 2.823

Height : 10.041

Entry



B1L002S, U8-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.17
Std Dev	12.21

Turn on : 24.7787

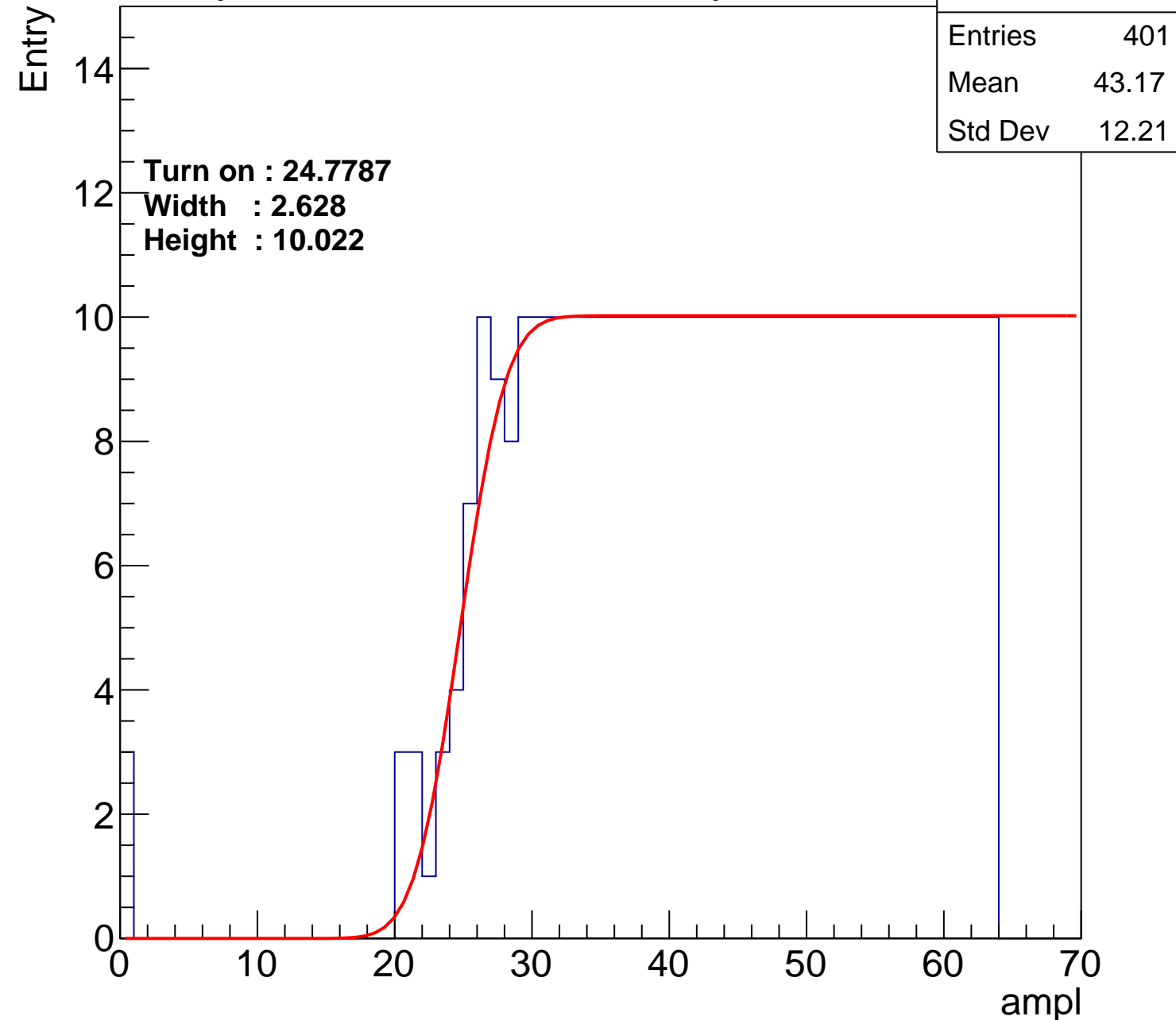
Width : 2.628

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.92
Std Dev	11.7

Turn on : 25.8186

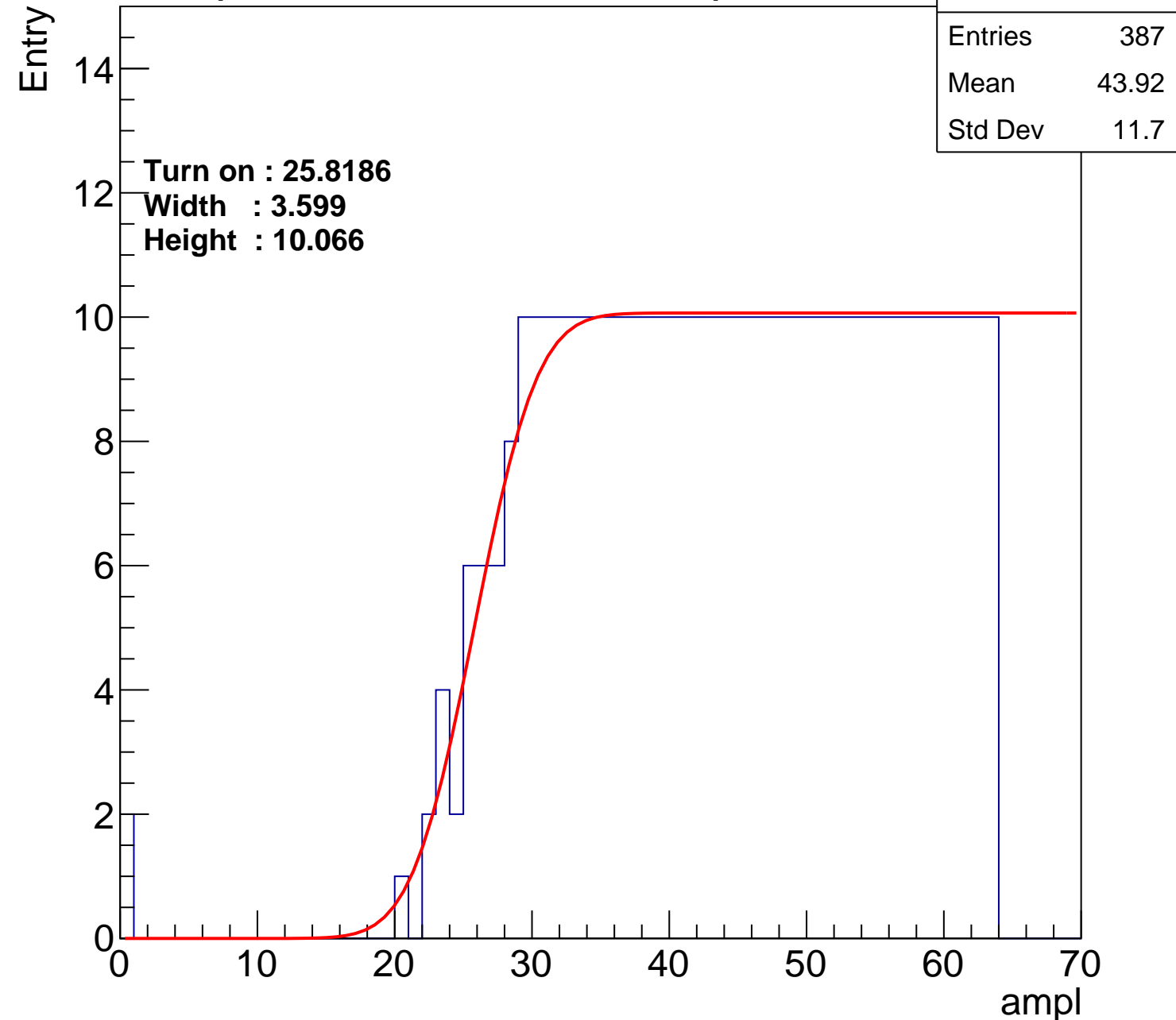
Width : 3.599

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.94
Std Dev	11.48

Turn on : 25.3431

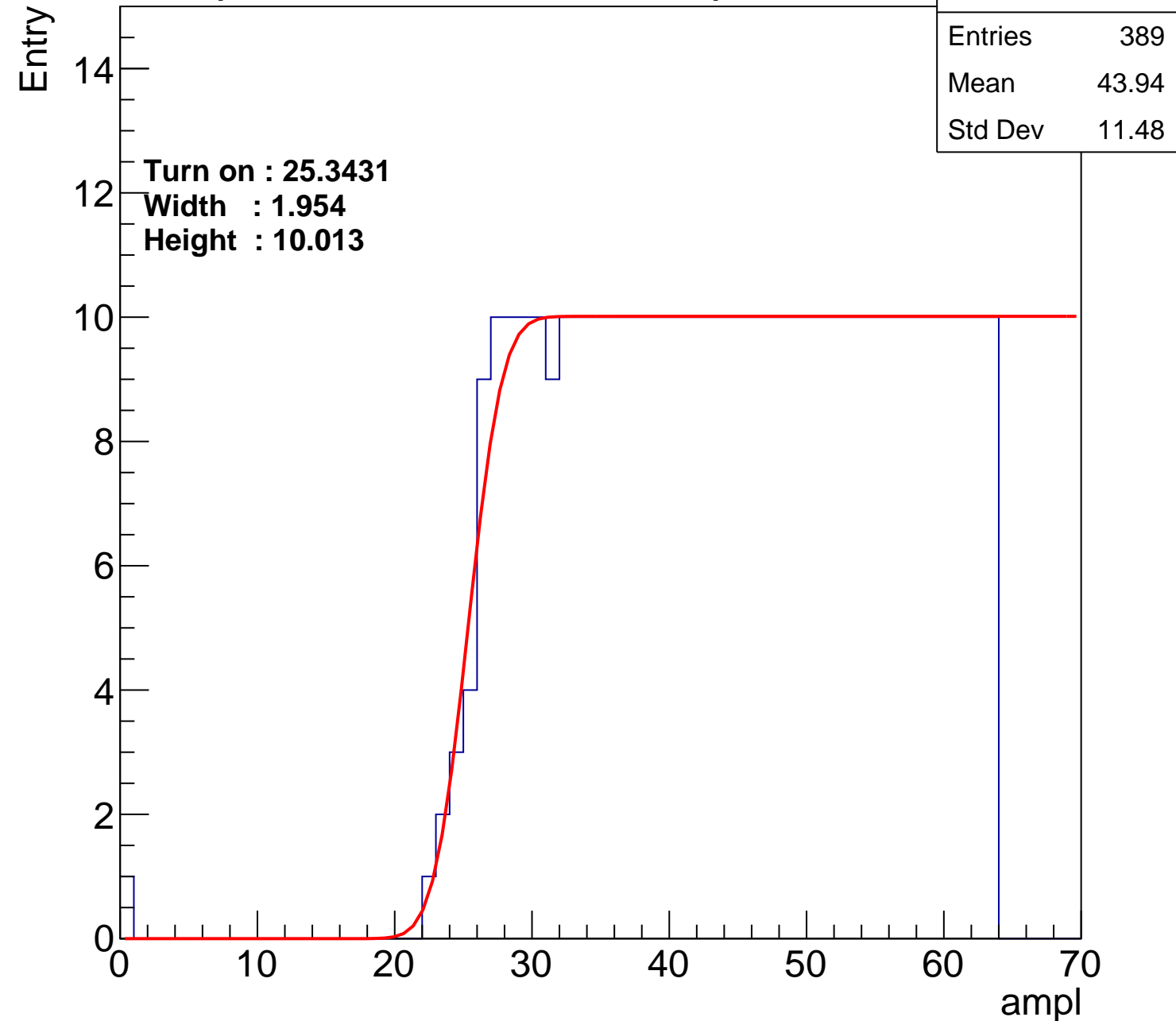
Width : 1.954

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 25.6545

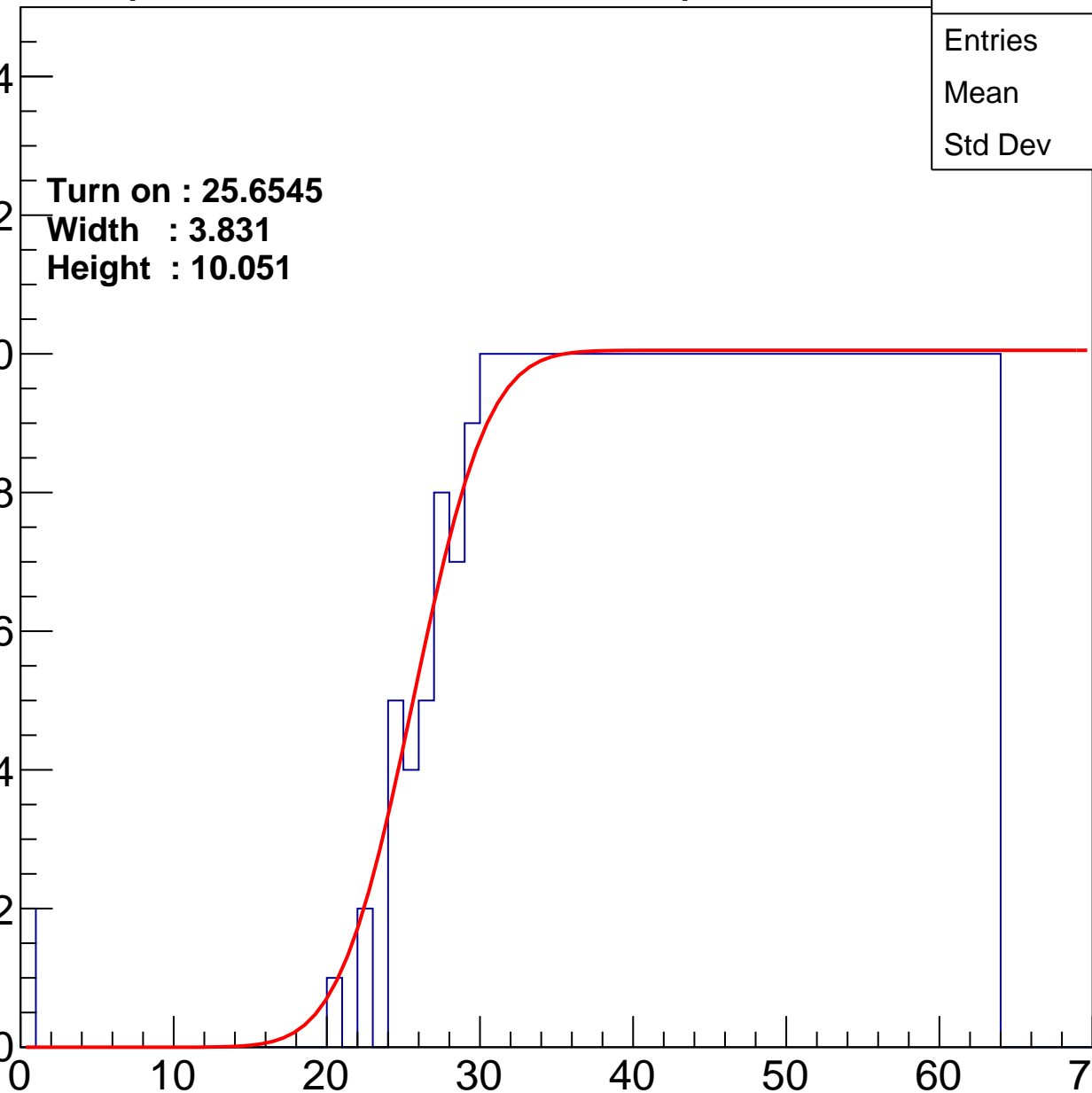
Width : 3.831

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.23
Std Dev	11.81

Turn on : 26.7450

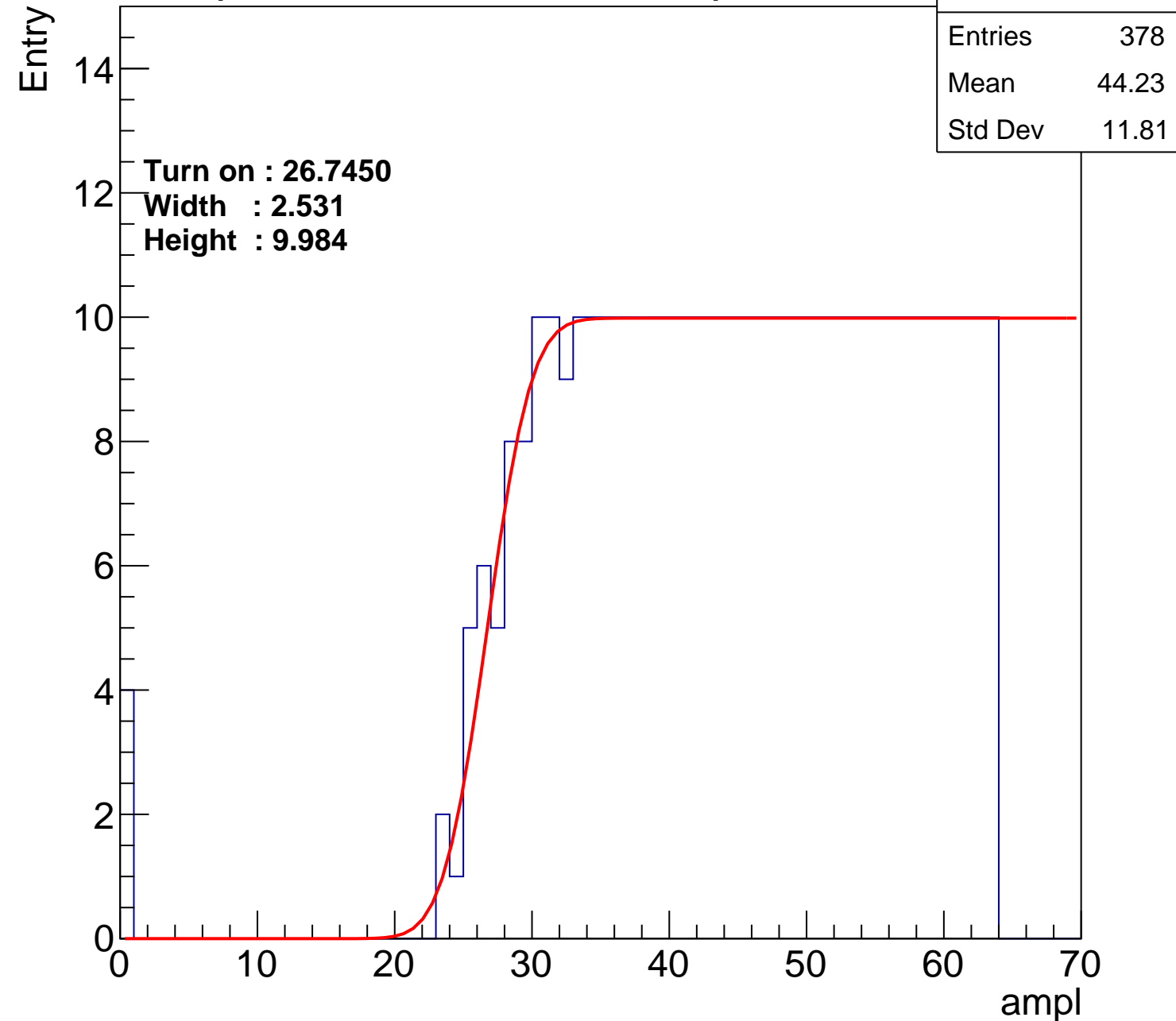
Width : 2.531

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.98
Std Dev	11.53

Turn on : 25.4773

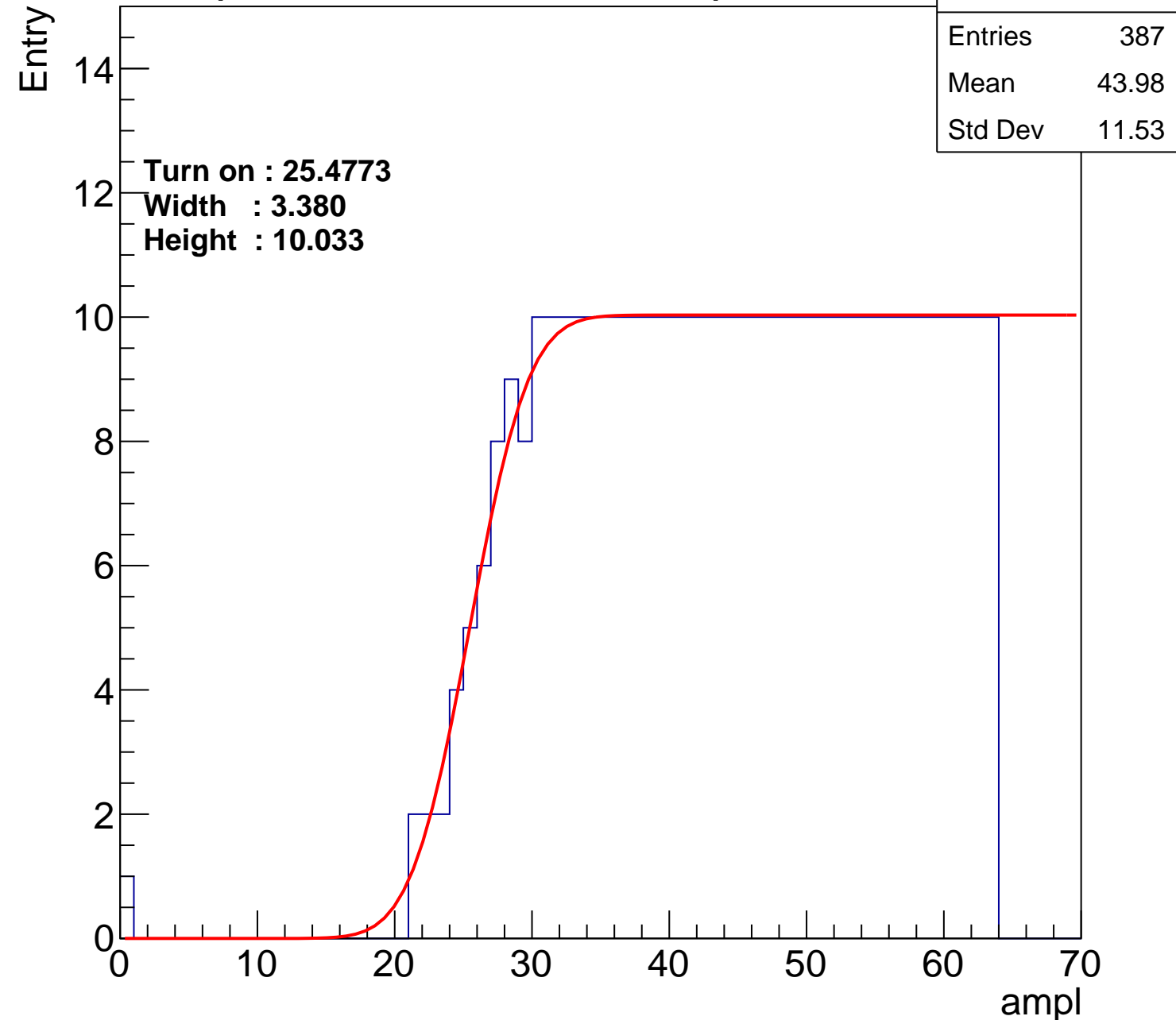
Width : 3.380

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.8
Std Dev	11.97

Turn on : 25.4788

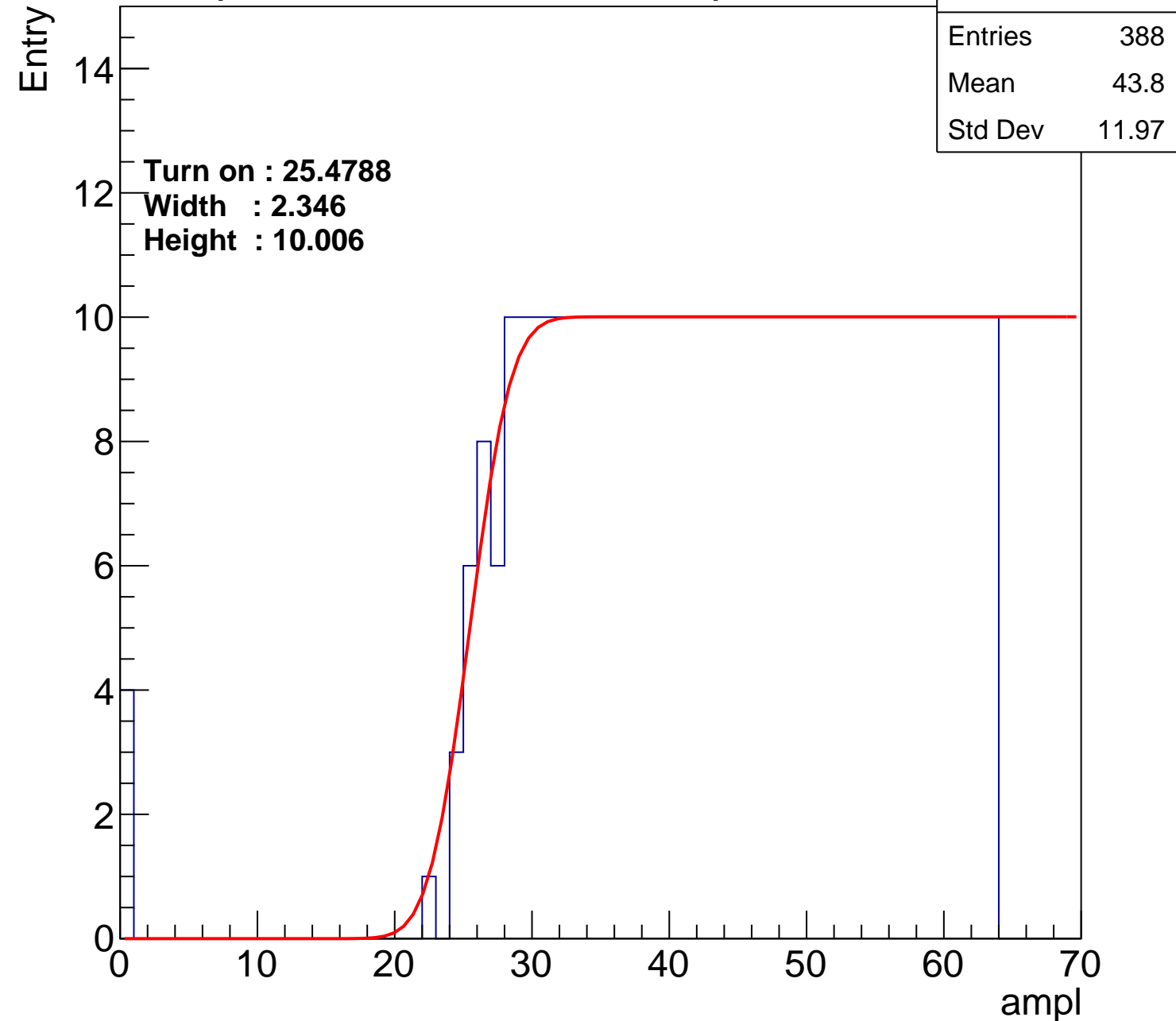
Width : 2.346

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.47
Std Dev	11.95

Turn on : 25.0955

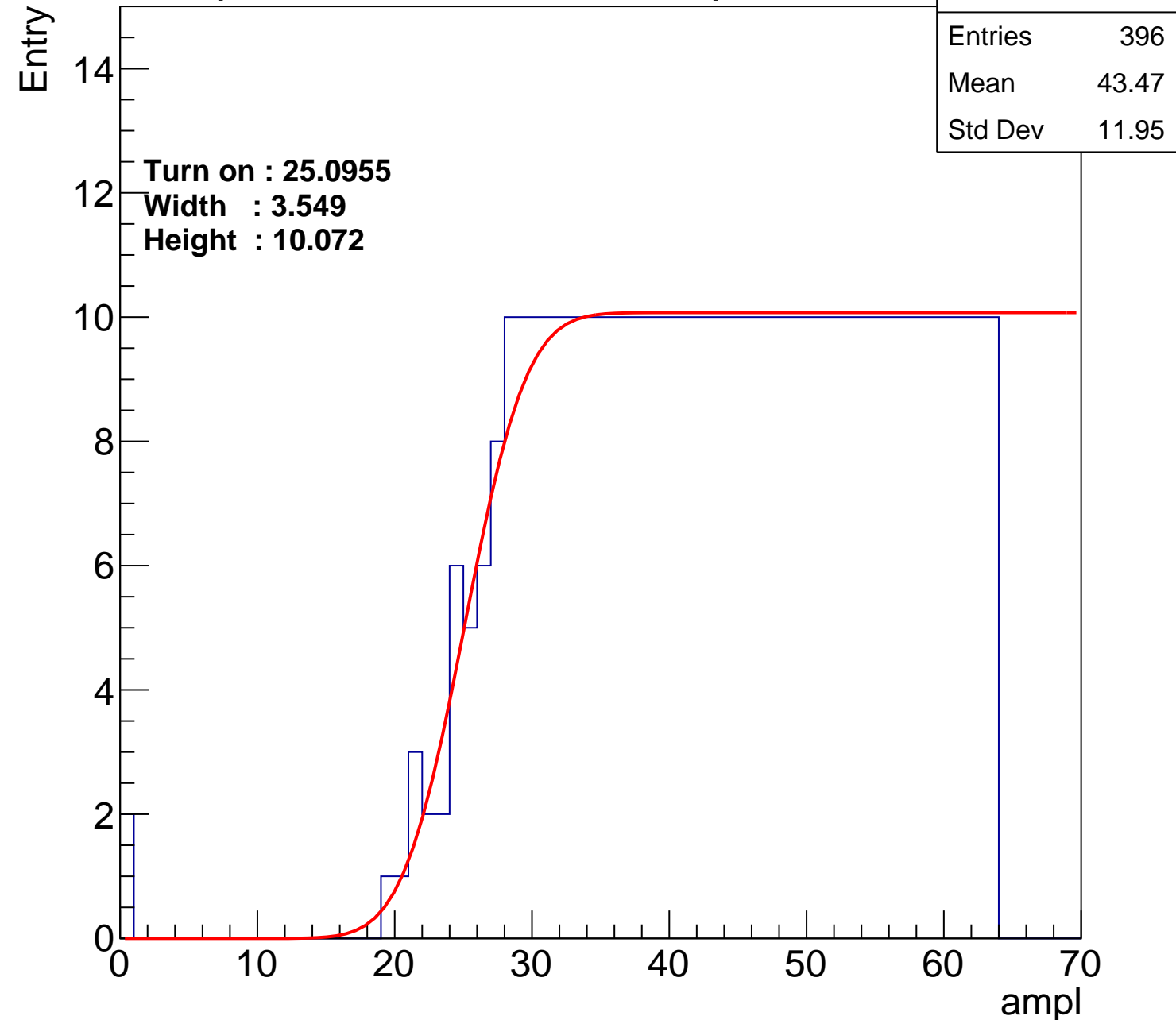
Width : 3.549

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.53
Std Dev	11.49

Turn on : 27.7098

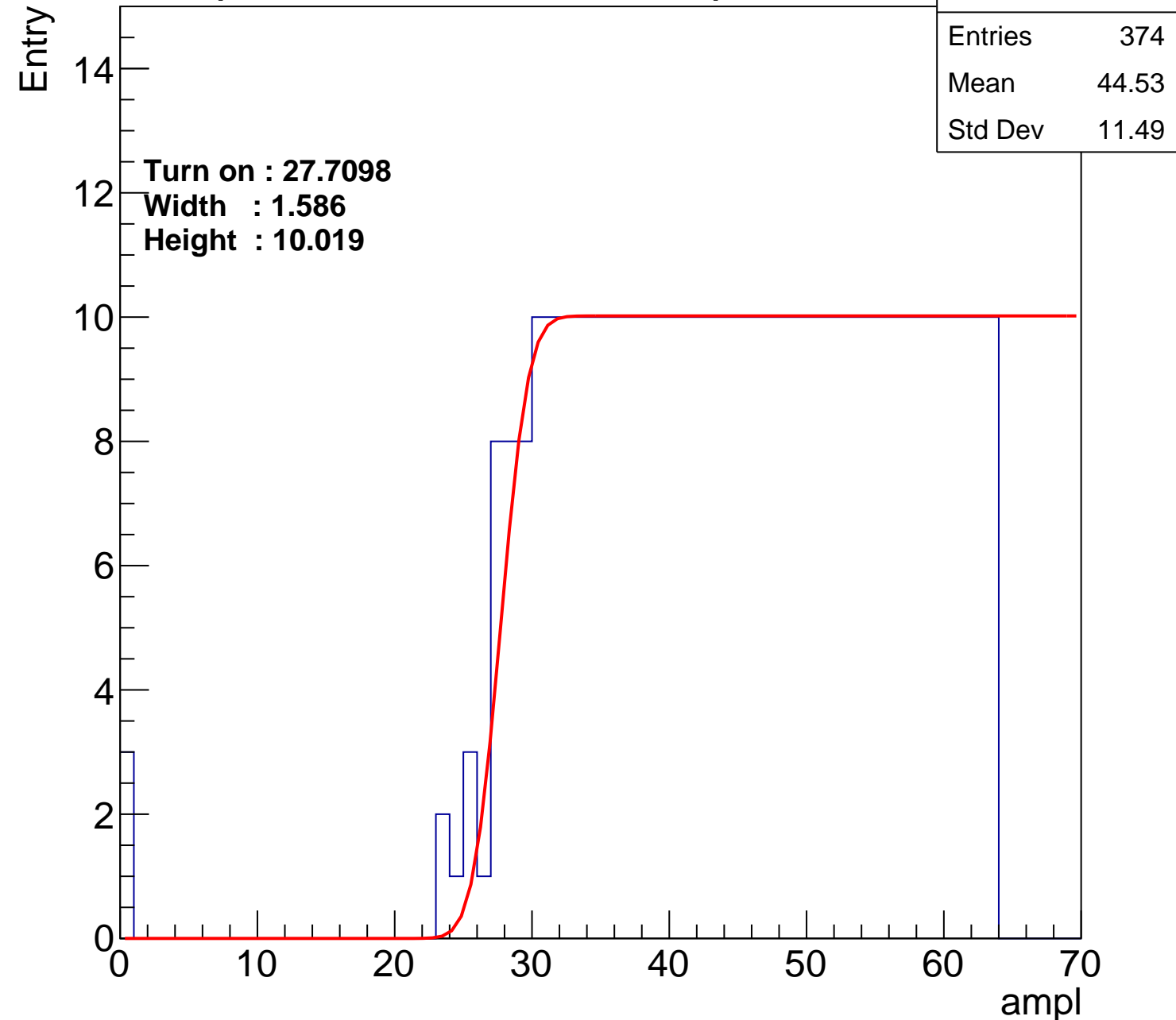
Width : 1.586

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 24.7510

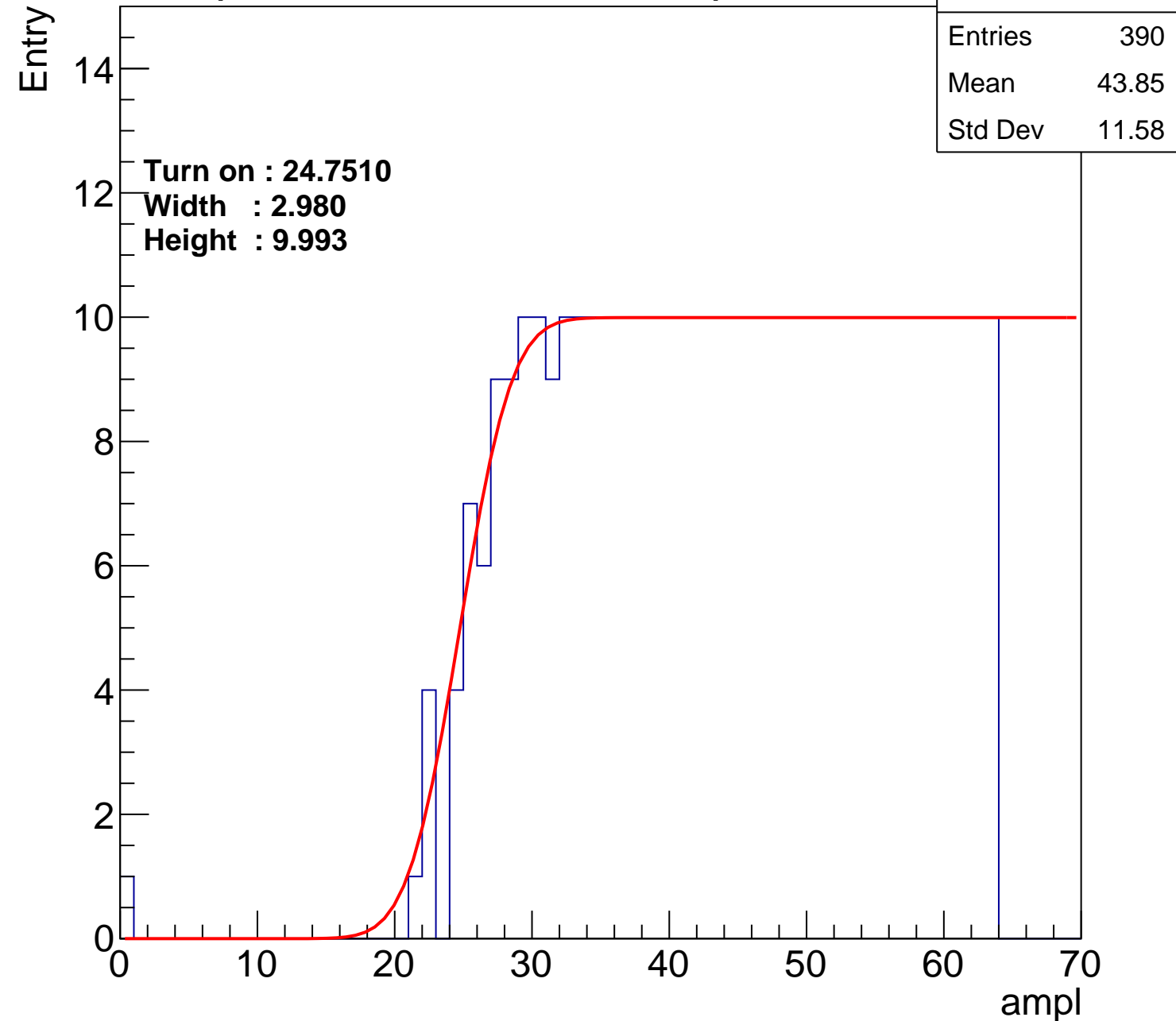
Width : 2.980

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.4267

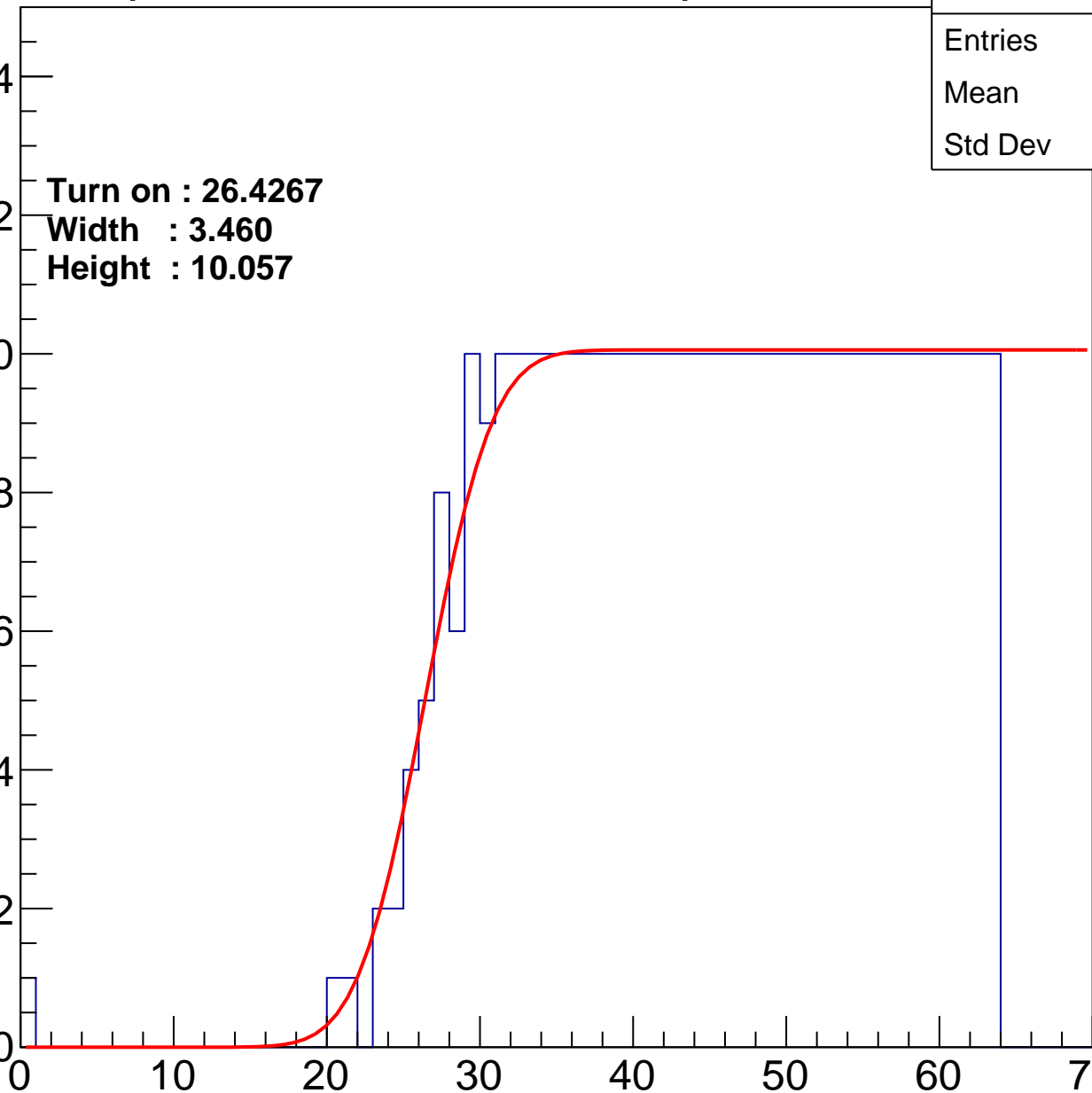
Width : 3.460

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U8-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.4267

Width : 3.460

Height : 10.057

Entry

