

B0L000S, U5-ch0

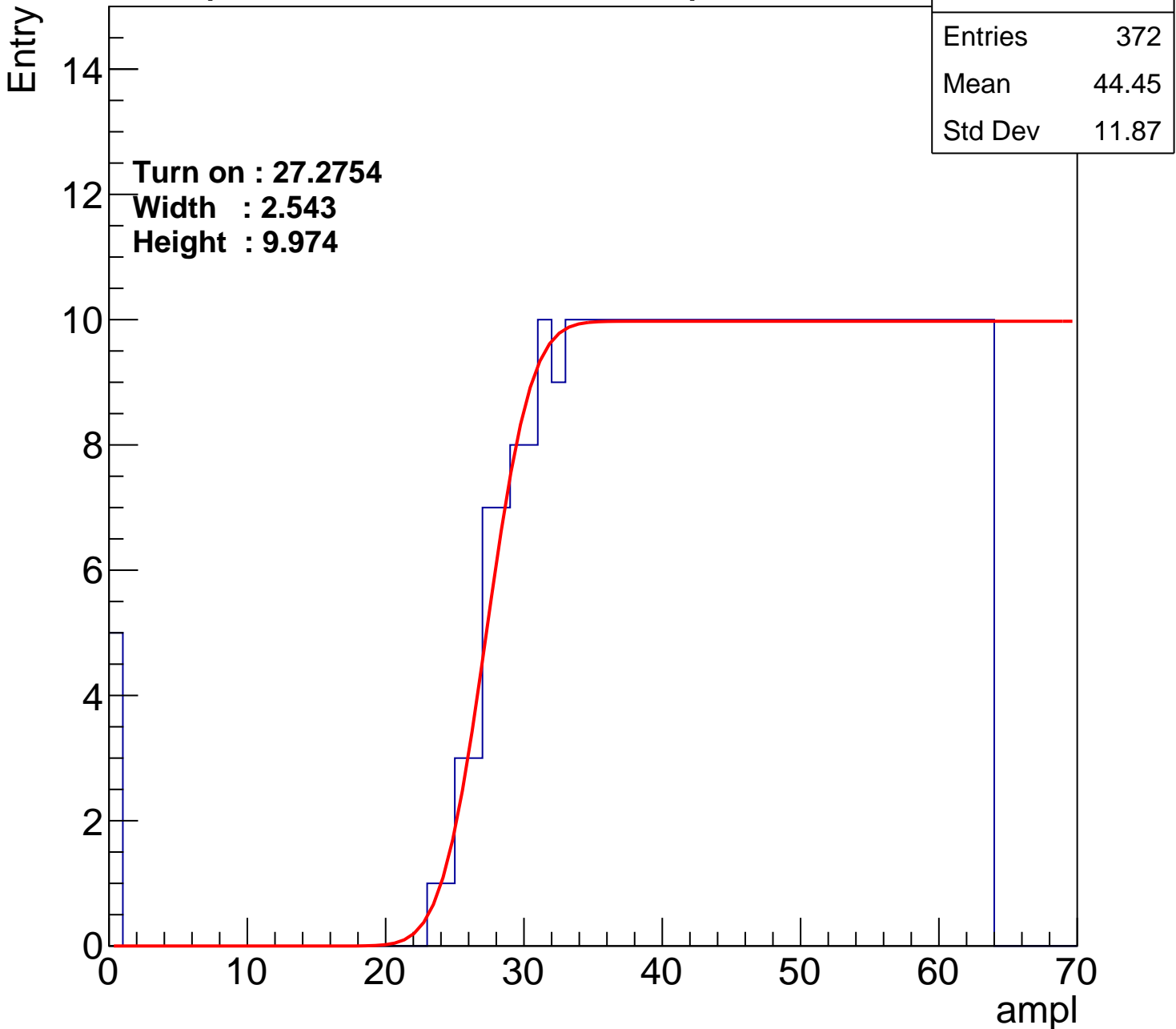
calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.45
Std Dev	11.87

Turn on : 27.2754

Width : 2.543

Height : 9.974



B0L000S, U5-ch1

calib_packv5_042523_0143.root, FC#5, port B1

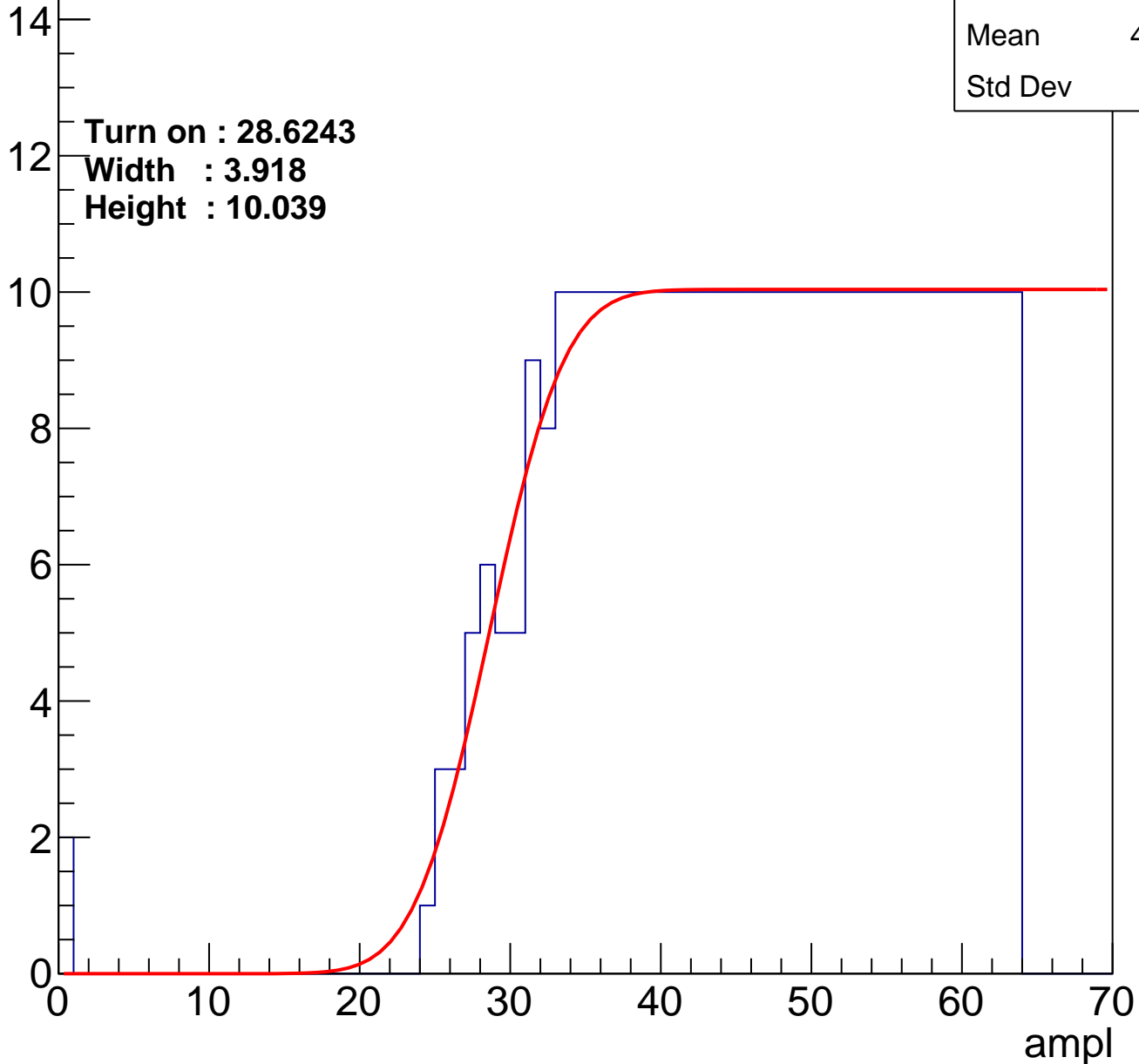
Entries	357
Mean	45.35
Std Dev	11

Turn on : 28.6243

Width : 3.918

Height : 10.039

Entry



B0L000S, U5-ch2

calib_packv5_042523_0143.root, FC#5, port B1

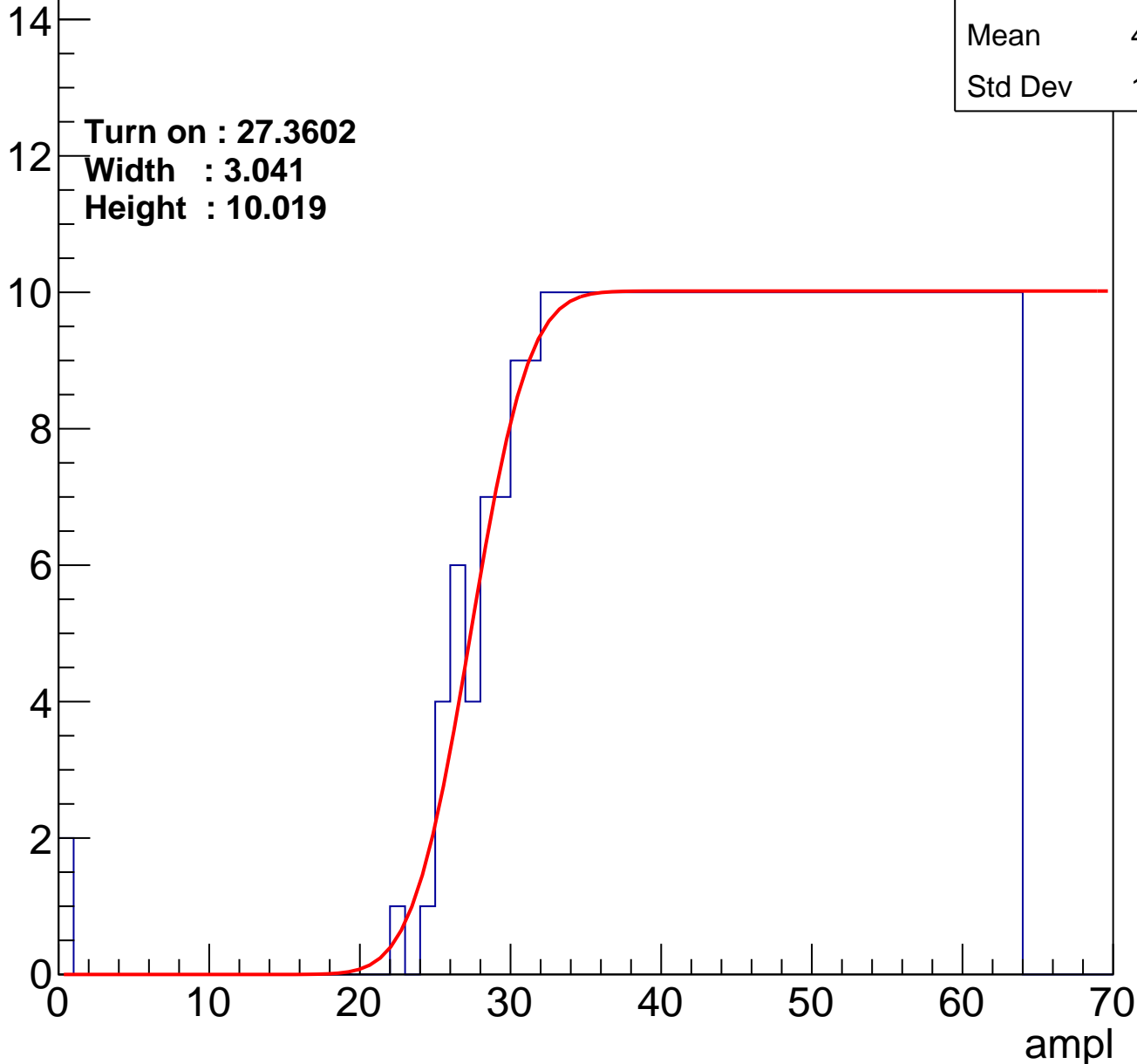
Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.3602

Width : 3.041

Height : 10.019

Entry



B0L000S, U5-ch3

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.06
Std Dev	11.1

Turn on : 28.3182

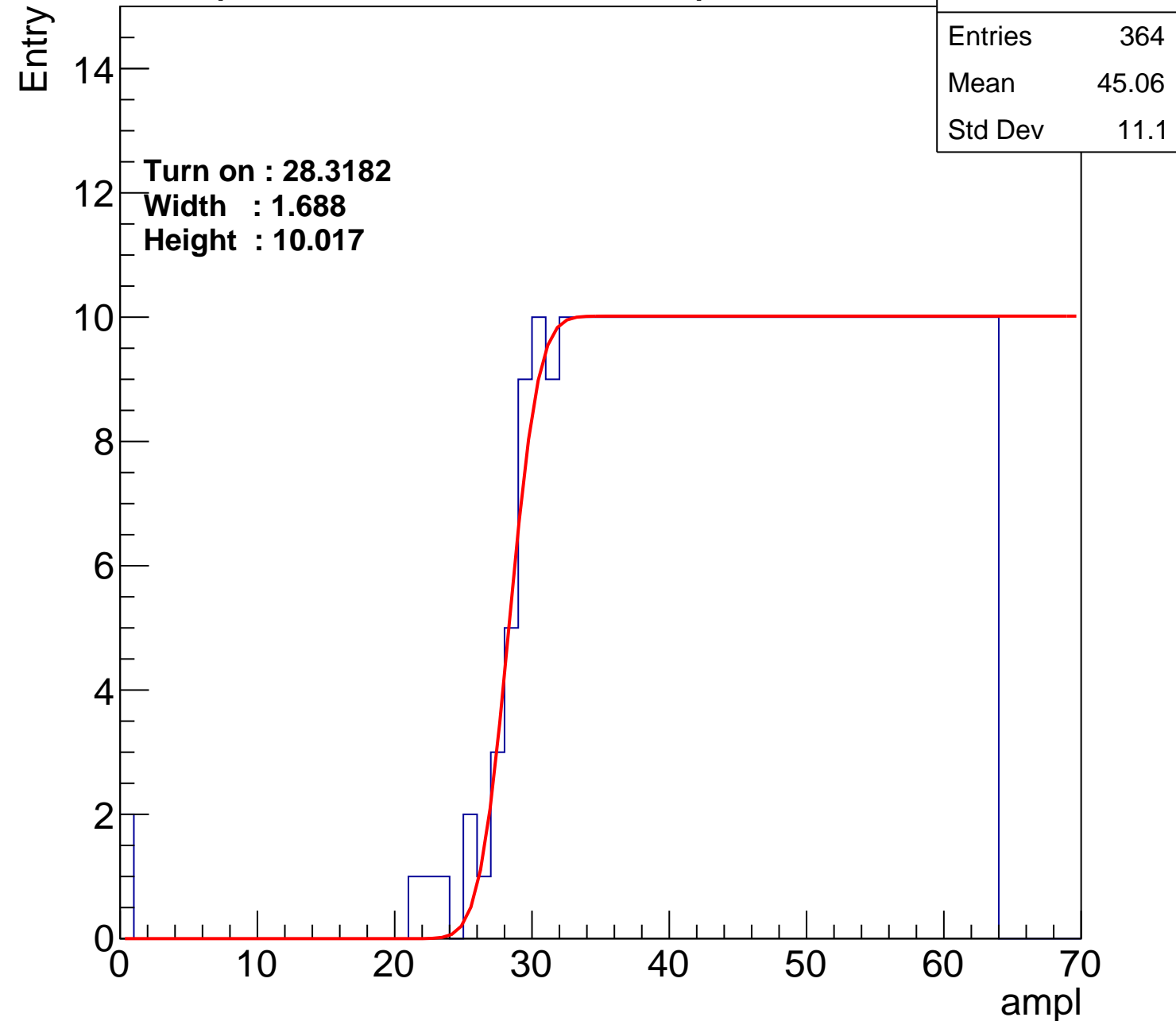
Width : 1.688

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch4

calib_packv5_042523_0143.root, FC#5, port B1

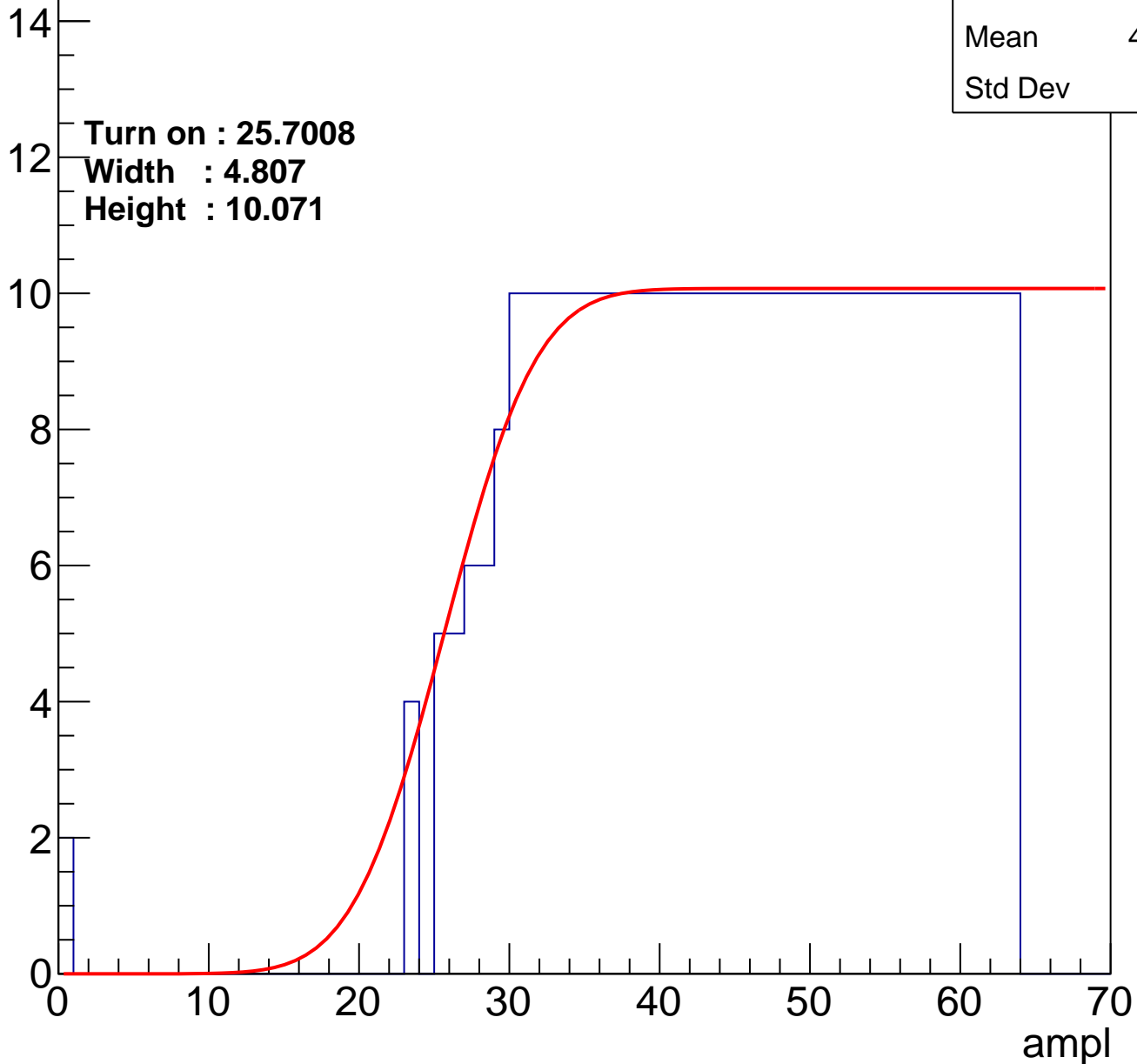
Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 25.7008

Width : 4.807

Height : 10.071

Entry



B0L000S, U5-ch5

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.7694

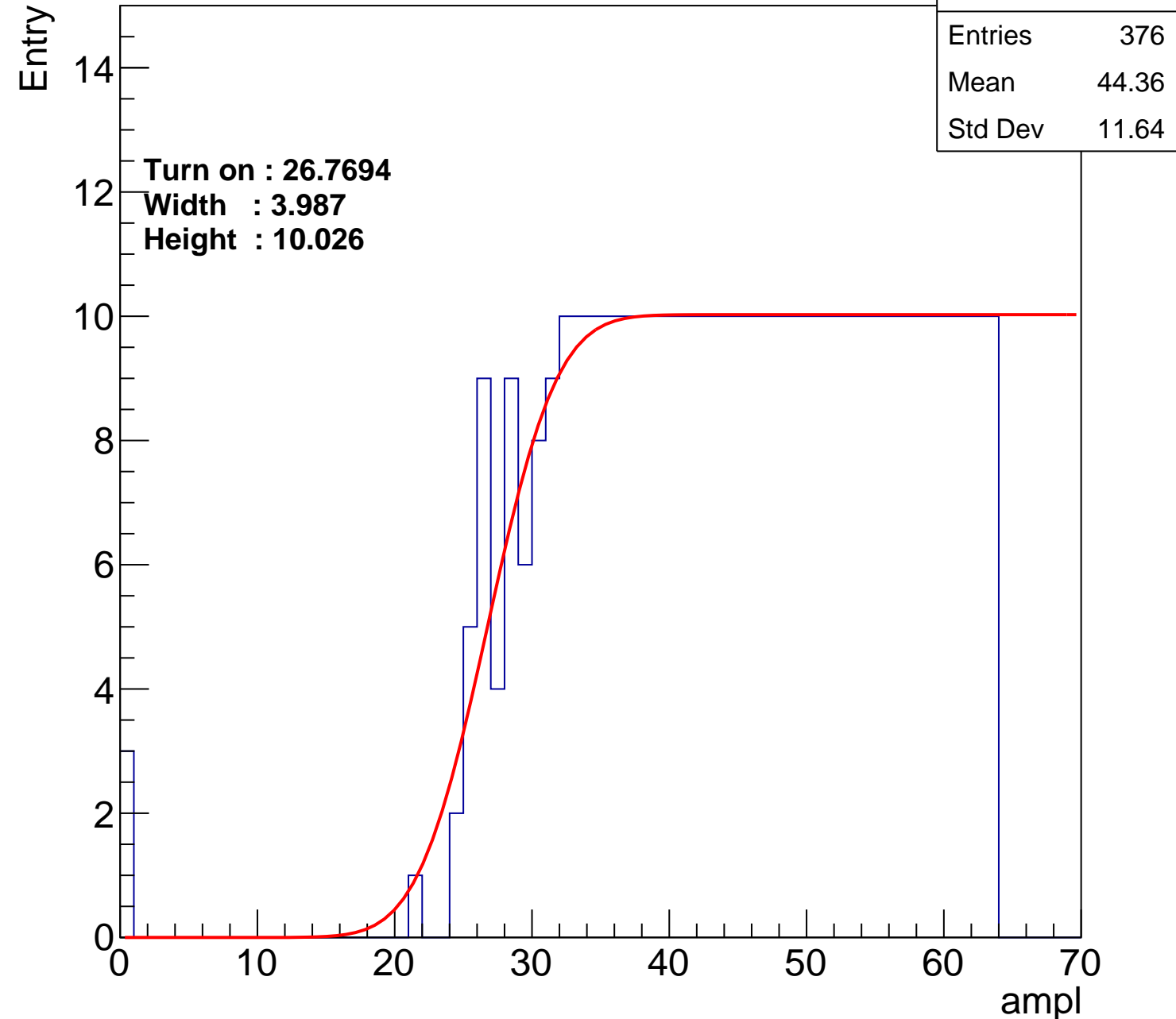
Width : 3.987

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.86
Std Dev	11.98

Turn on : 25.3746

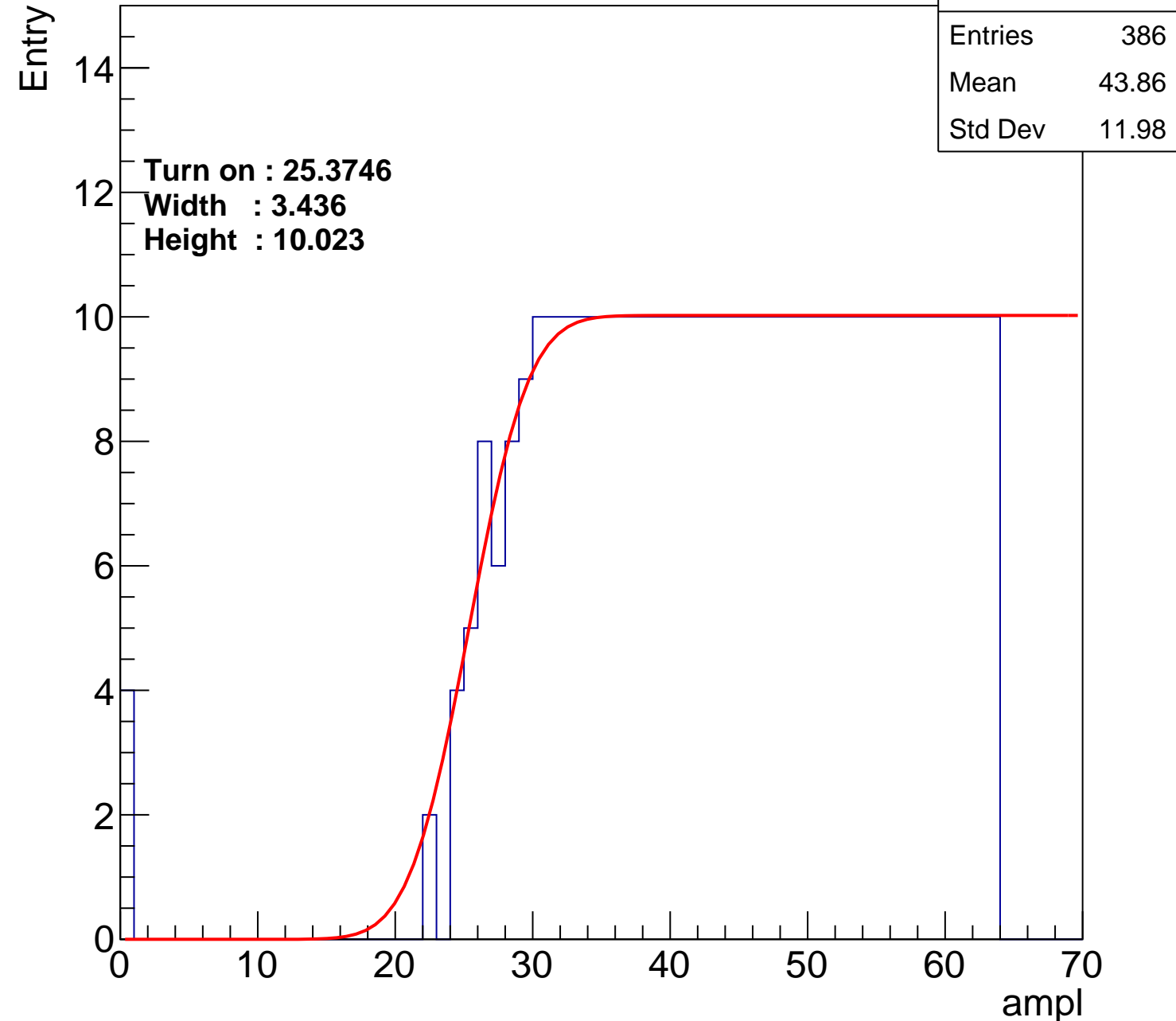
Width : 3.436

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 27.2611

Width : 3.093

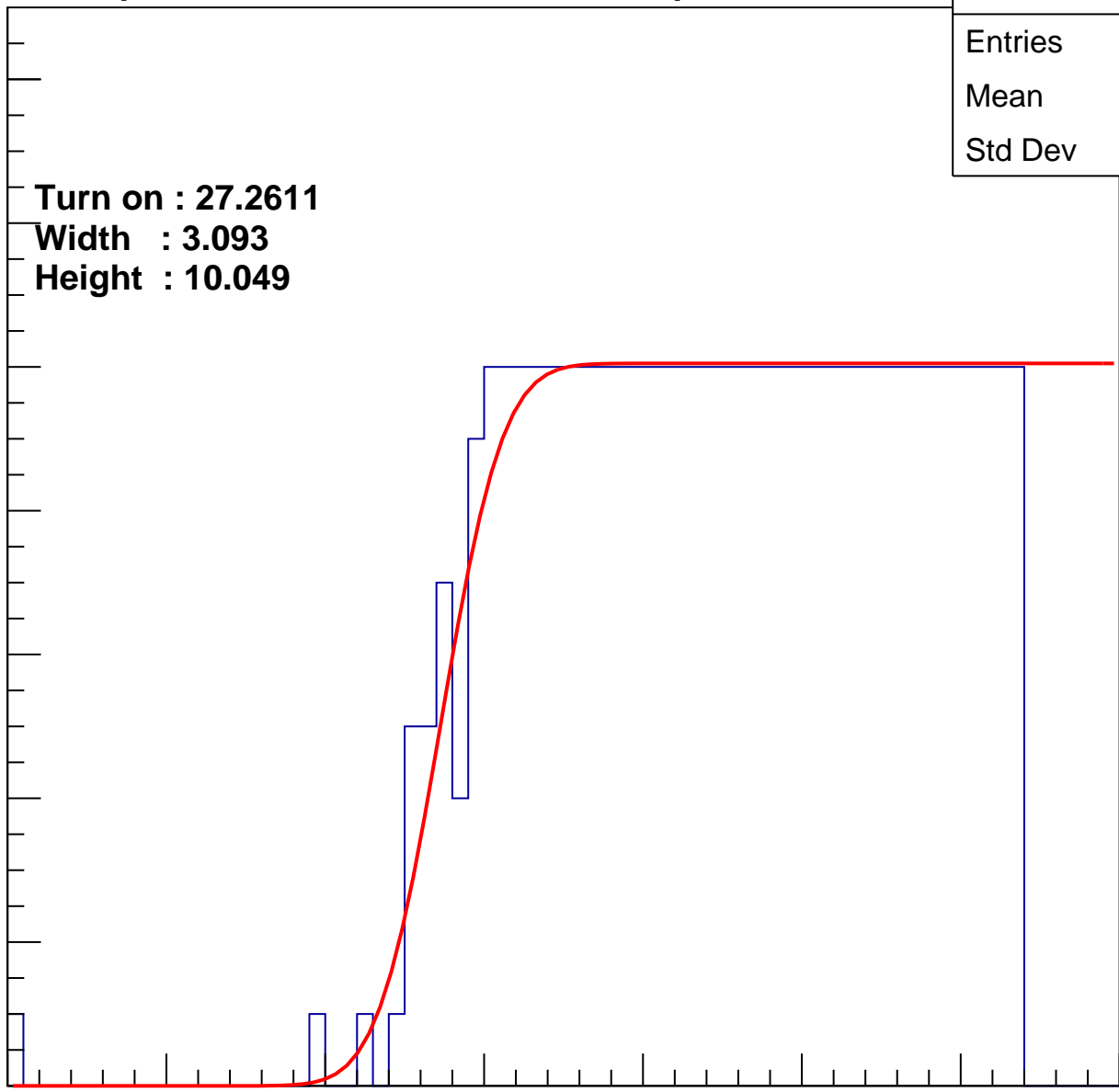
Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U5-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.9753

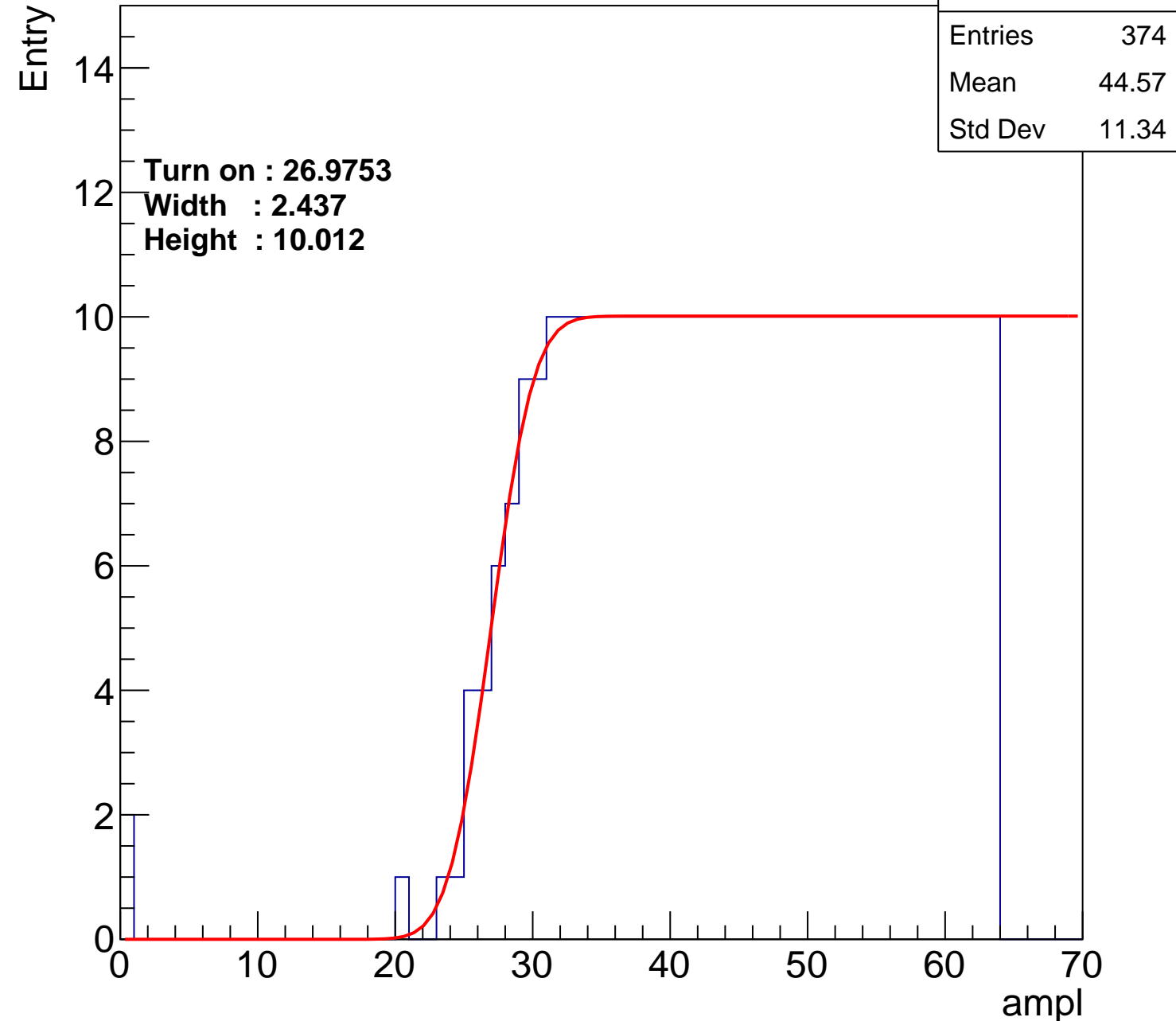
Width : 2.437

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch9

calib_packv5_042523_0143.root, FC#5, port B1

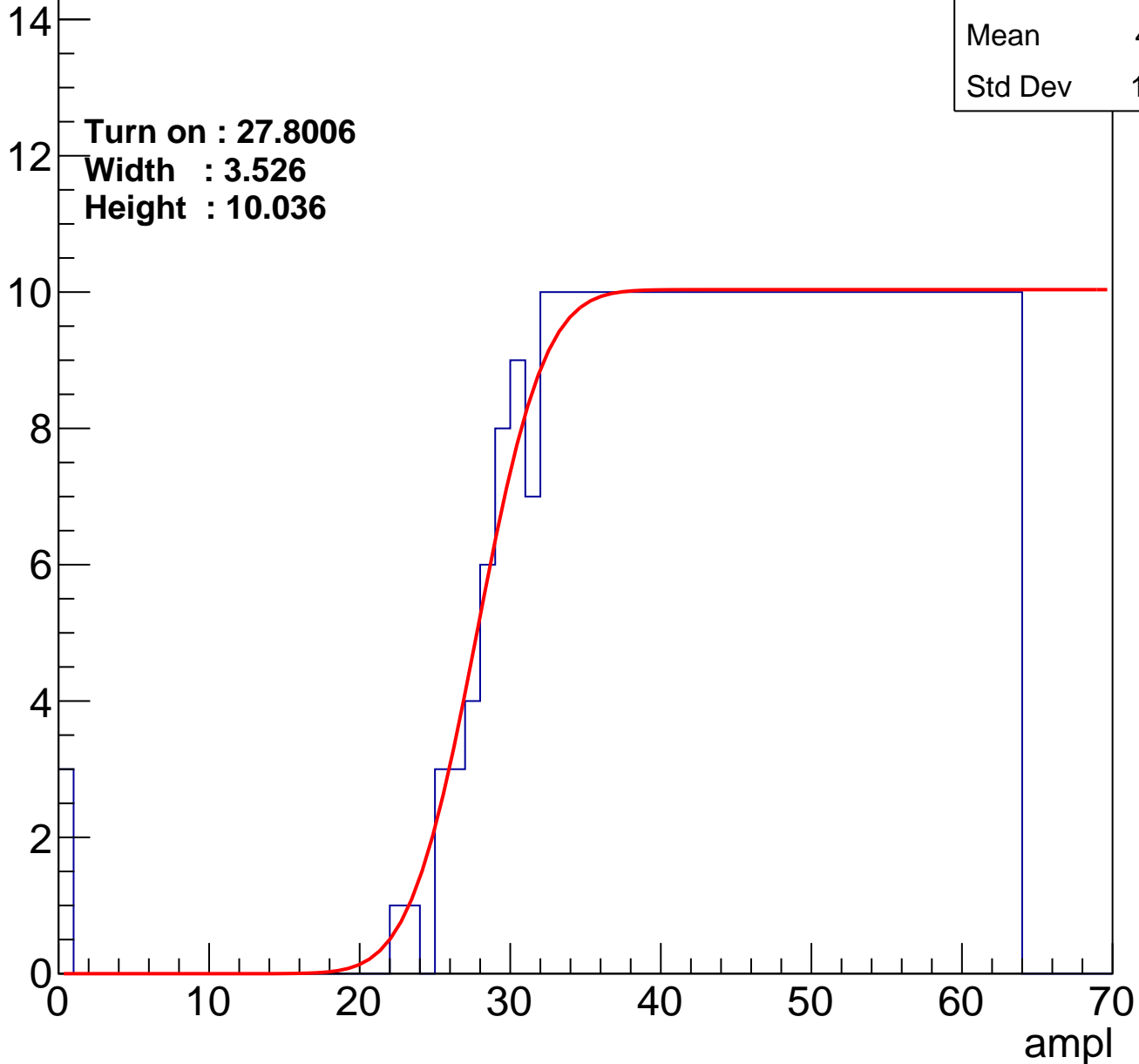
Entries	365
Mean	44.91
Std Dev	11.36

Turn on : 27.8006

Width : 3.526

Height : 10.036

Entry



B0L000S, U5-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.51
Std Dev	11.39

Turn on : 27.7397

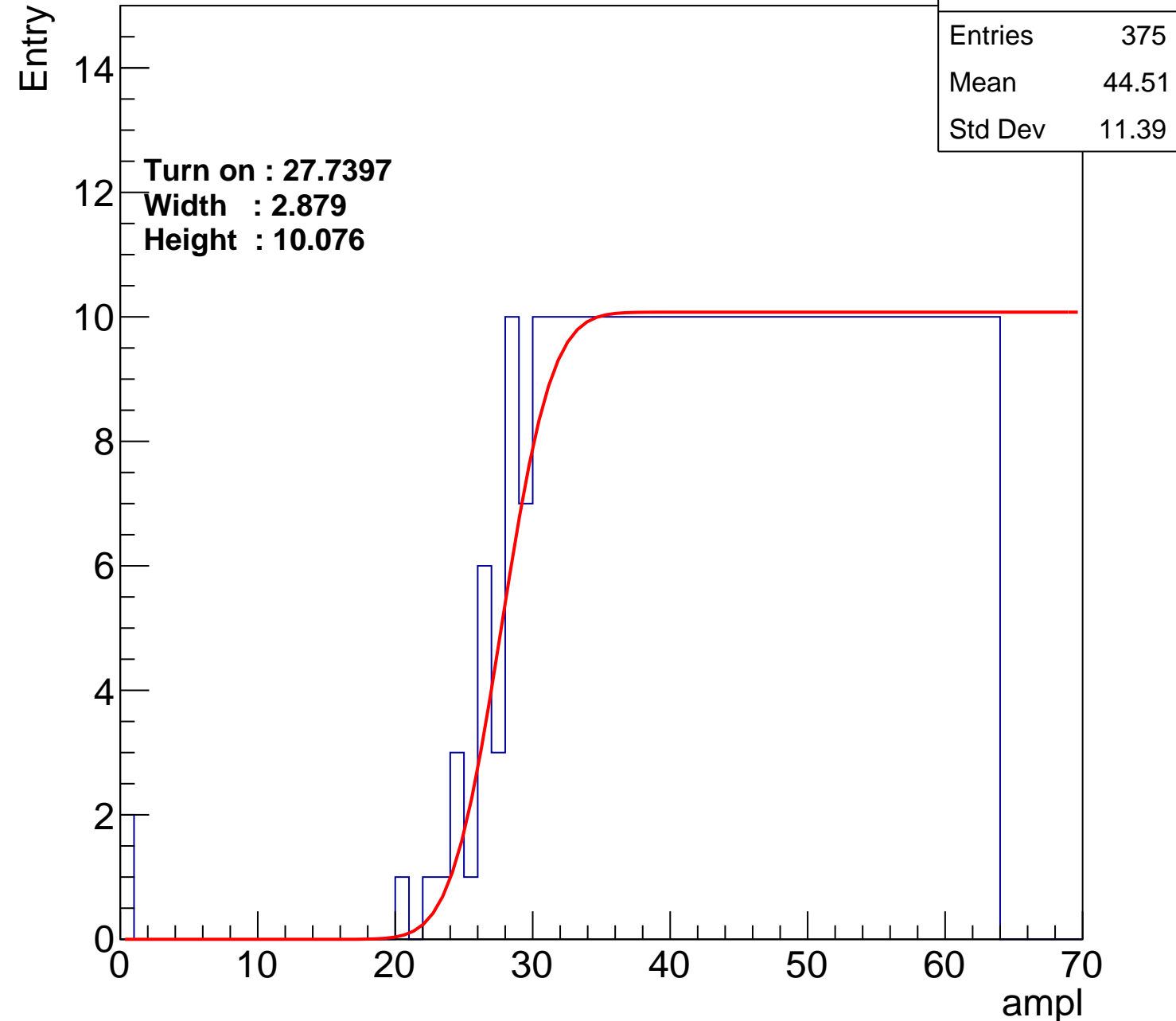
Width : 2.879

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.31
Std Dev	10.82

Turn on : 28.5833

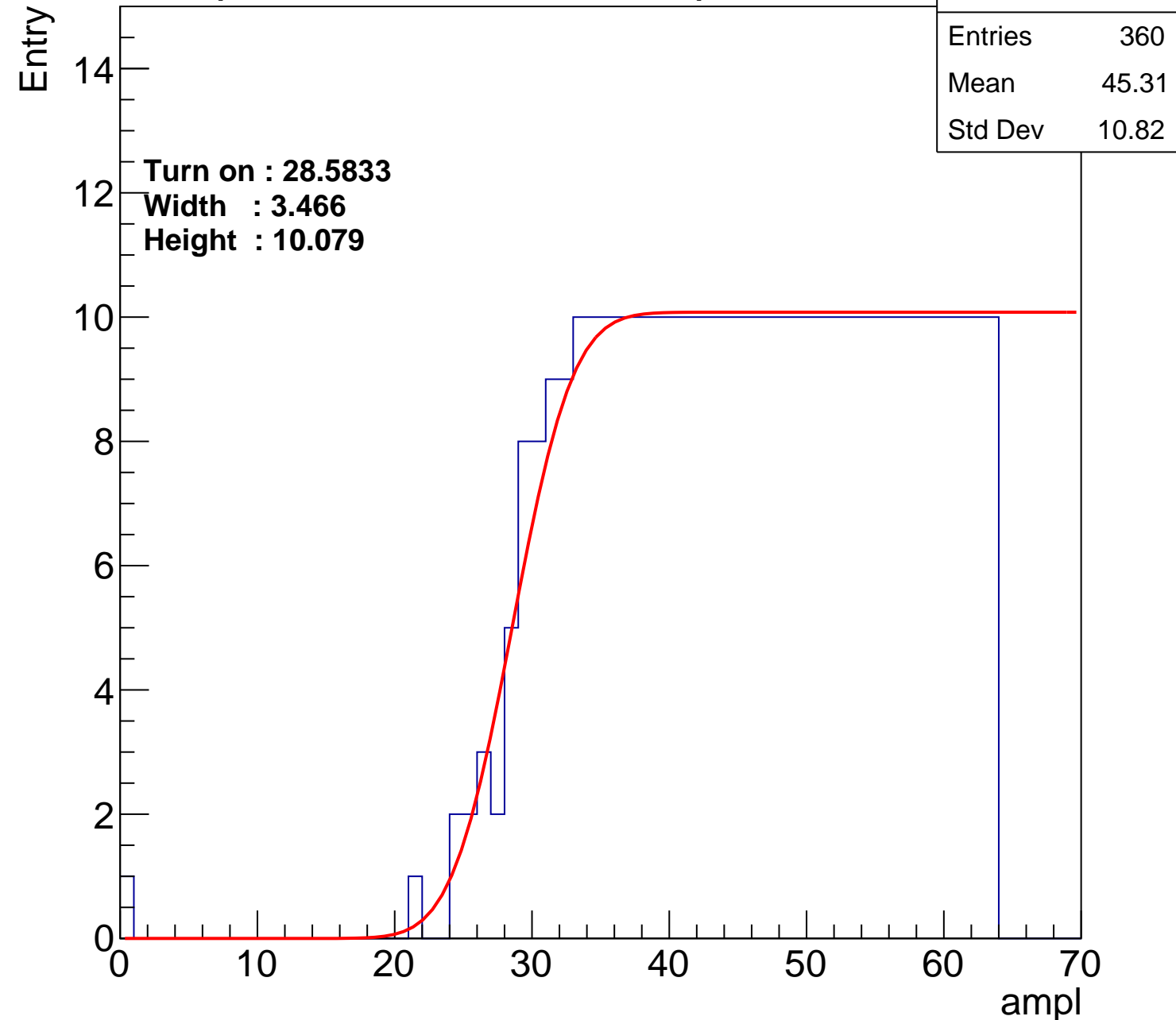
Width : 3.466

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch12

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 26.2910

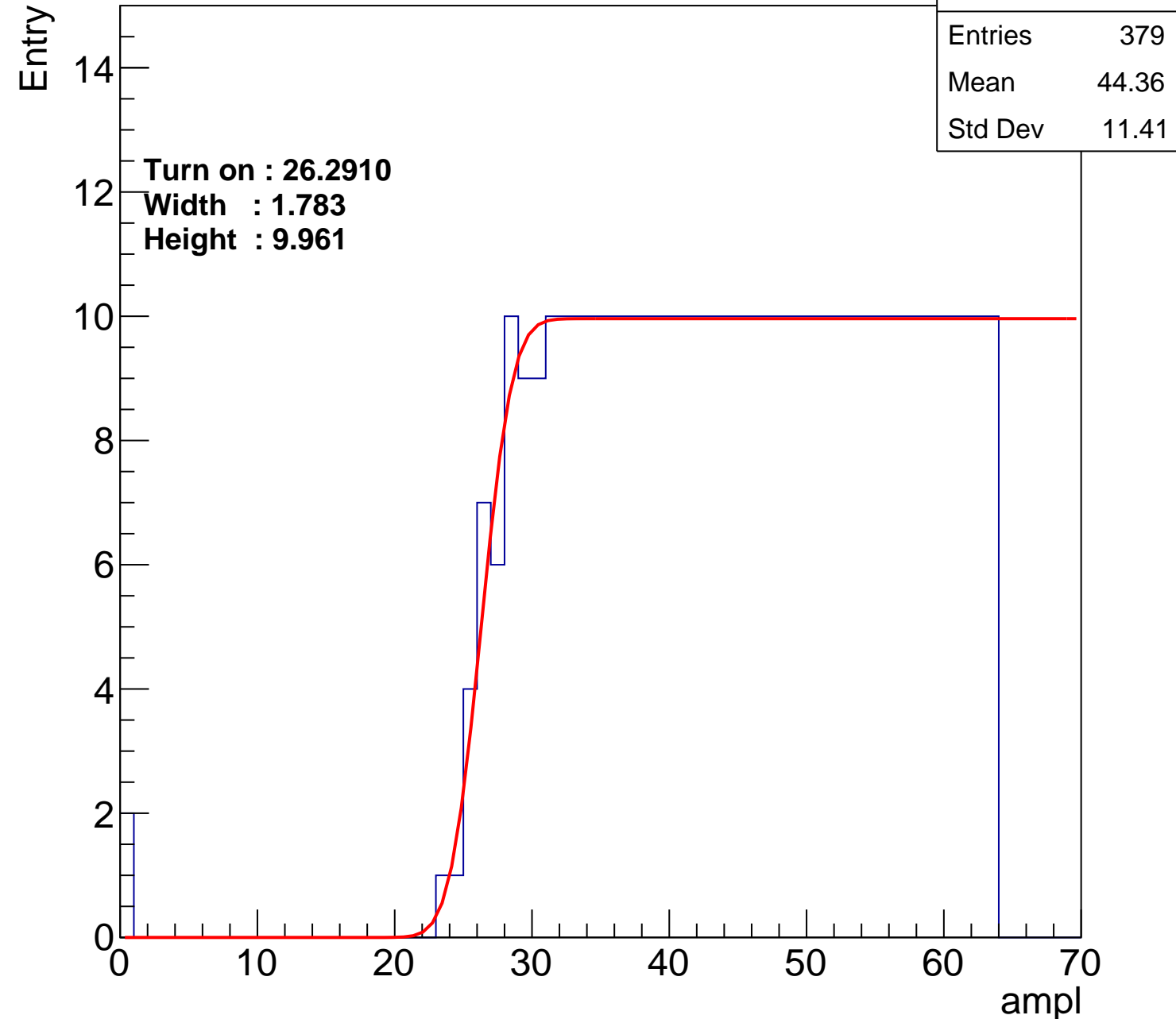
Width : 1.783

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.74
Std Dev	11.1

Turn on : 27.1099

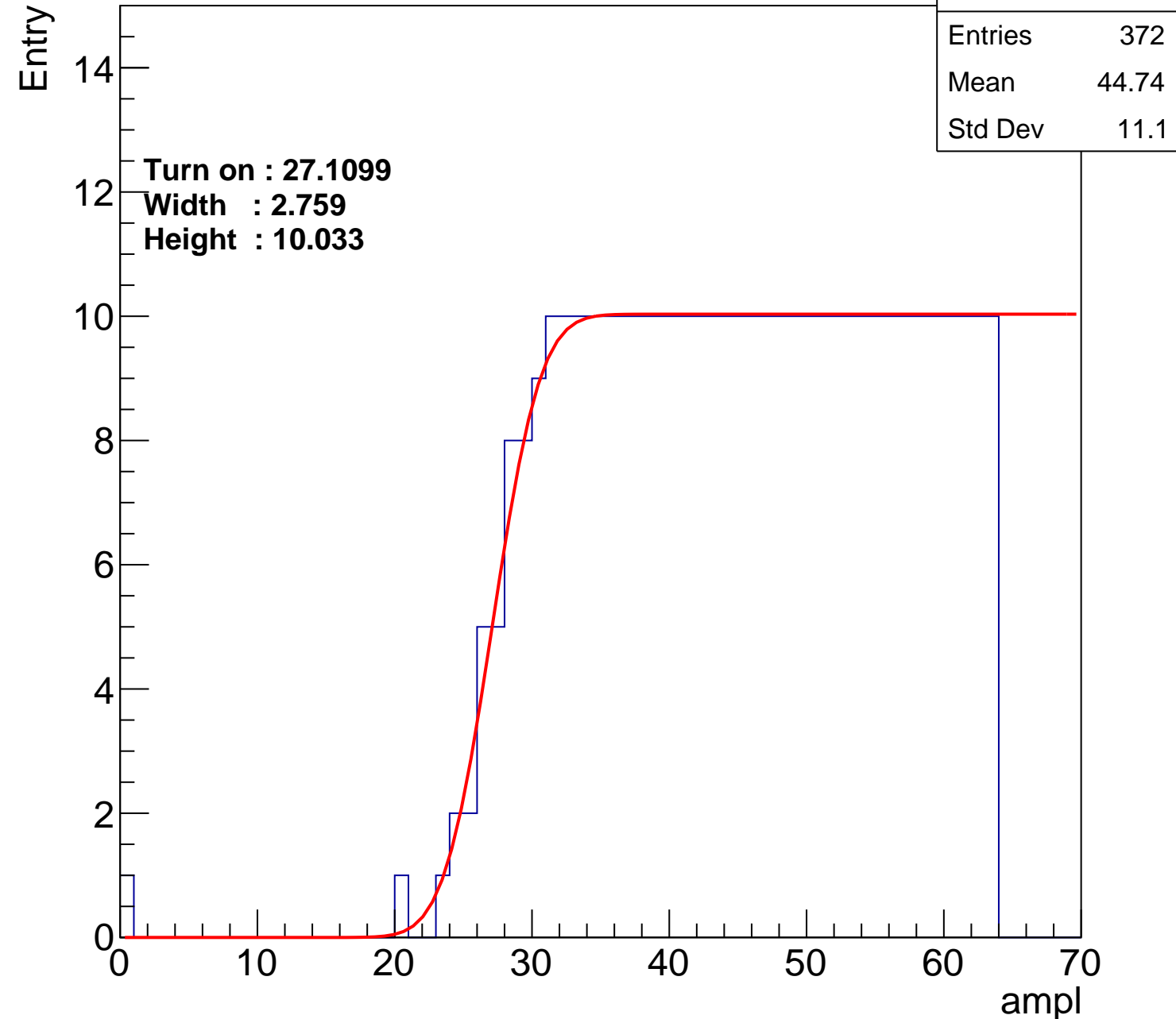
Width : 2.759

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.49
Std Dev	11.63

Turn on : 27.2932

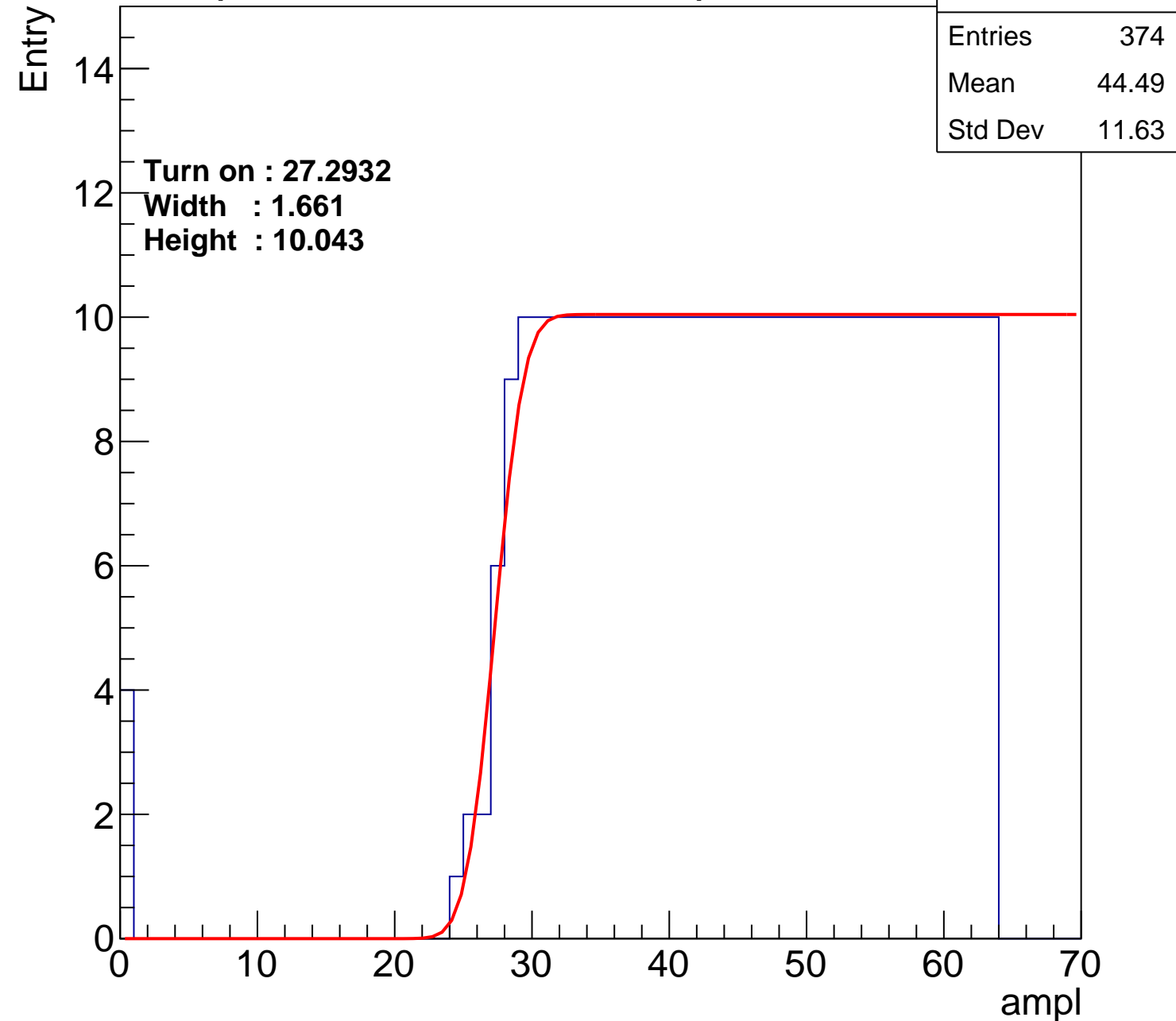
Width : 1.661

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.99
Std Dev	11.16

Turn on : 27.6421

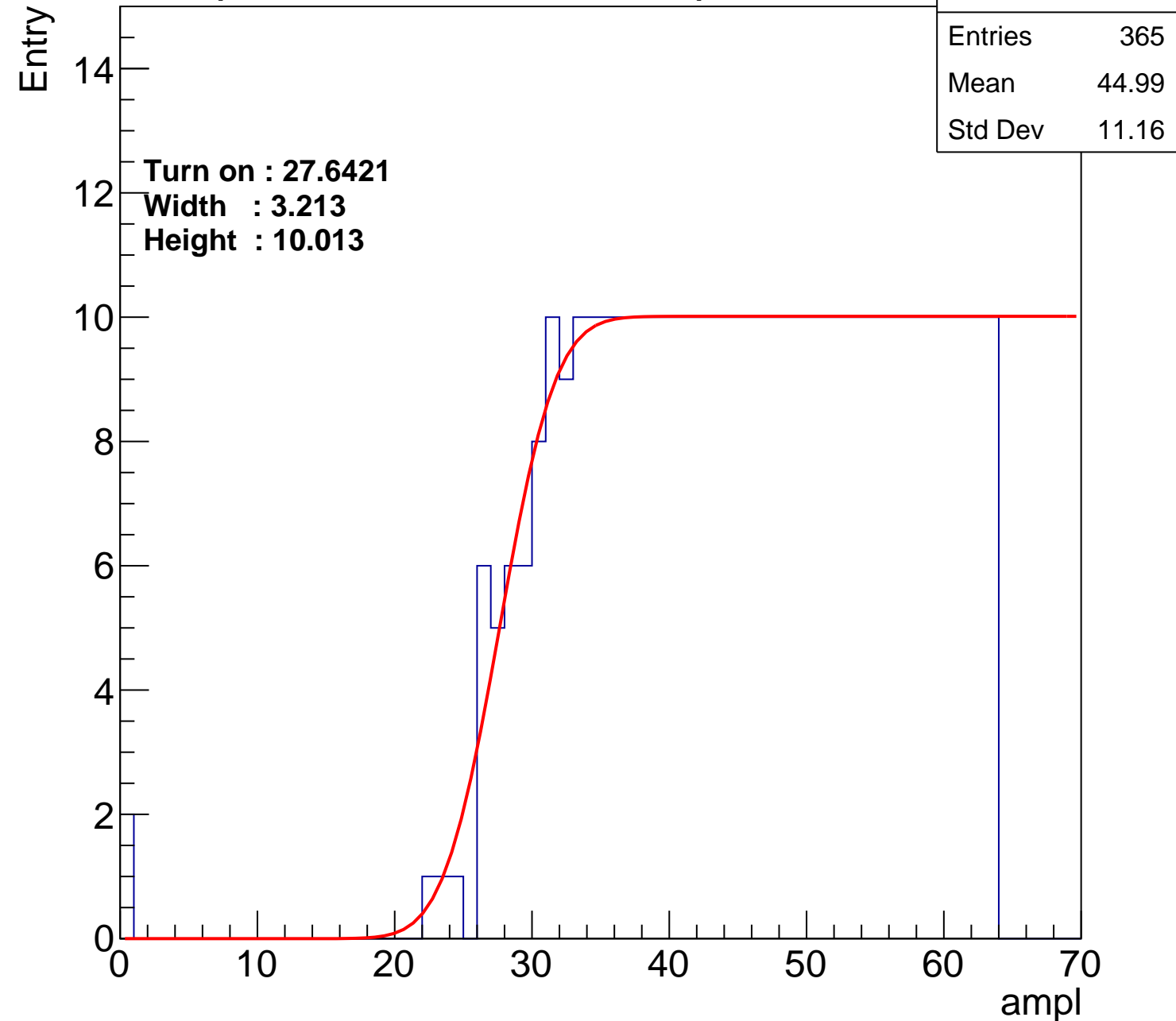
Width : 3.213

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch16

calib_packv5_042523_0143.root, FC#5, port B1

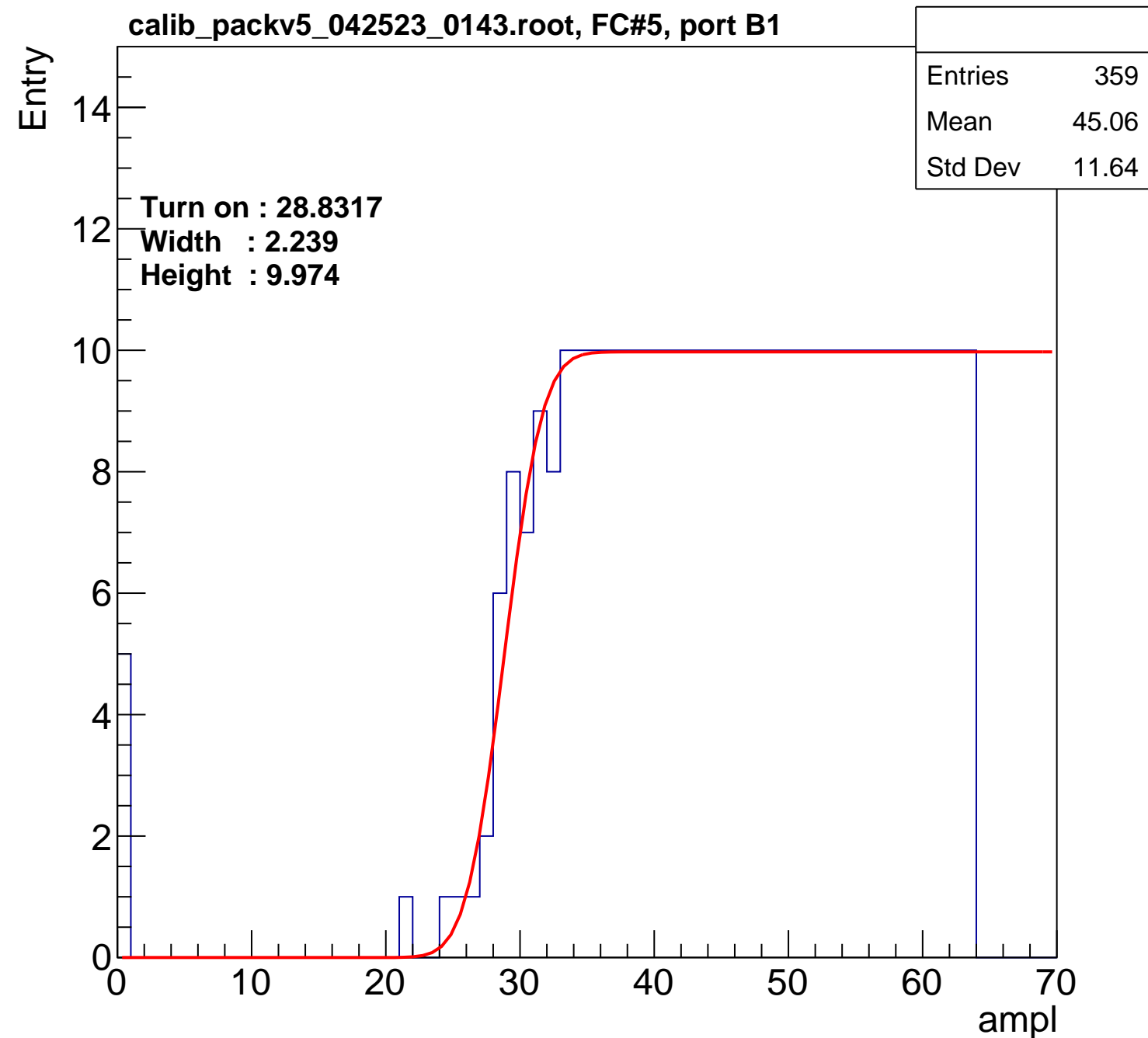
Entry

14
12
10
8
6
4
2
0

Turn on : 28.8317
Width : 2.239
Height : 9.974

Entries	359
Mean	45.06
Std Dev	11.64

ampl



B0L000S, U5-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.56
Std Dev	10.67

Turn on : 28.5598

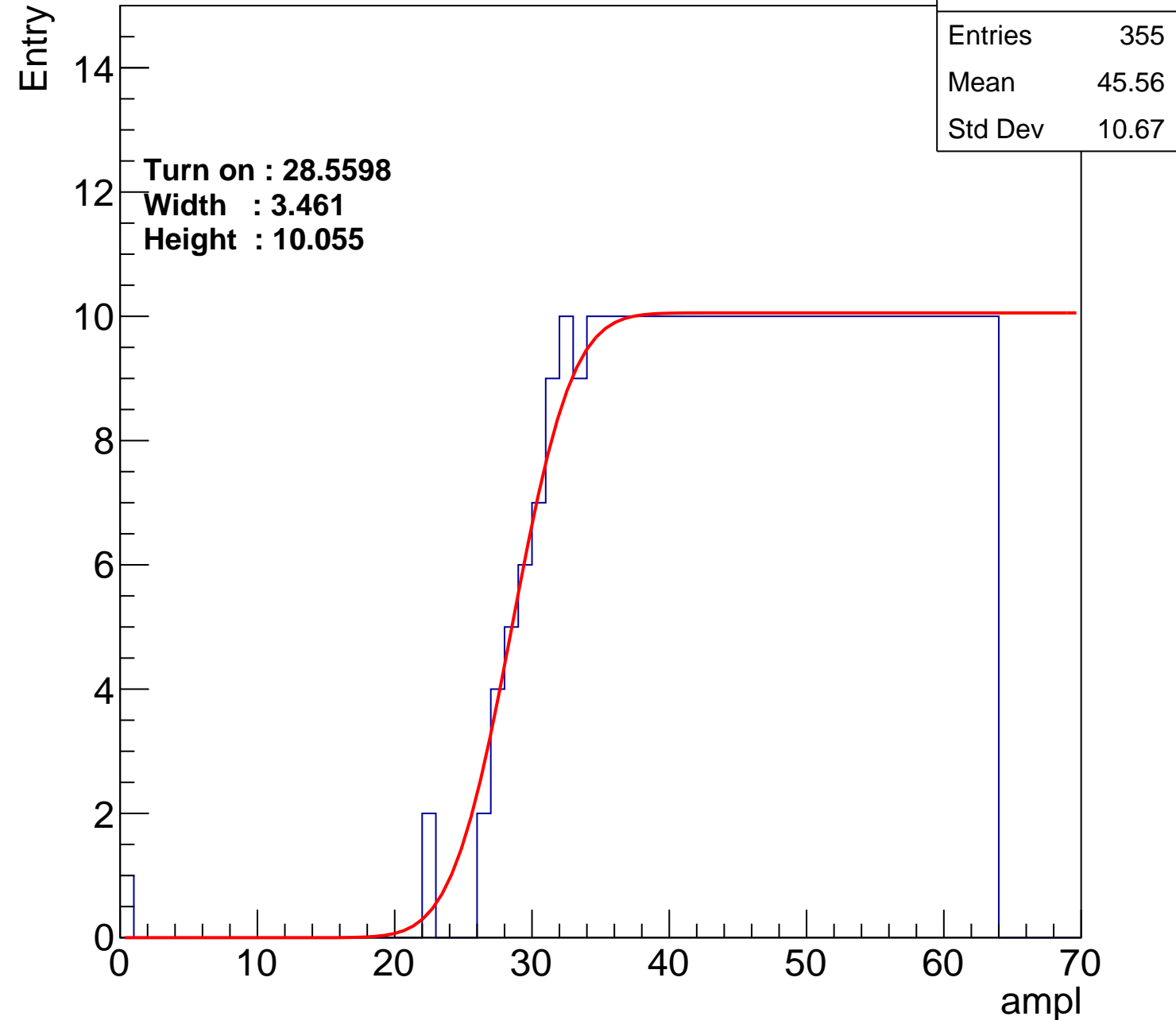
Width : 3.461

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch18

calib_packv5_042523_0143.root, FC#5, port B1

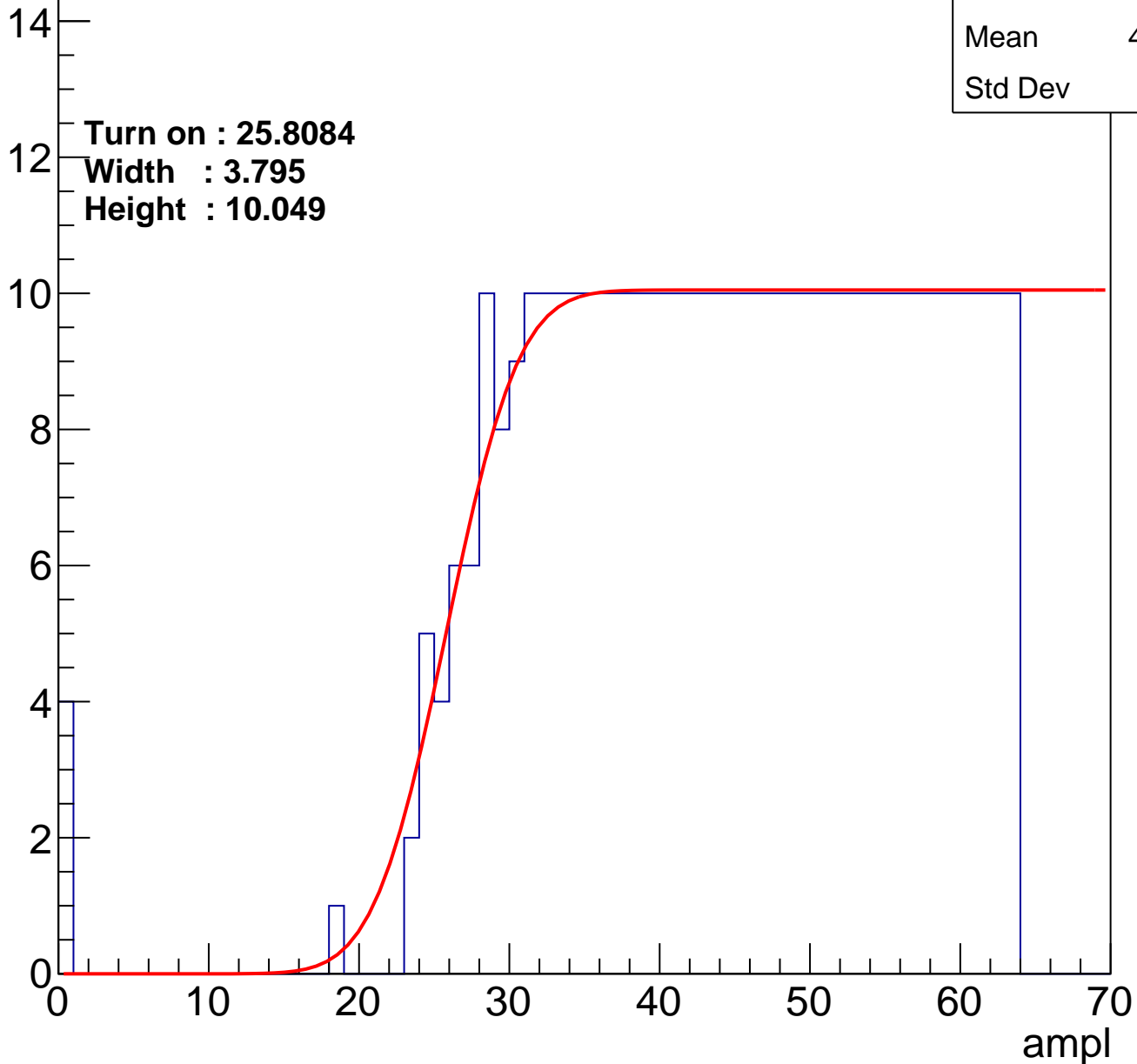
Entries	385
Mean	43.88
Std Dev	12

Turn on : 25.8084

Width : 3.795

Height : 10.049

Entry



B0L000S, U5-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.9692

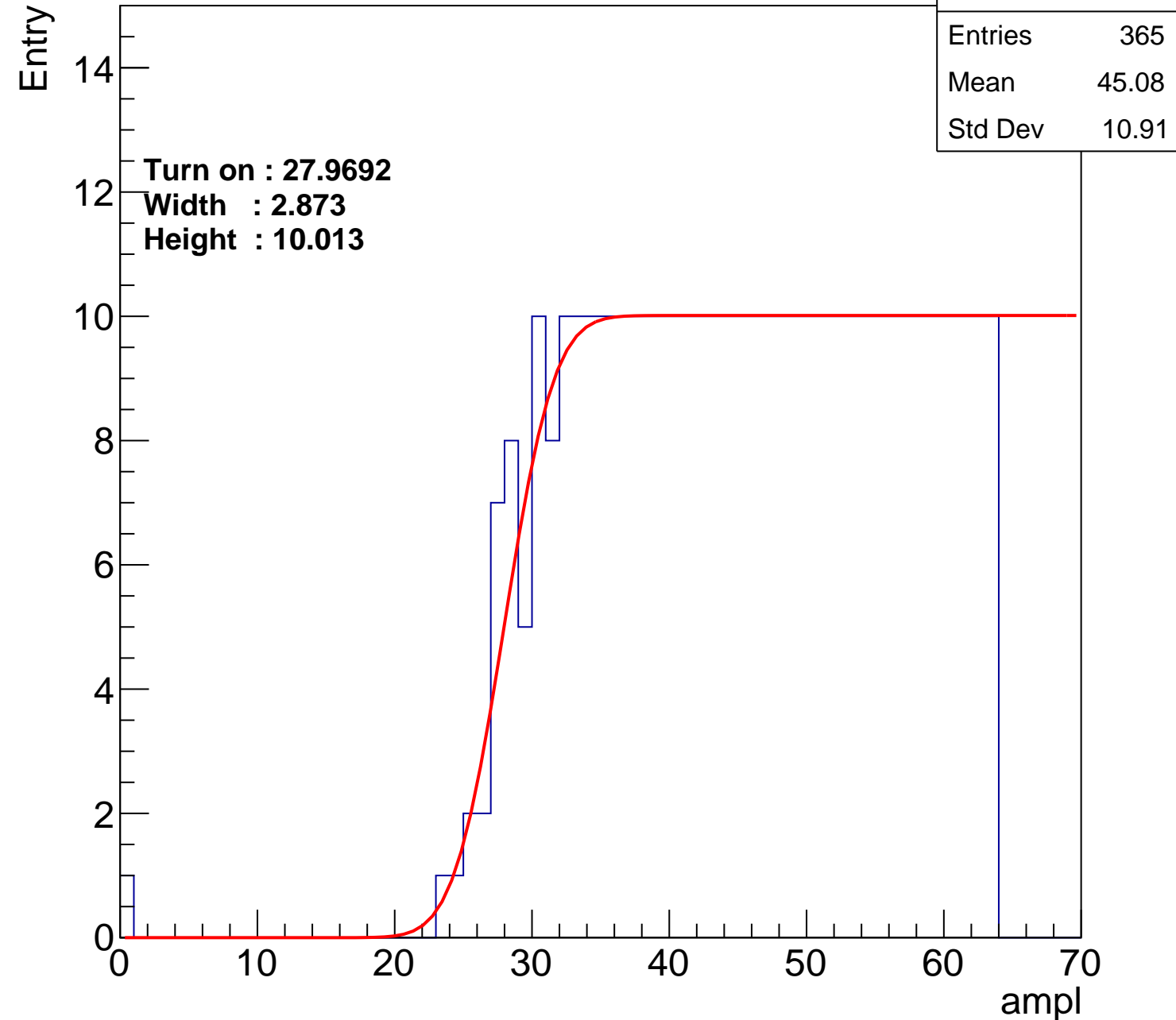
Width : 2.873

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch20

calib_packv5_042523_0143.root, FC#5, port B1

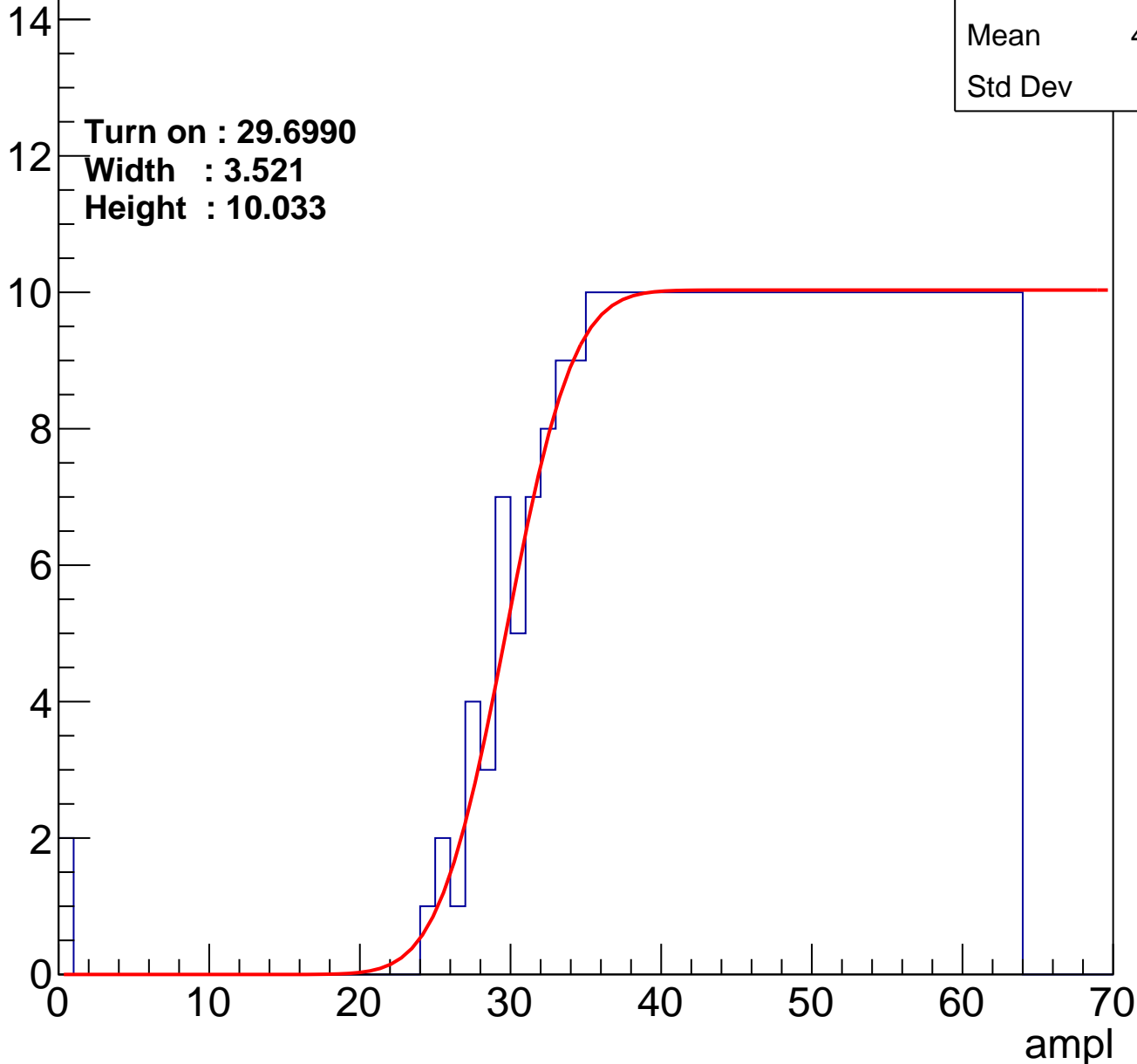
Entries	348
Mean	45.78
Std Dev	10.8

Turn on : 29.6990

Width : 3.521

Height : 10.033

Entry



B0L000S, U5-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.1926

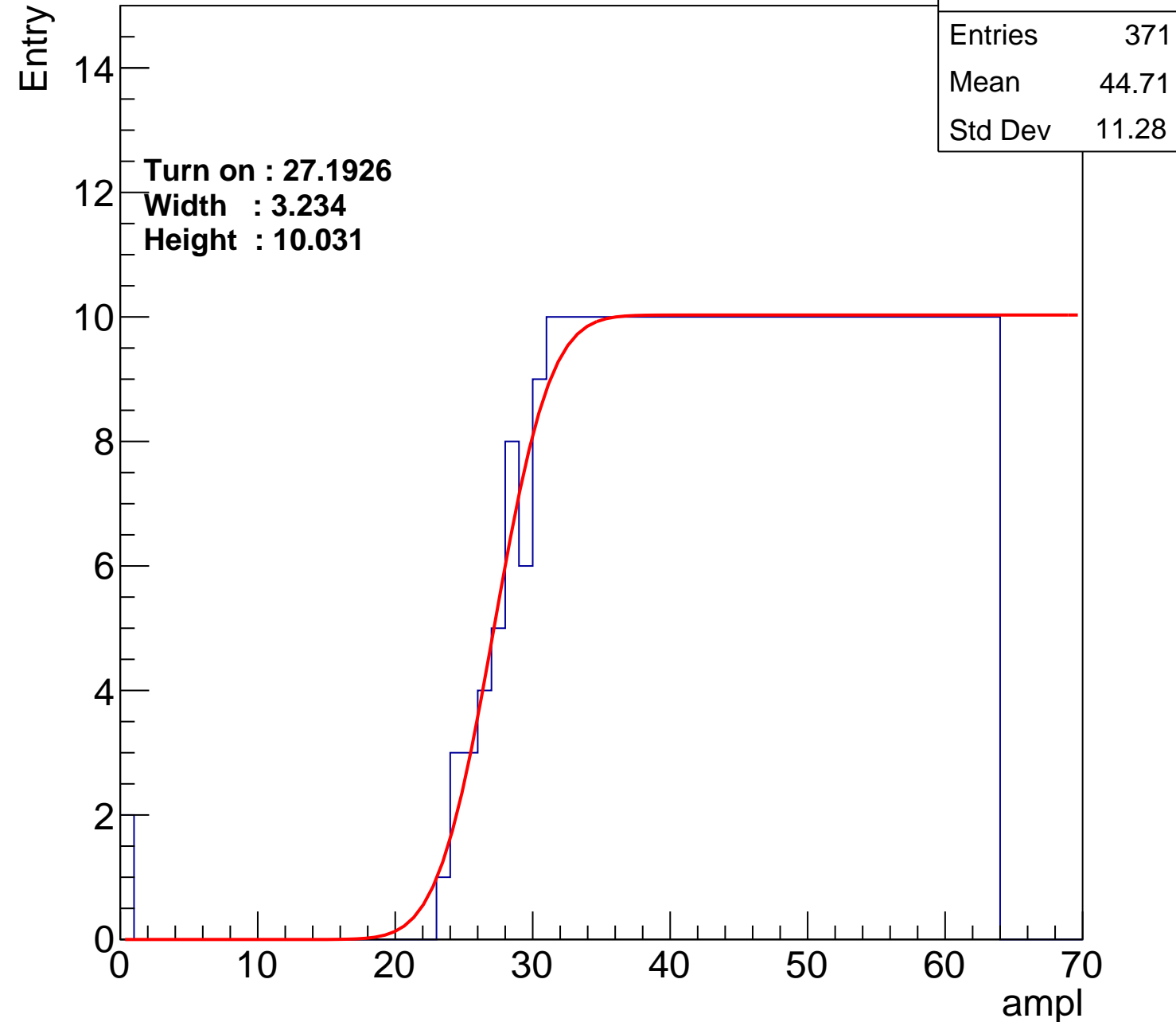
Width : 3.234

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.47
Std Dev	11.57

Turn on : 26.7711

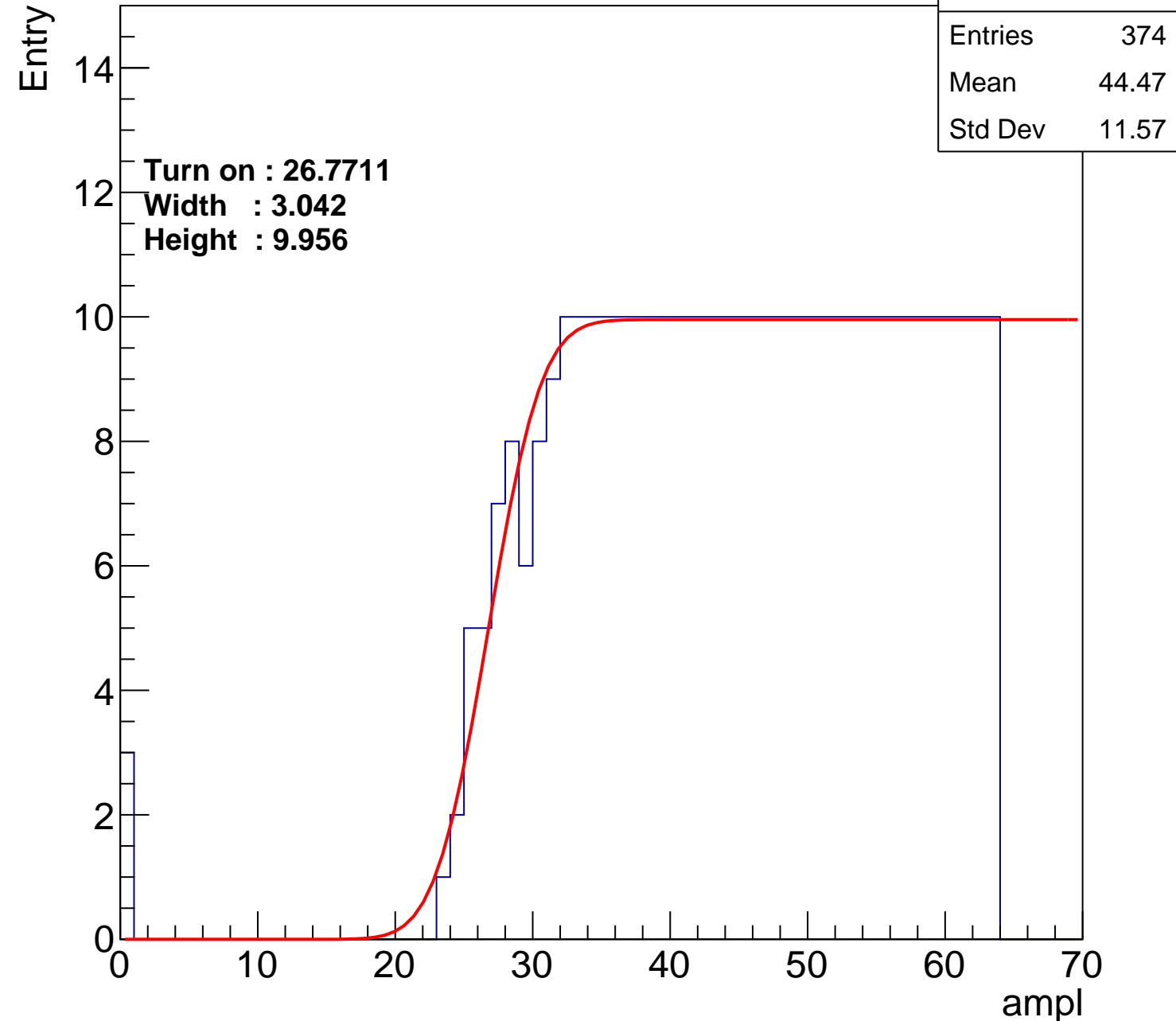
Width : 3.042

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.1305

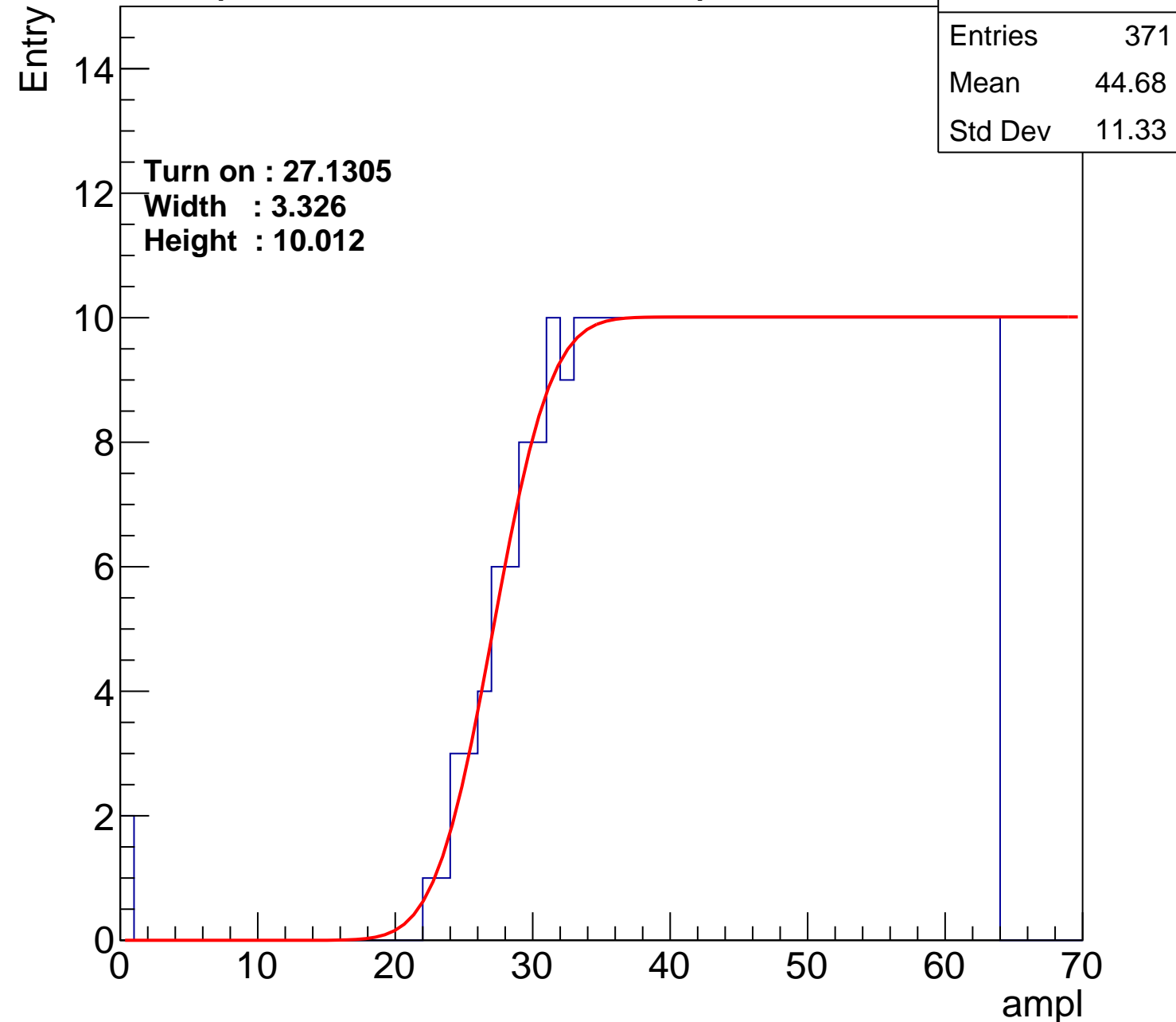
Width : 3.326

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch24

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.03
Std Dev	11.14

Turn on : 28.2890

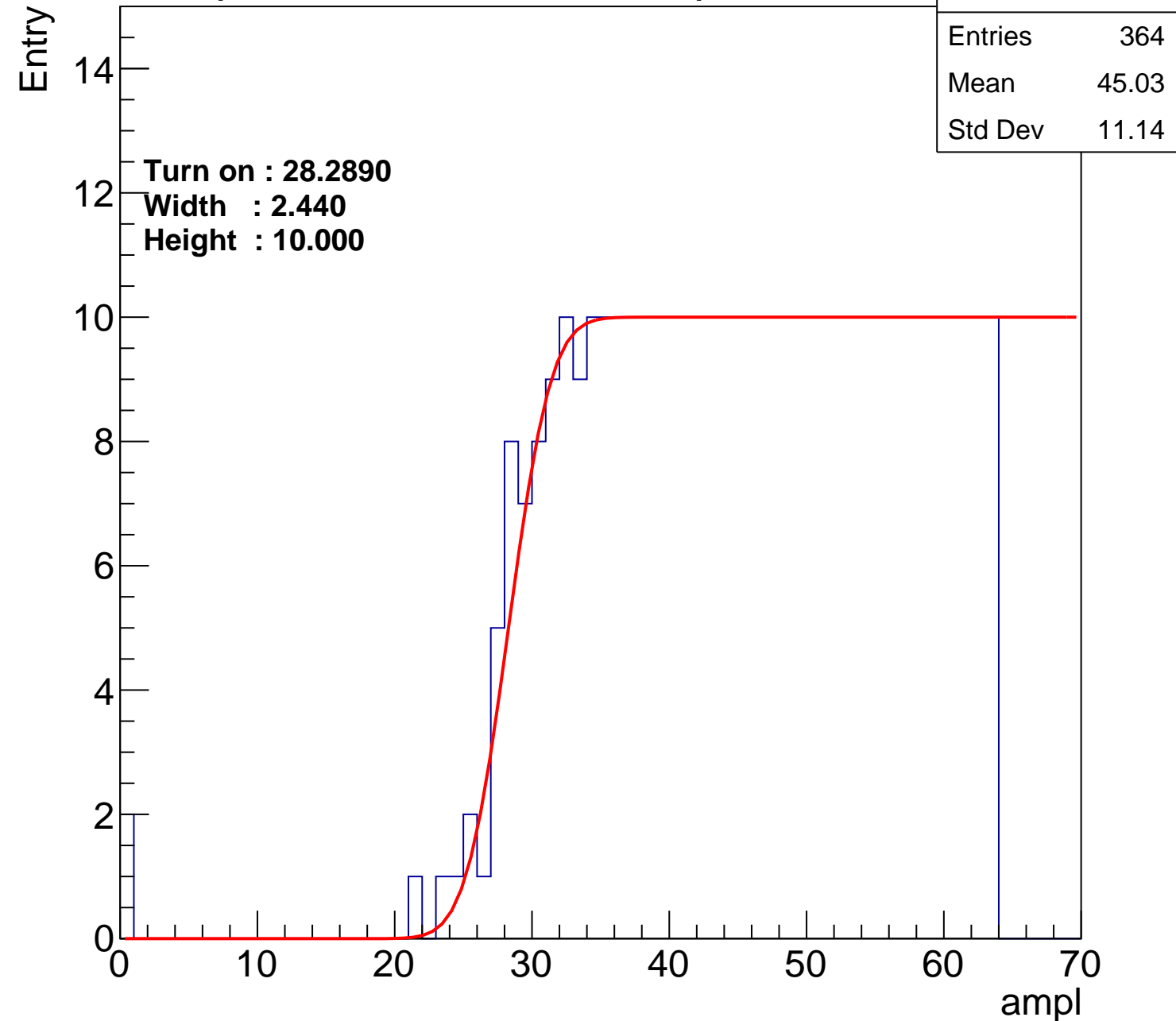
Width : 2.440

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch25

calib_packv5_042523_0143.root, FC#5, port B1

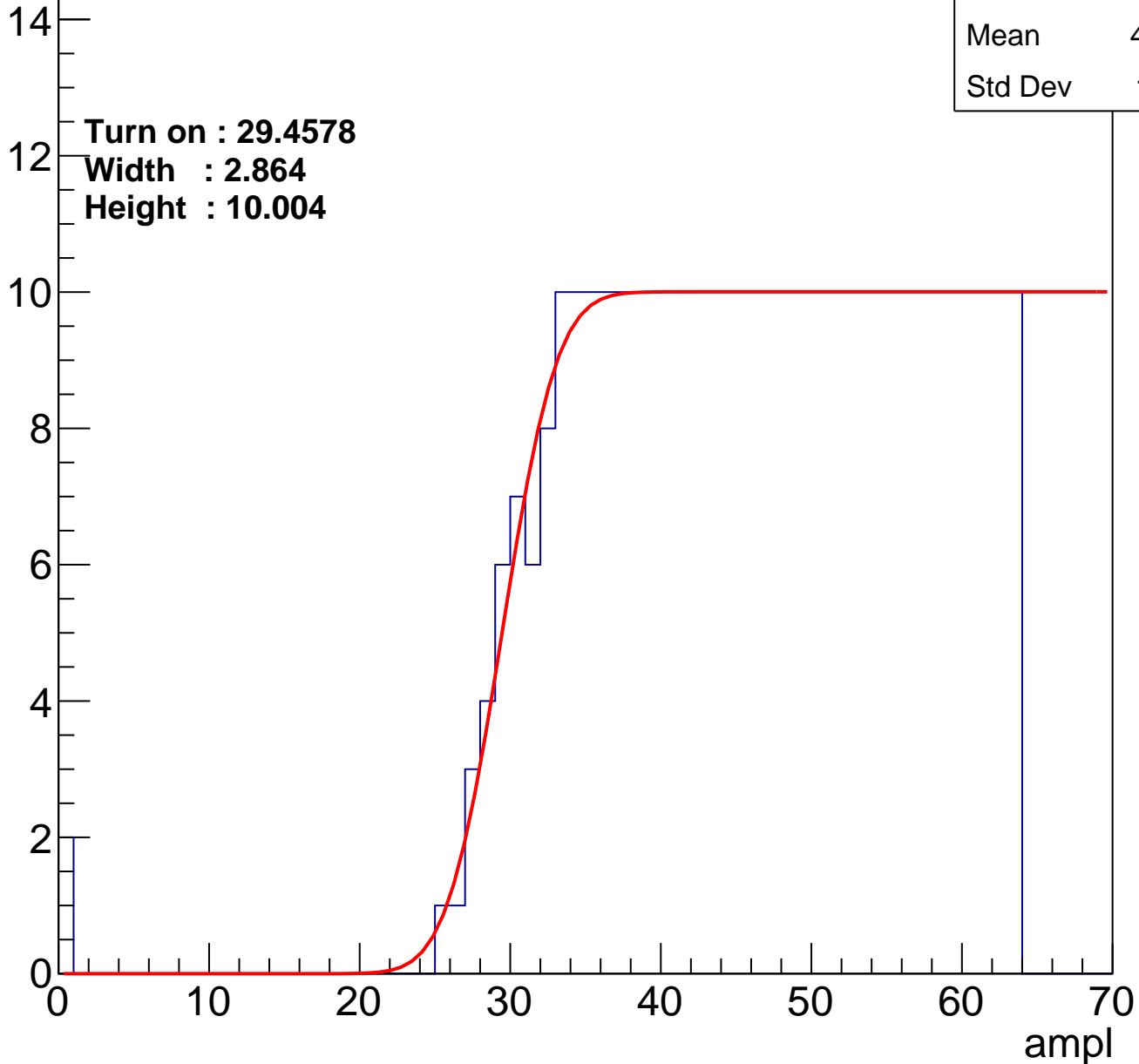
Entries	348
Mean	45.83
Std Dev	10.71

Turn on : 29.4578

Width : 2.864

Height : 10.004

Entry



B0L000S, U5-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	391
Mean	43.43
Std Dev	12.51

Turn on : 25.9289

Width : 4.414

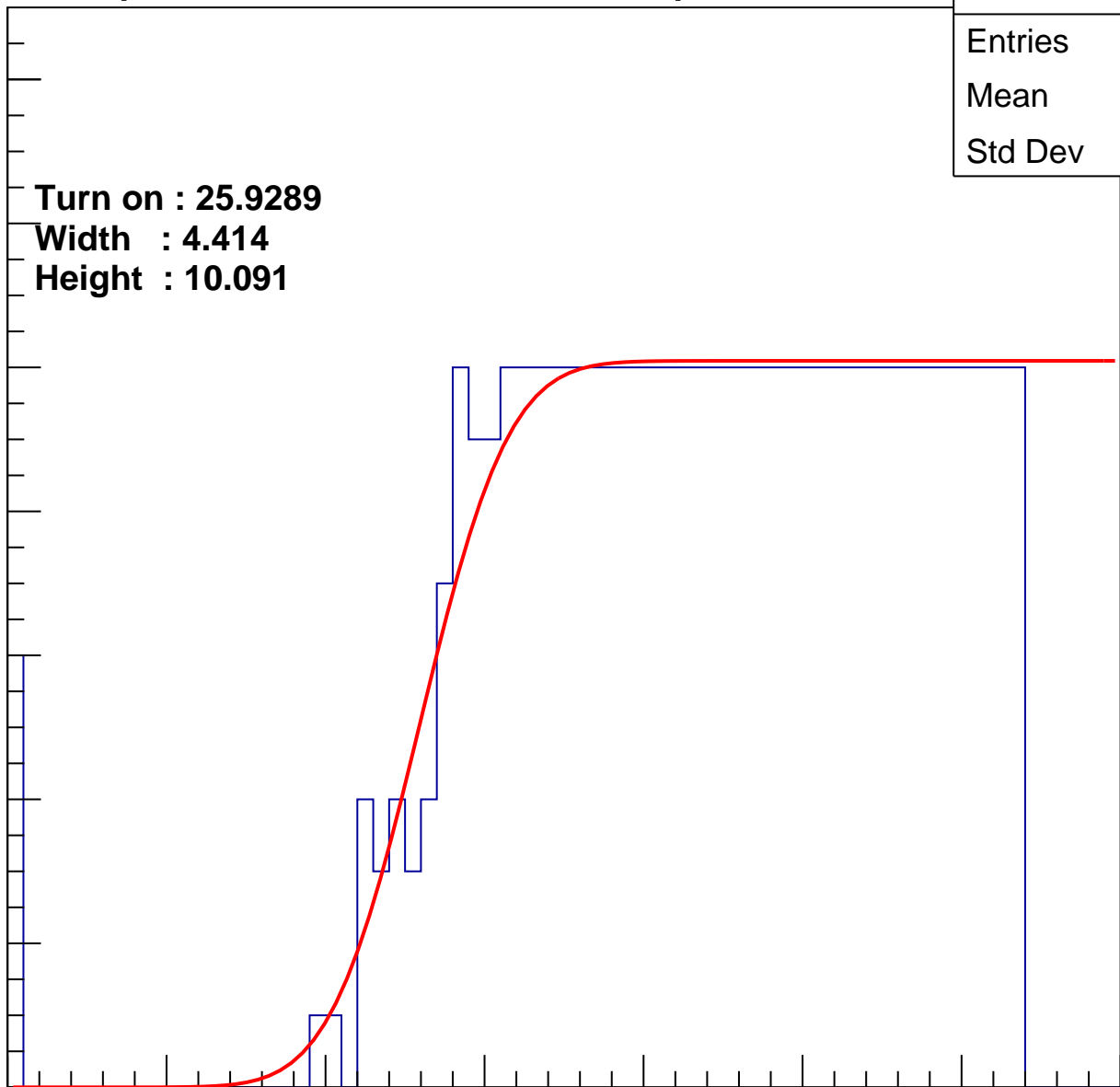
Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U5-ch27

calib_packv5_042523_0143.root, FC#5, port B1

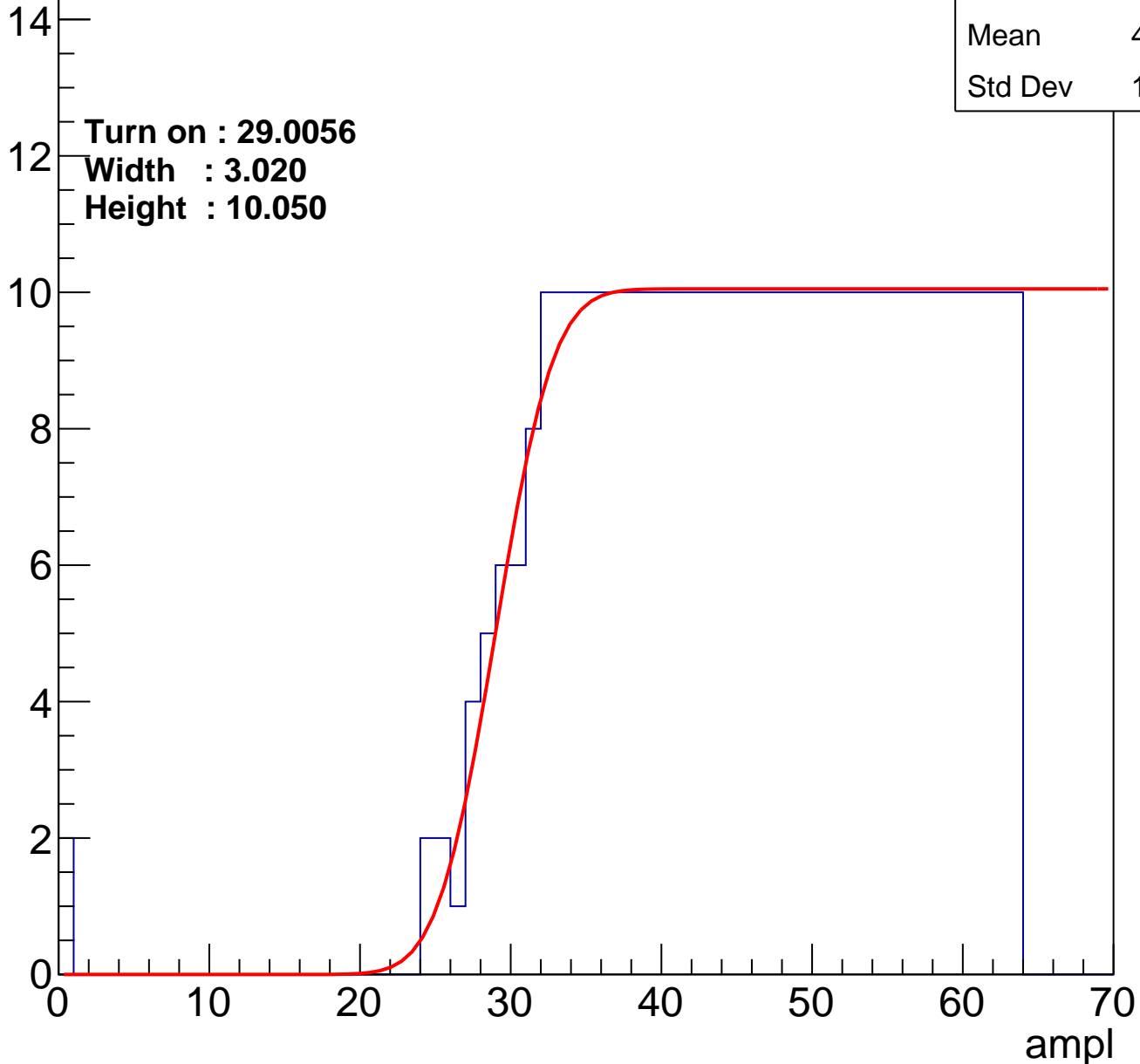
Entries	356
Mean	45.43
Std Dev	10.93

Turn on : 29.0056

Width : 3.020

Height : 10.050

Entry



B0L000S, U5-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.3697

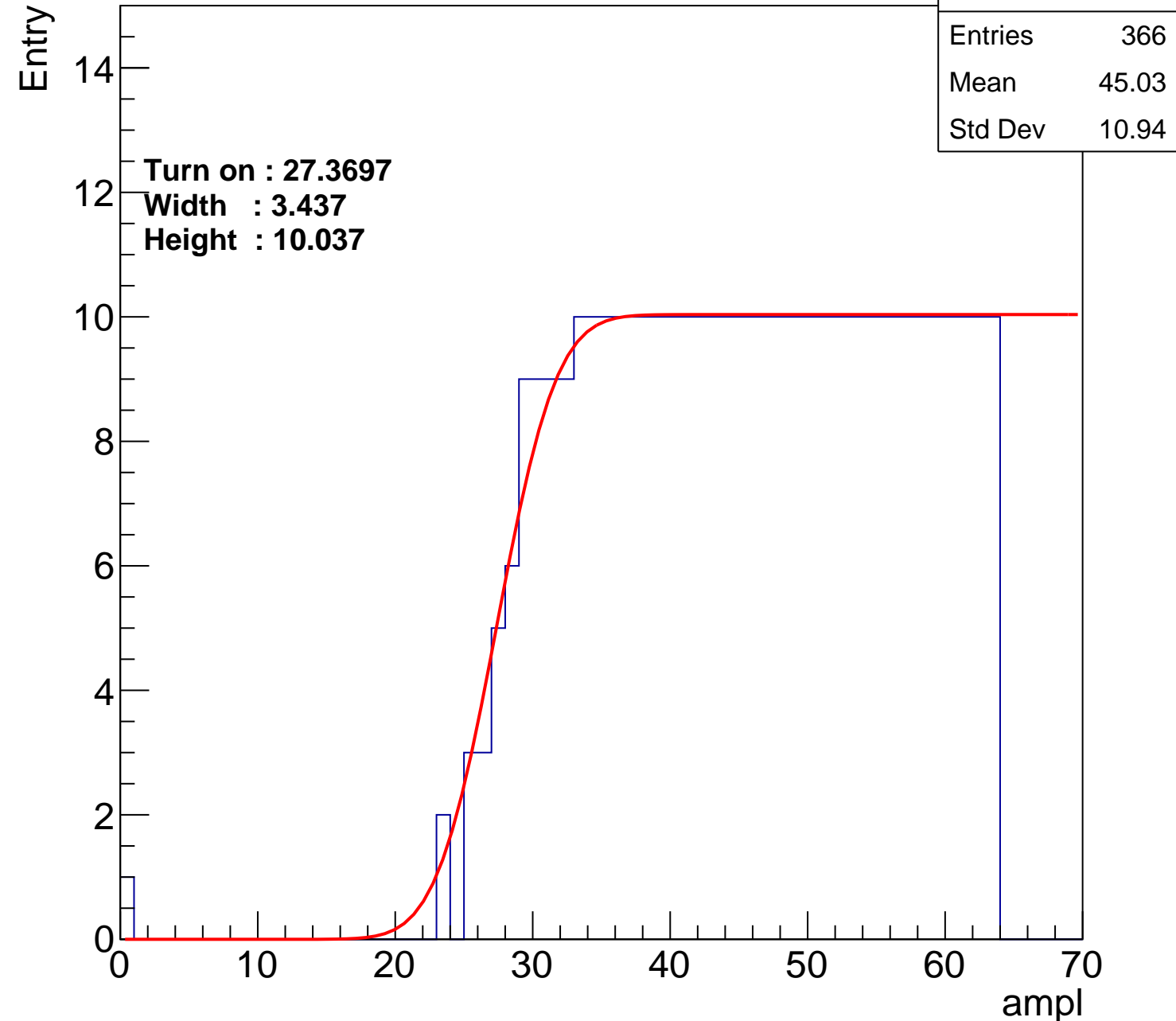
Width : 3.437

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch29

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.79
Std Dev	12.04

Turn on : 24.7817

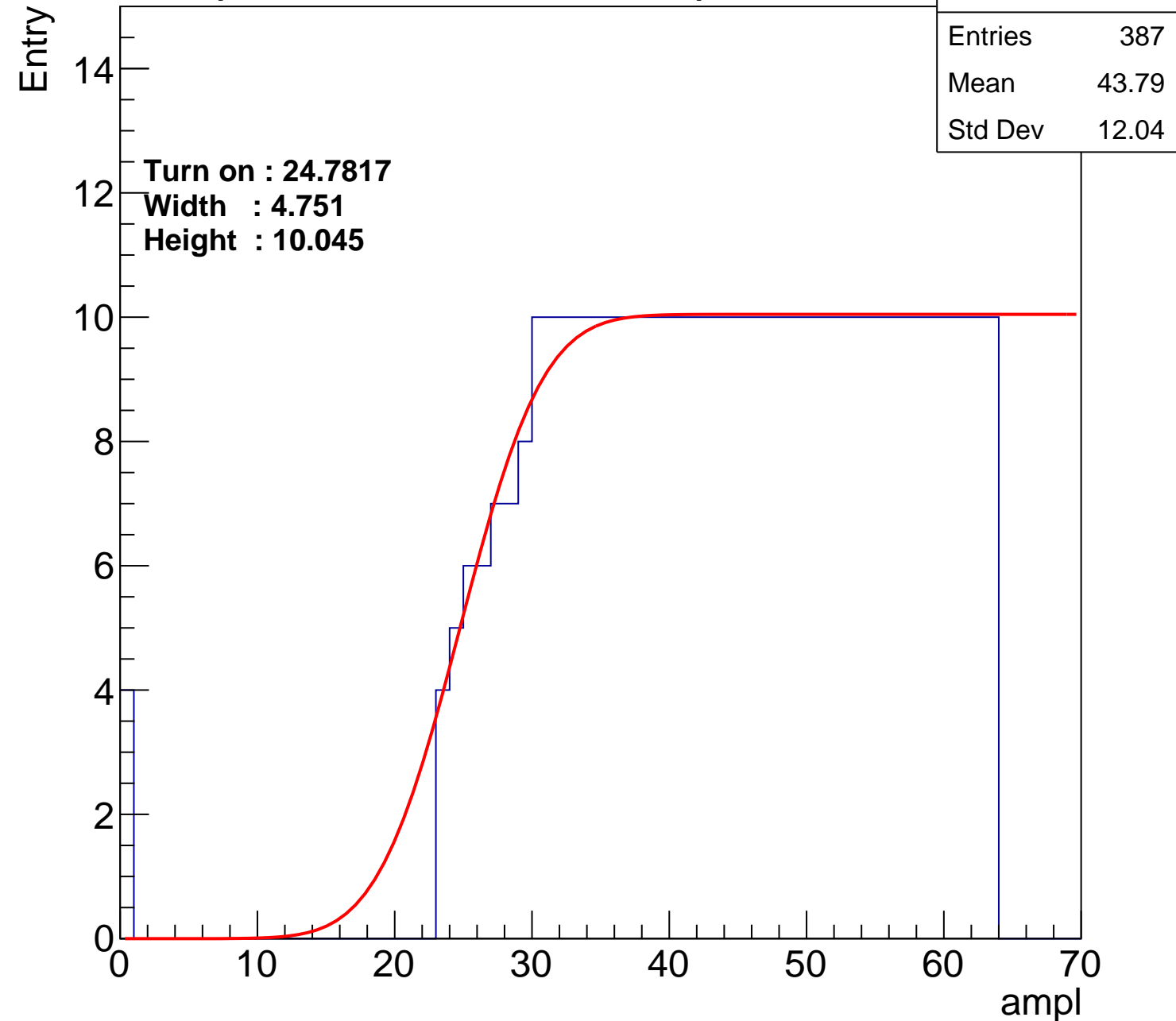
Width : 4.751

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch30

calib_packv5_042523_0143.root, FC#5, port B1

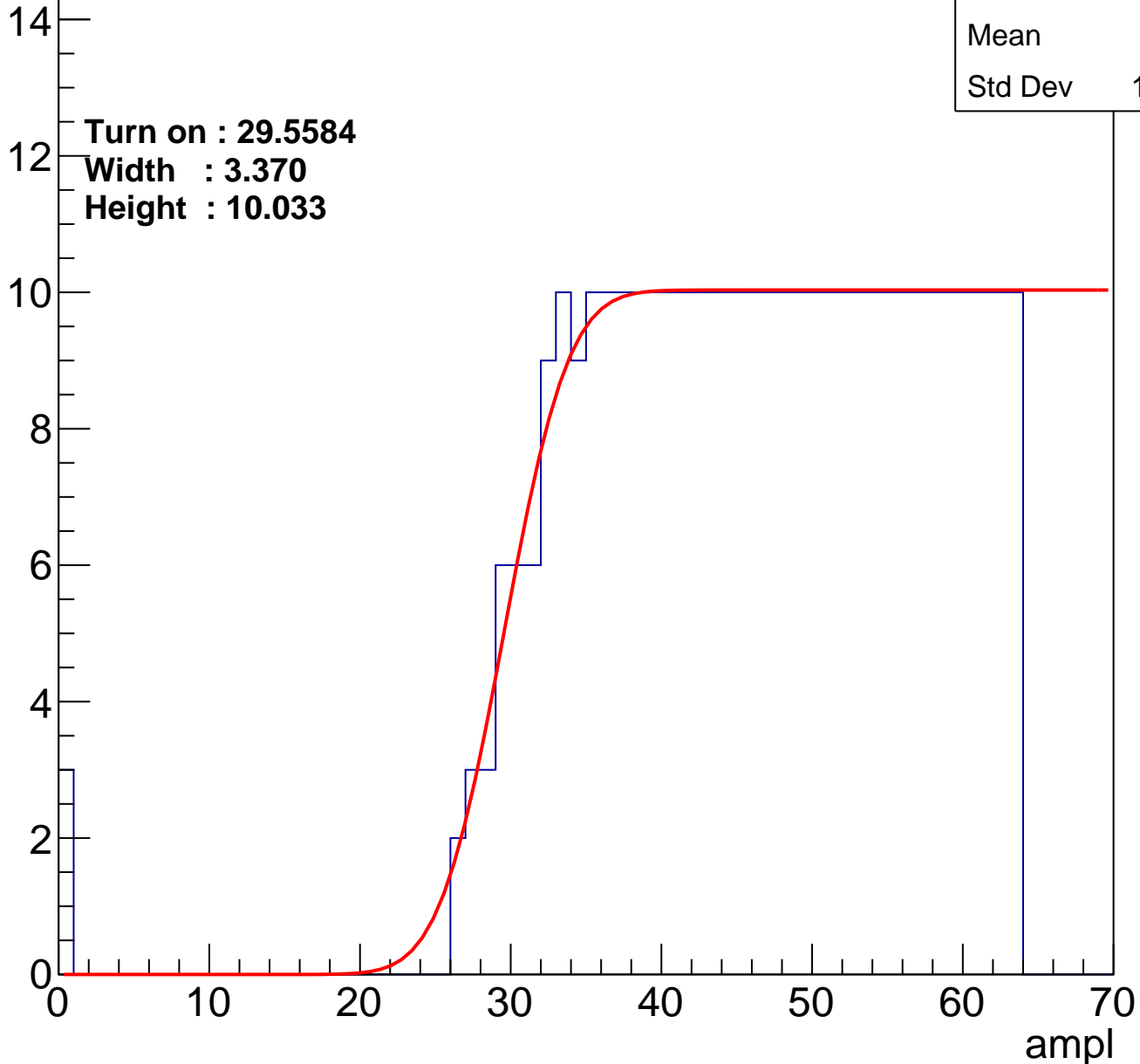
Entries	347
Mean	45.8
Std Dev	10.93

Turn on : 29.5584

Width : 3.370

Height : 10.033

Entry



B0L000S, U5-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.28
Std Dev	10.81

Turn on : 28.5498

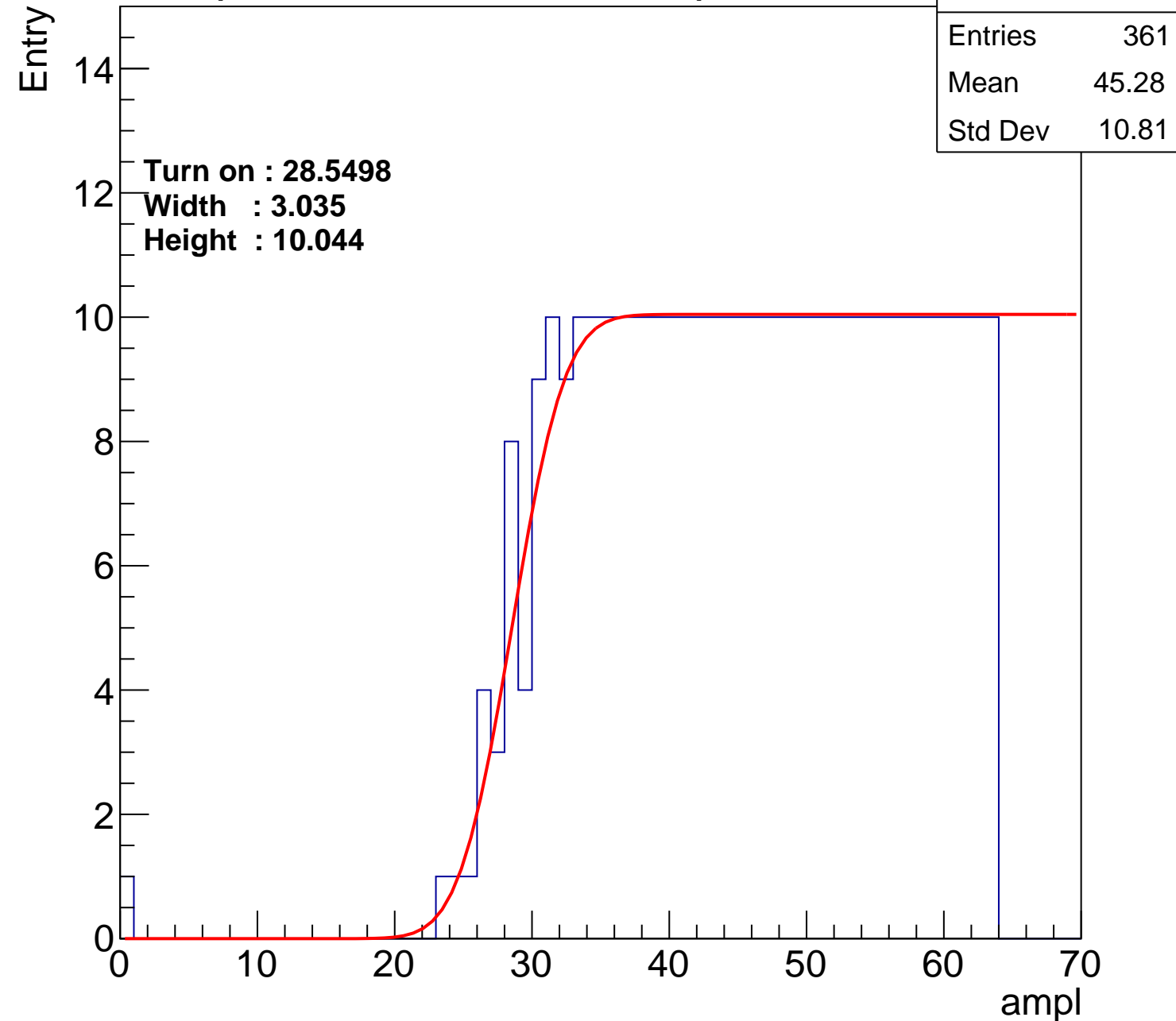
Width : 3.035

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	346
Mean	45.75
Std Dev	11.16

Turn on : 30.0933

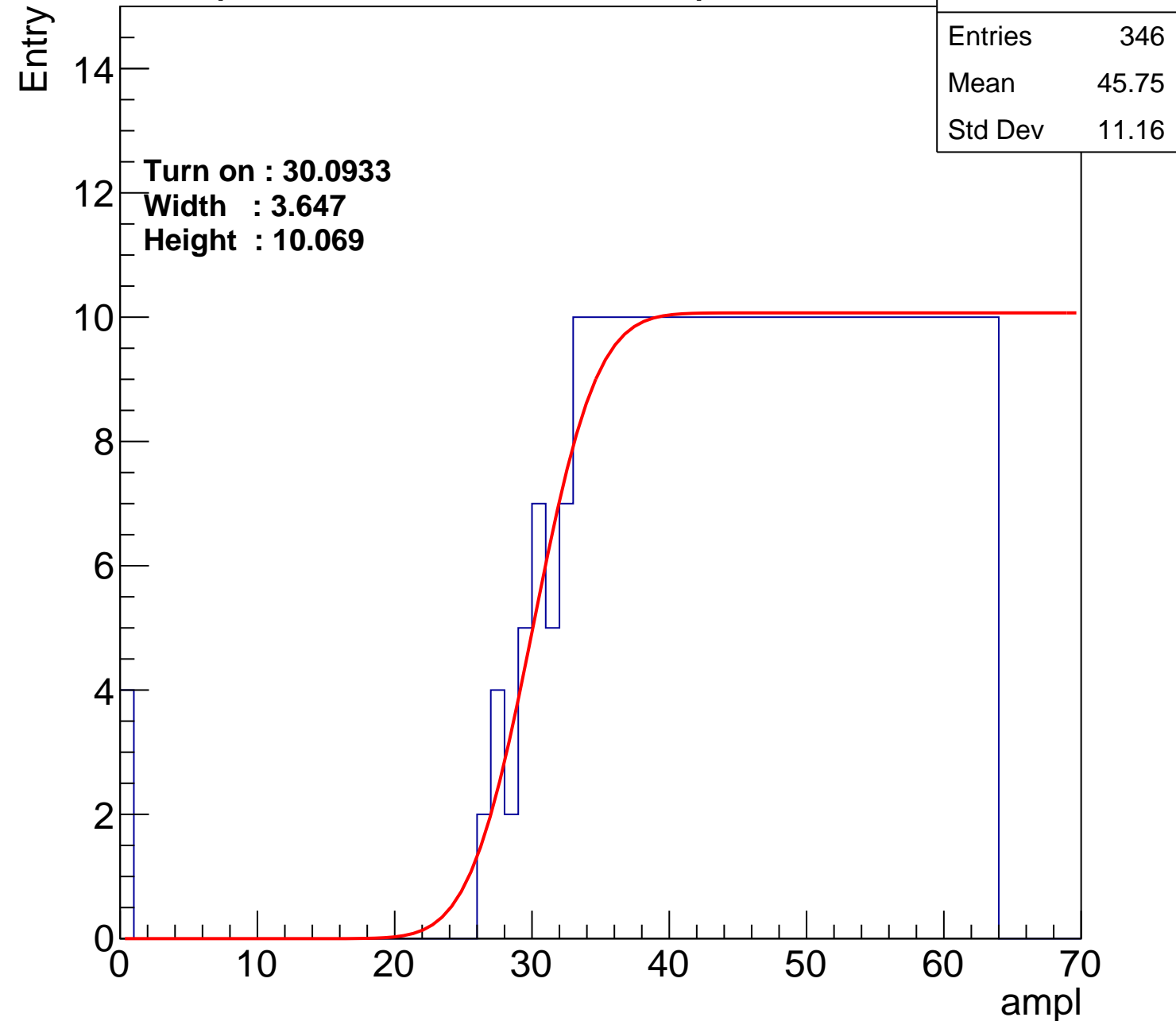
Width : 3.647

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch33

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.21
Std Dev	11.26

Turn on : 27.9273

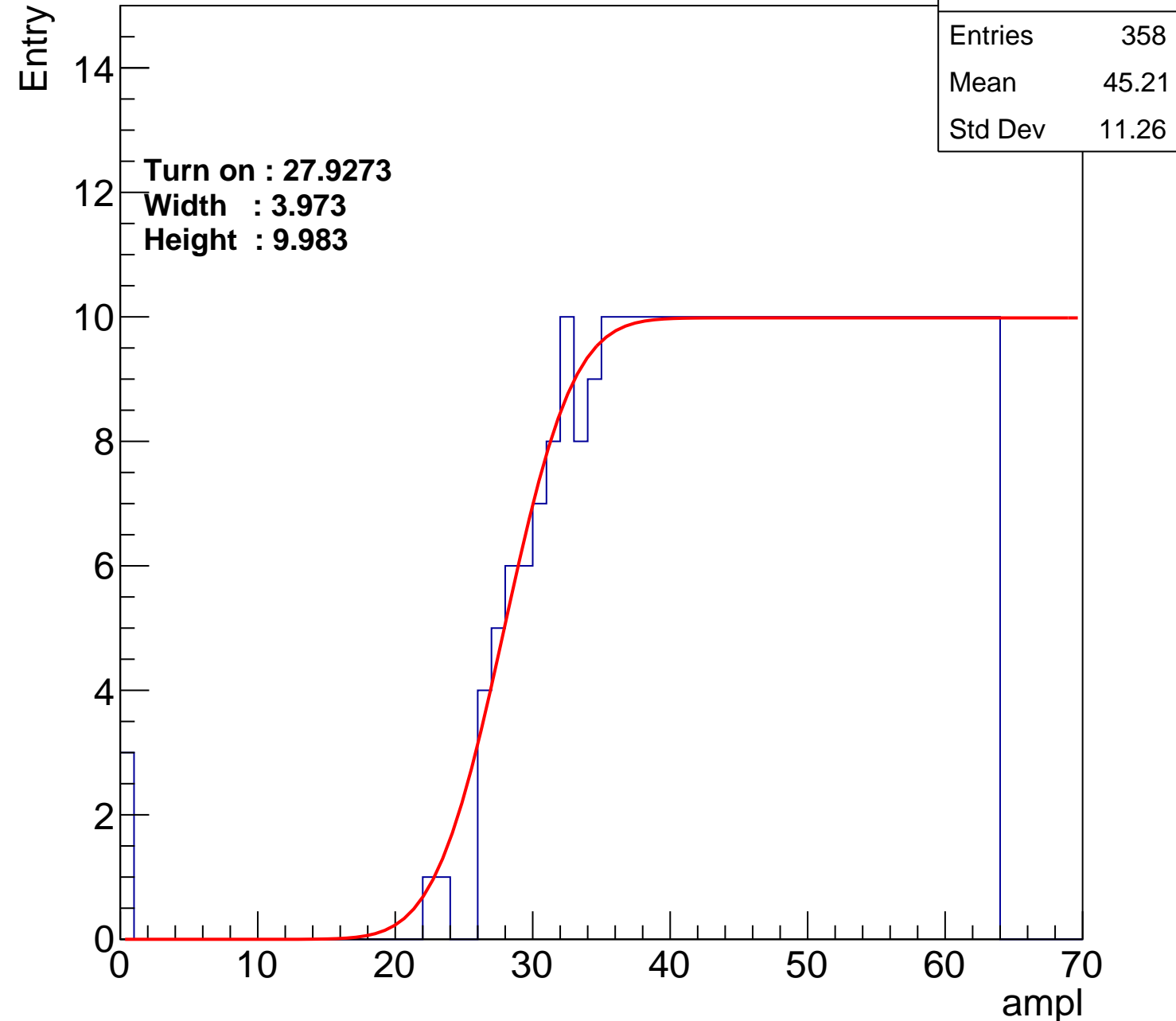
Width : 3.973

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch34

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.75
Std Dev	11.6

Turn on : 28.1076

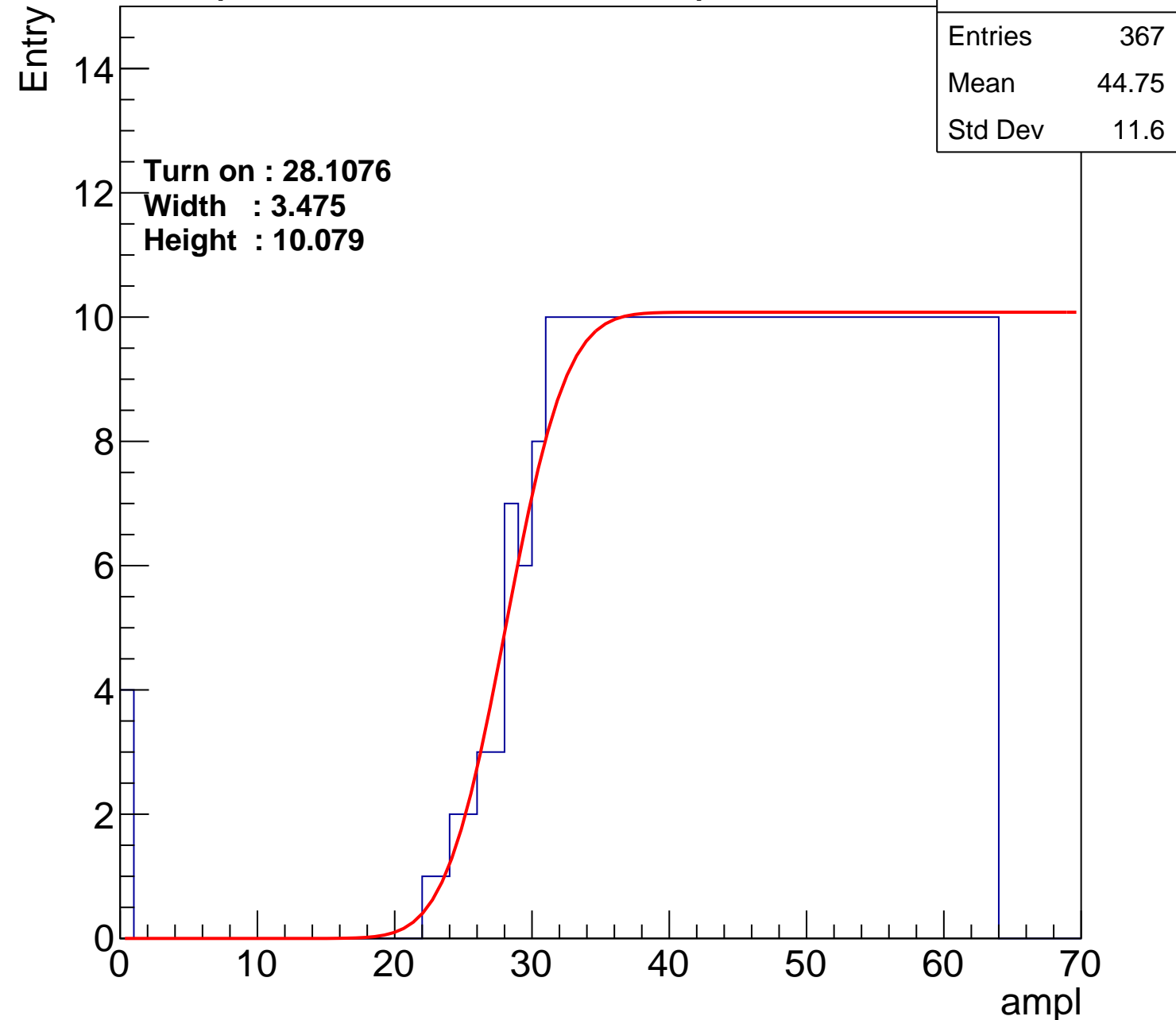
Width : 3.475

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch35

calib_packv5_042523_0143.root, FC#5, port B1

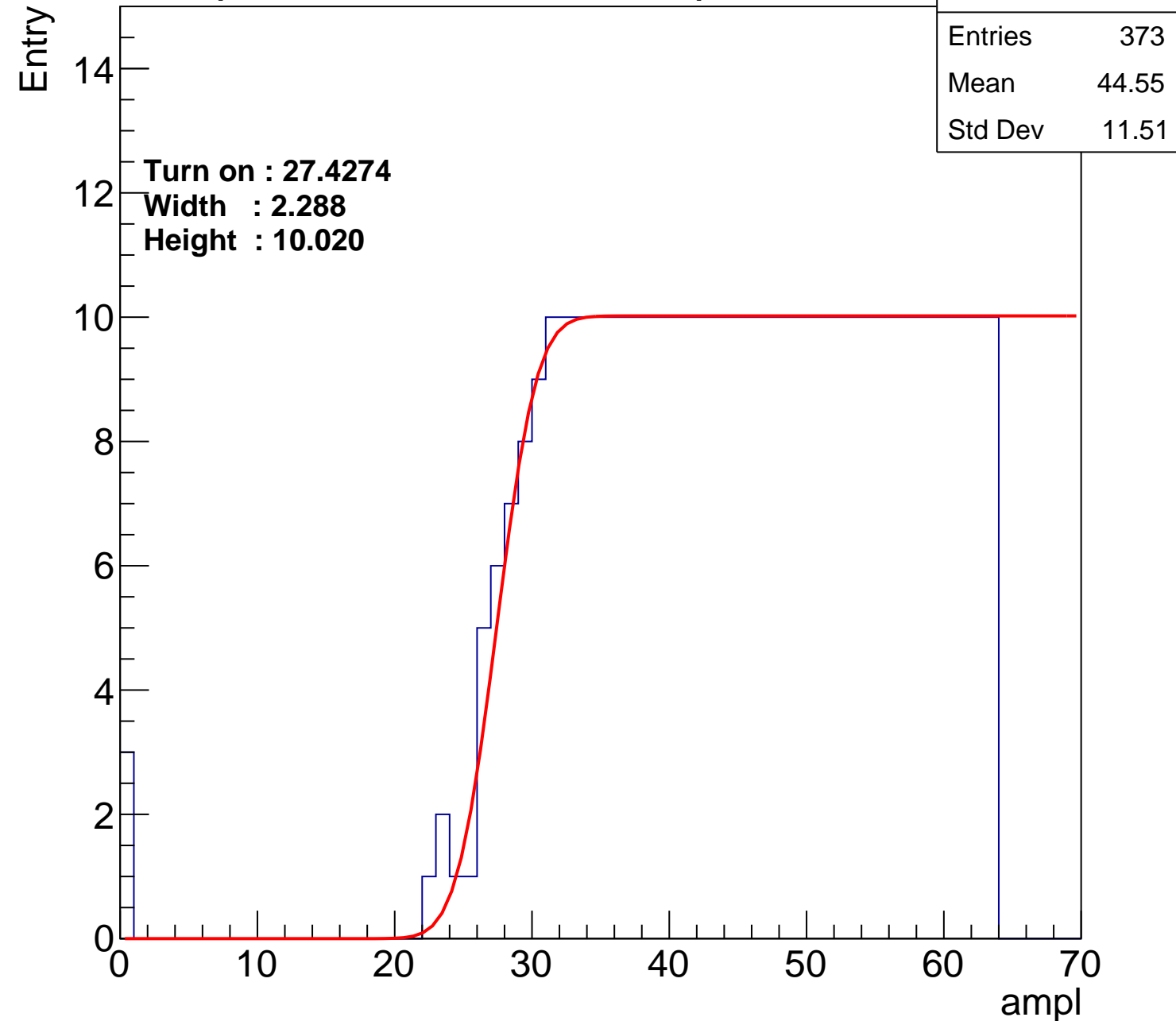
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4274
Width : 2.288
Height : 10.020

Entries	373
Mean	44.55
Std Dev	11.51

ampl



B0L000S, U5-ch36

calib_packv5_042523_0143.root, FC#5, port B1

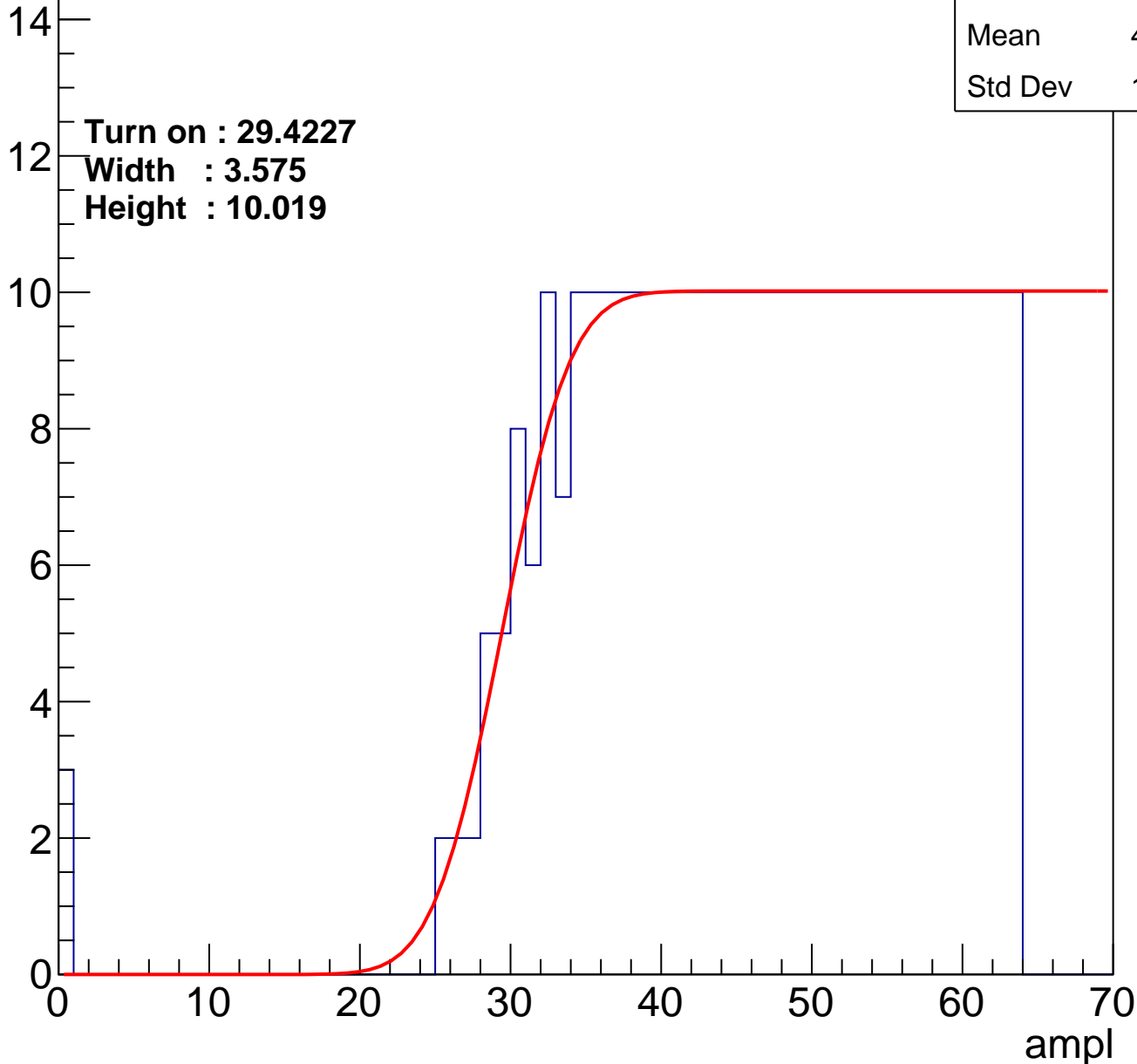
Entry

Entries	350
Mean	45.62
Std Dev	11.04

Turn on : 29.4227

Width : 3.575

Height : 10.019



B0L000S, U5-ch37

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.92
Std Dev	11.15

Turn on : 27.7853

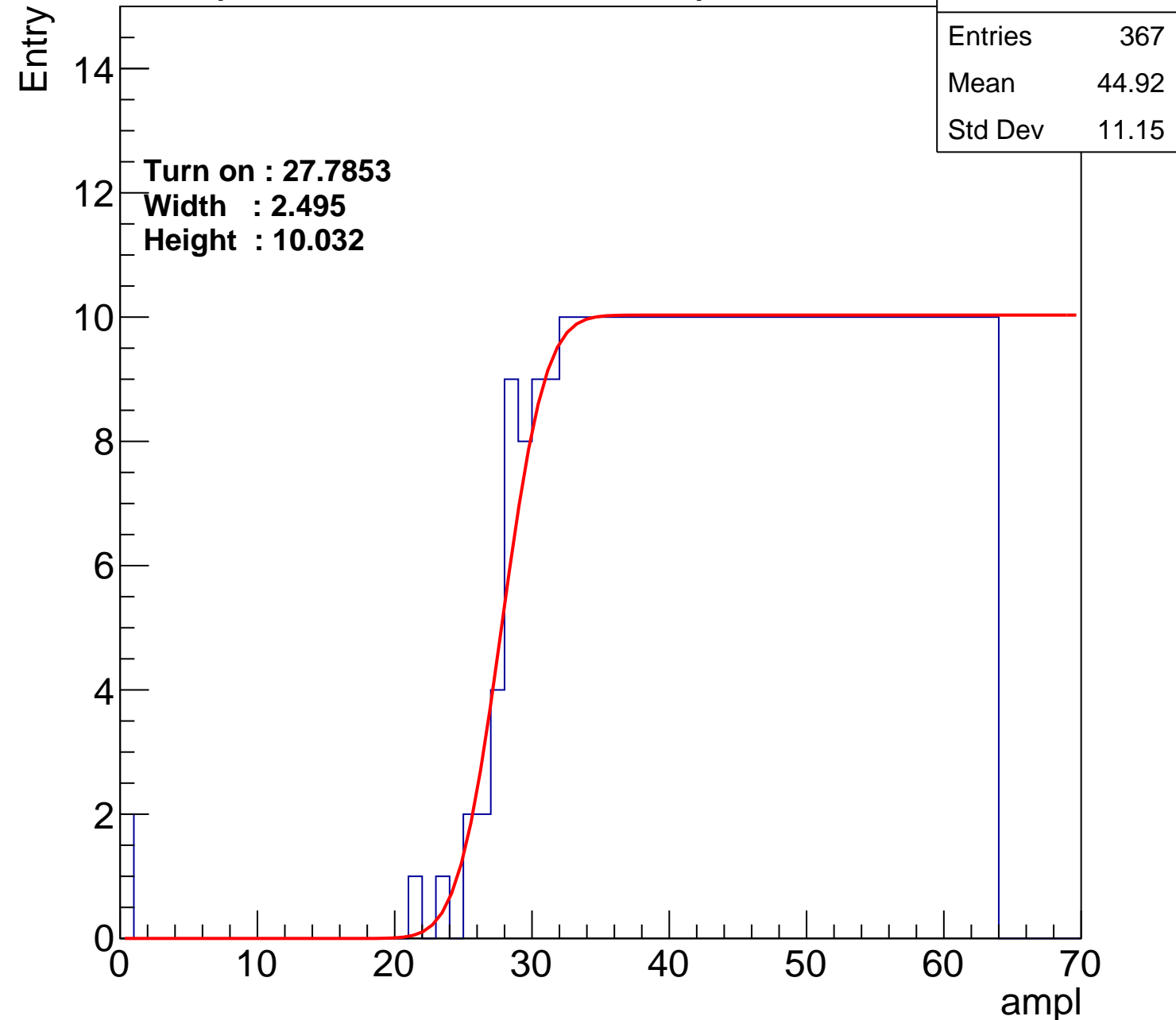
Width : 2.495

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch38

calib_packv5_042523_0143.root, FC#5, port B1

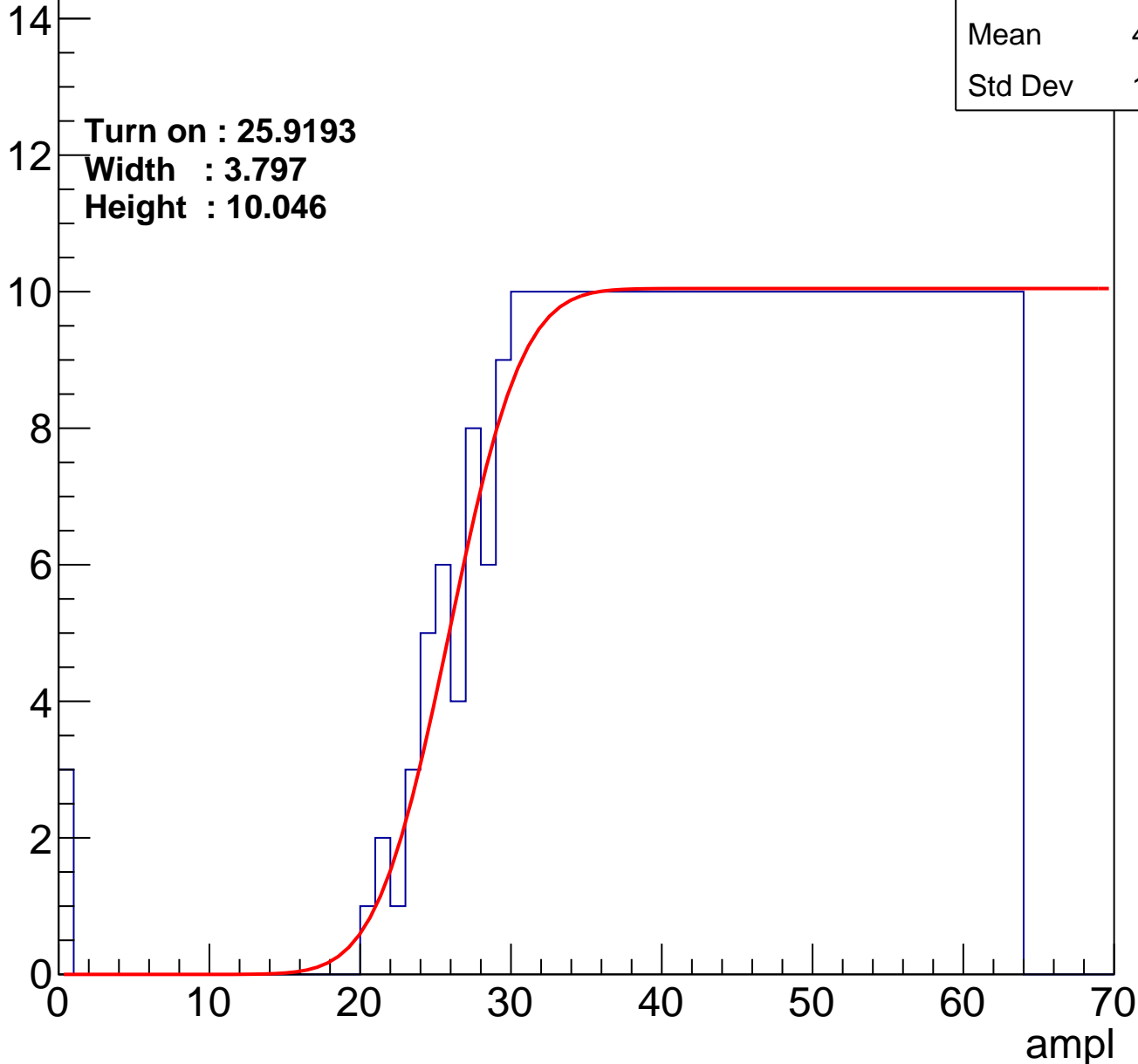
Entry

Entries	388
Mean	43.77
Std Dev	11.95

Turn on : 25.9193

Width : 3.797

Height : 10.046



B0L000S, U5-ch39

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.3
Std Dev	10.99

Turn on : 28.4998

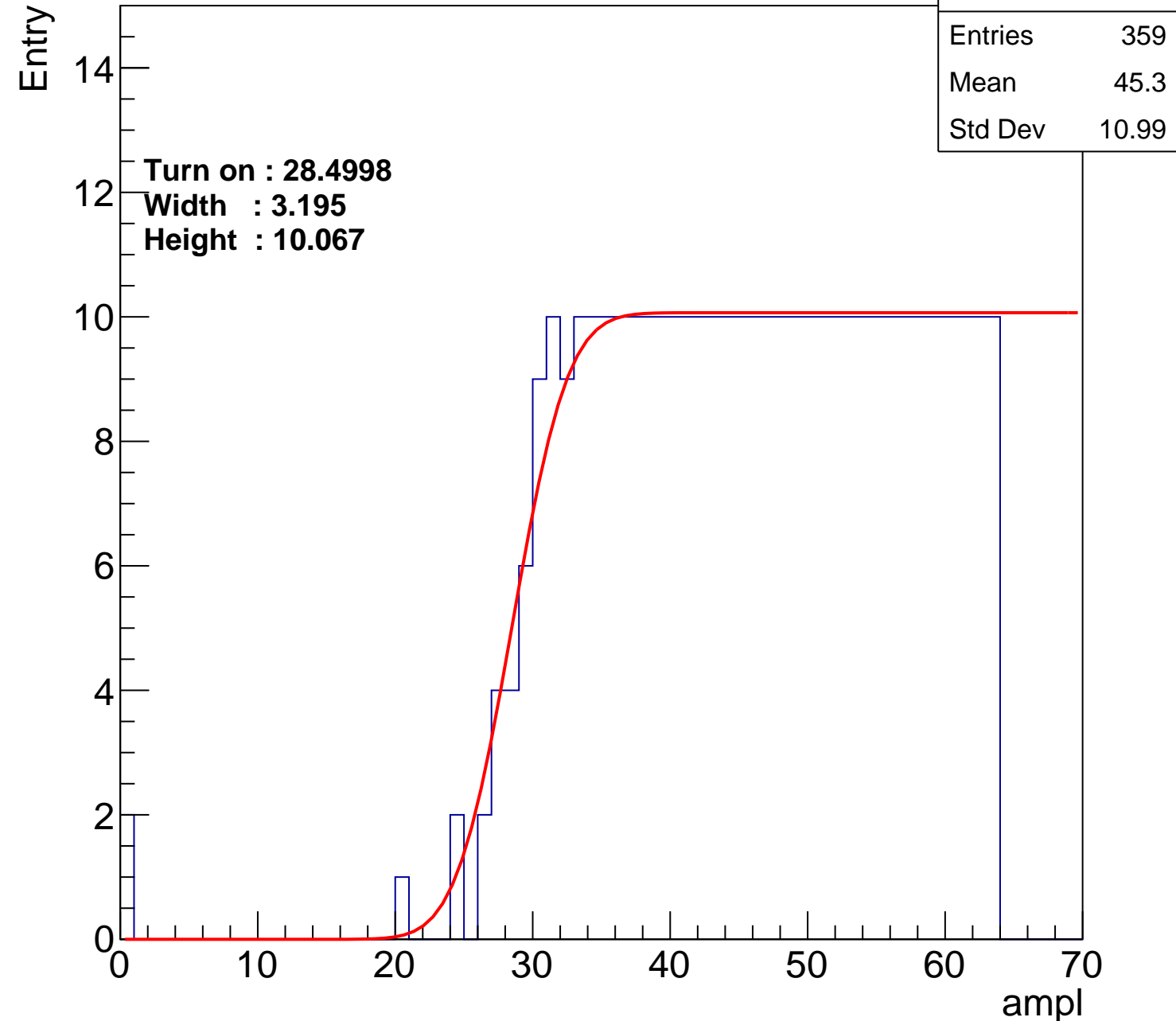
Width : 3.195

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch40

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 27.2220

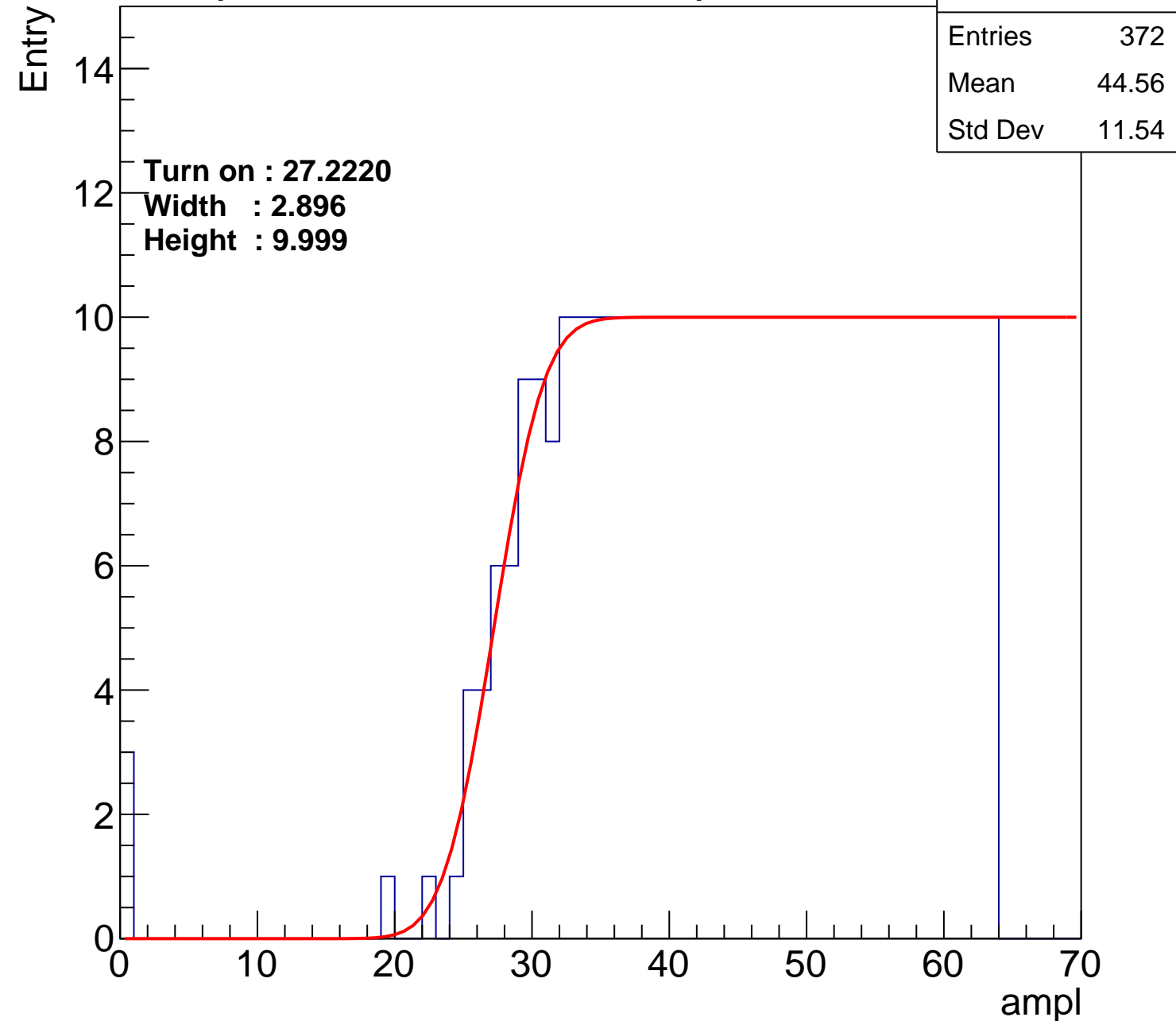
Width : 2.896

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch41

calib_packv5_042523_0143.root, FC#5, port B1

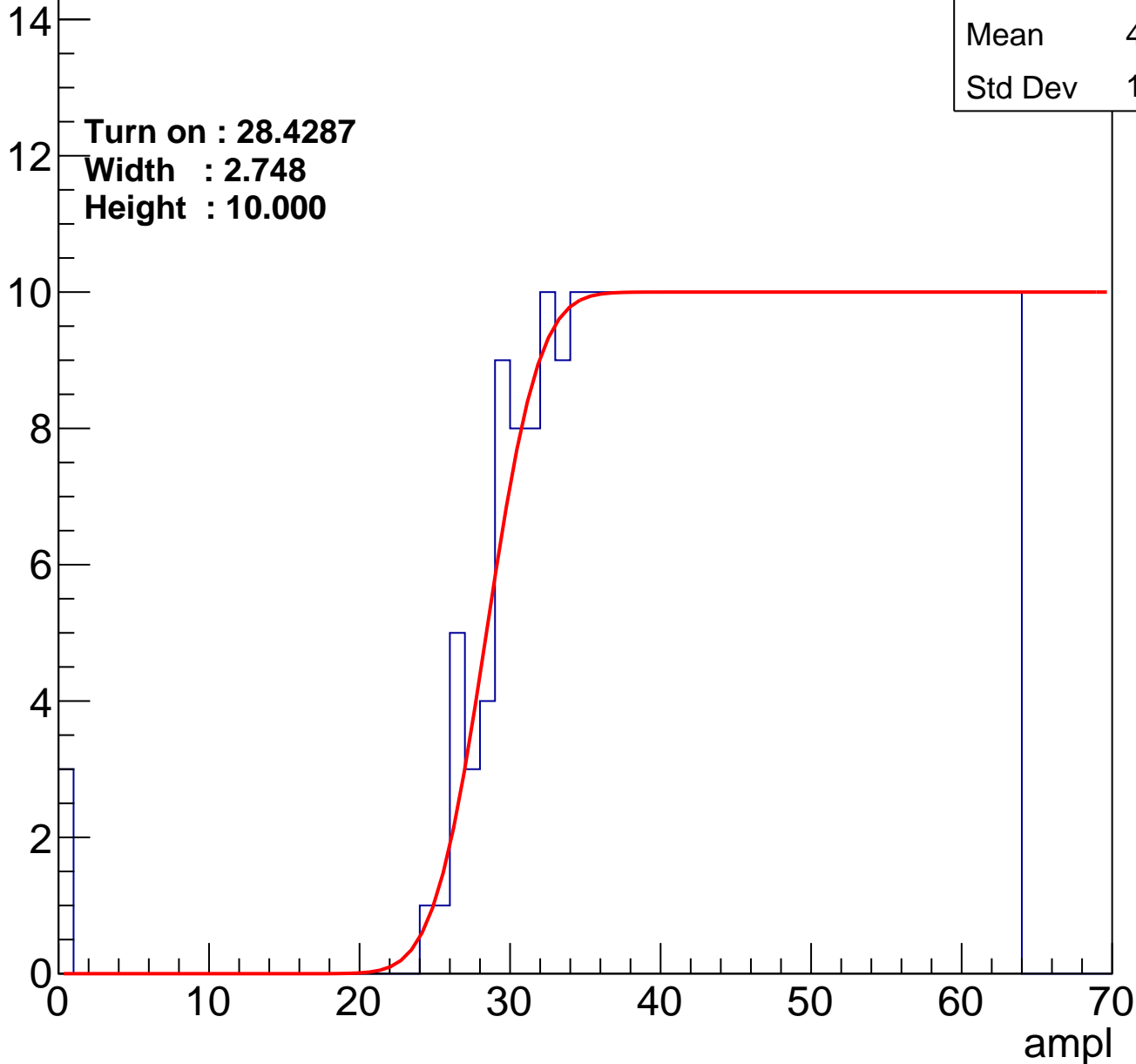
Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.4287

Width : 2.748

Height : 10.000

Entry



B0L000S, U5-ch42

calib_packv5_042523_0143.root, FC#5, port B1

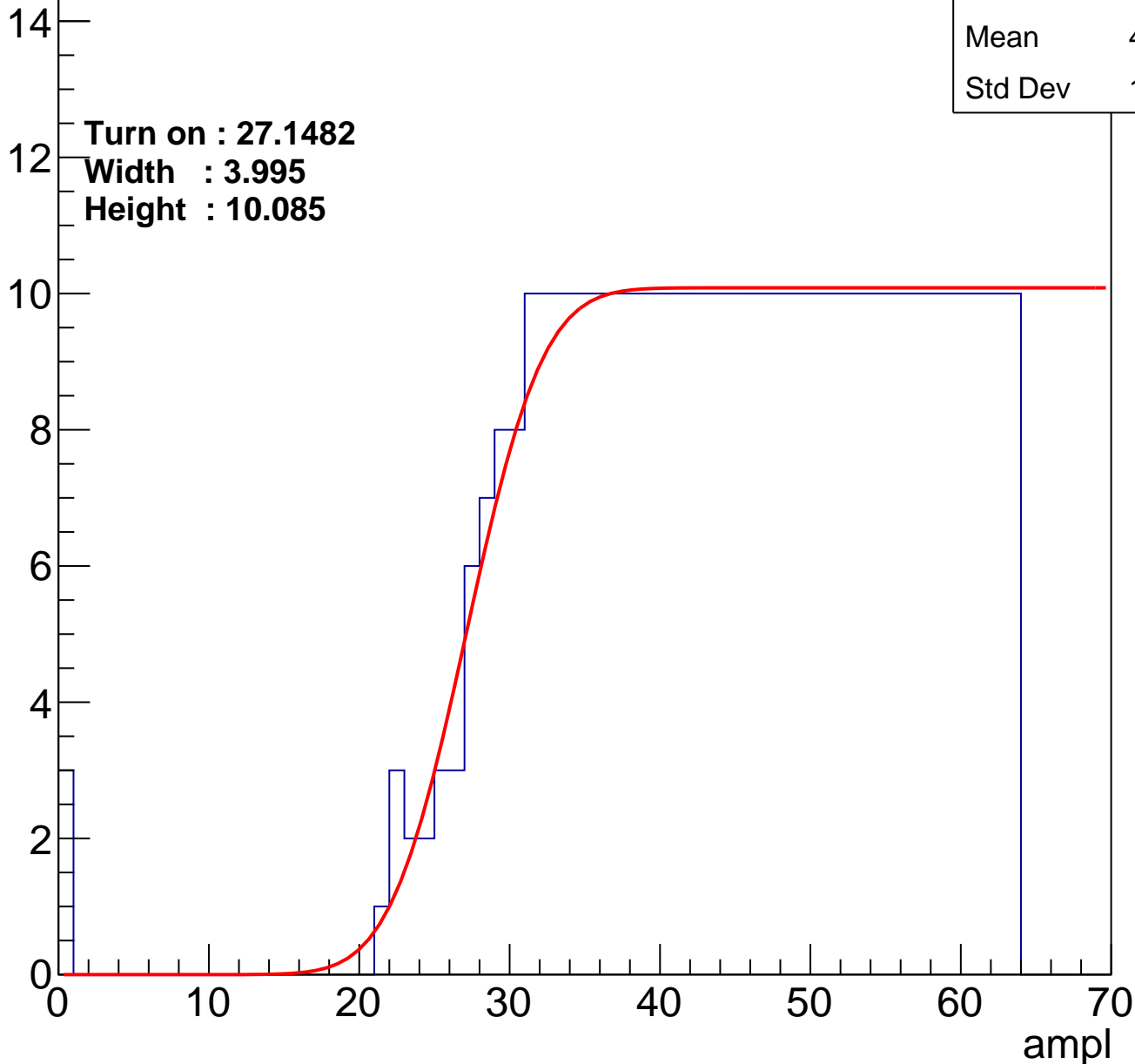
Entry

Entries	376
Mean	44.35
Std Dev	11.68

Turn on : 27.1482

Width : 3.995

Height : 10.085



B0L000S, U5-ch43

calib_packv5_042523_0143.root, FC#5, port B1

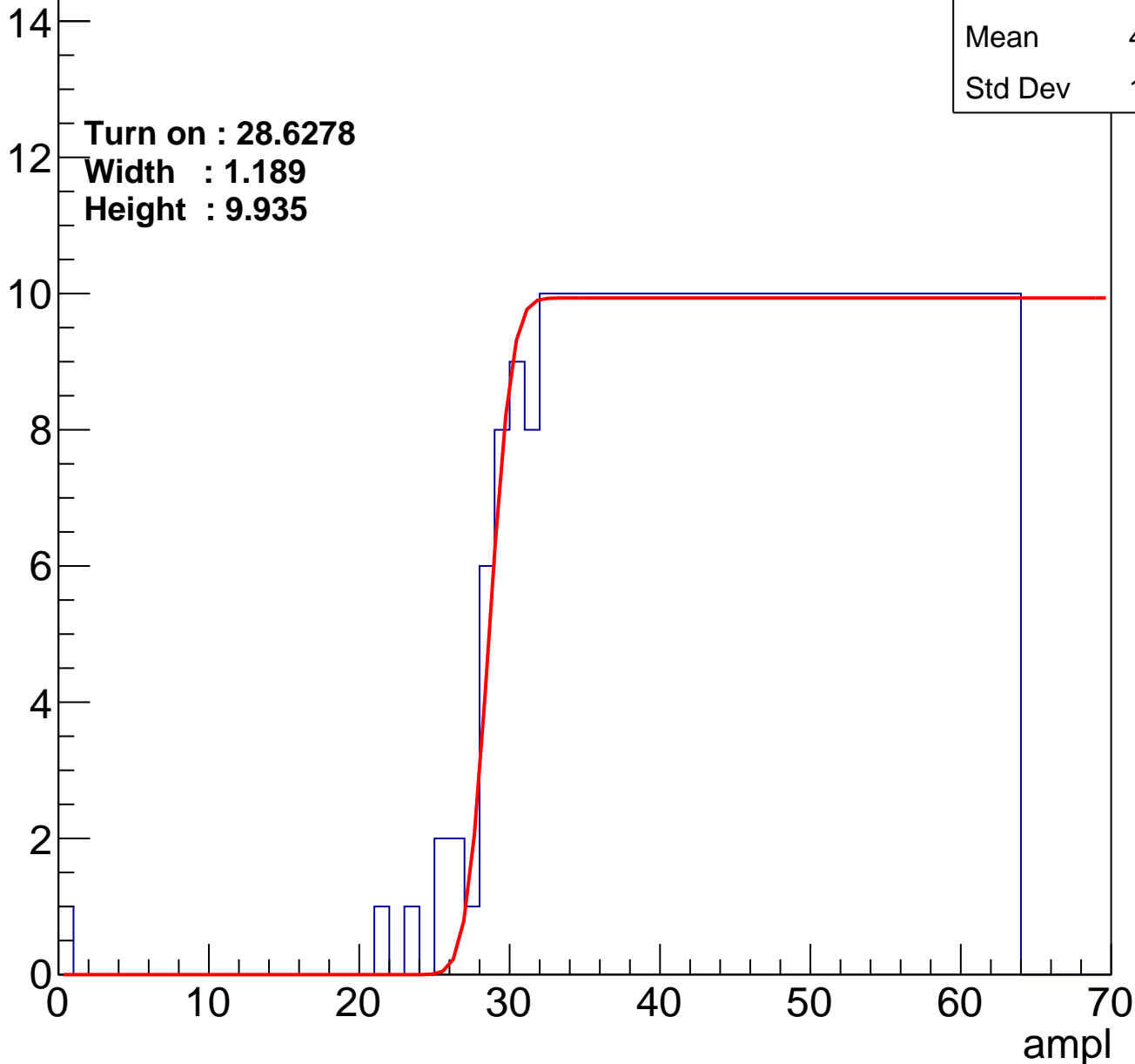
Entry

Entries	359
Mean	45.38
Std Dev	10.76

Turn on : 28.6278

Width : 1.189

Height : 9.935



B0L000S, U5-ch44

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	43.96
Std Dev	11.85

Turn on : 25.9683

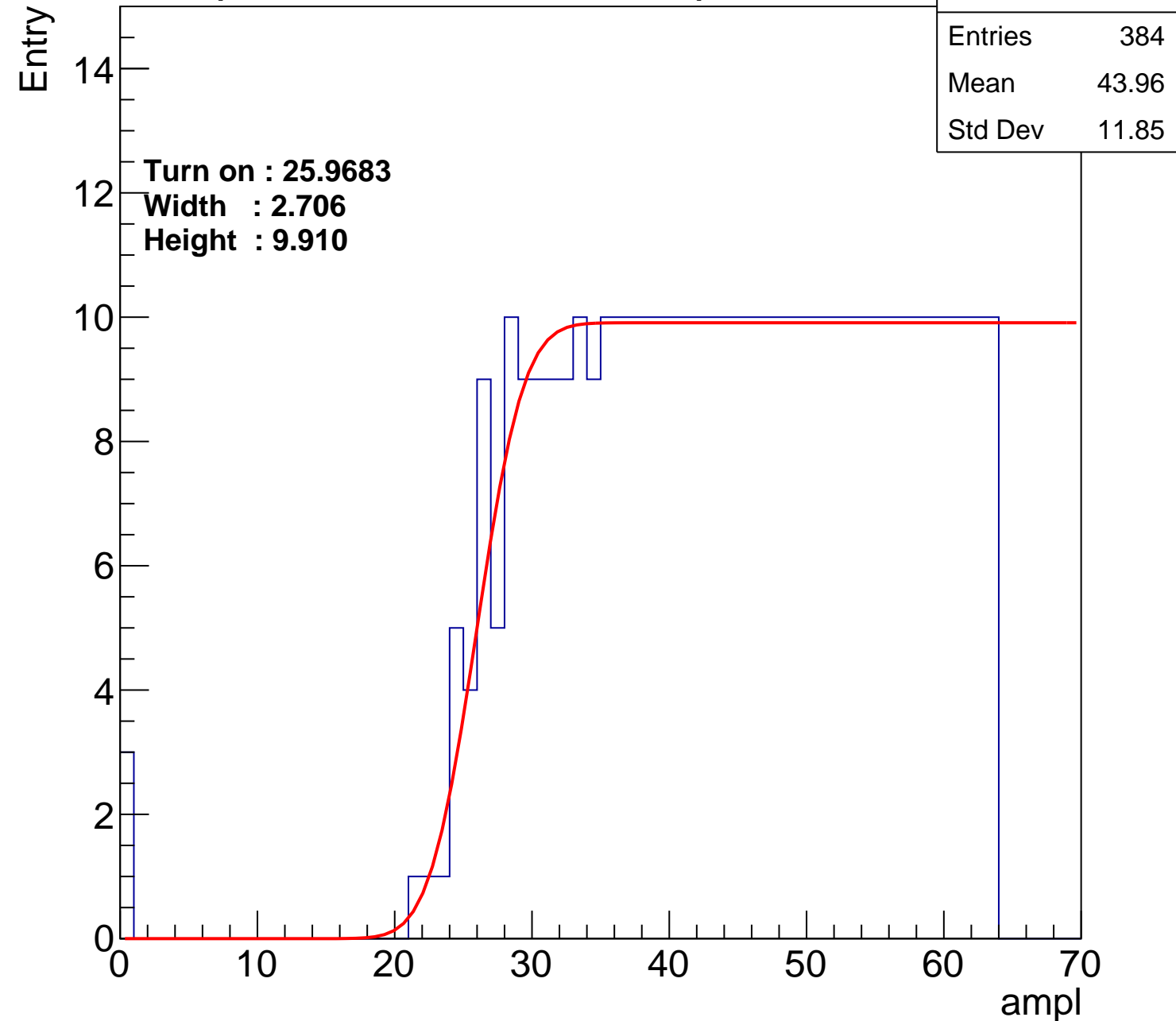
Width : 2.706

Height : 9.910

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch45

calib_packv5_042523_0143.root, FC#5, port B1

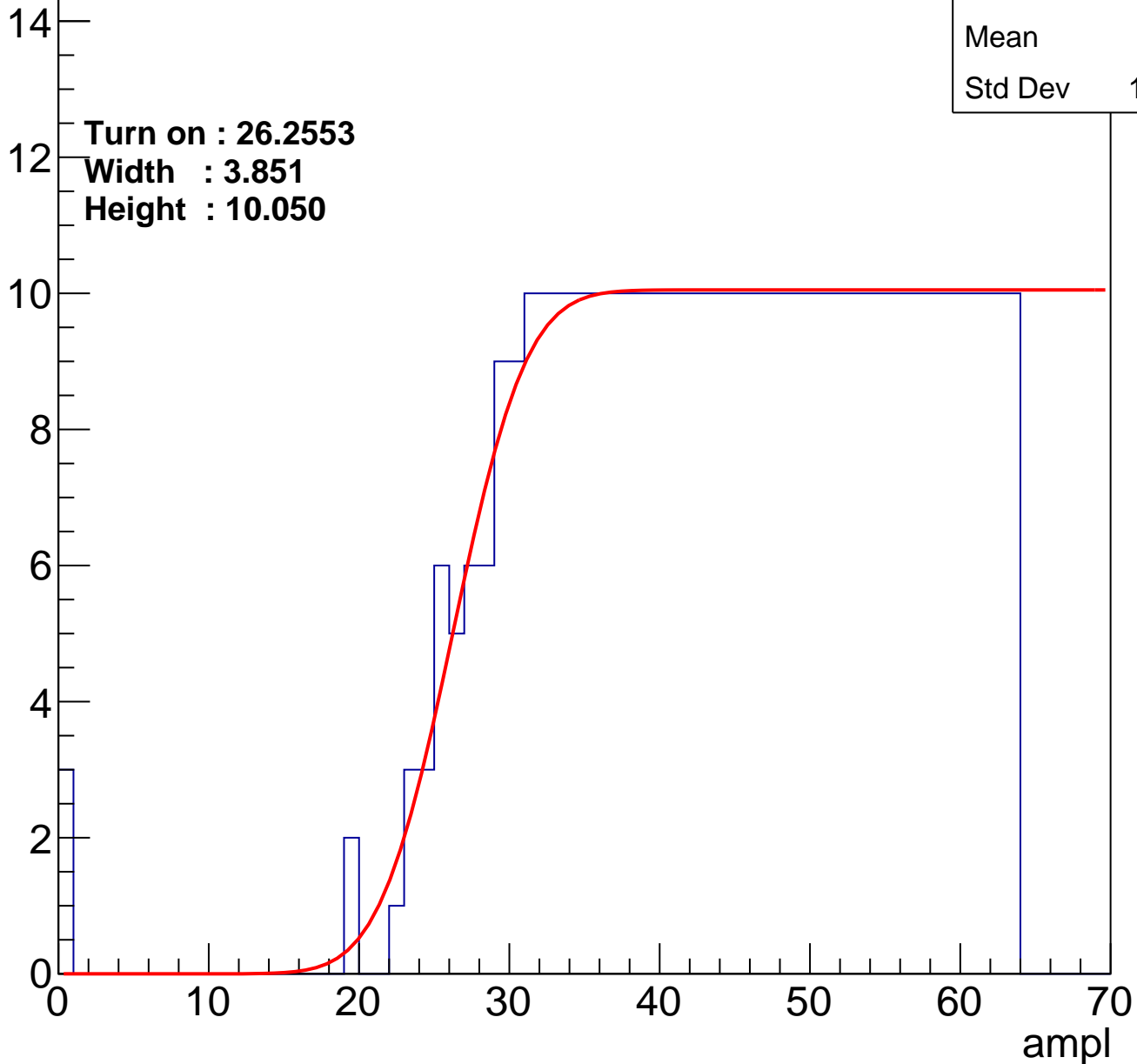
Entries	383
Mean	44
Std Dev	11.85

Turn on : 26.2553

Width : 3.851

Height : 10.050

Entry



B0L000S, U5-ch46

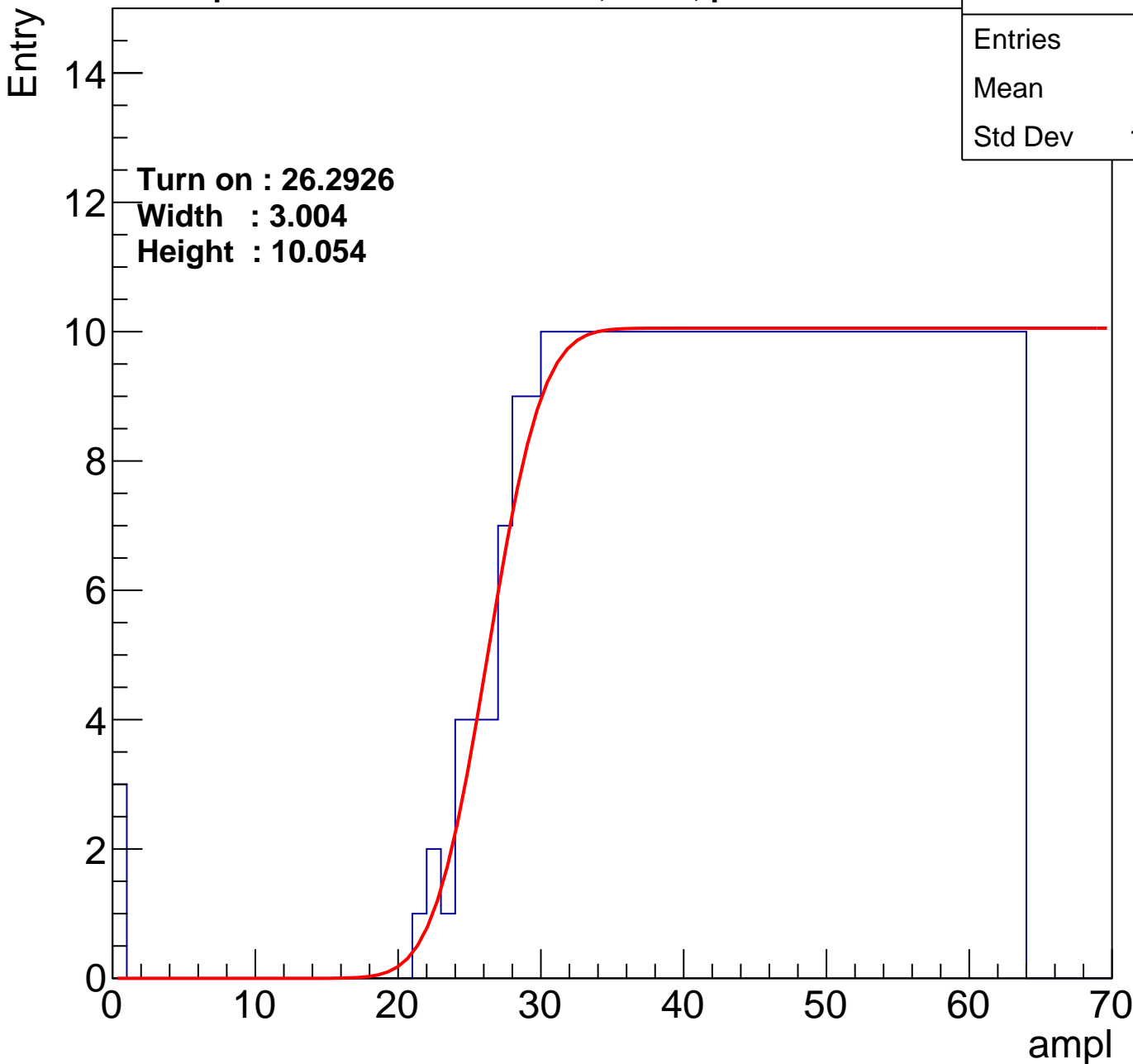
calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.01
Std Dev	11.78

Turn on : 26.2926

Width : 3.004

Height : 10.054



B0L000S, U5-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.42
Std Dev	11.41

Turn on : 26.4103

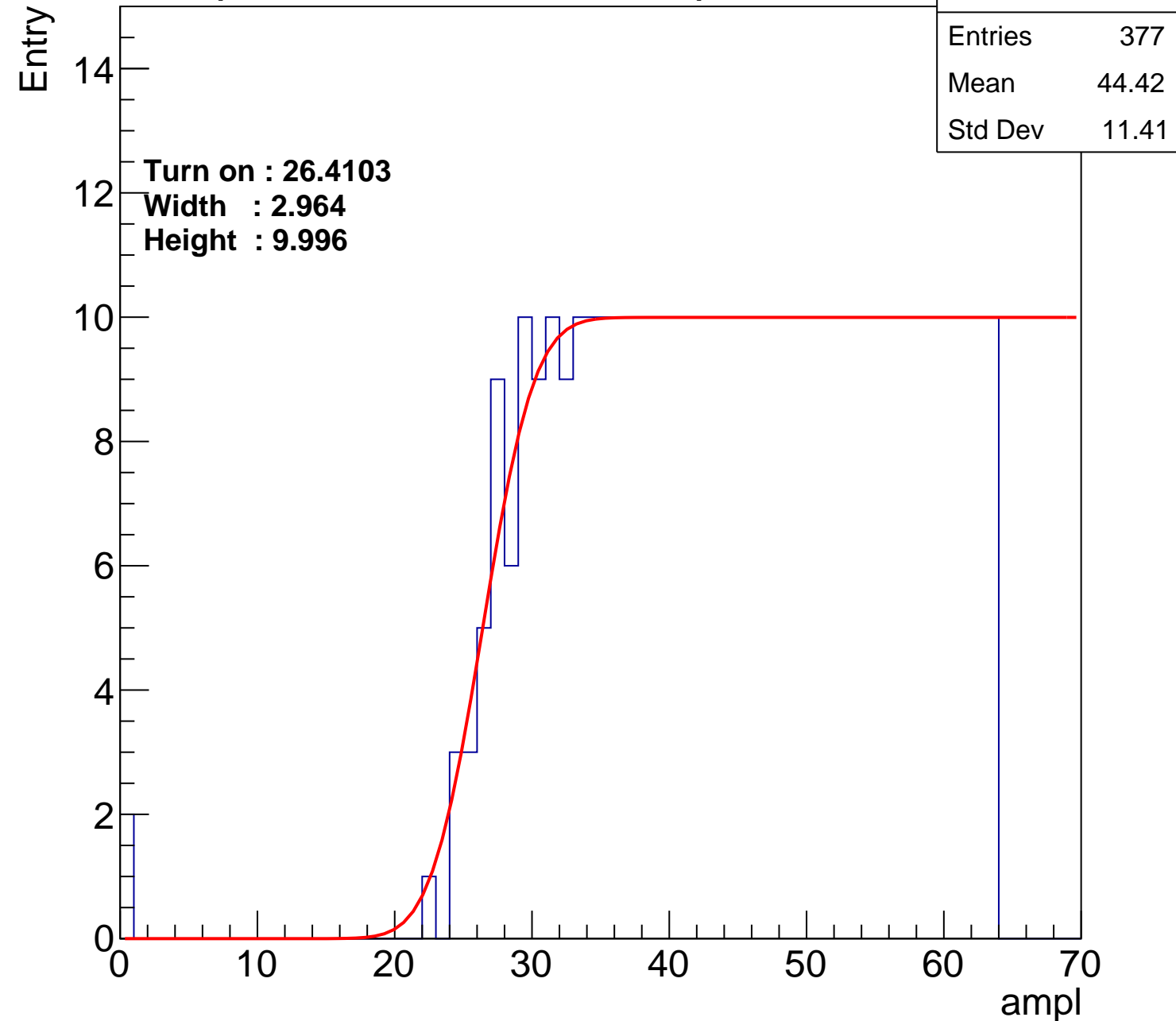
Width : 2.964

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.02
Std Dev	11.02

Turn on : 28.1908

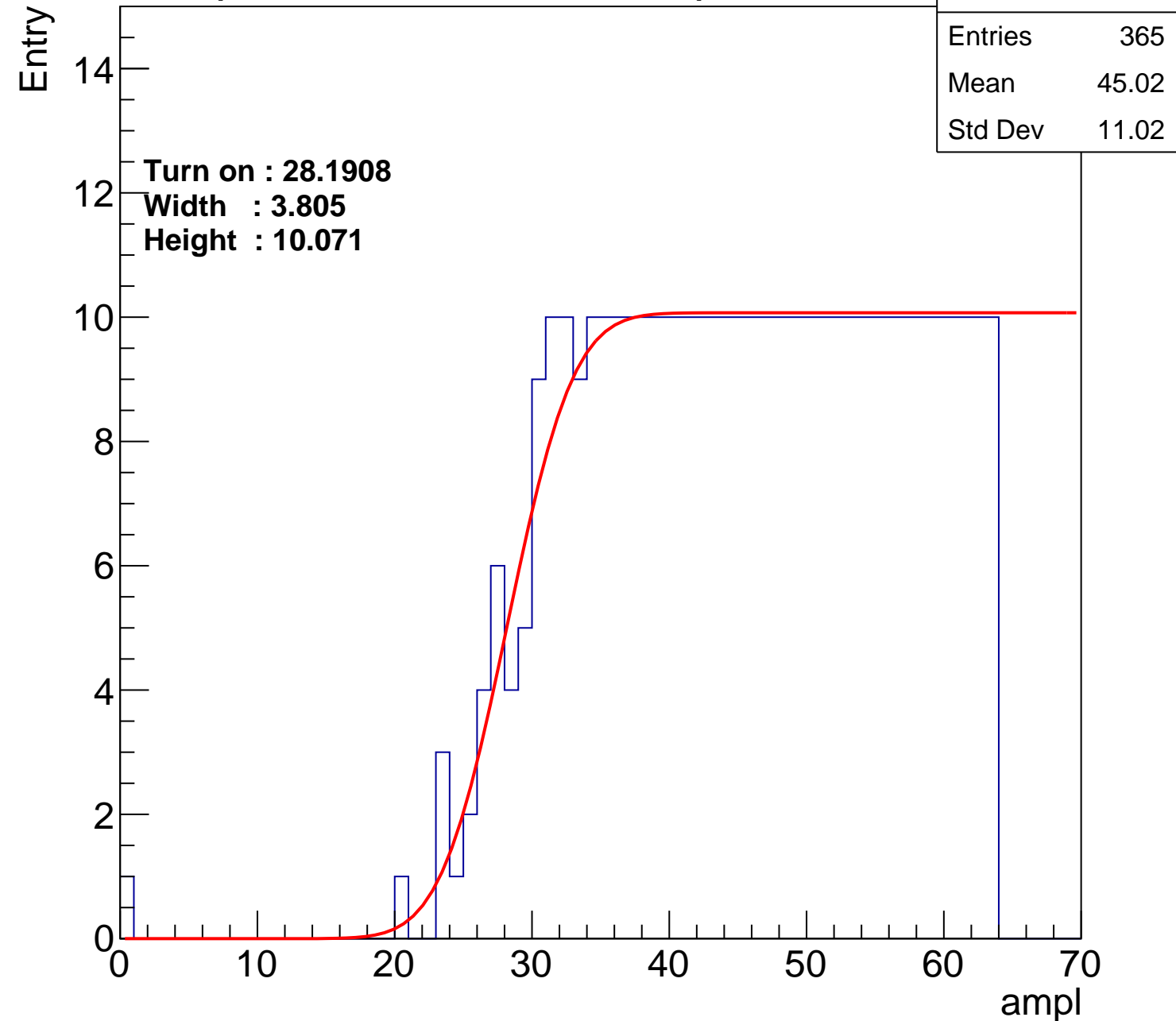
Width : 3.805

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.56
Std Dev	11.49

Turn on : 27.2723

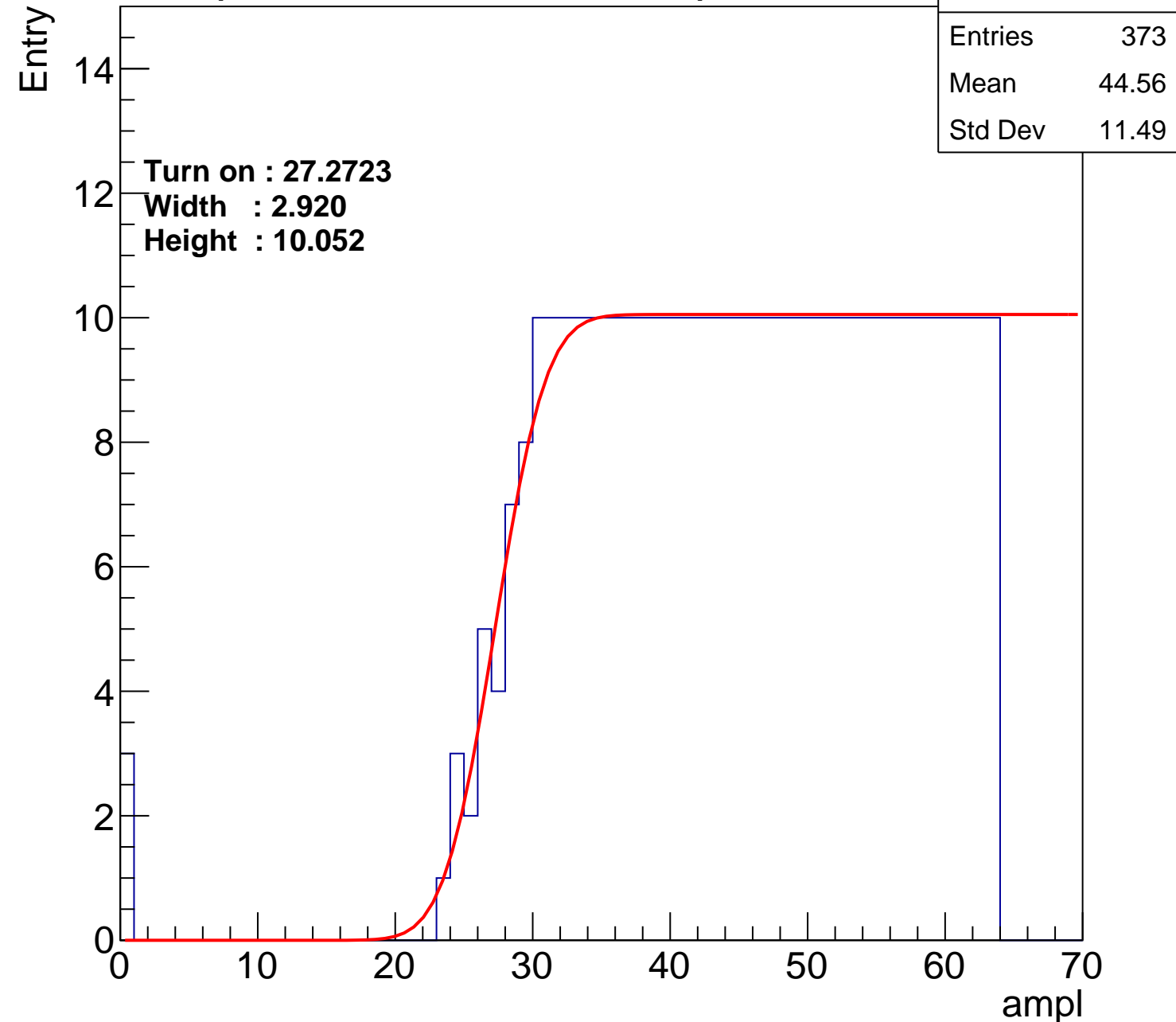
Width : 2.920

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch50

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.76
Std Dev	11.31

Turn on : 27.3690

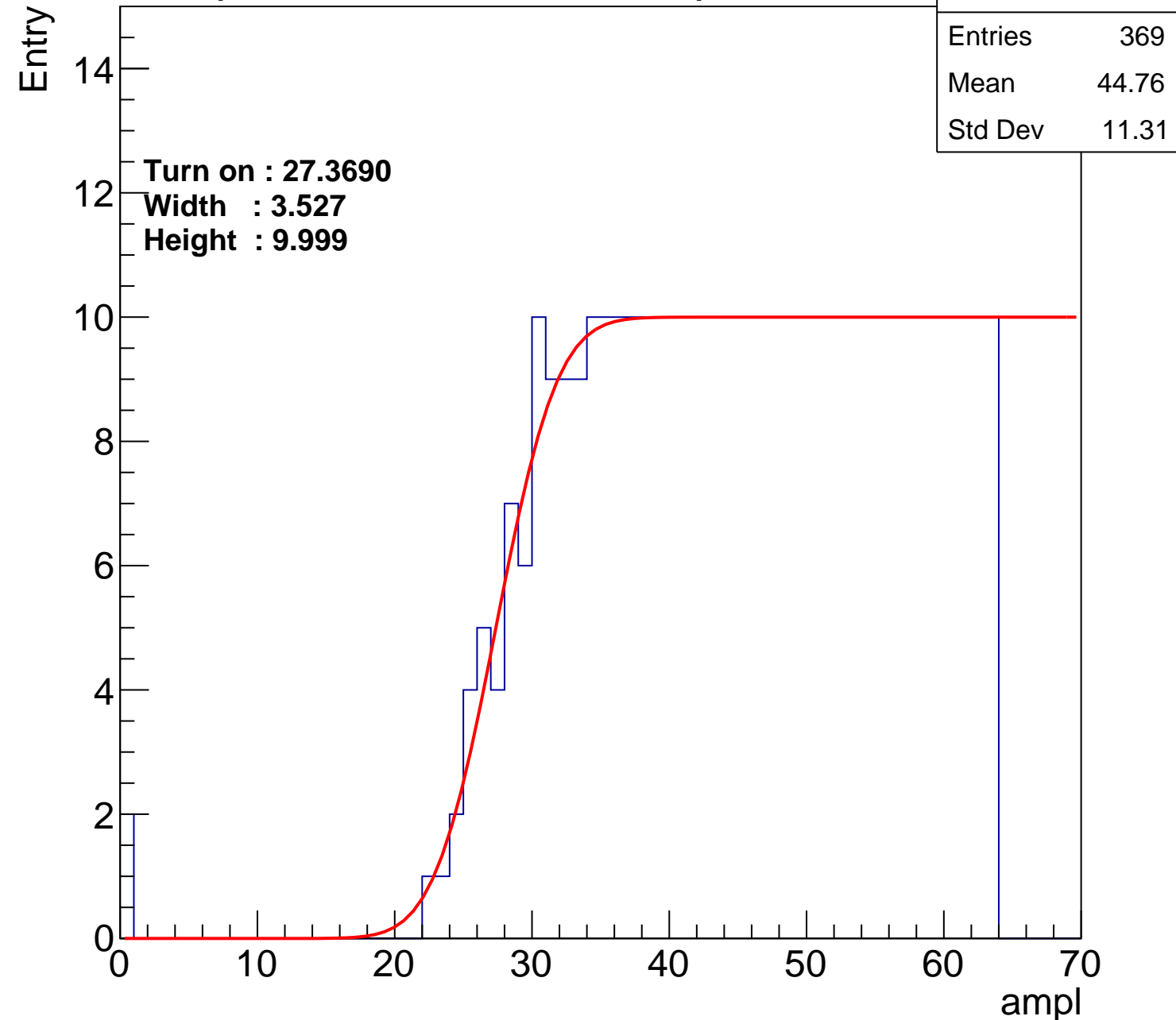
Width : 3.527

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.22
Std Dev	11.66

Turn on : 26.3670

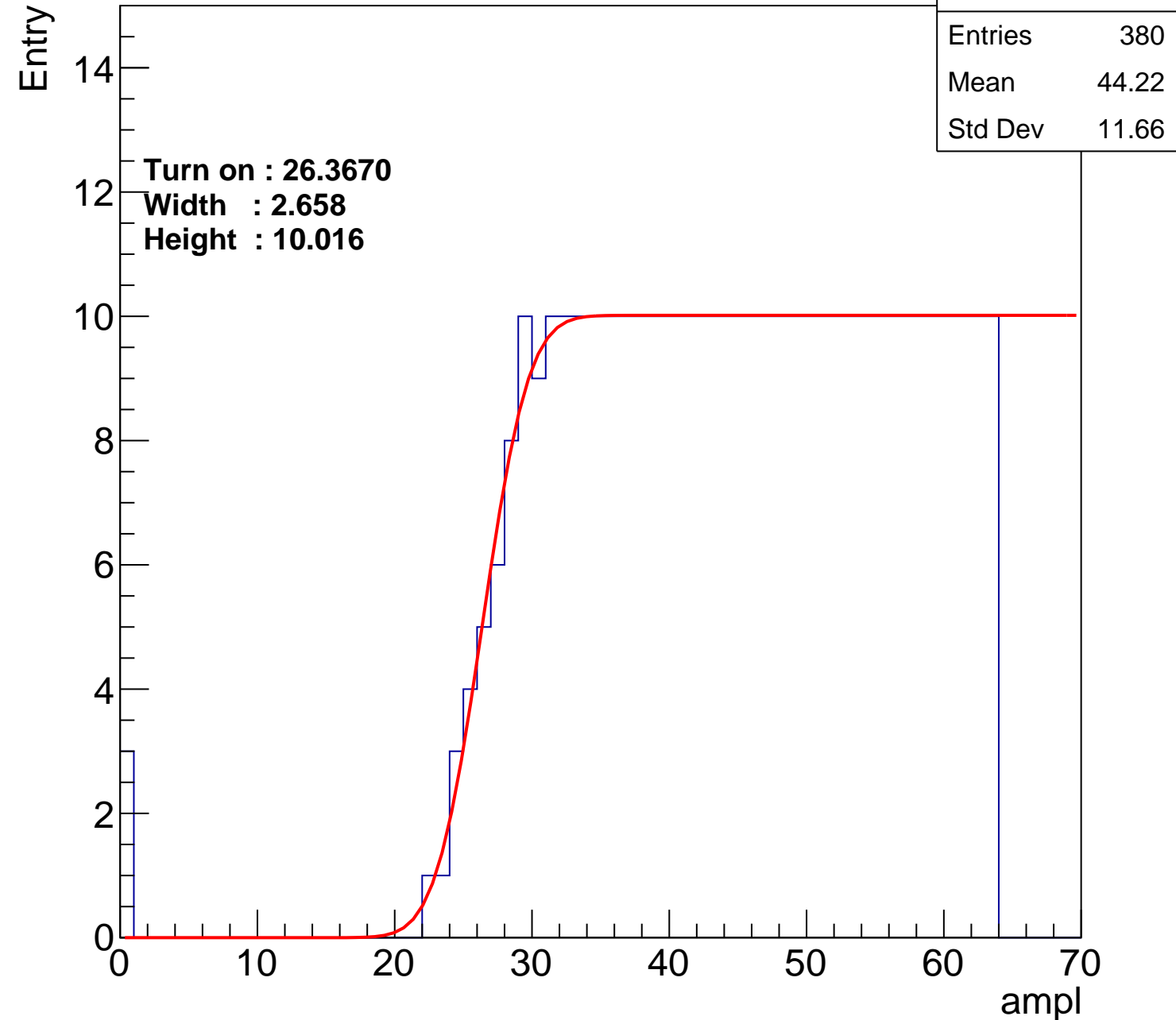
Width : 2.658

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.42
Std Dev	11.1

Turn on : 28.9692

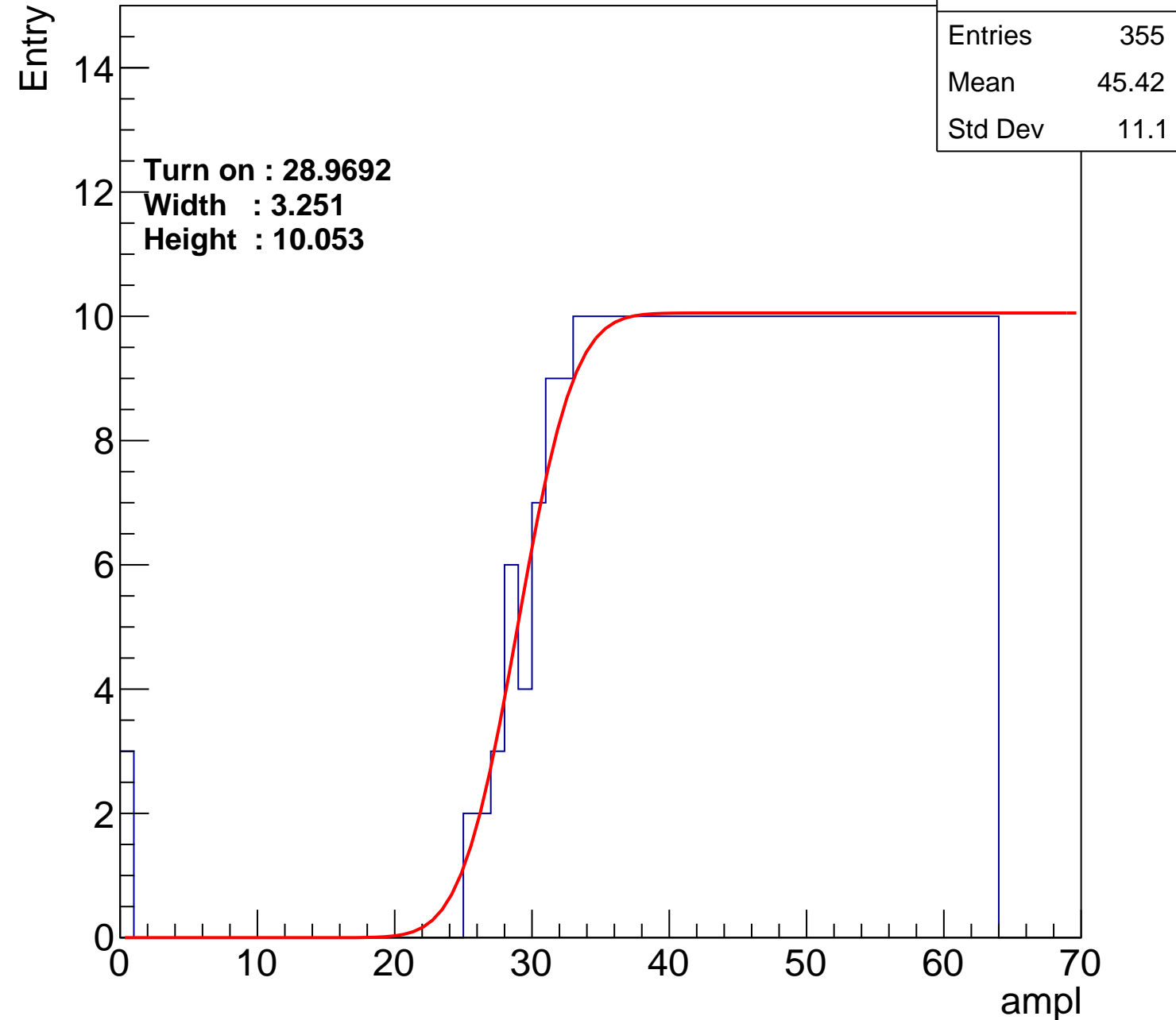
Width : 3.251

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch53

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.92
Std Dev	11.07

Turn on : 27.6276

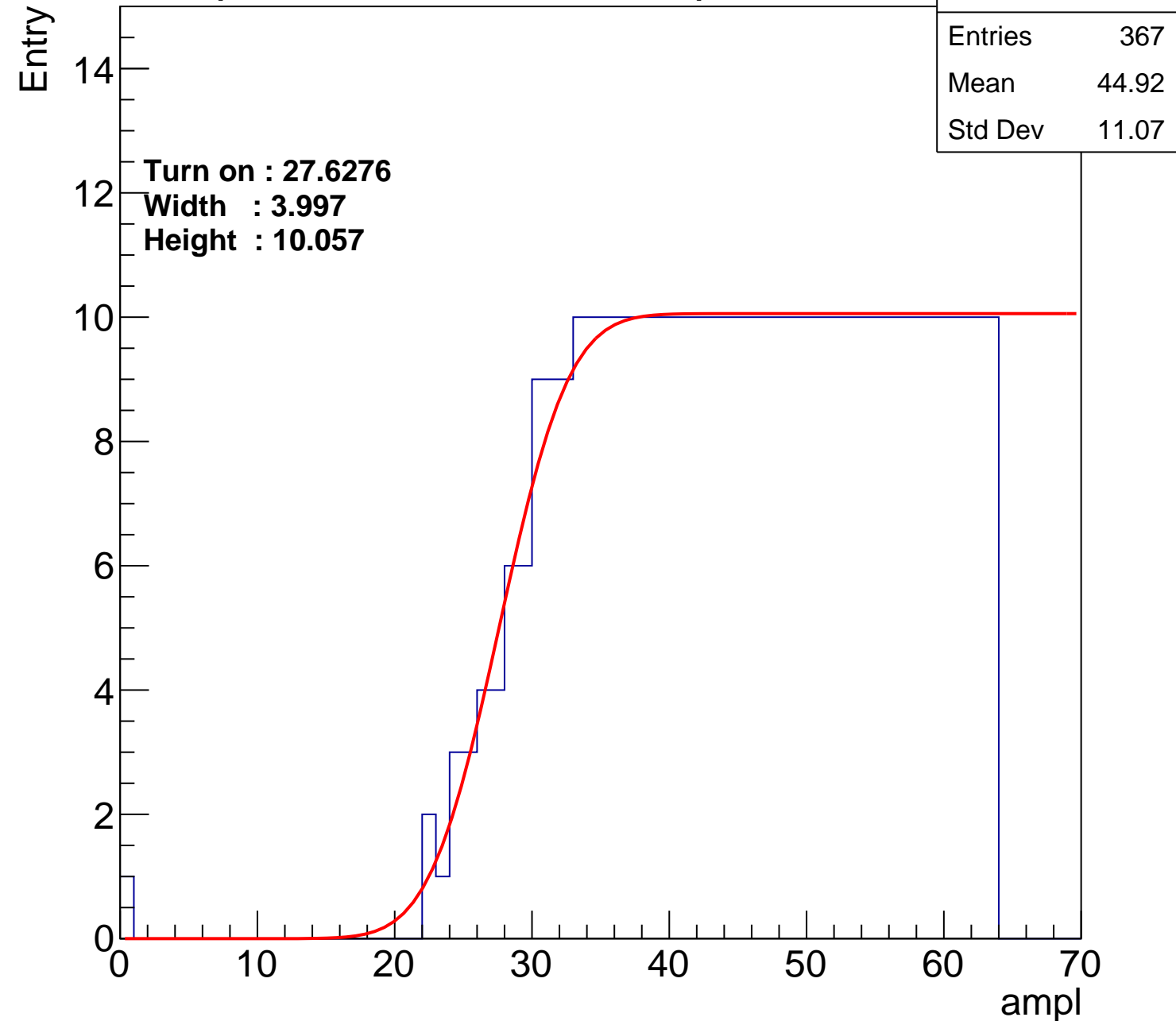
Width : 3.997

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.85
Std Dev	11.22

Turn on : 27.8058

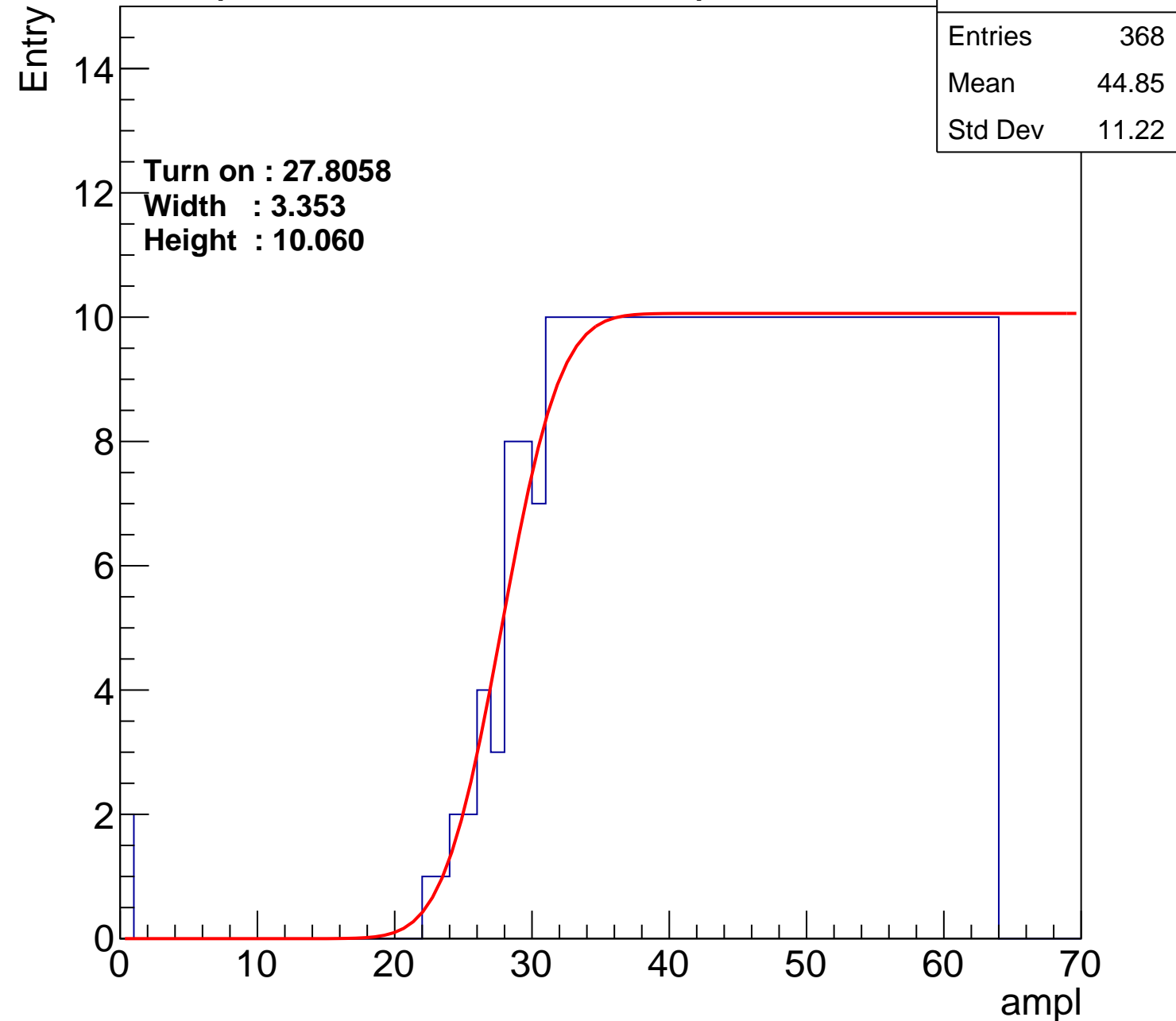
Width : 3.353

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.74
Std Dev	11.28

Turn on : 27.2145

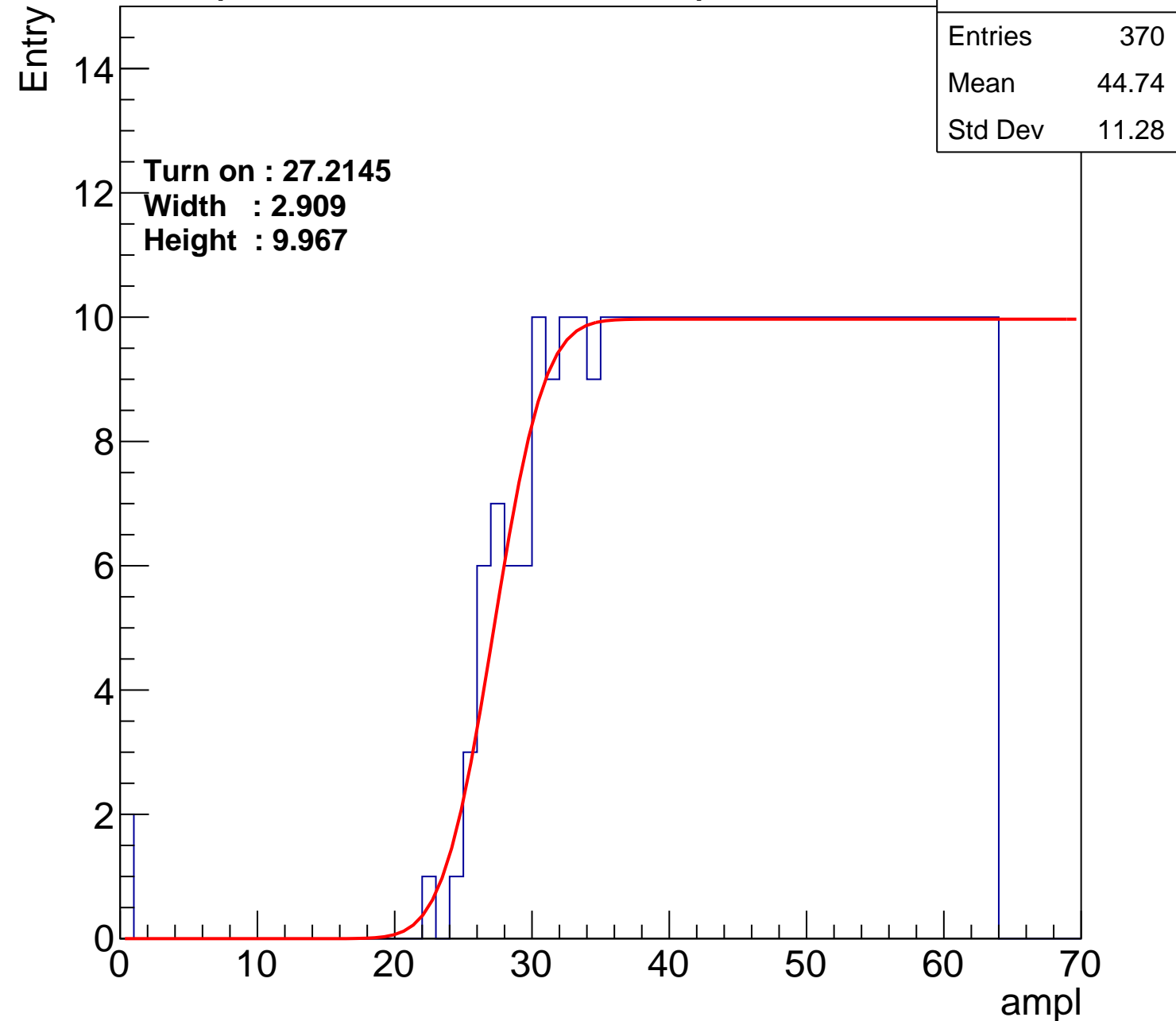
Width : 2.909

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch56

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.07
Std Dev	11.47

Turn on : 28.0208

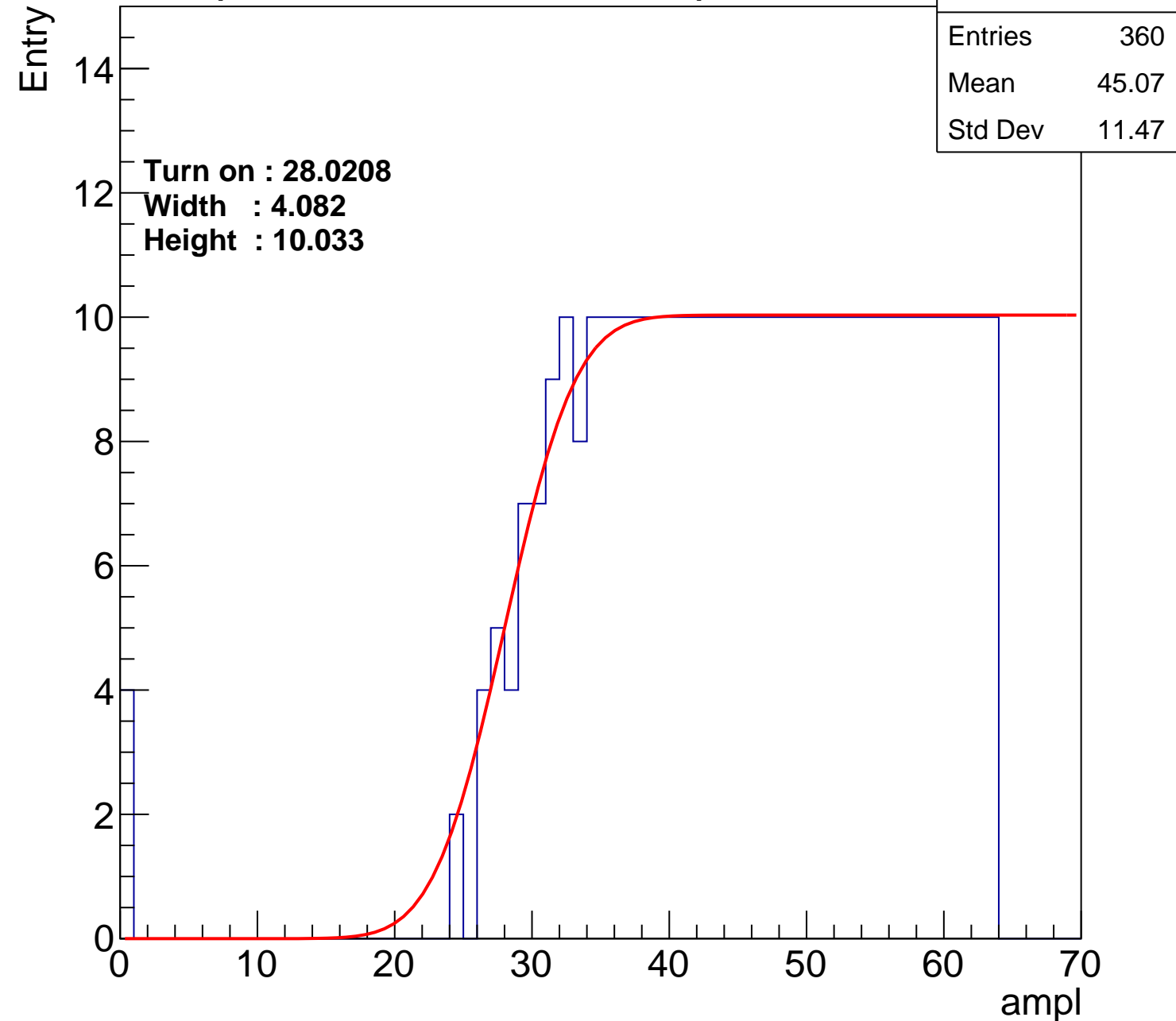
Width : 4.082

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.95
Std Dev	11.71

Turn on : 27.0305

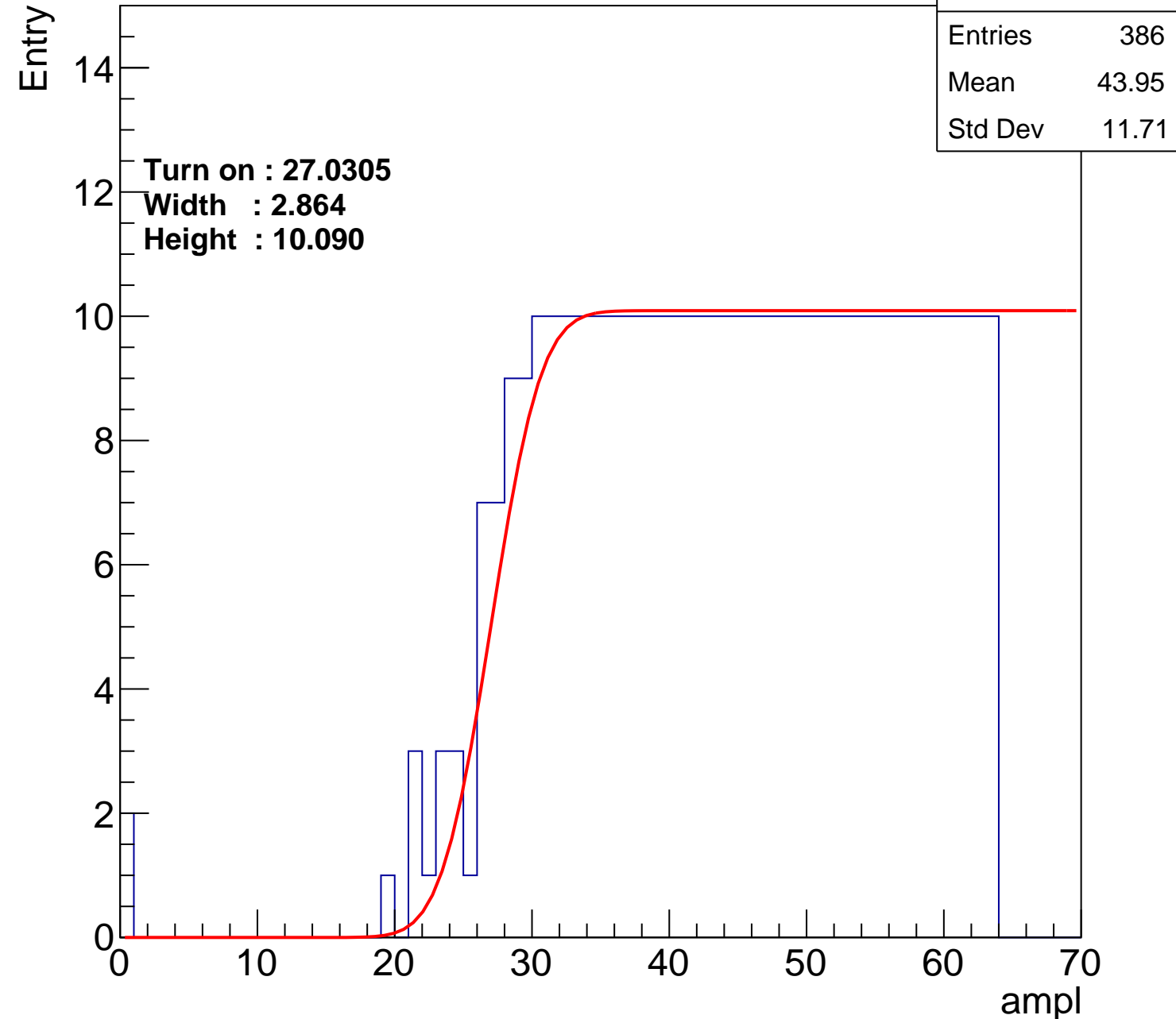
Width : 2.864

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch58

calib_packv5_042523_0143.root, FC#5, port B1

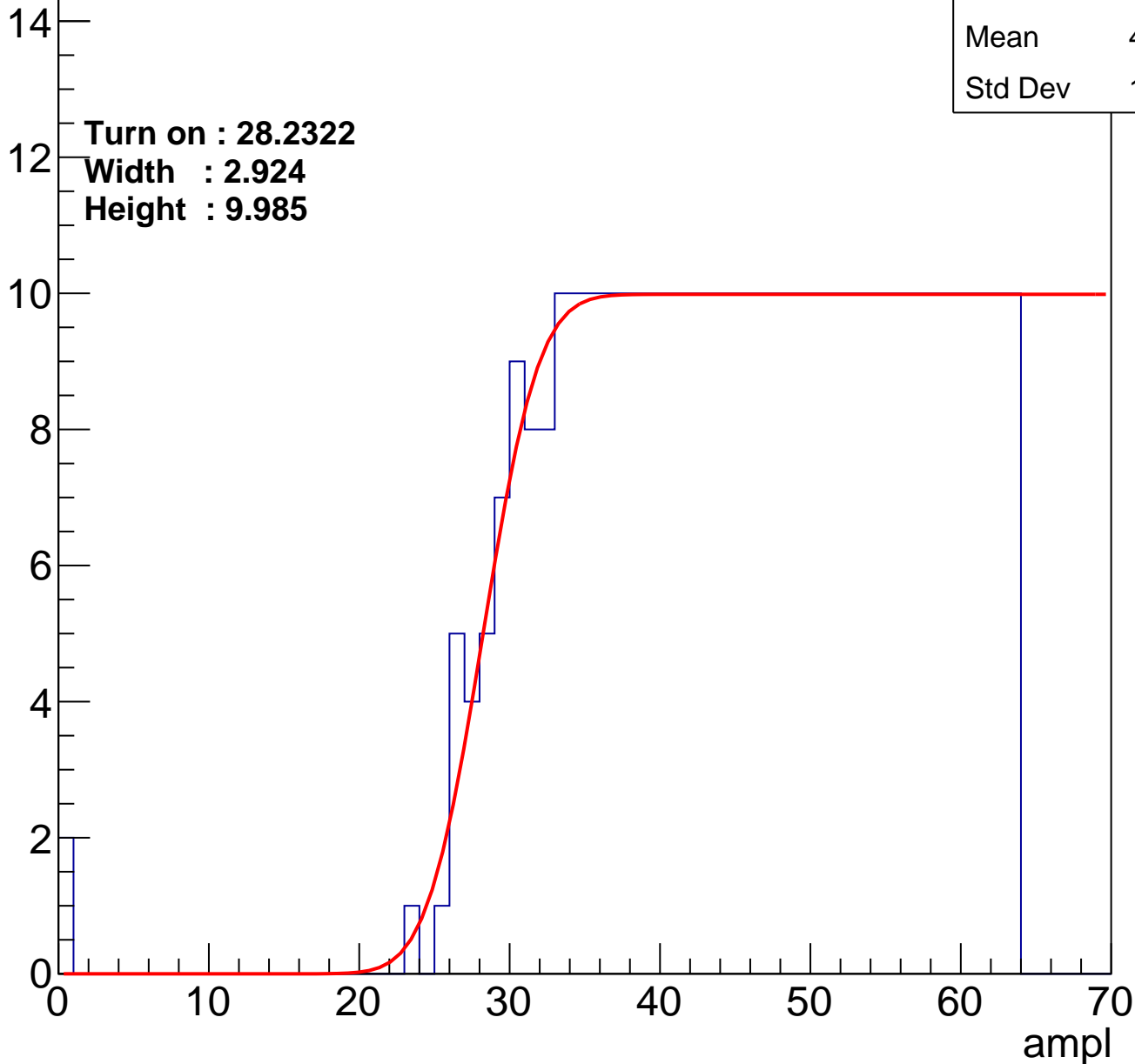
Entry

Entries	360
Mean	45.23
Std Dev	11.03

Turn on : 28.2322

Width : 2.924

Height : 9.985



B0L000S, U5-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.45
Std Dev	11.42

Turn on : 27.2429

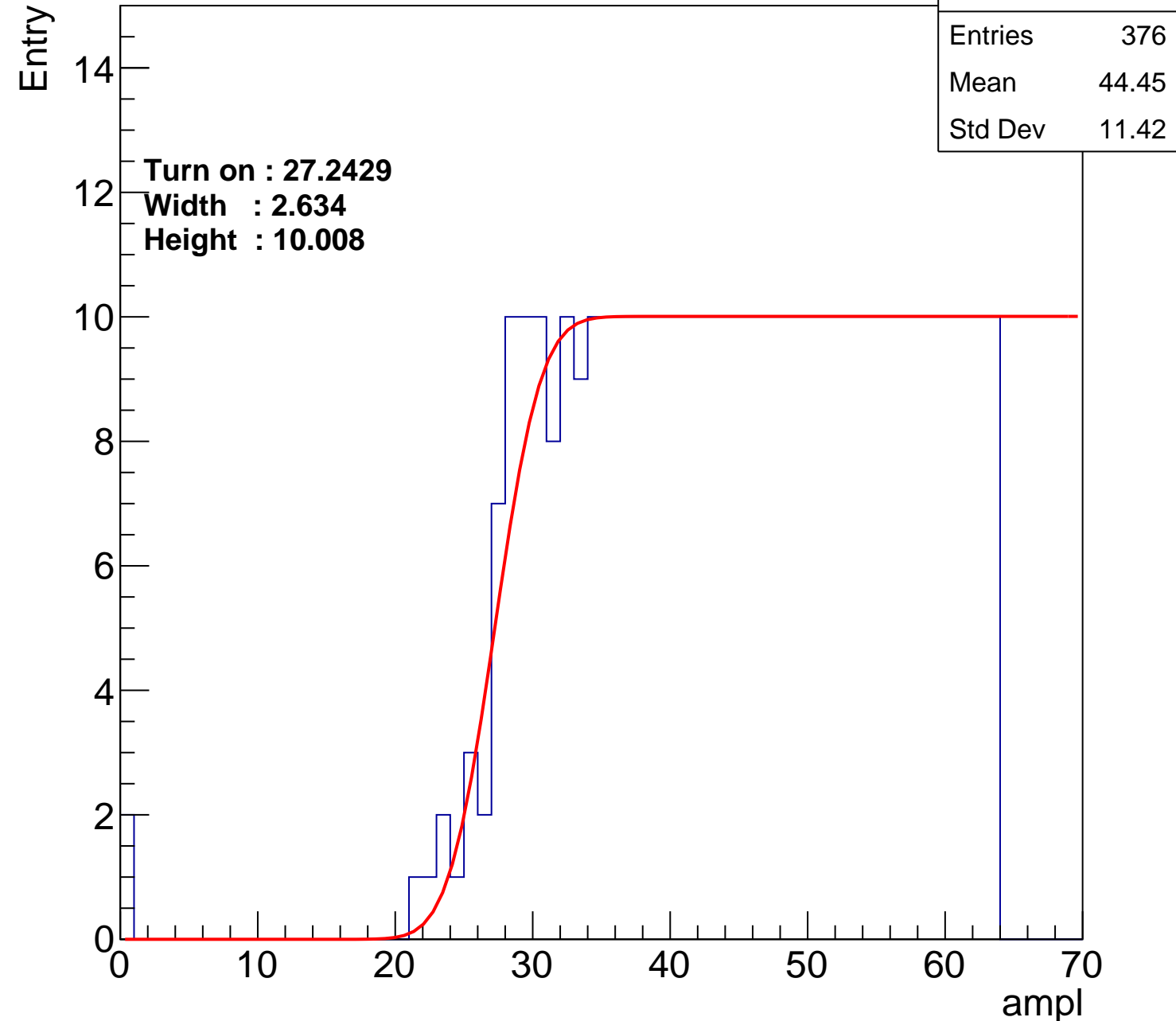
Width : 2.634

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch60

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	43.95
Std Dev	12.11

Turn on : 26.4348

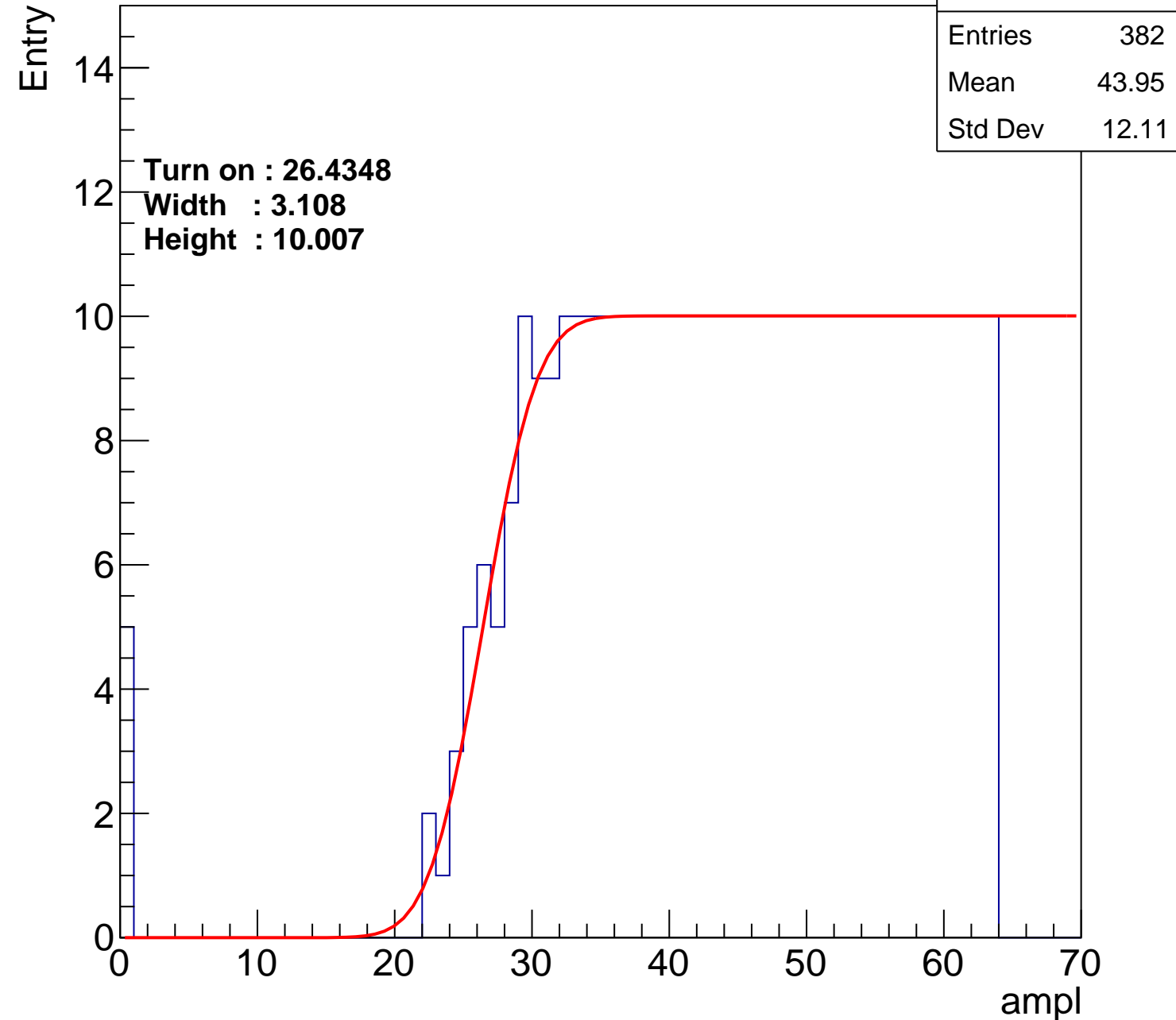
Width : 3.108

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 27.1820

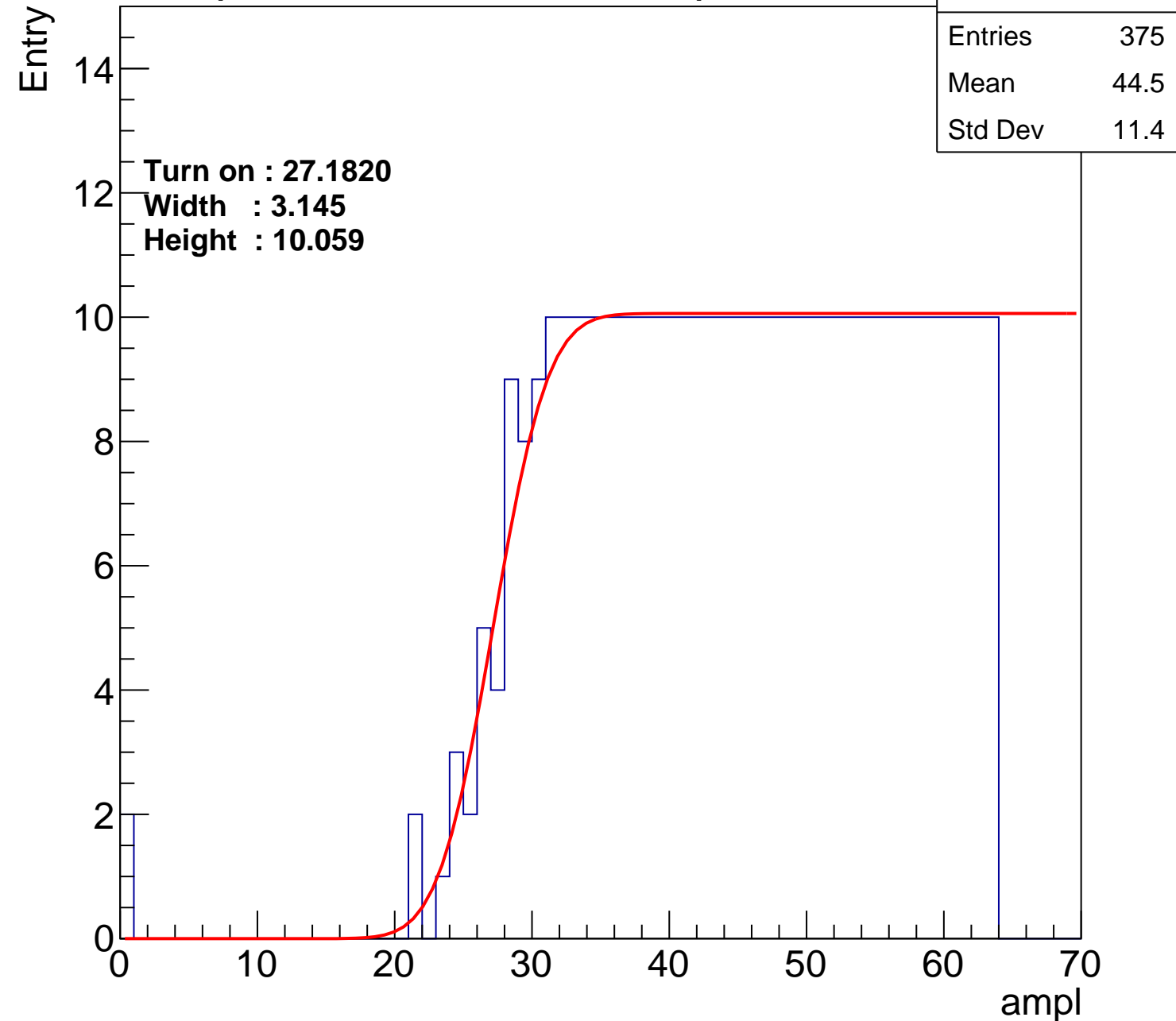
Width : 3.145

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	359
Mean	45.22
Std Dev	11.2

Turn on : 29.1354

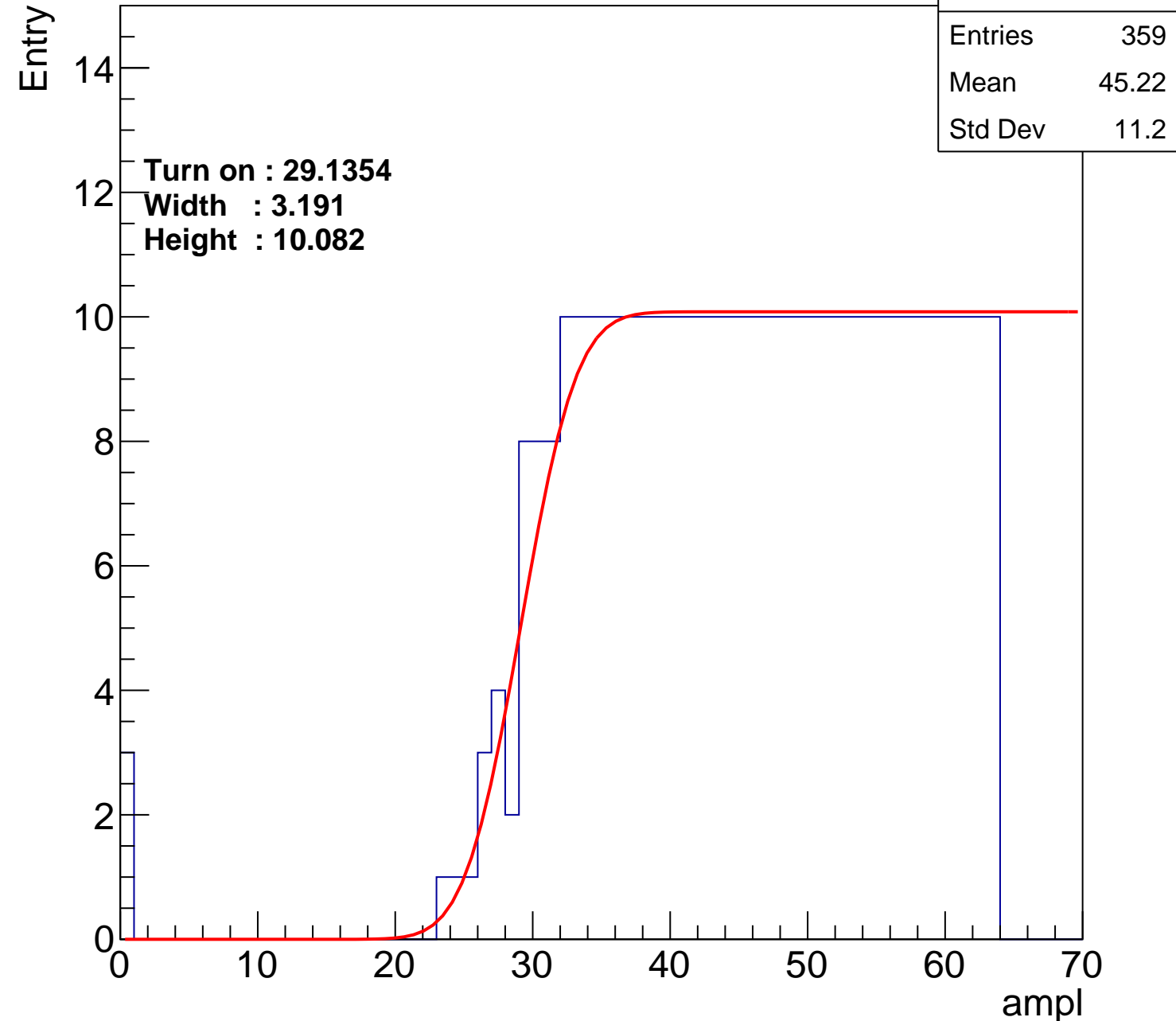
Width : 3.191

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch63

calib_packv5_042523_0143.root, FC#5, port B1

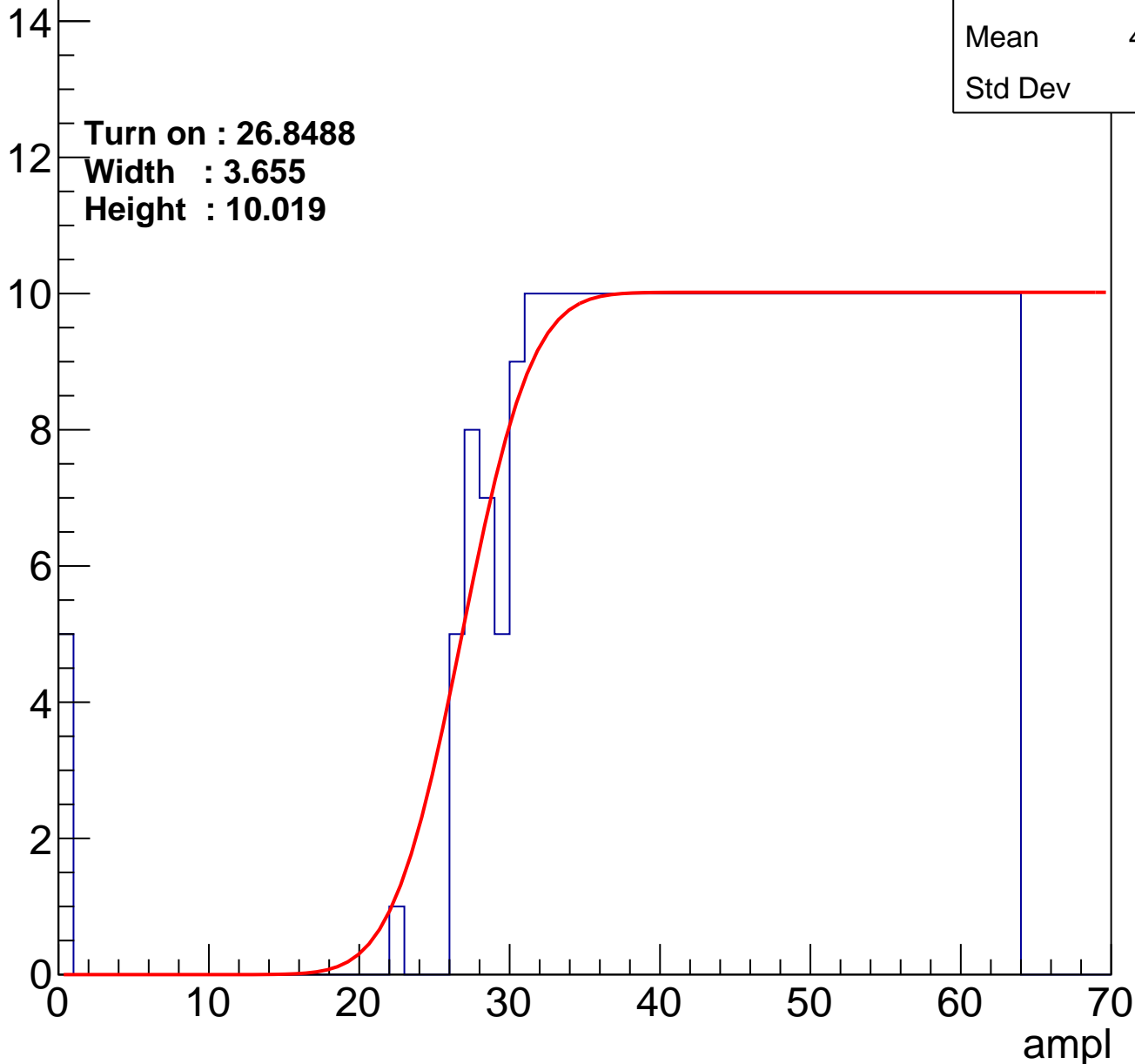
Entry

Entries	370
Mean	44.56
Std Dev	11.8

Turn on : 26.8488

Width : 3.655

Height : 10.019



B0L000S, U5-ch64

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	43.64
Std Dev	12.6

Turn on : 26.8264

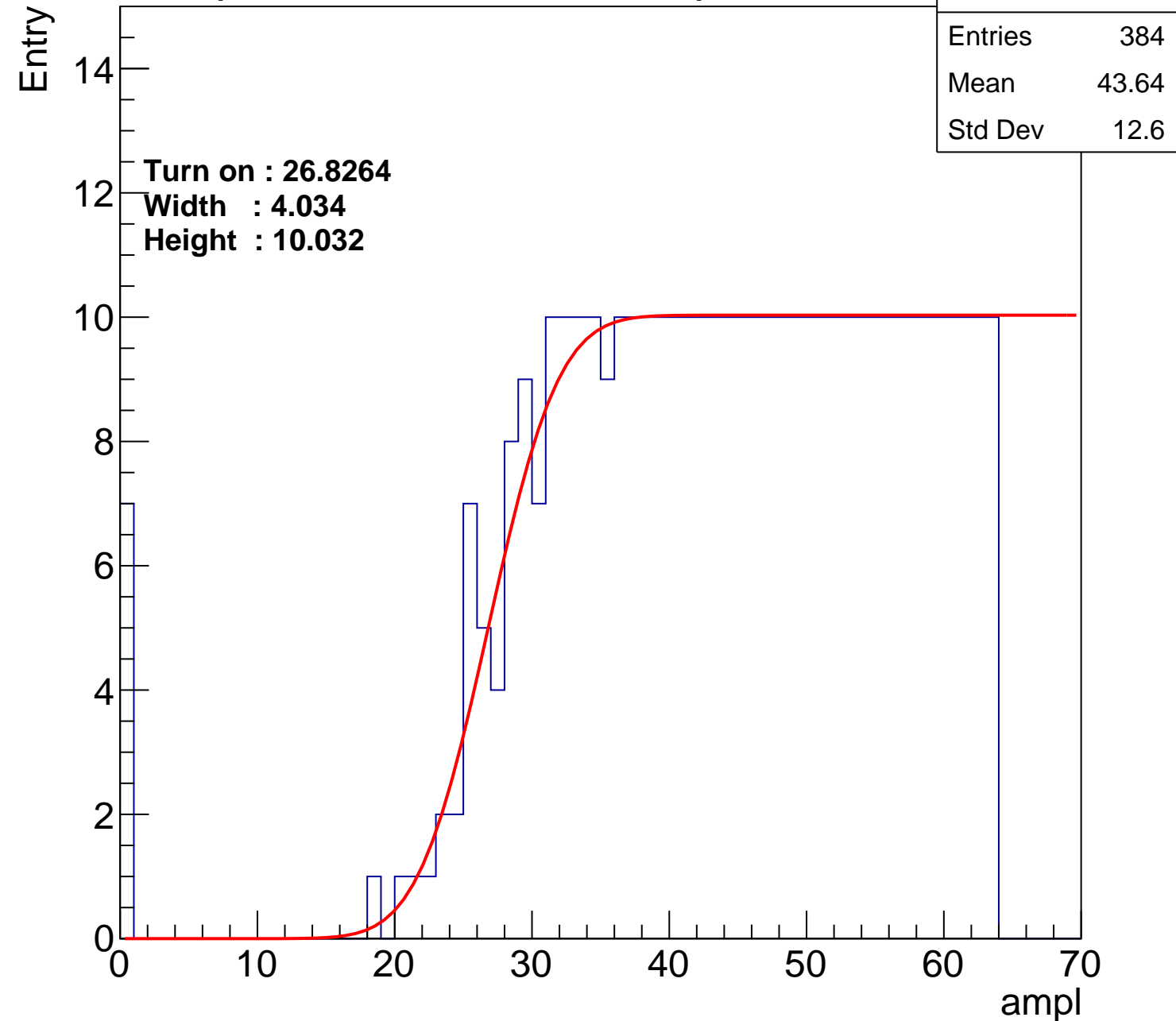
Width : 4.034

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.6
Std Dev	11.21

Turn on : 26.7800

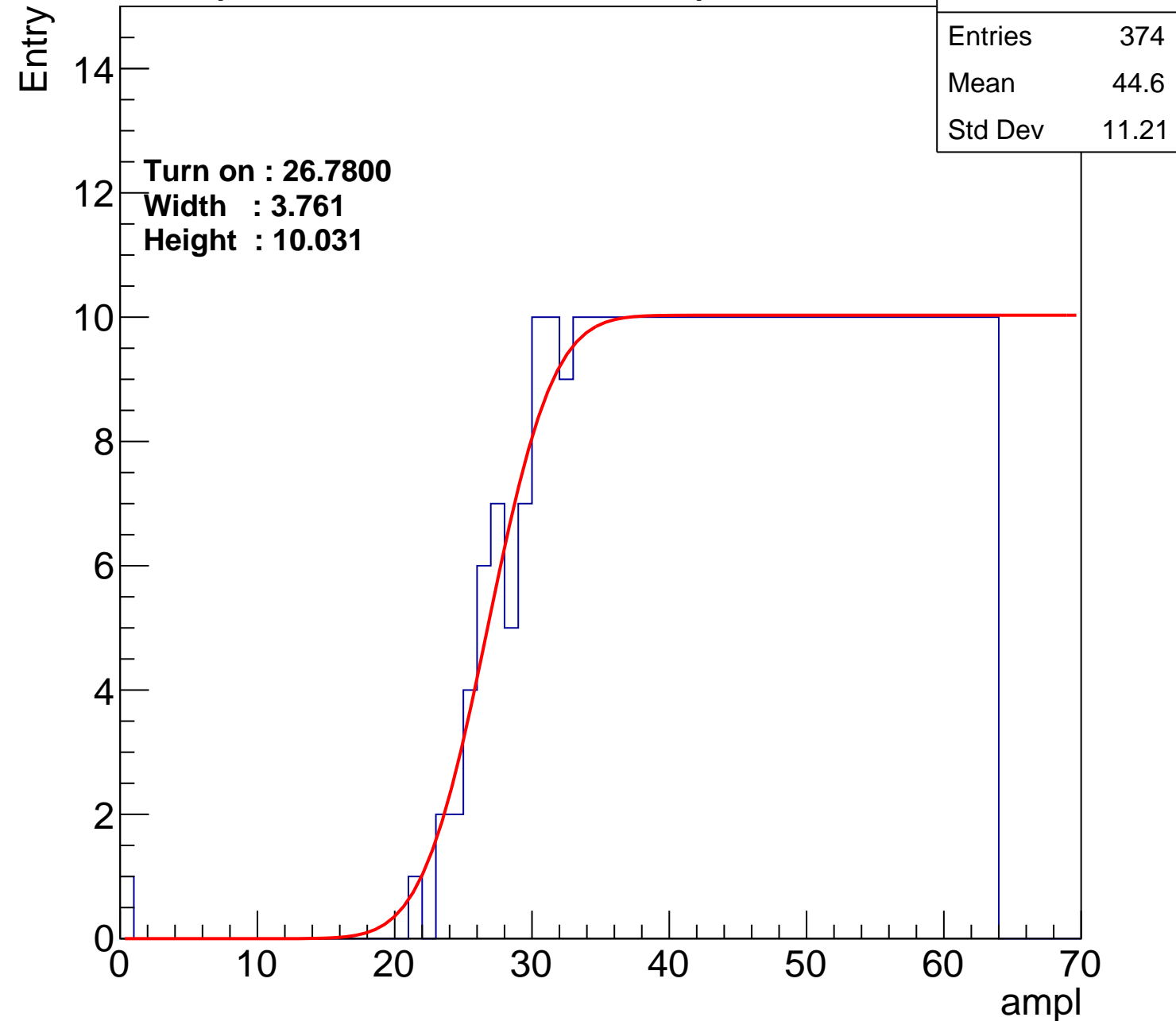
Width : 3.761

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch66

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.37
Std Dev	11.32

Turn on : 26.4653

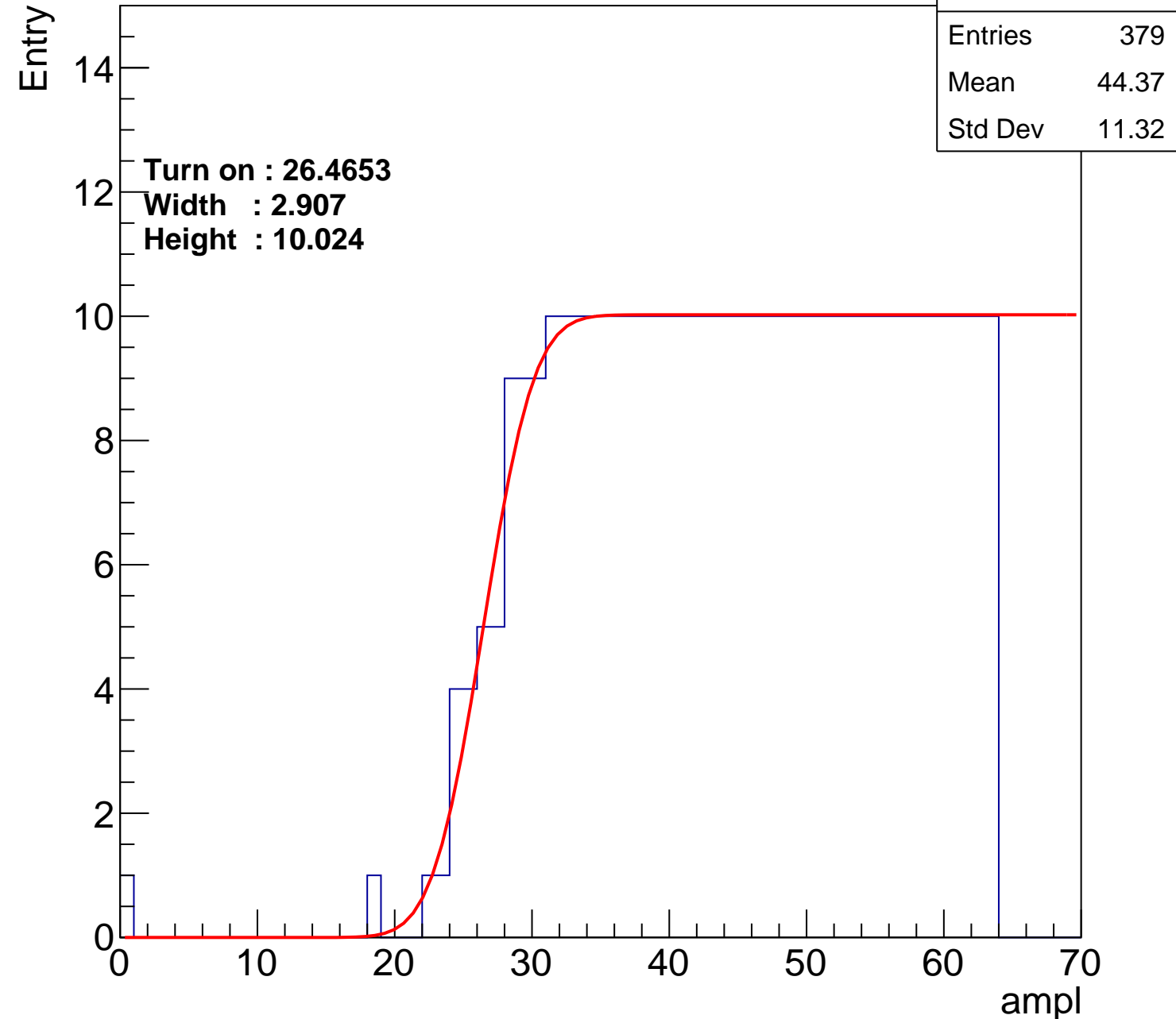
Width : 2.907

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch67

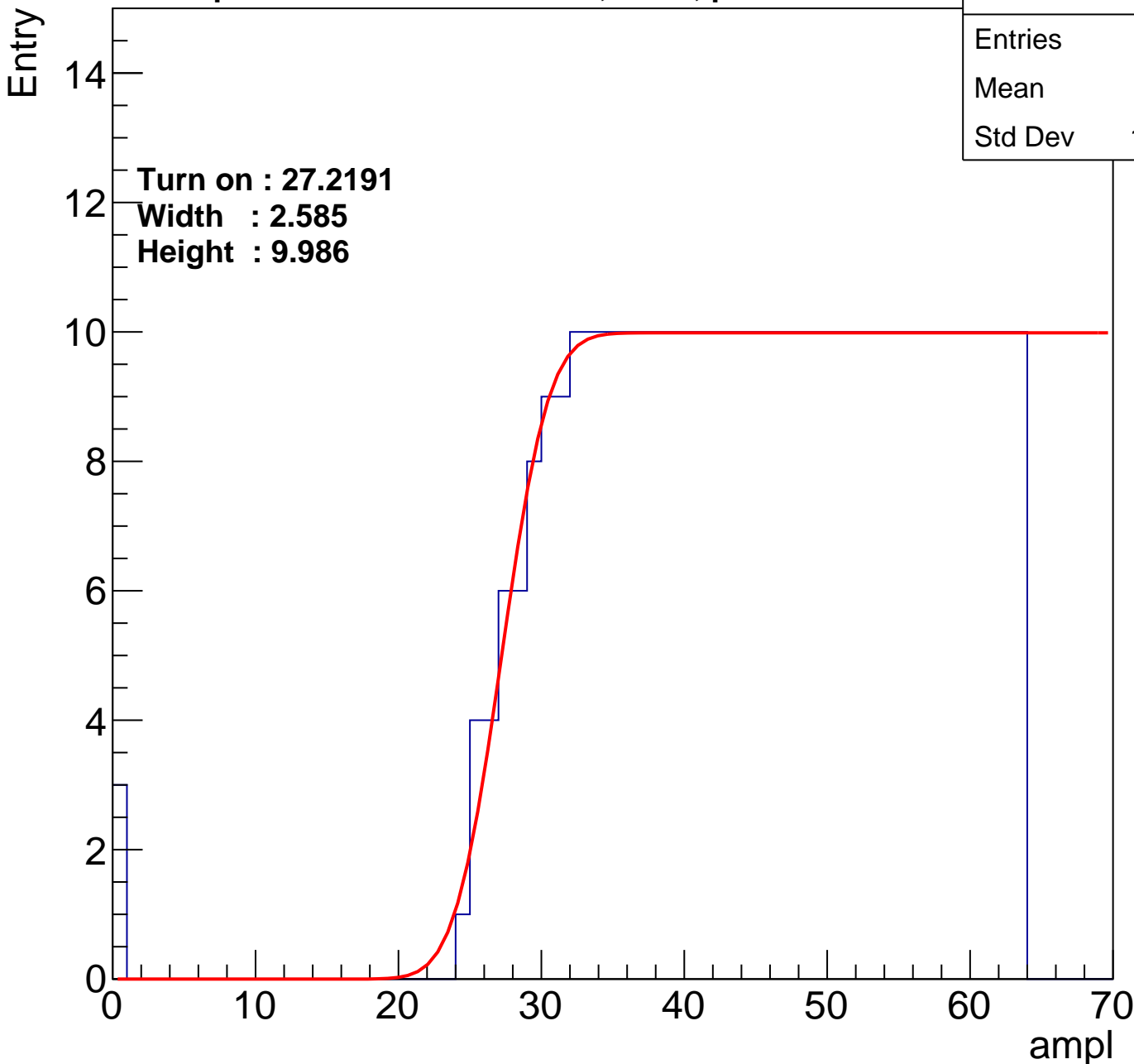
calib_packv5_042523_0143.root, FC#5, port B1

Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.2191

Width : 2.585

Height : 9.986



B0L000S, U5-ch68

calib_packv5_042523_0143.root, FC#5, port B1

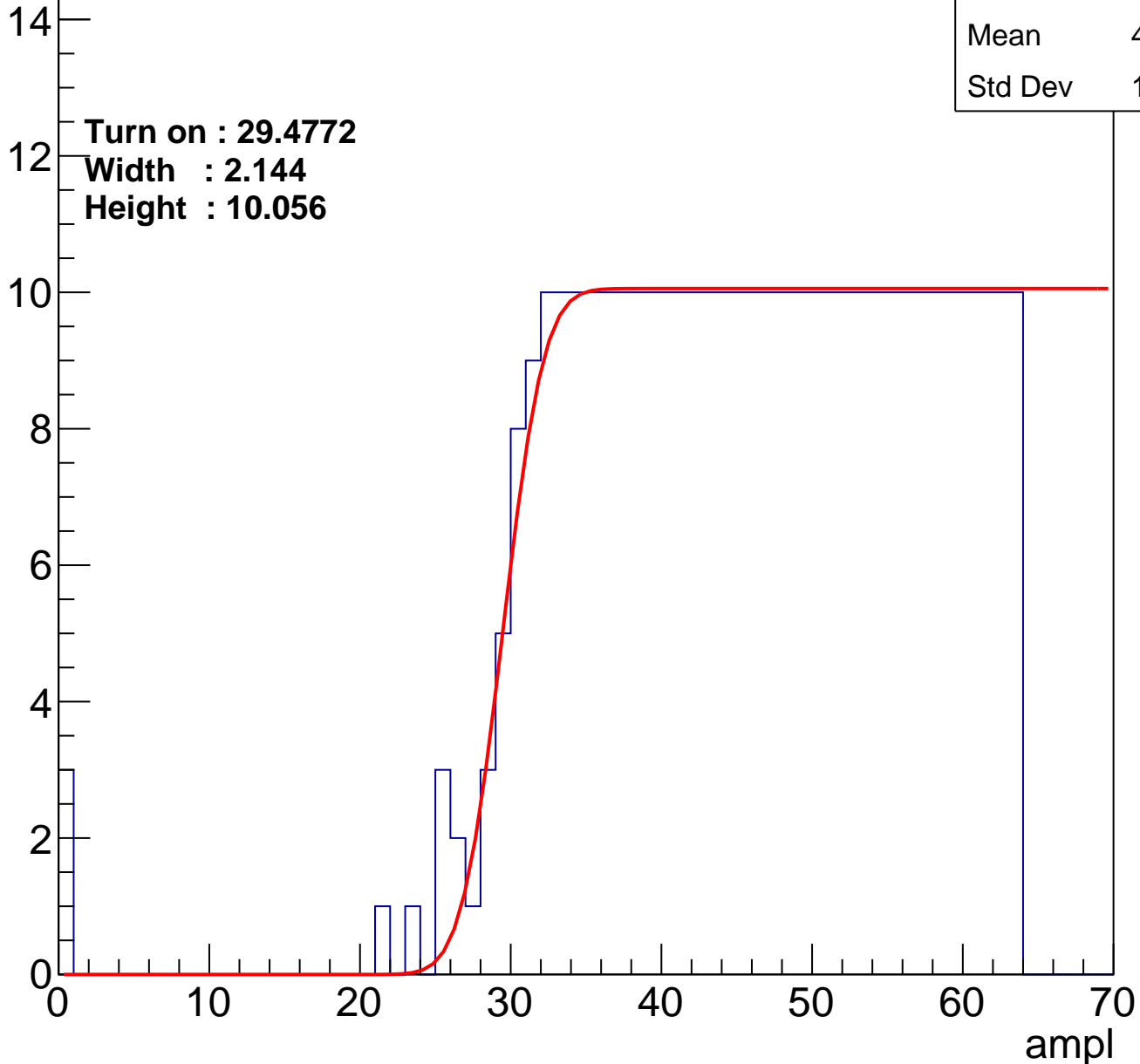
Entries	356
Mean	45.35
Std Dev	11.16

Turn on : 29.4772

Width : 2.144

Height : 10.056

Entry



B0L000S, U5-ch69

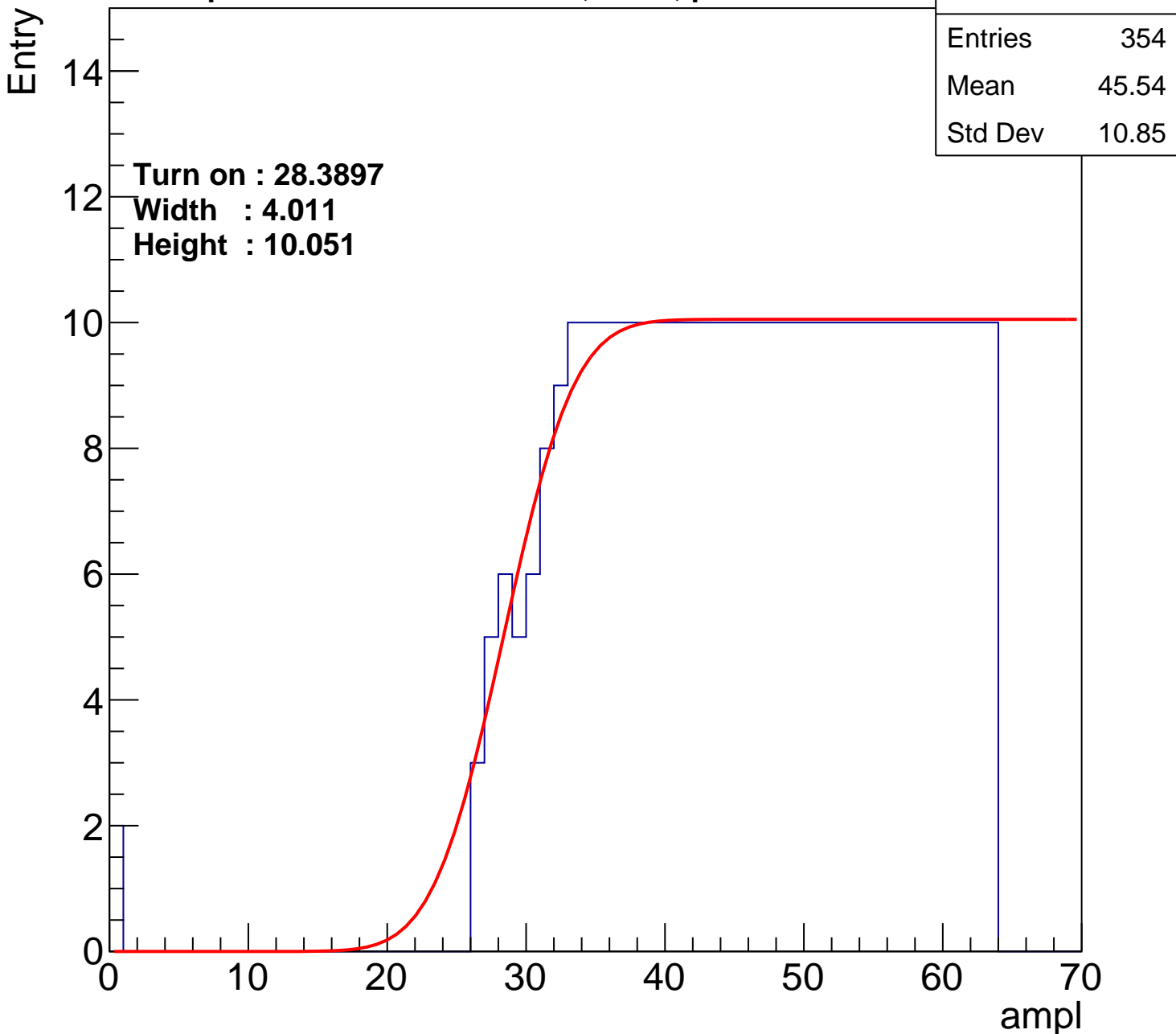
calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.54
Std Dev	10.85

Turn on : 28.3897

Width : 4.011

Height : 10.051



B0L000S, U5-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.0873

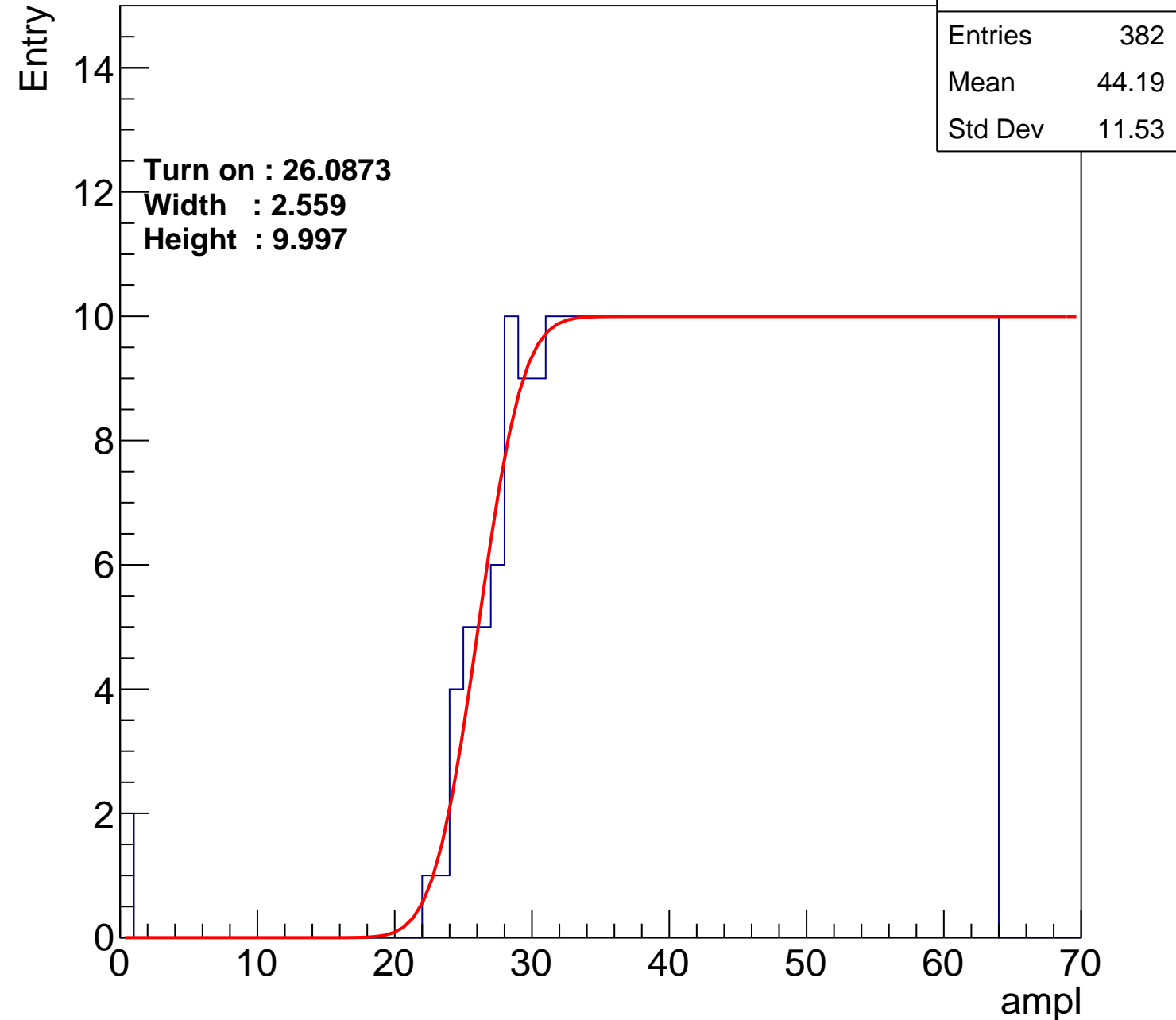
Width : 2.559

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.4
Std Dev	11.9

Turn on : 27.2862

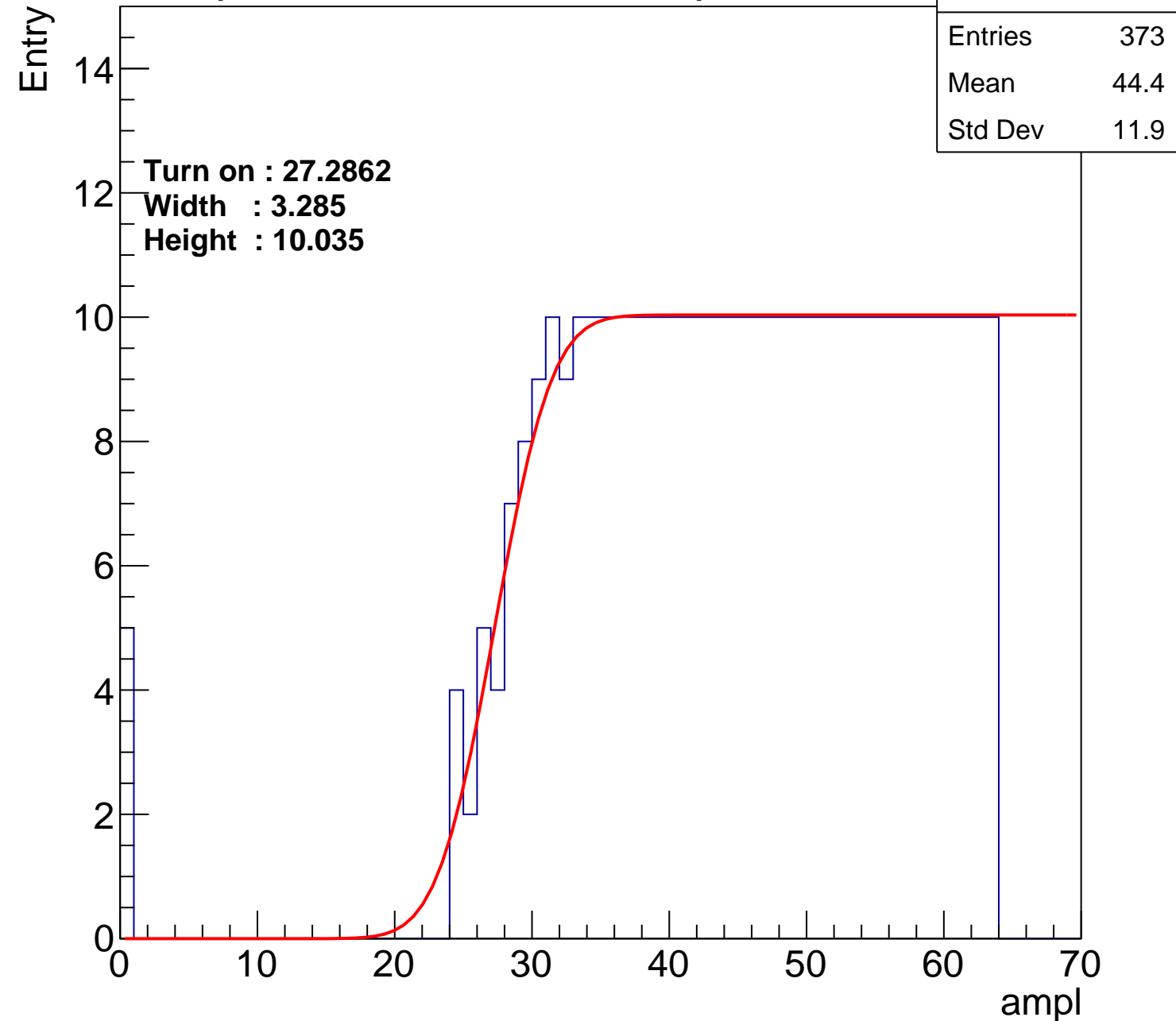
Width : 3.285

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch72

calib_packv5_042523_0143.root, FC#5, port B1

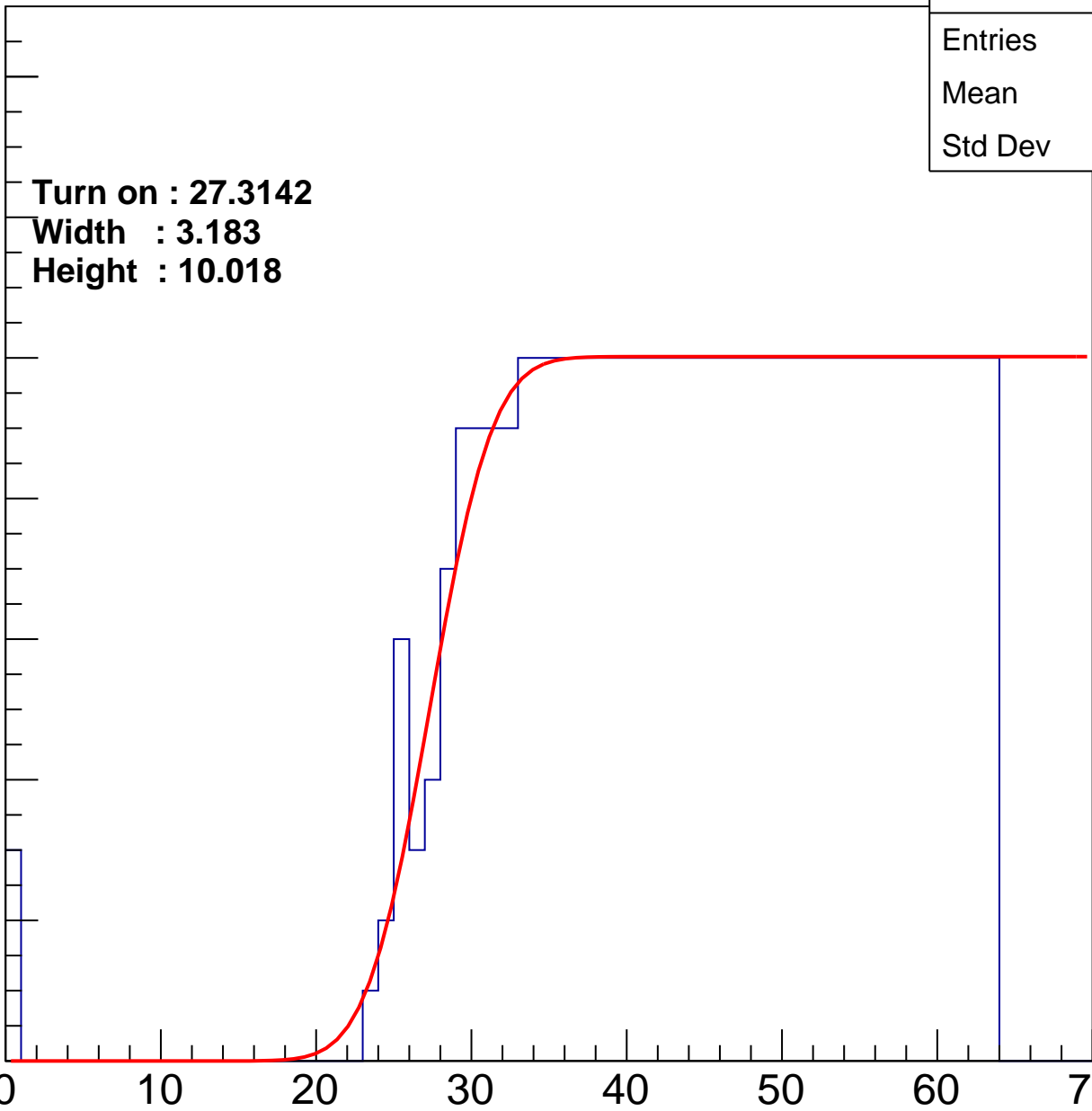
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3142
Width : 3.183
Height : 10.018

Entries	372
Mean	44.57
Std Dev	11.52

ampl



B0L000S, U5-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.8509

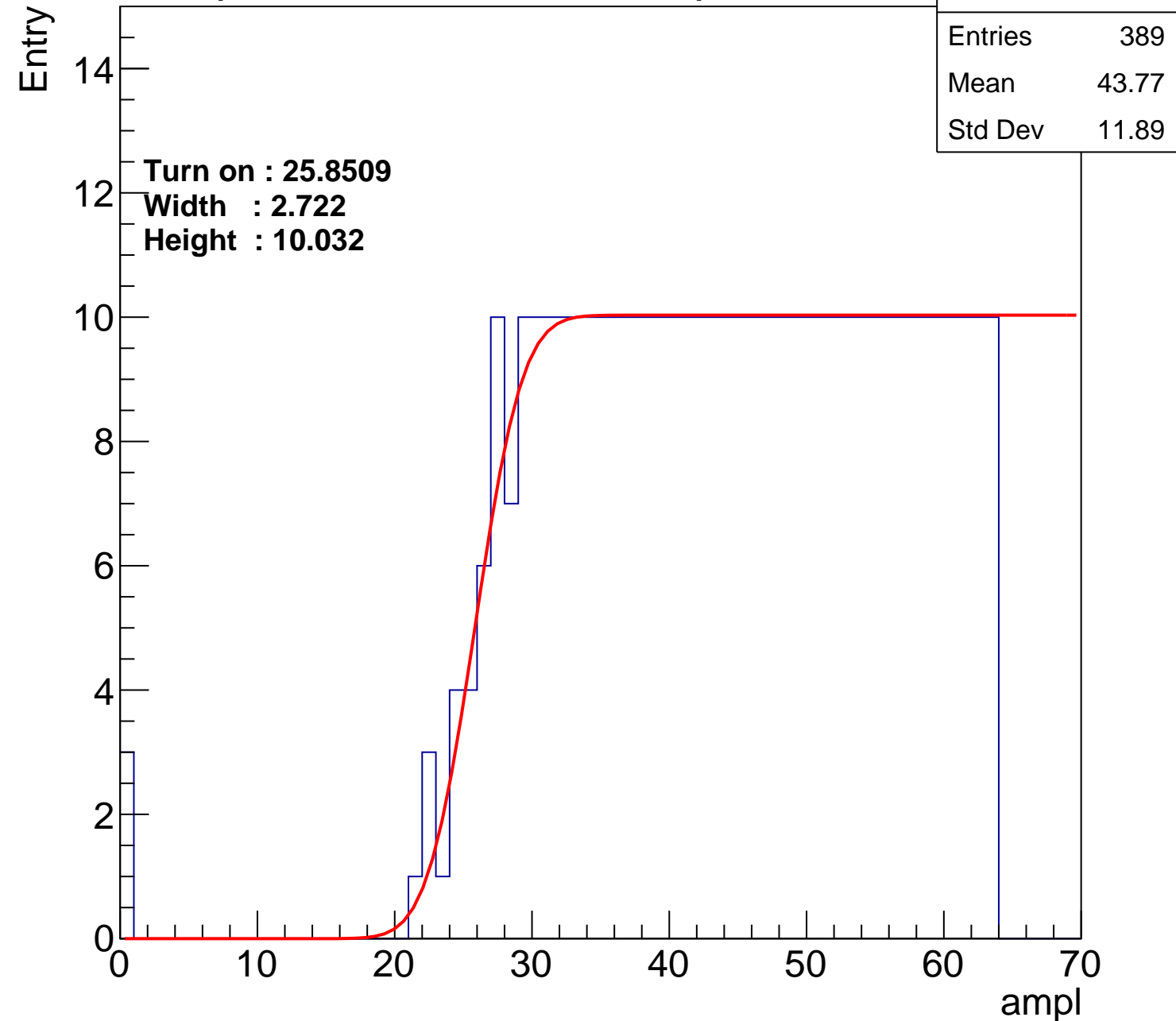
Width : 2.722

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	393
Mean	43.54
Std Dev	12.11

Turn on : 25.2040

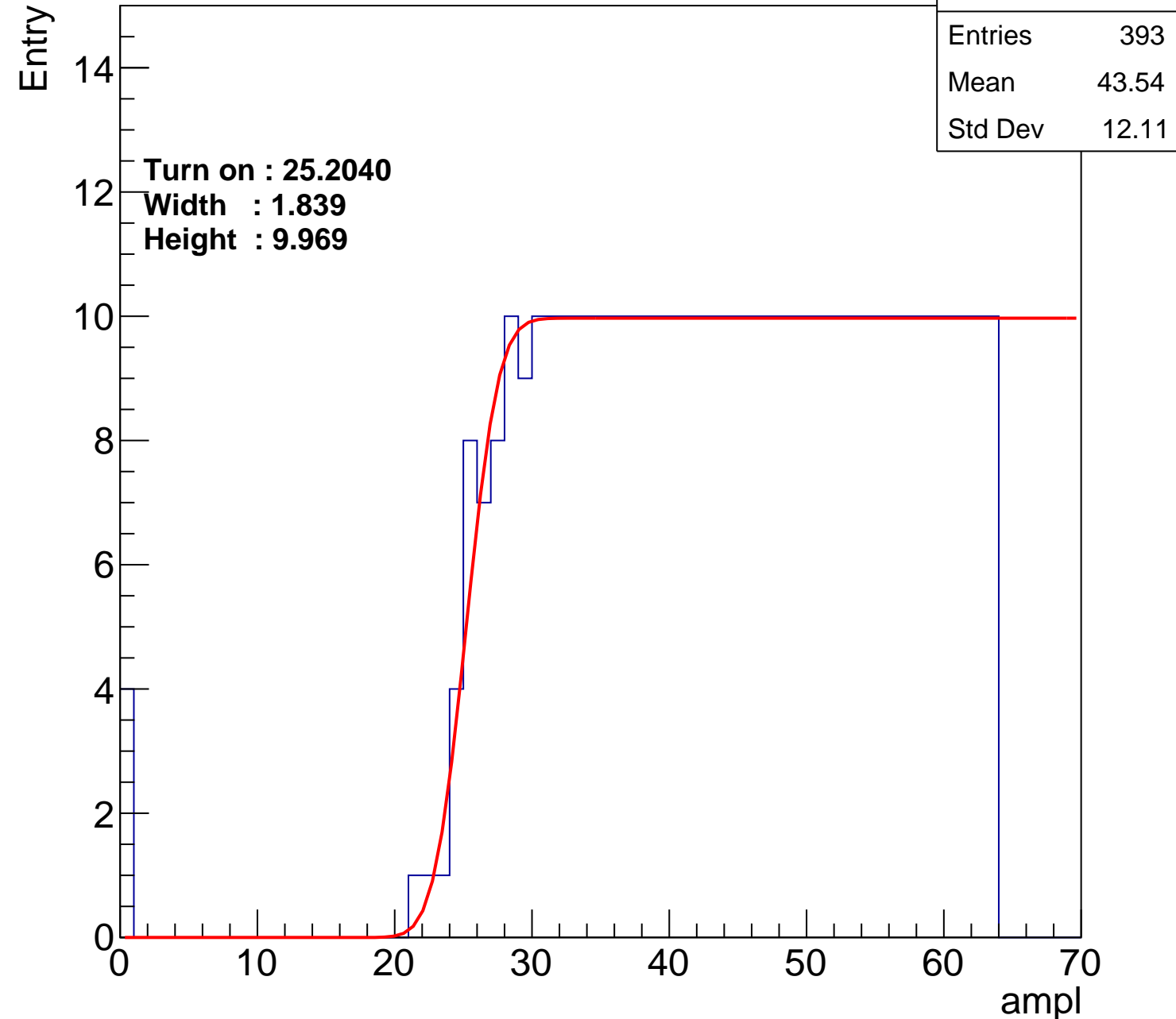
Width : 1.839

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.0101

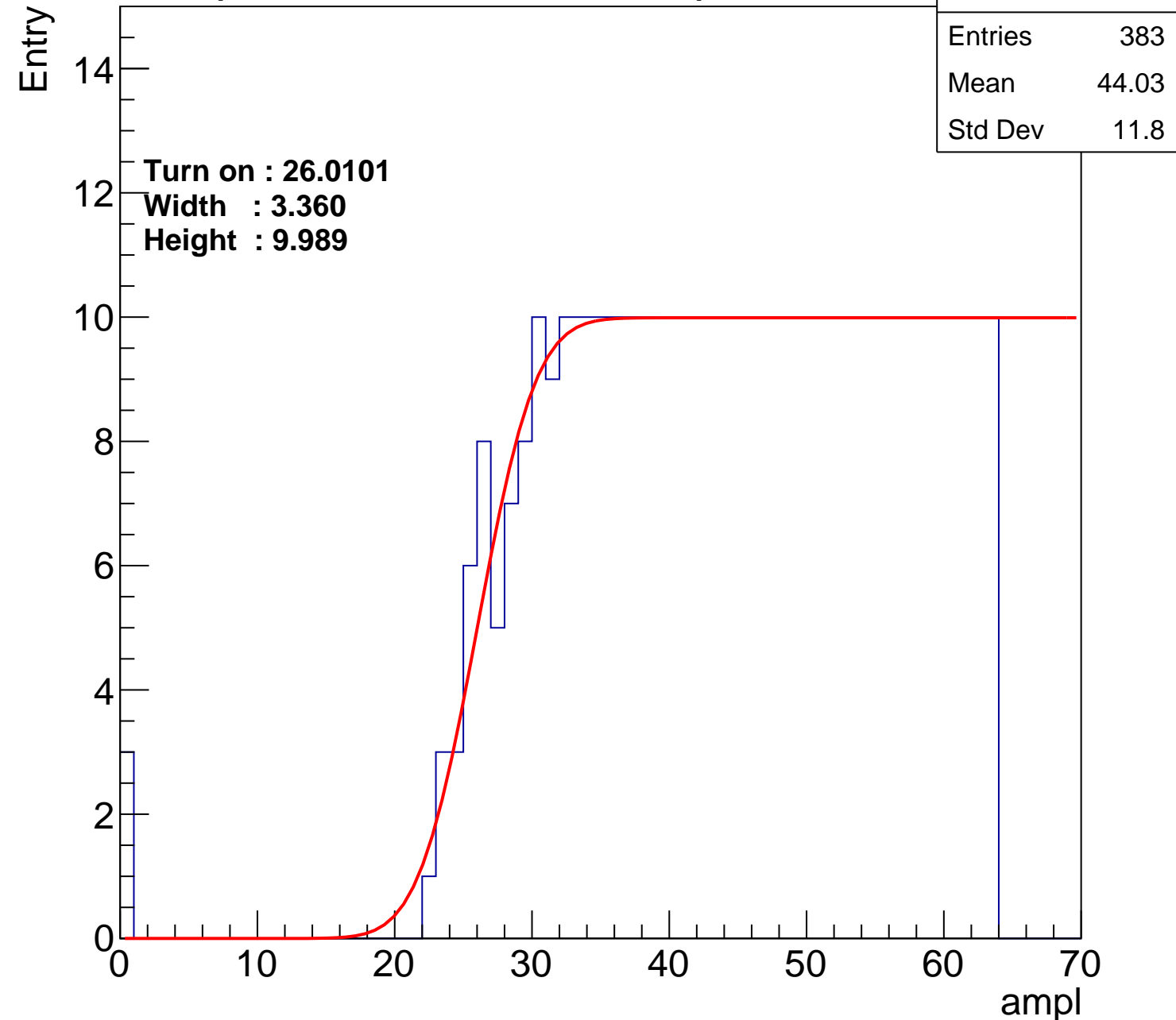
Width : 3.360

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch76

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	44.02
Std Dev	11.94

Turn on : 26.3218

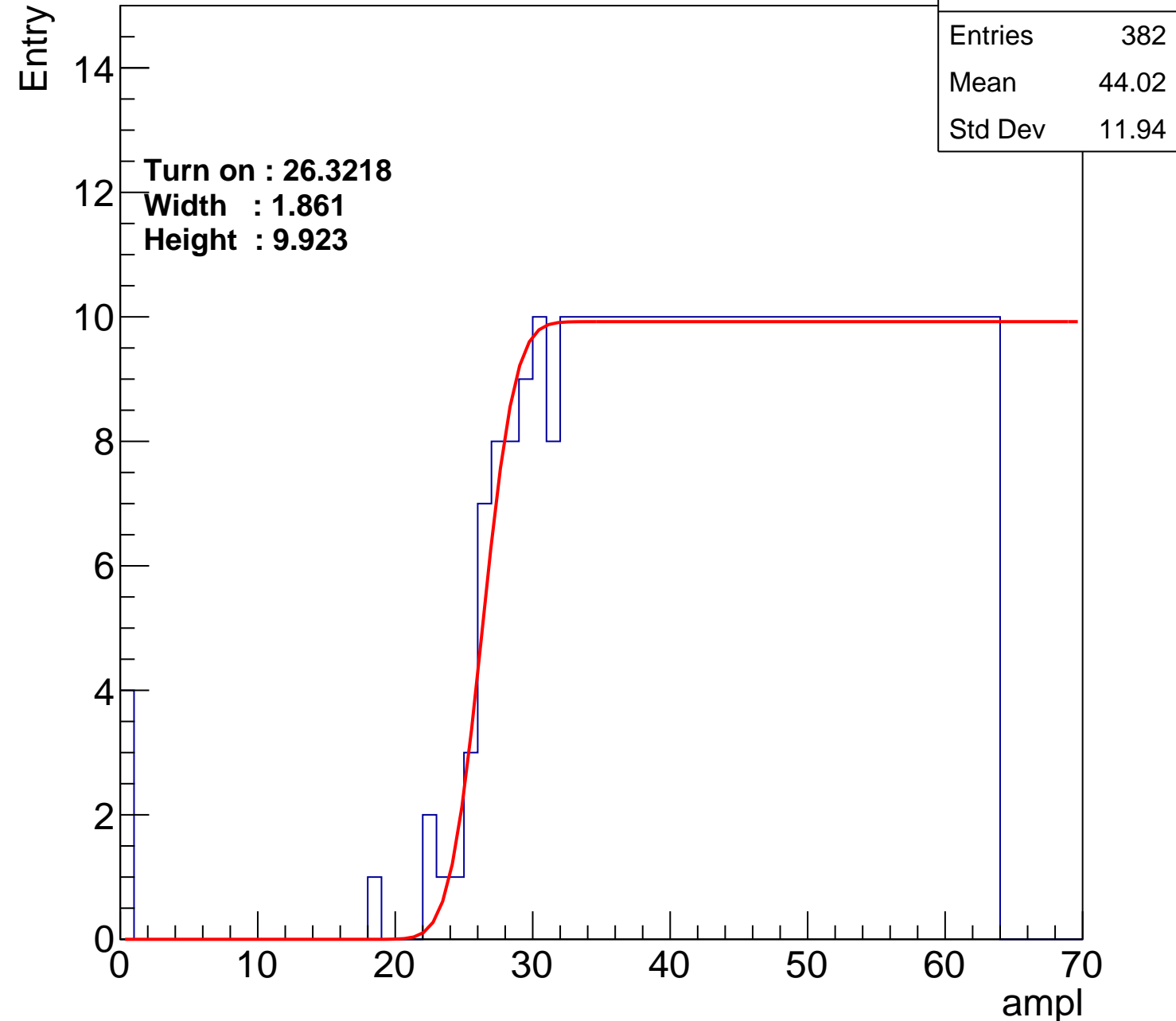
Width : 1.861

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.32
Std Dev	11.14

Turn on : 28.7414

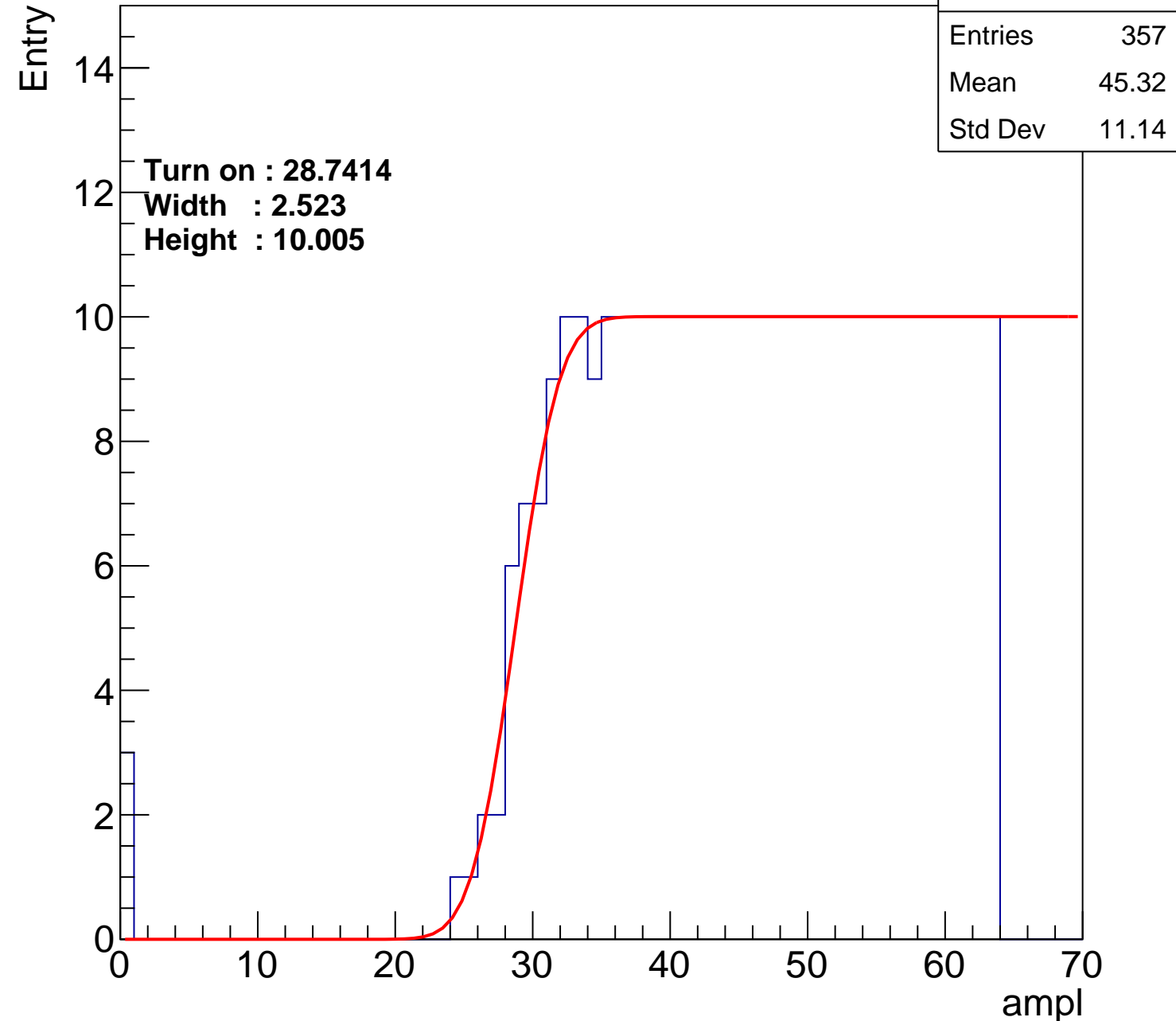
Width : 2.523

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch78

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.13
Std Dev	11.87

Turn on : 26.7154

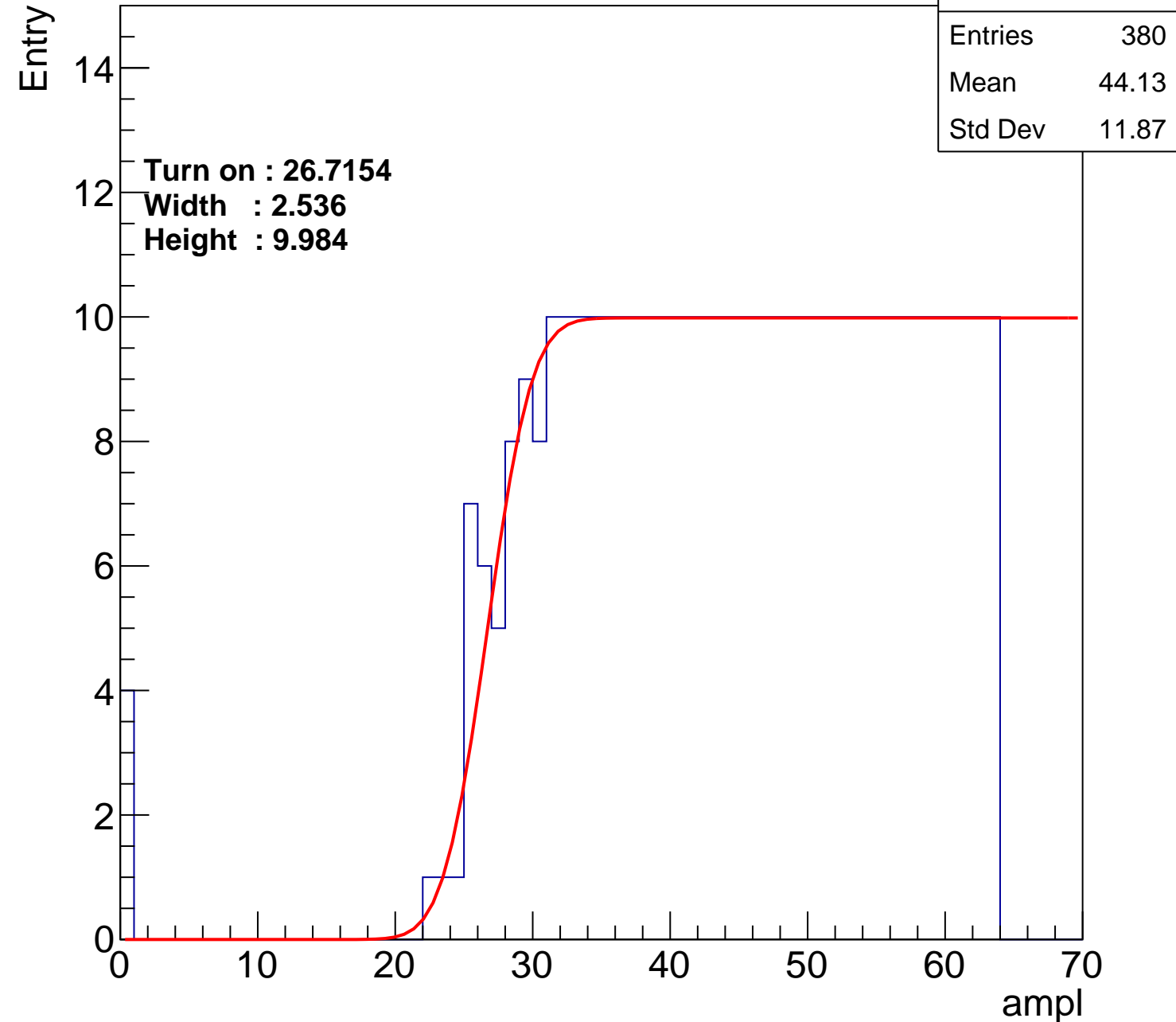
Width : 2.536

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.27
Std Dev	11.69

Turn on : 26.7375

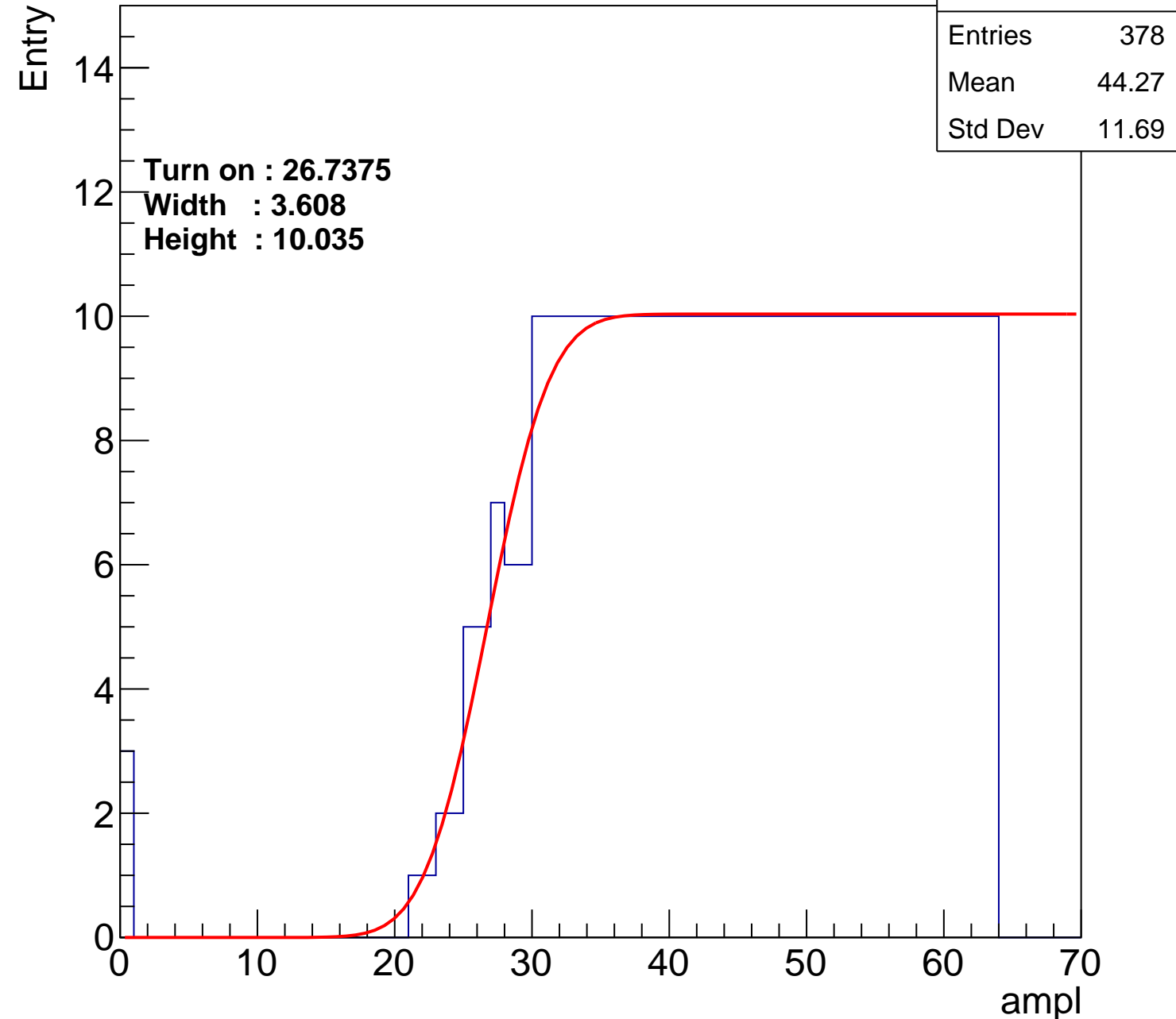
Width : 3.608

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch80

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	44.82
Std Dev	11.74

Turn on : 28.5431

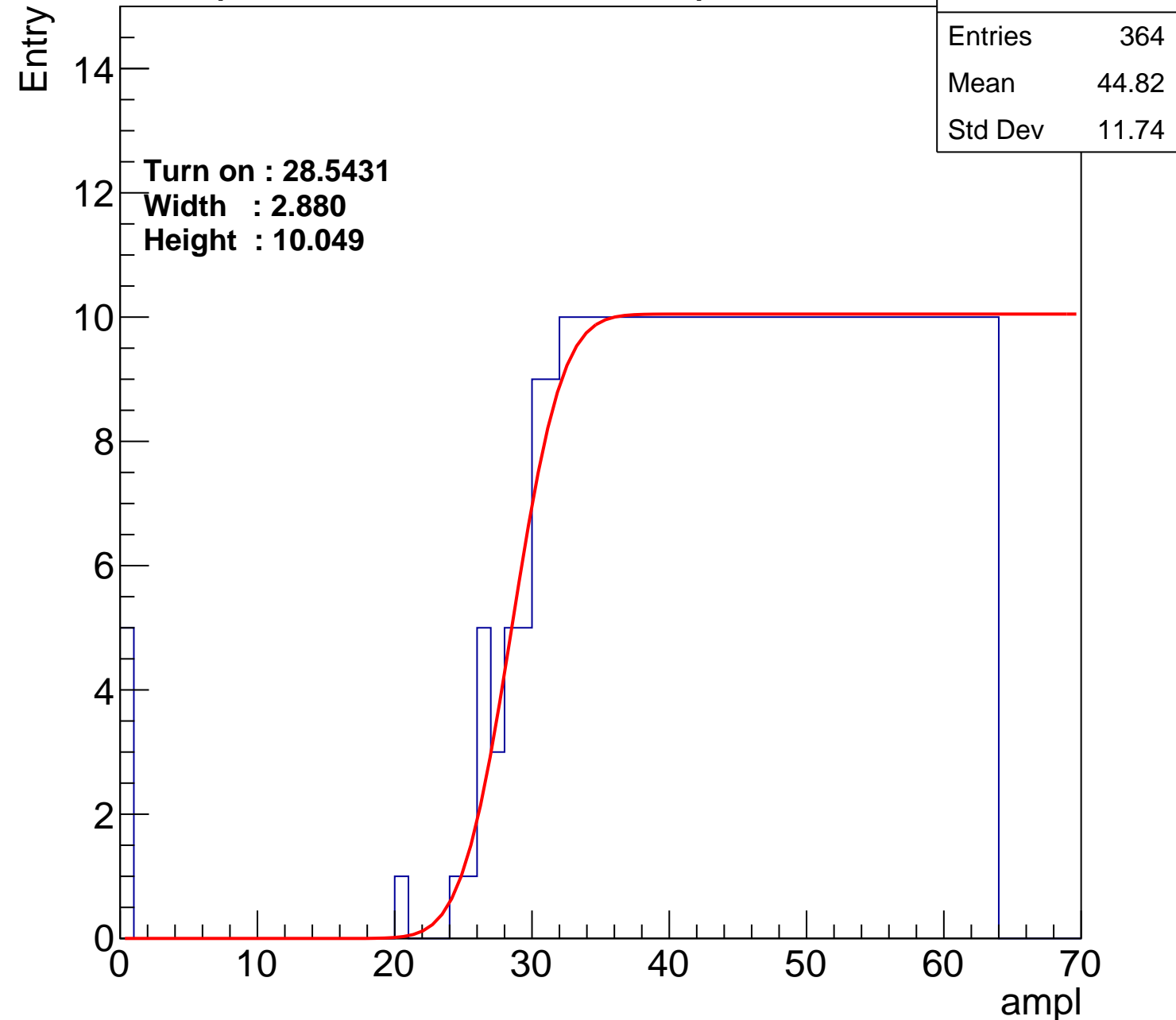
Width : 2.880

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.22
Std Dev	11.75

Turn on : 26.0219

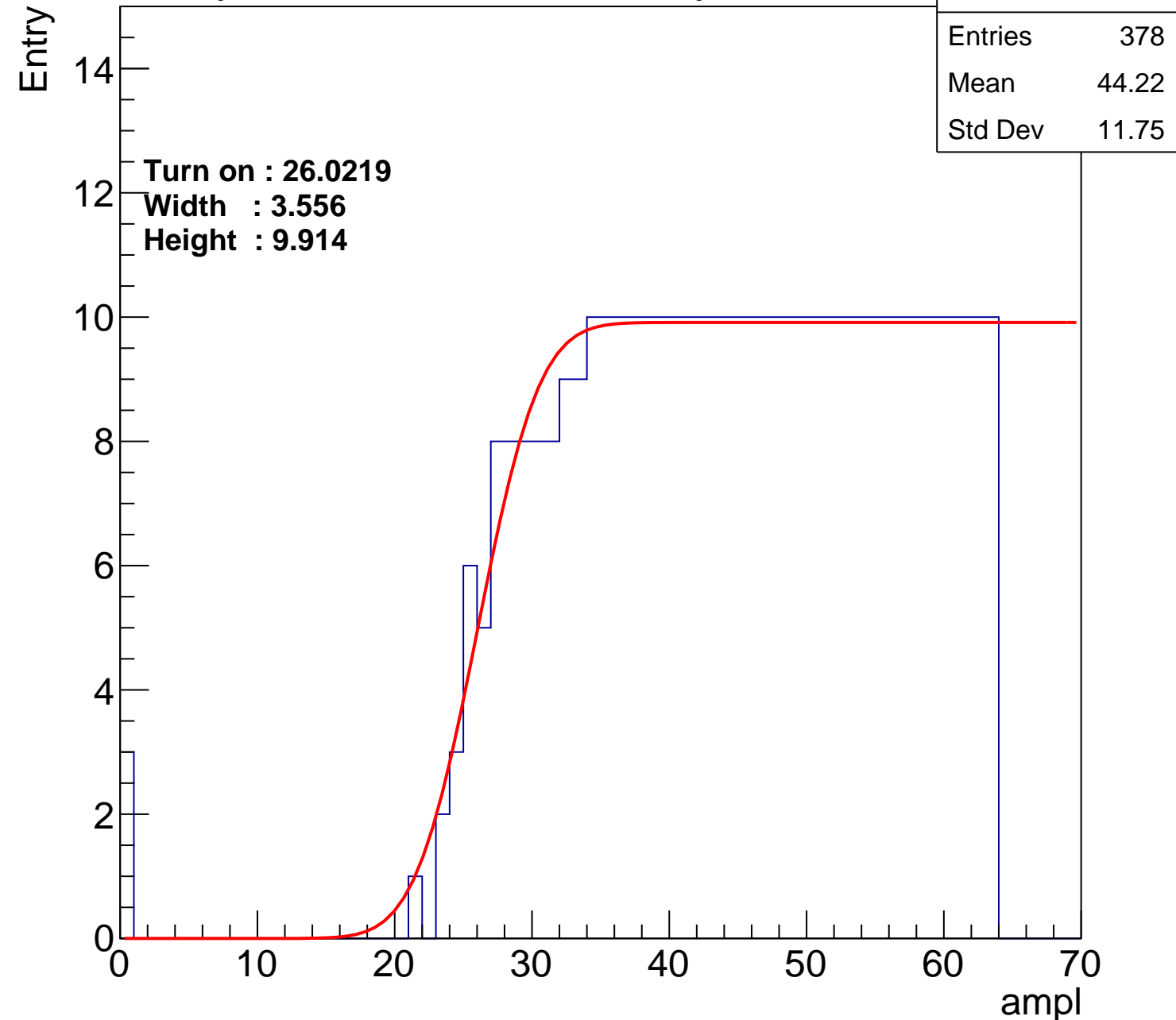
Width : 3.556

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.76
Std Dev	11.72

Turn on : 28.0961

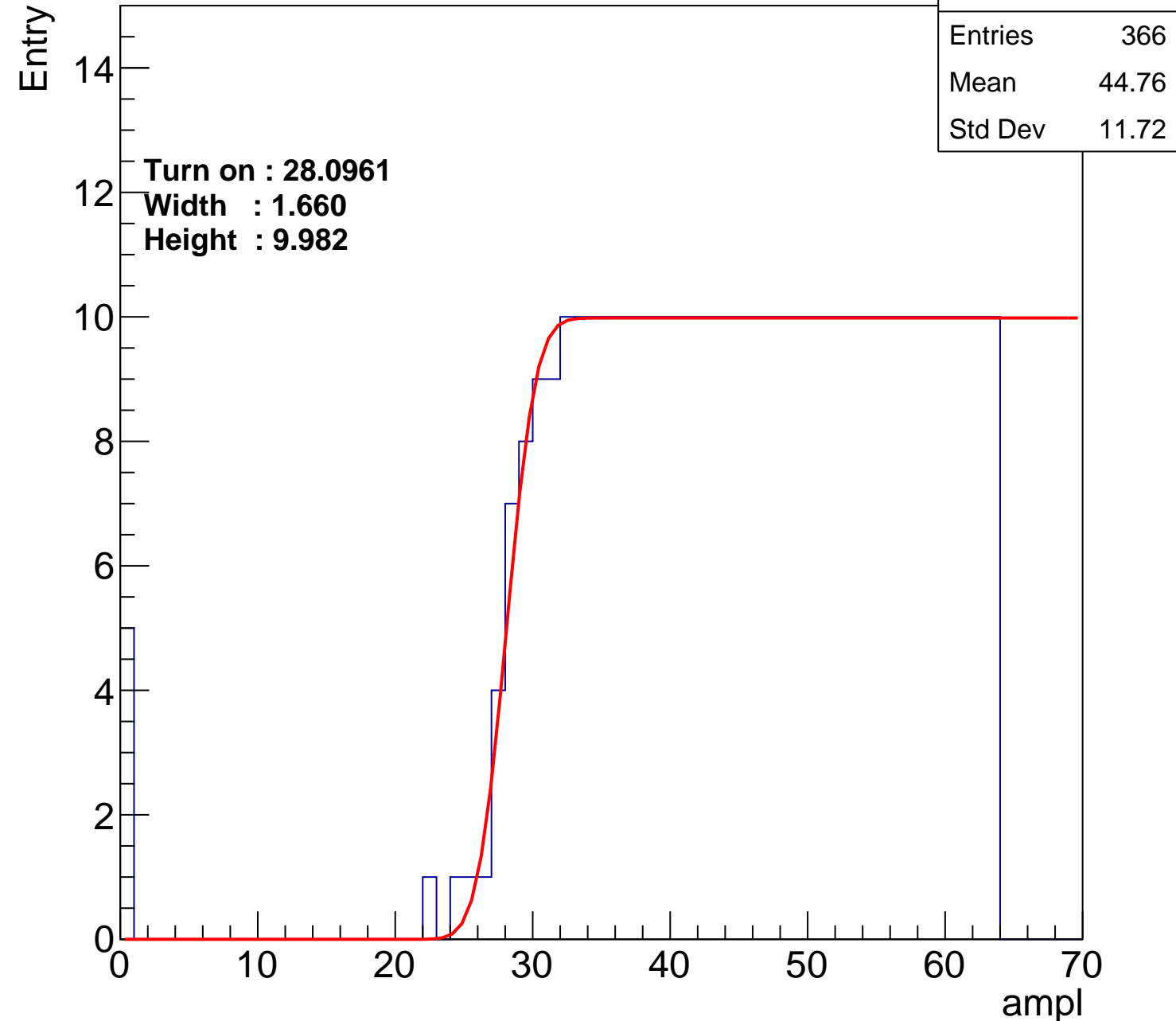
Width : 1.660

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.0879

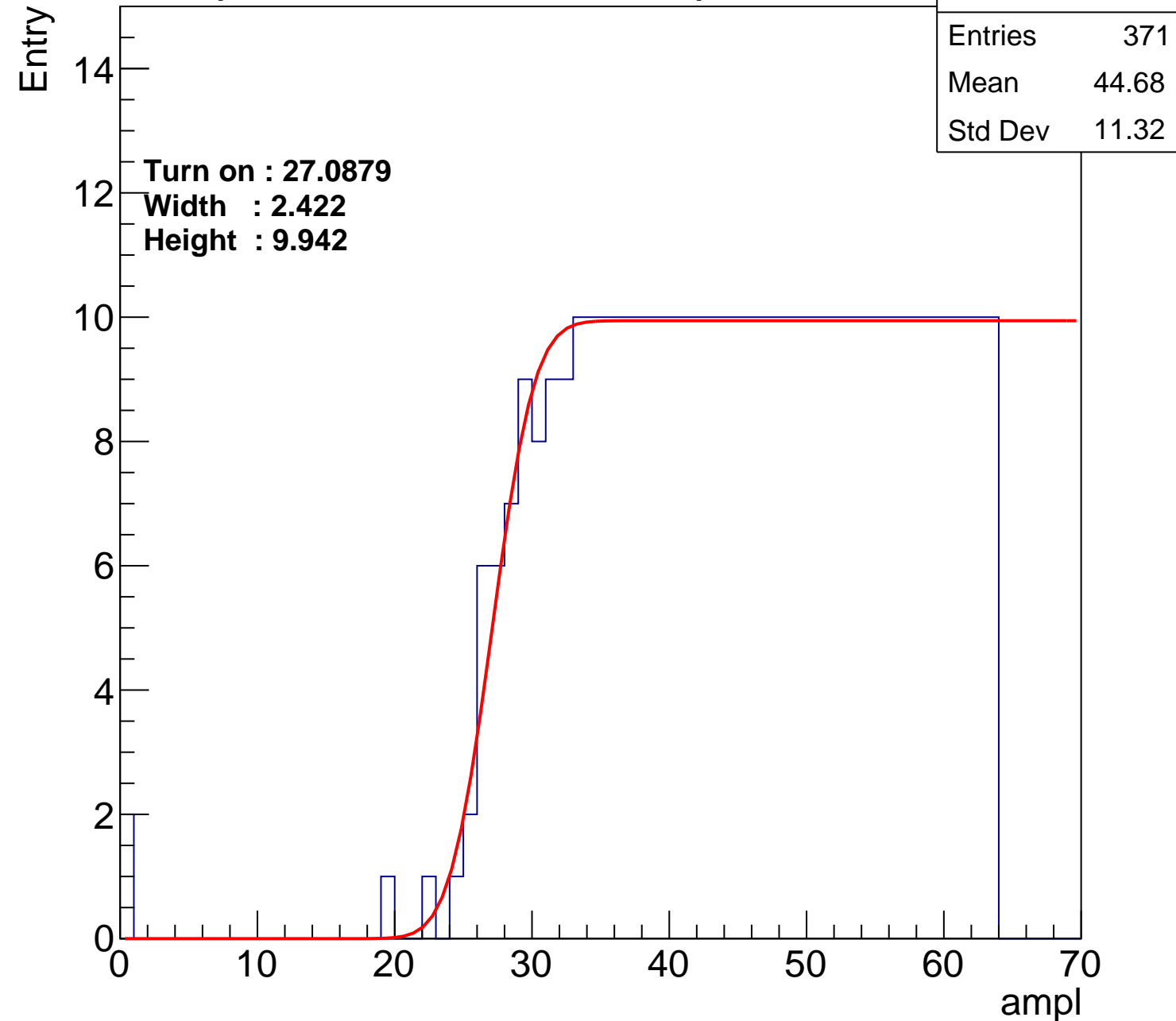
Width : 2.422

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	377
Mean	44.4
Std Dev	11.47

Turn on : 26.5909

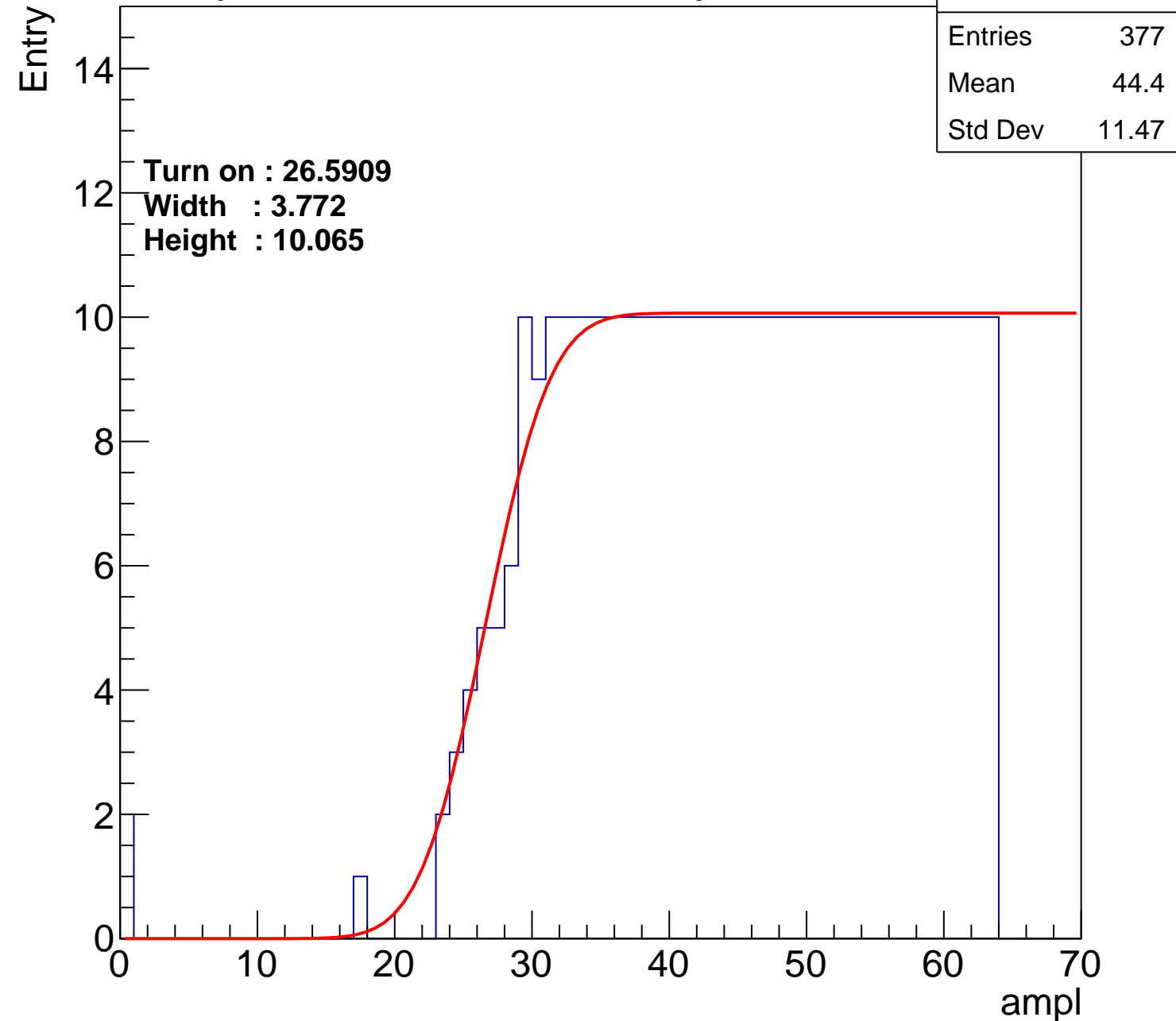
Width : 3.772

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.12
Std Dev	11.89

Turn on : 26.3577

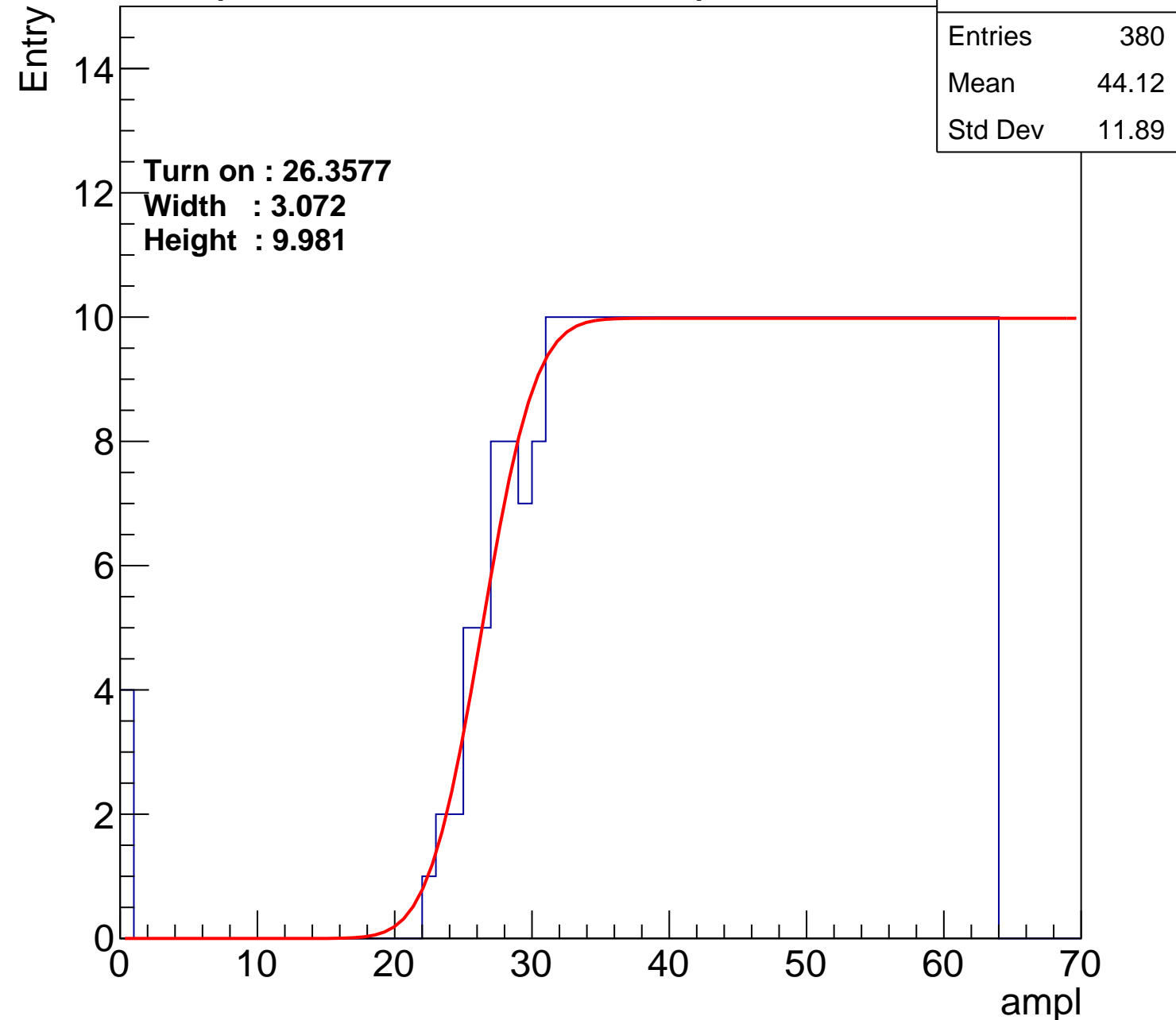
Width : 3.072

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.19
Std Dev	10.9

Turn on : 28.2510

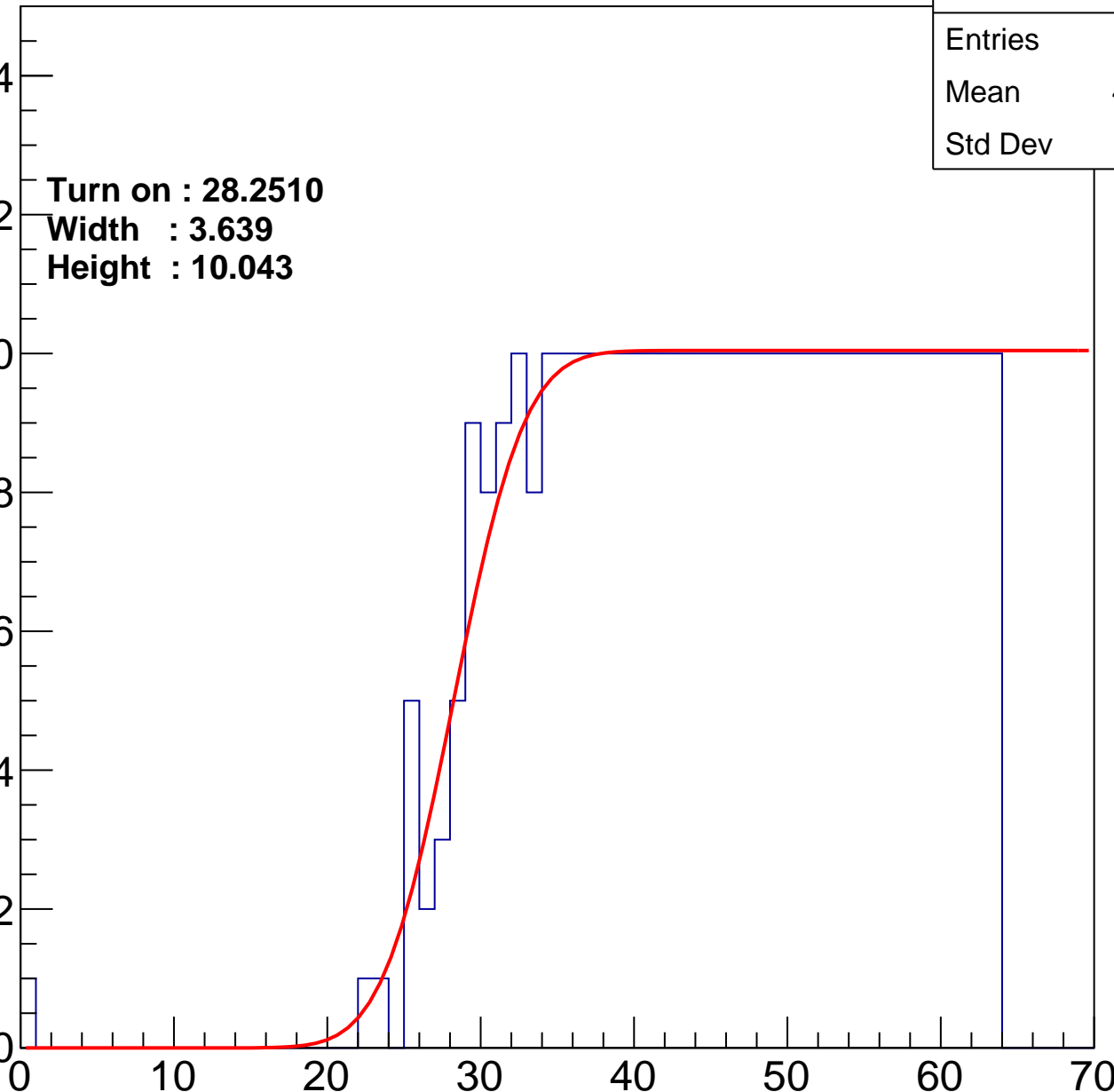
Width : 3.639

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 27.4494

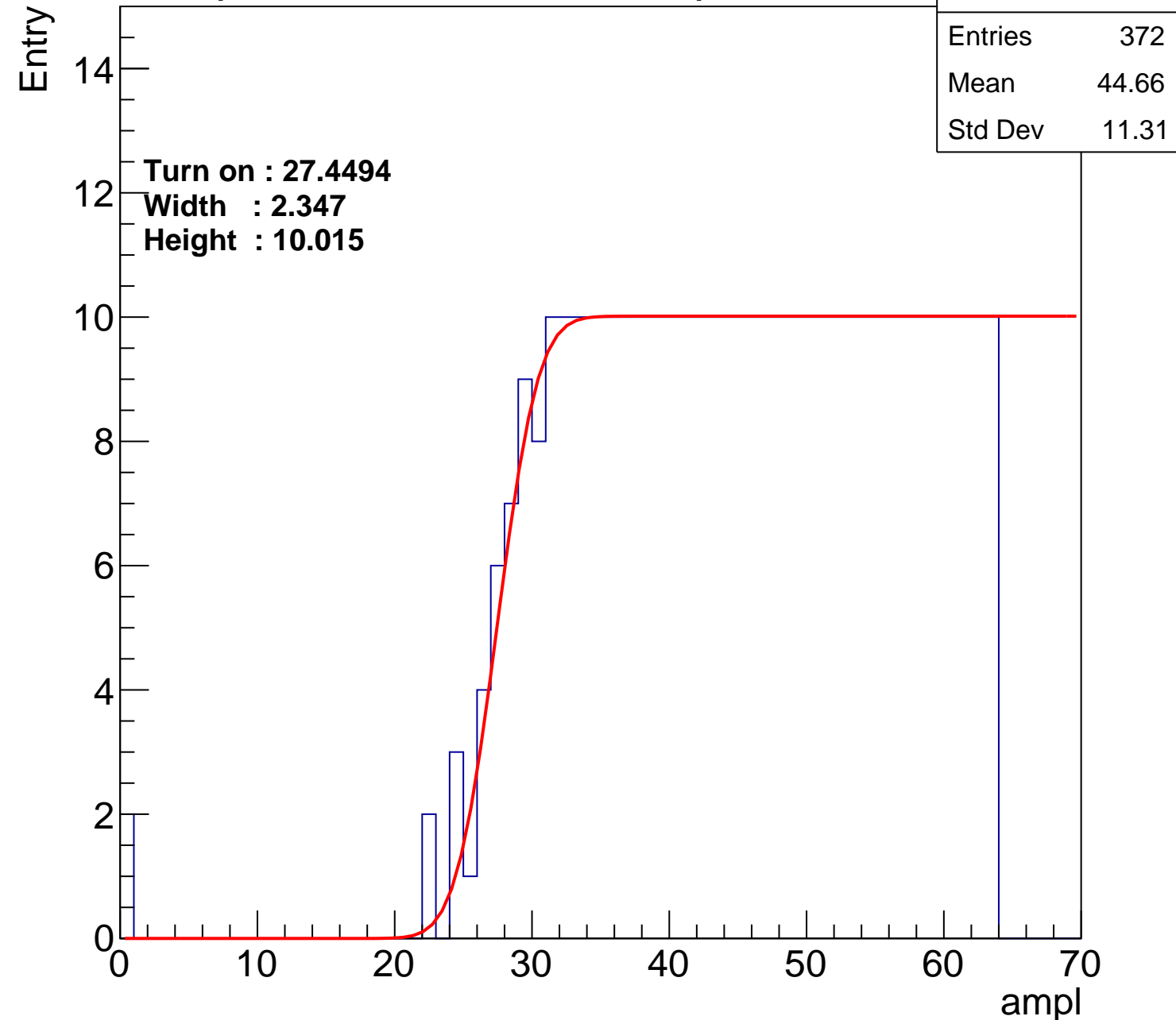
Width : 2.347

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch88

calib_packv5_042523_0143.root, FC#5, port B1

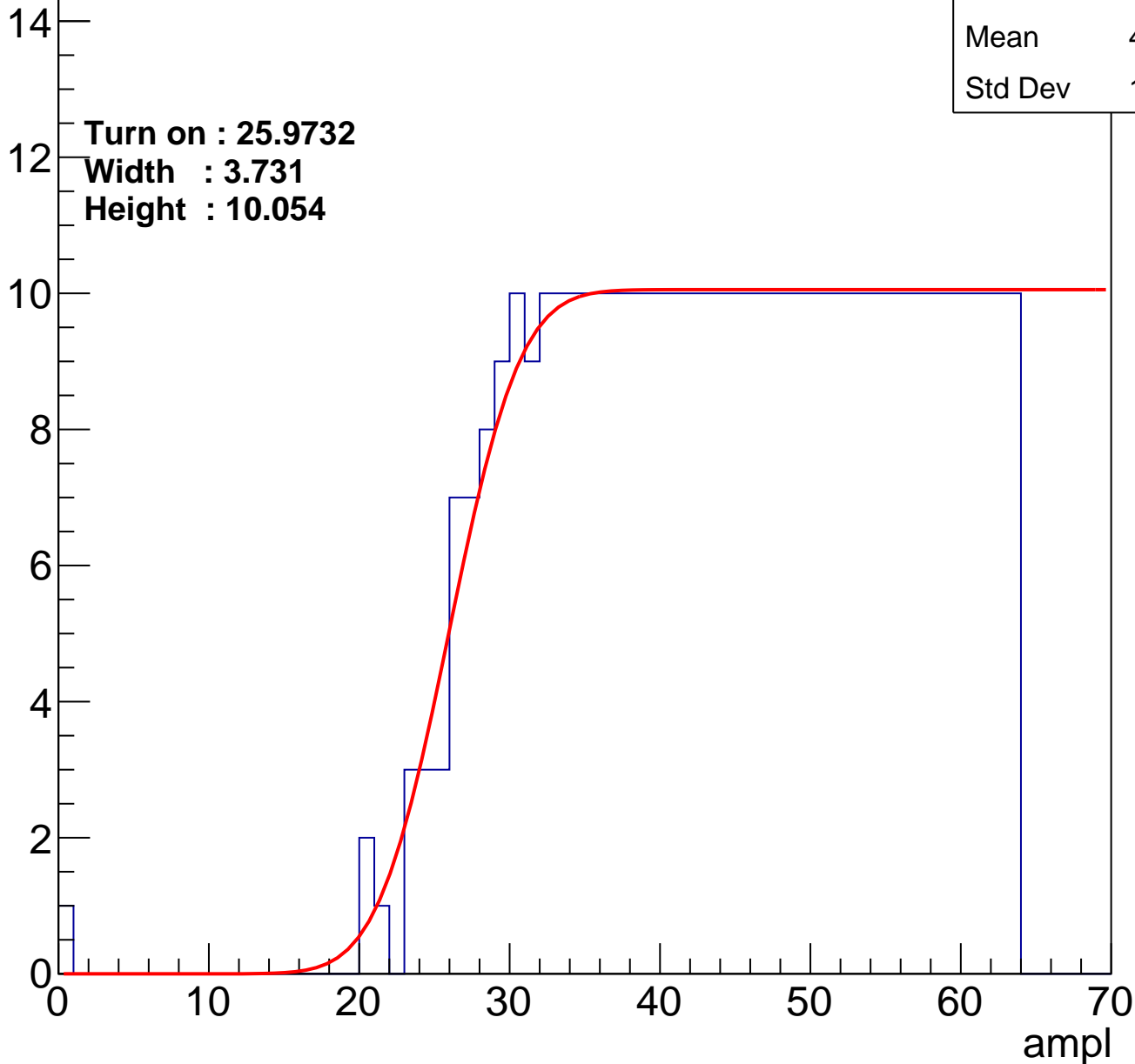
Entry

Entries	383
Mean	44.16
Std Dev	11.46

Turn on : 25.9732

Width : 3.731

Height : 10.054



B0L000S, U5-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.84
Std Dev	11.57

Turn on : 27.8236

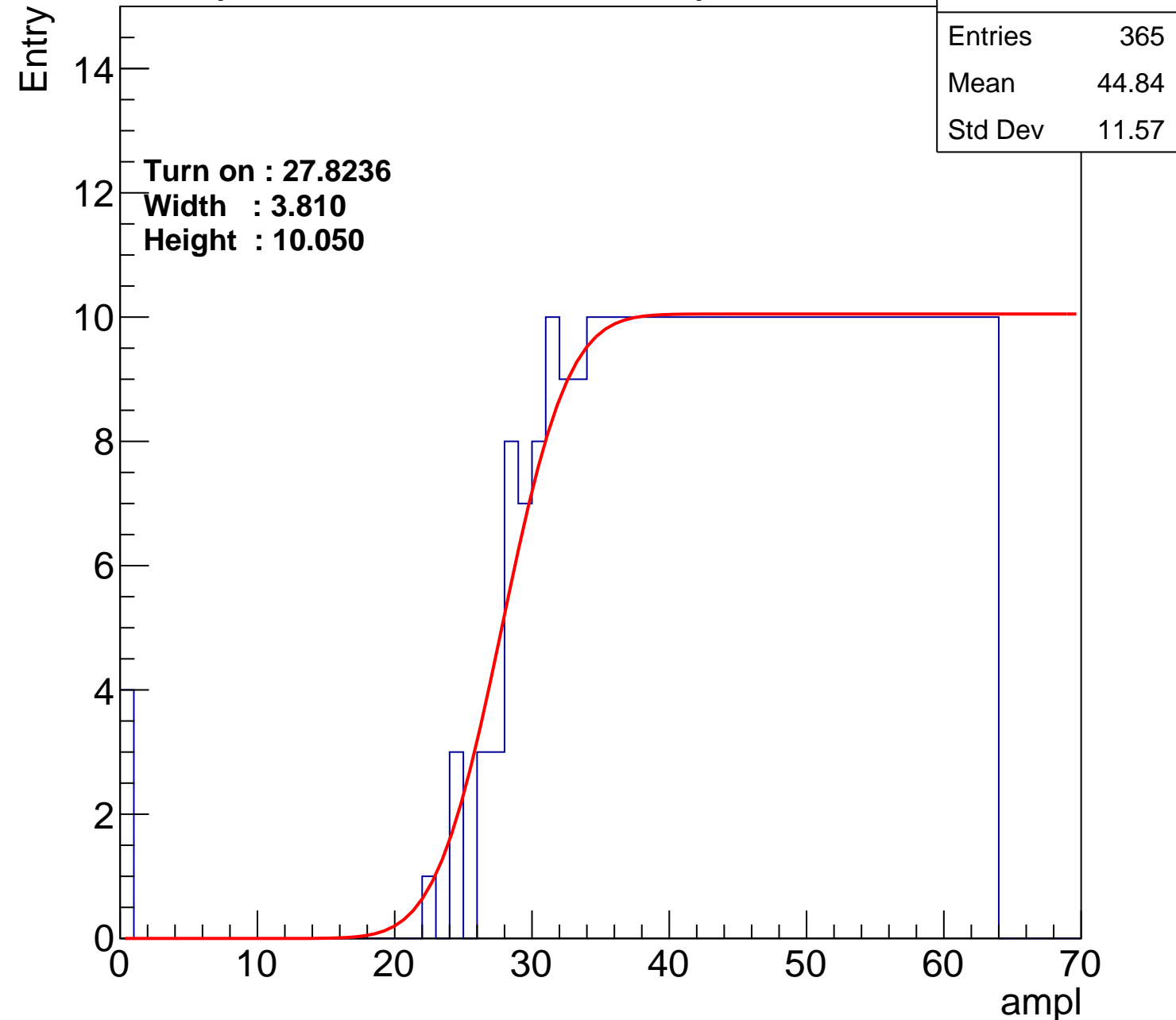
Width : 3.810

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch90

calib_packv5_042523_0143.root, FC#5, port B1

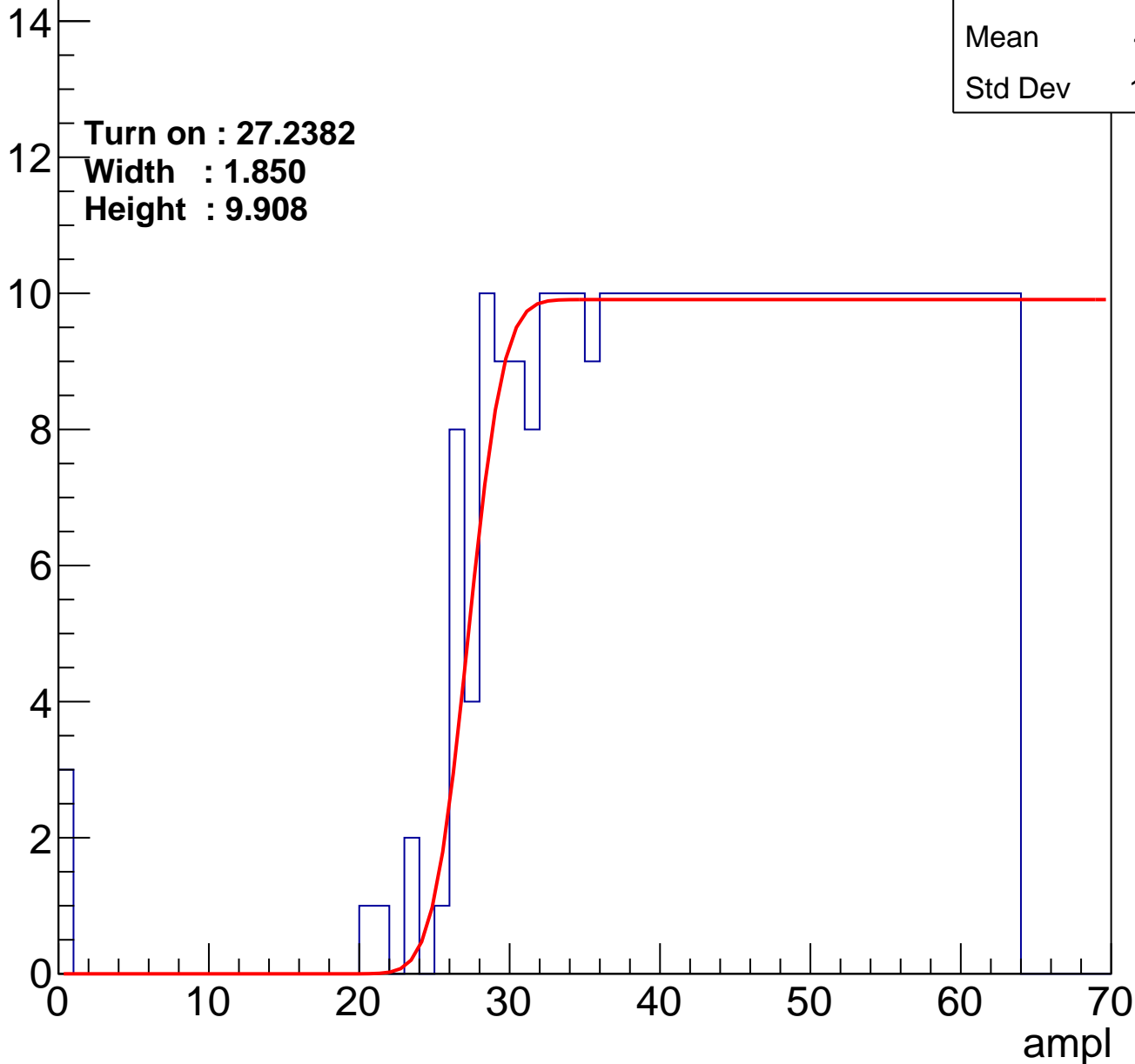
Entry

Entries	375
Mean	44.41
Std Dev	11.62

Turn on : 27.2382

Width : 1.850

Height : 9.908



B0L000S, U5-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.5670

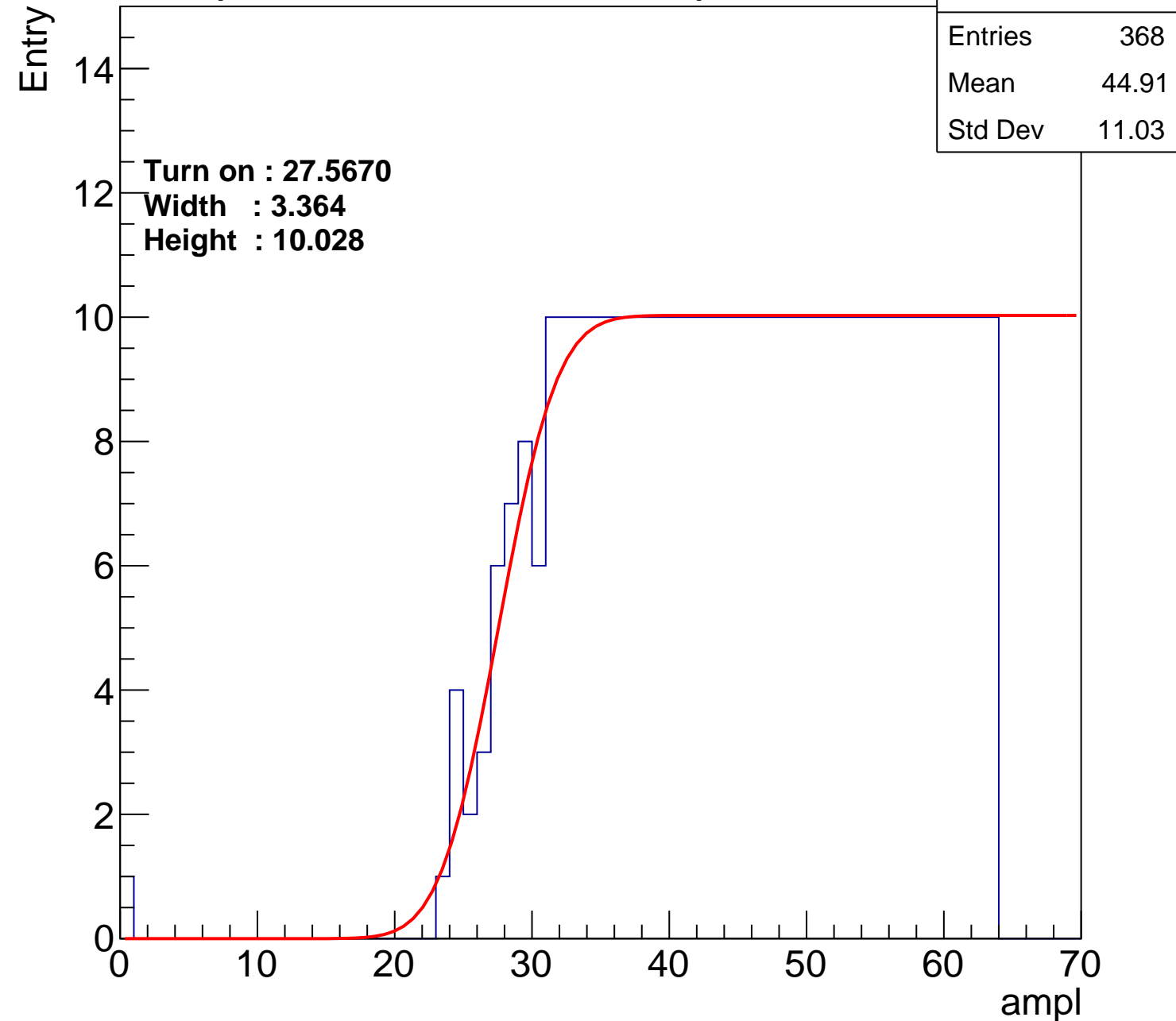
Width : 3.364

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch92

calib_packv5_042523_0143.root, FC#5, port B1

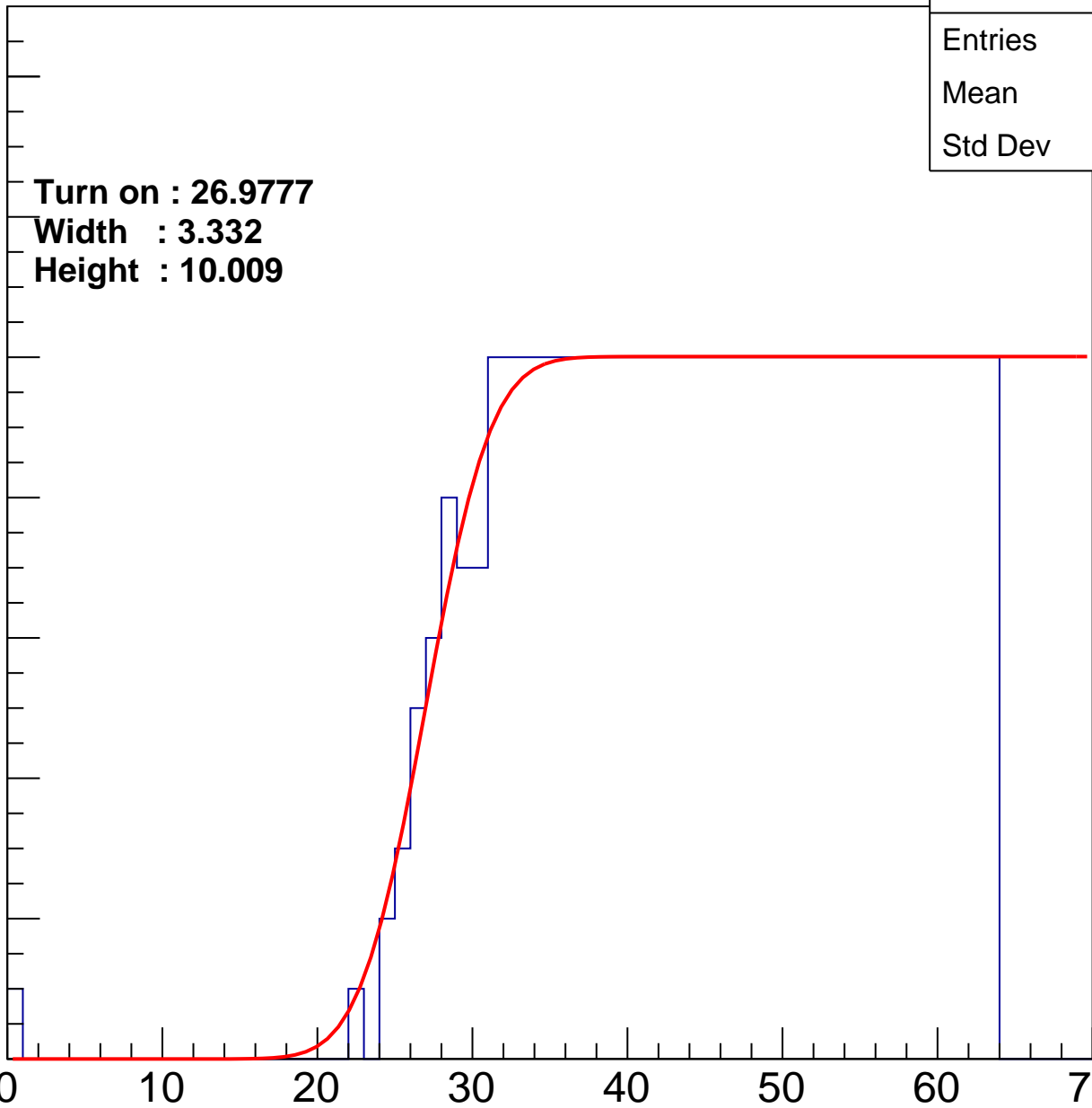
Entry

14
12
10
8
6
4
2
0

Turn on : 26.9777
Width : 3.332
Height : 10.009

Entries	370
Mean	44.82
Std Dev	11.06

ampl



B0L000S, U5-ch93

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.48
Std Dev	10.75

Turn on : 28.1638

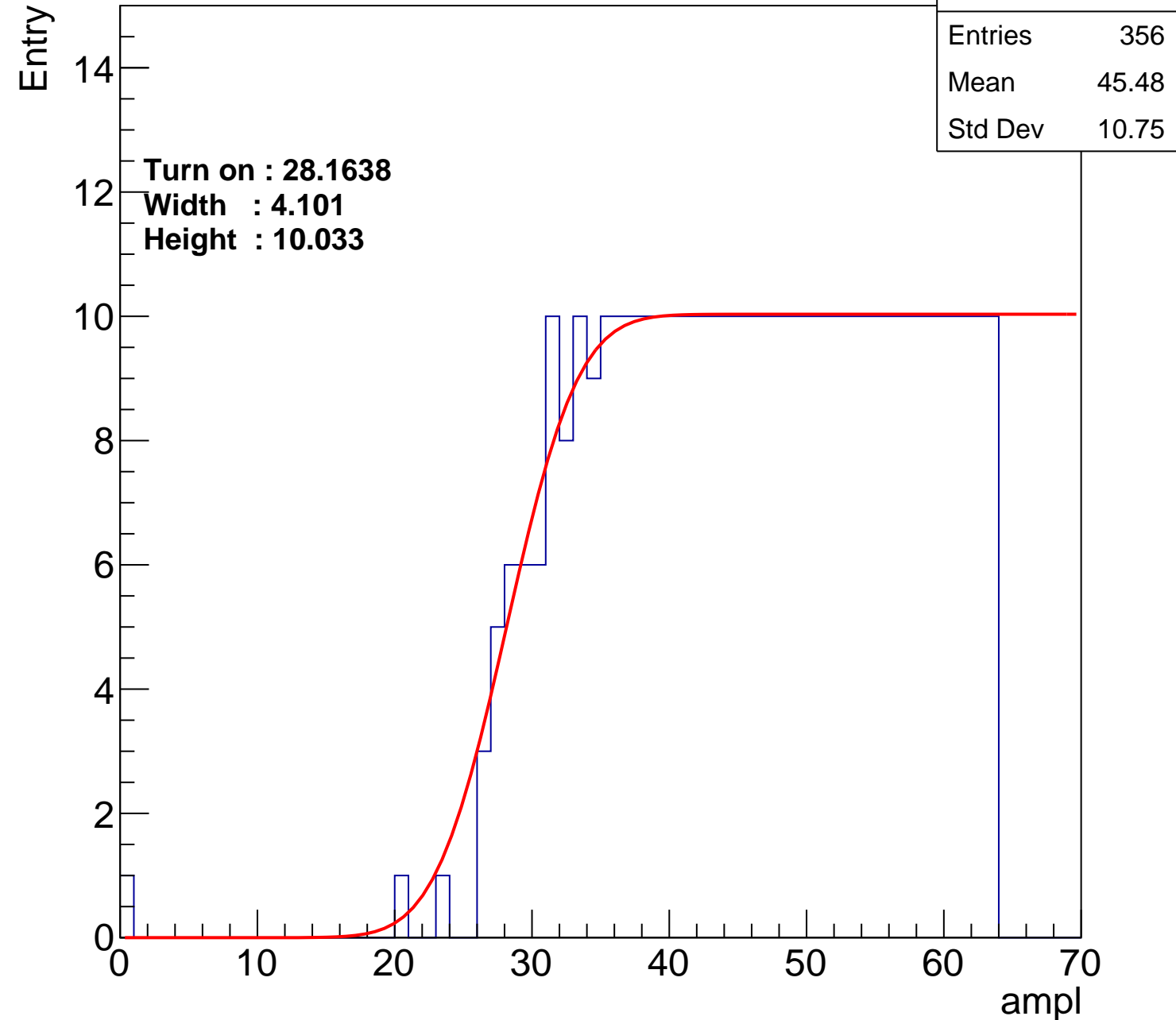
Width : 4.101

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch94

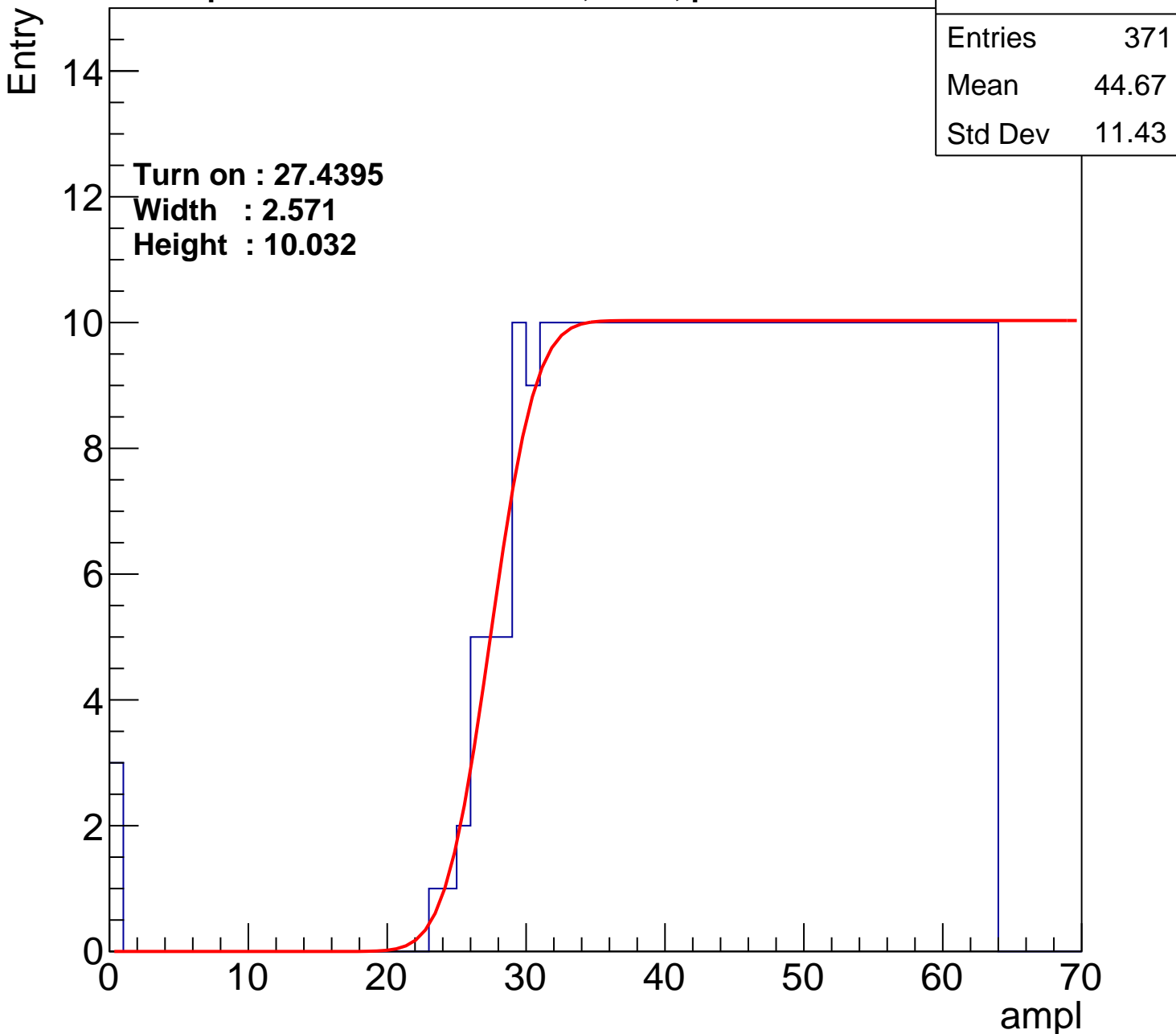
calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.4395

Width : 2.571

Height : 10.032

Entries	371
Mean	44.67
Std Dev	11.43



B0L000S, U5-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.1353

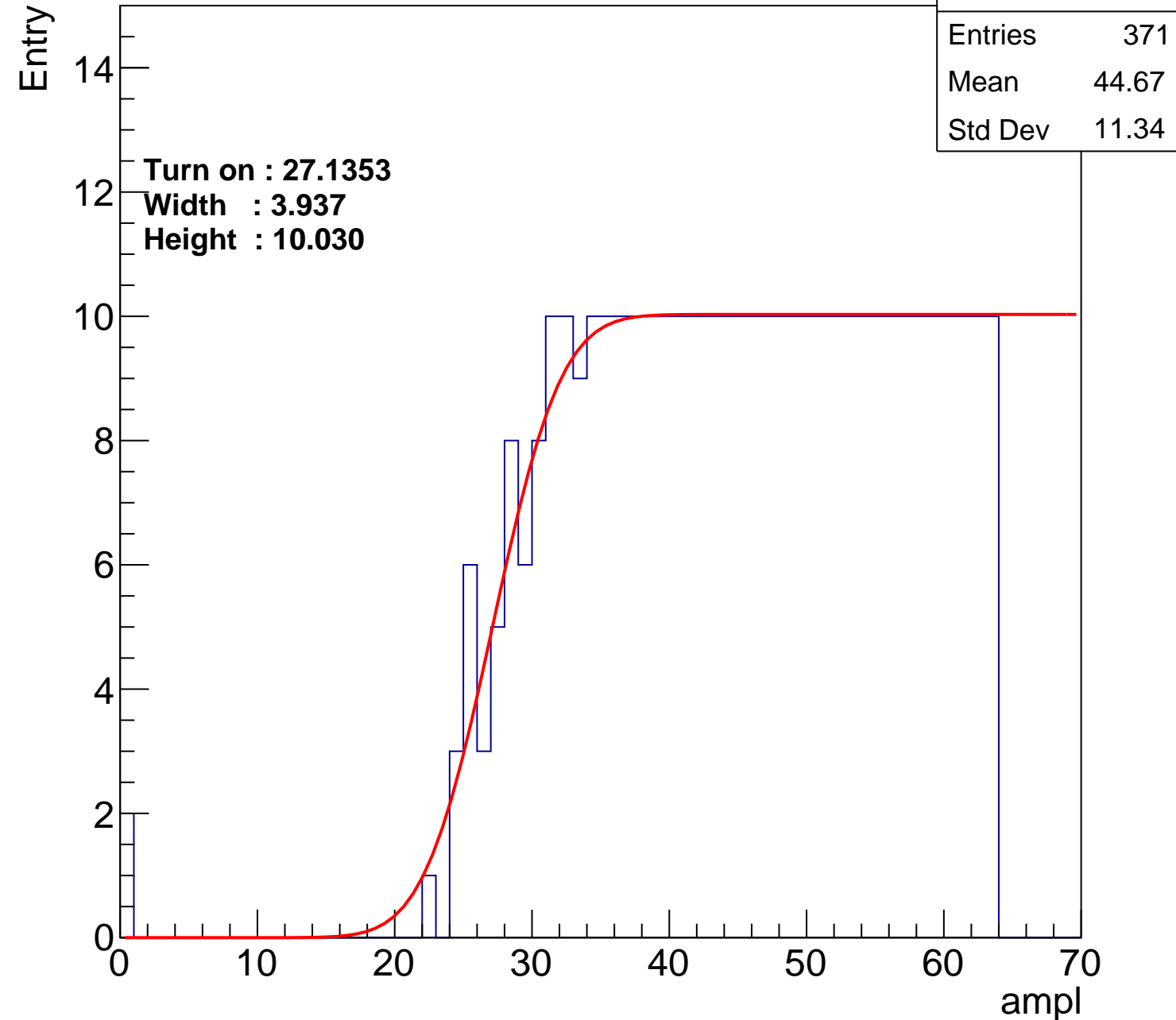
Width : 3.937

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch96

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.1206

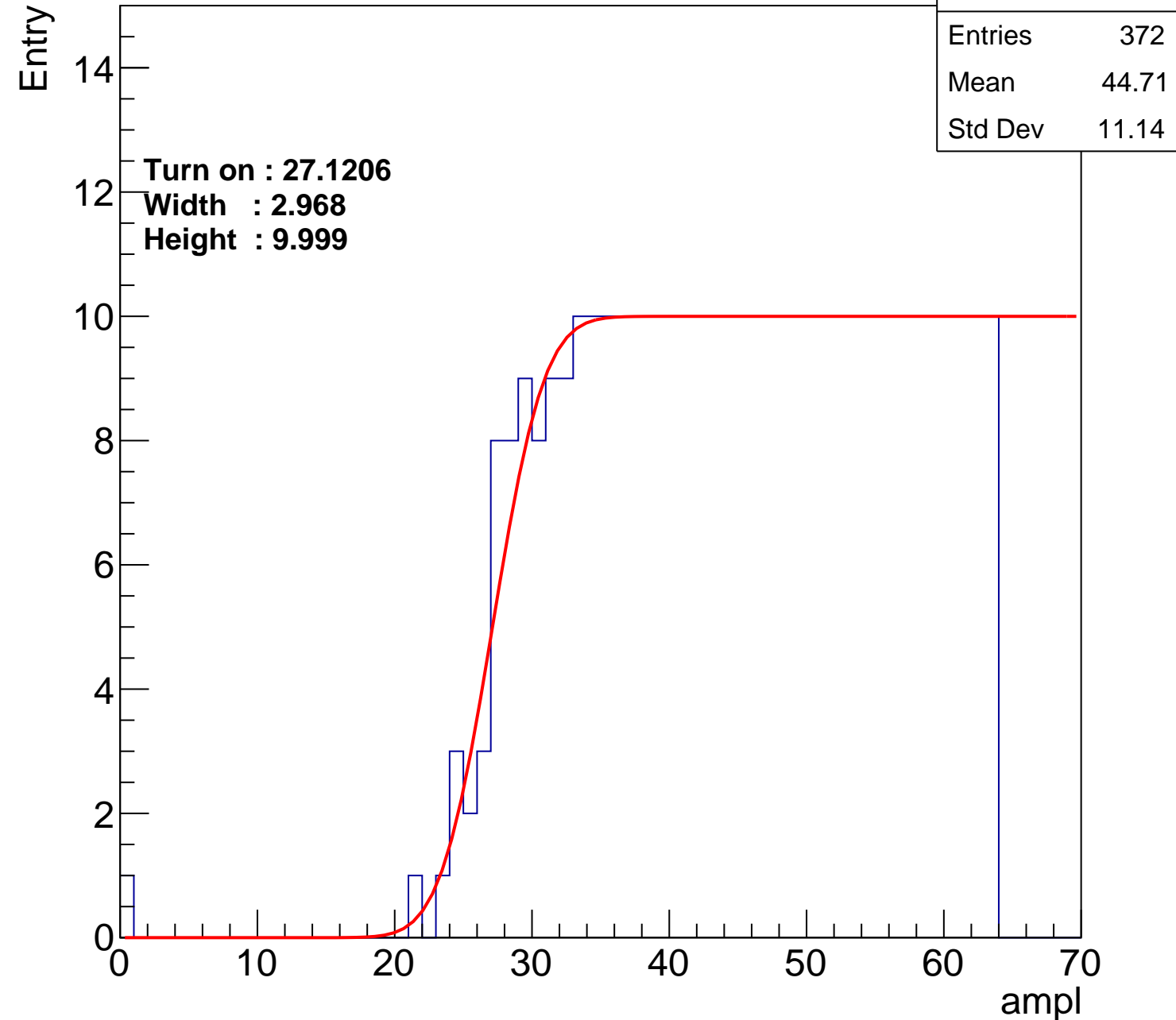
Width : 2.968

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.16
Std Dev	11.09

Turn on : 27.2836

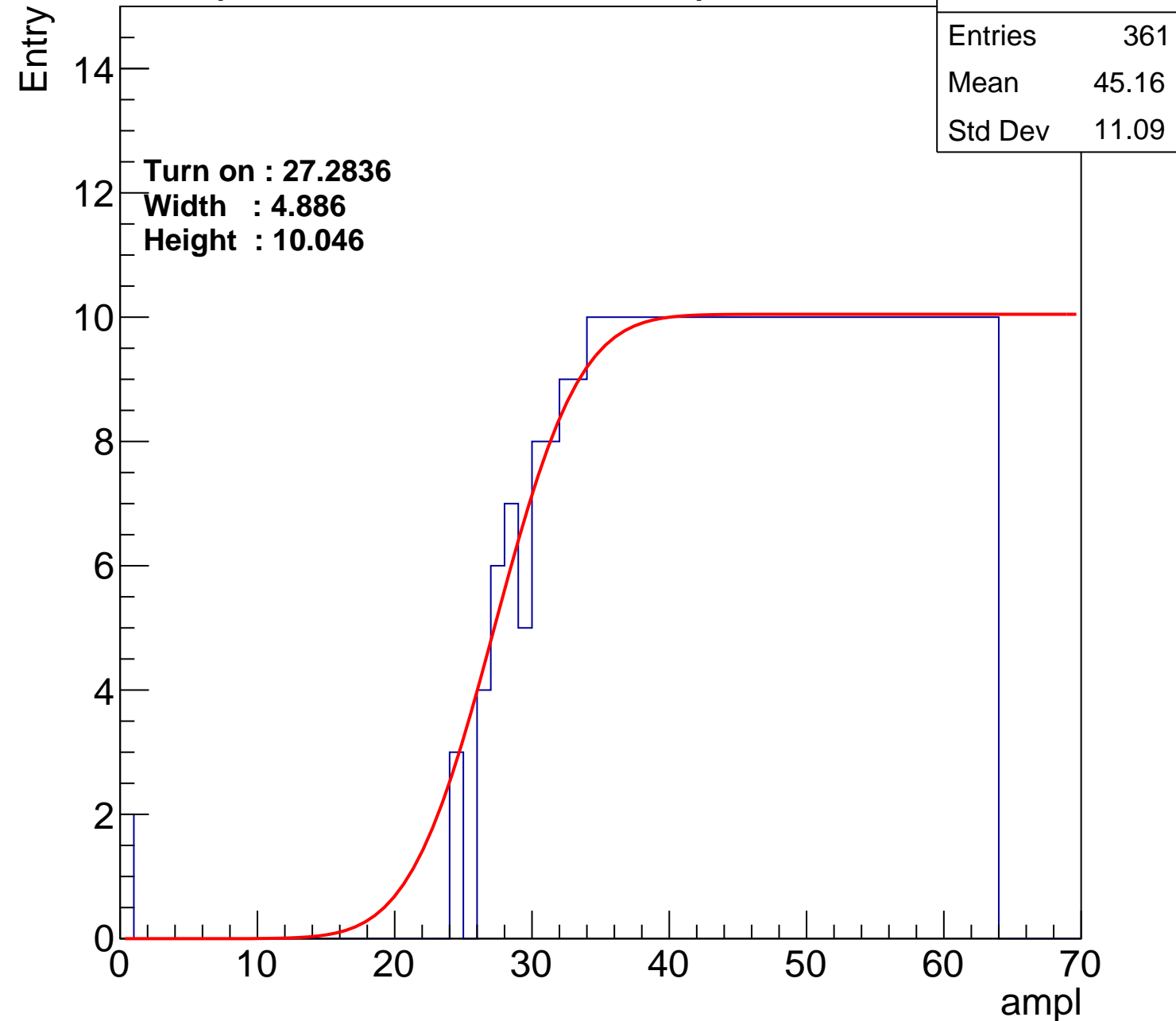
Width : 4.886

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.41
Std Dev	11.81

Turn on : 27.5124

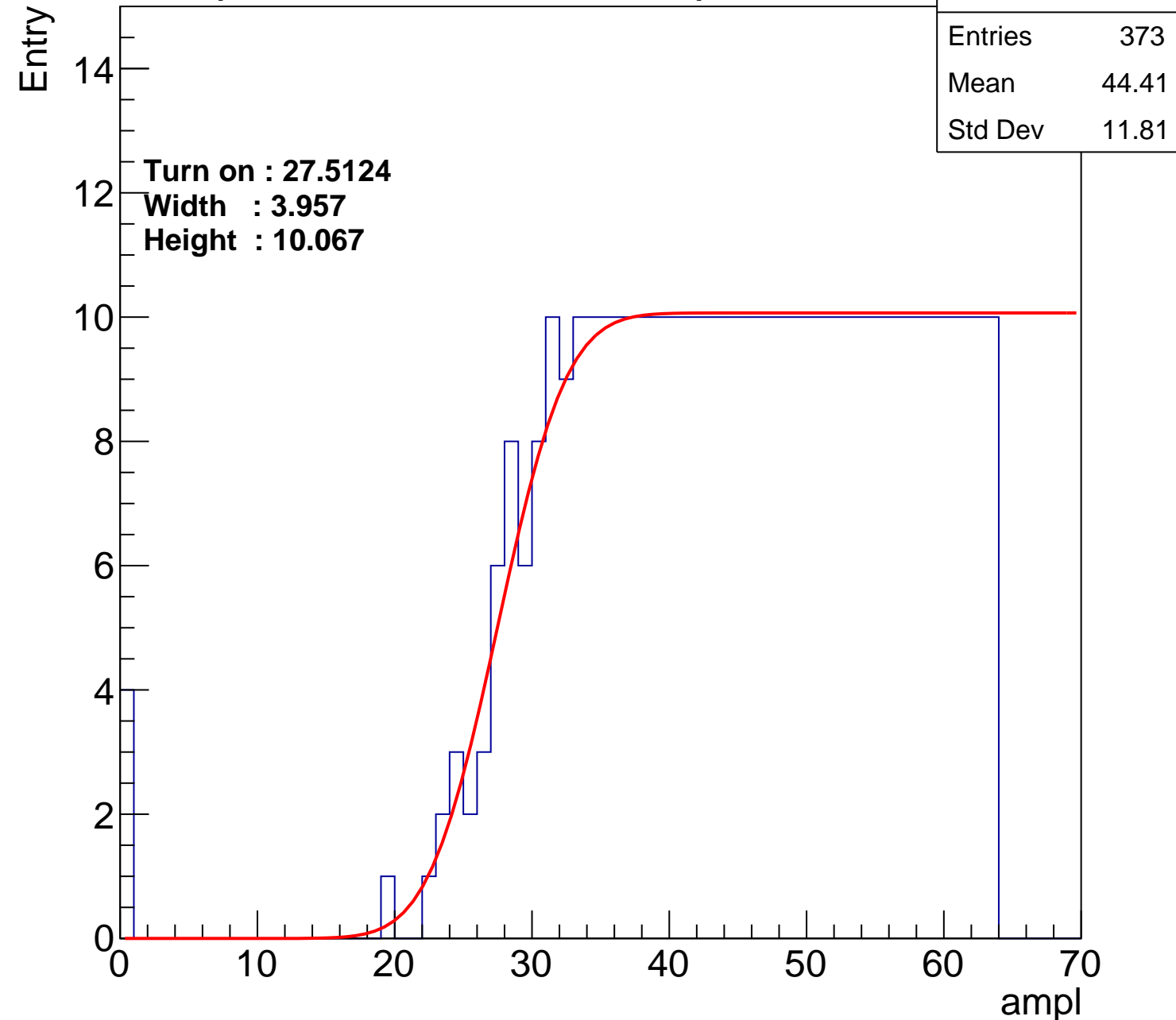
Width : 3.957

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.76
Std Dev	11.45

Turn on : 28.1425

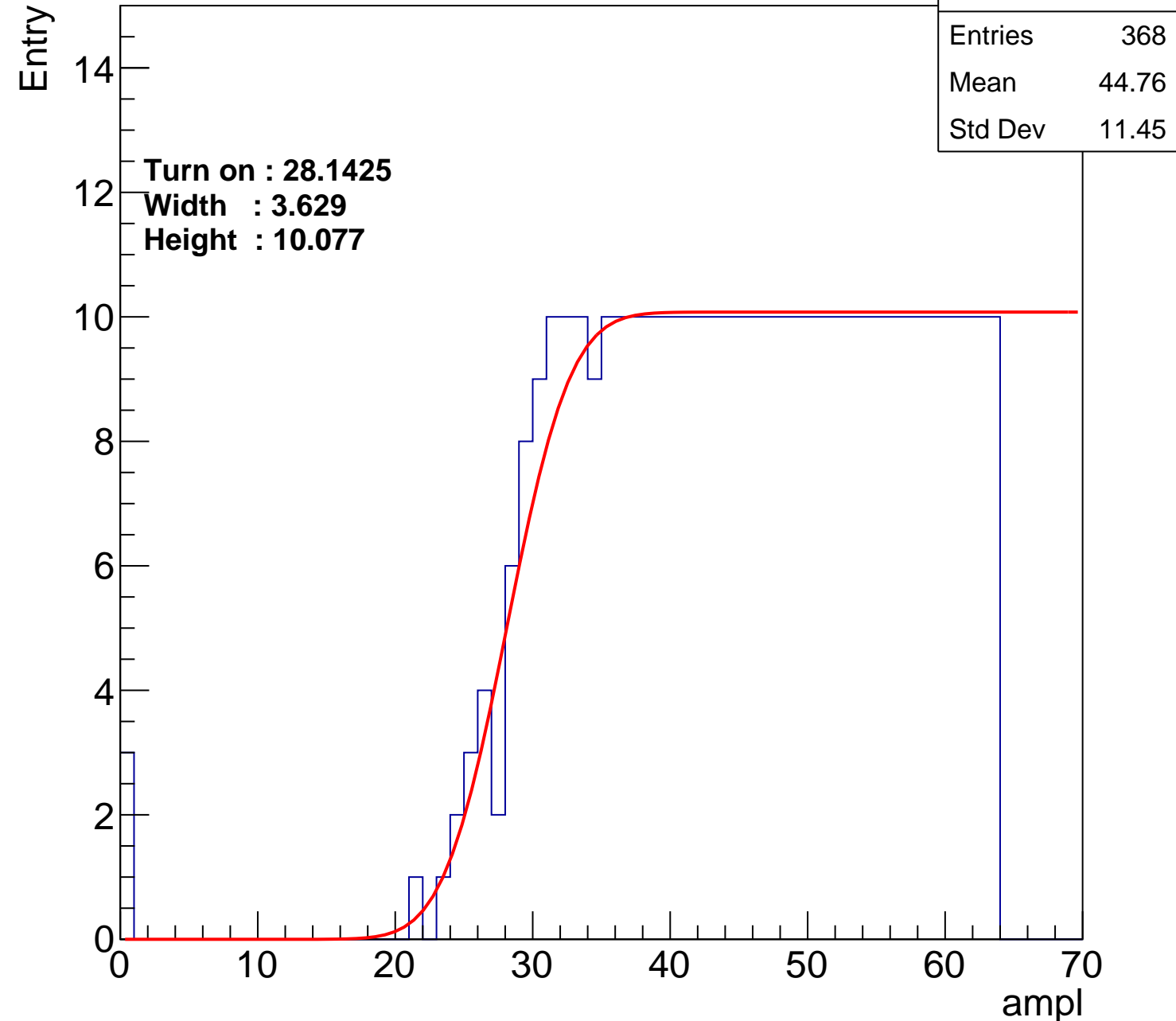
Width : 3.629

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch100

calib_packv5_042523_0143.root, FC#5, port B1

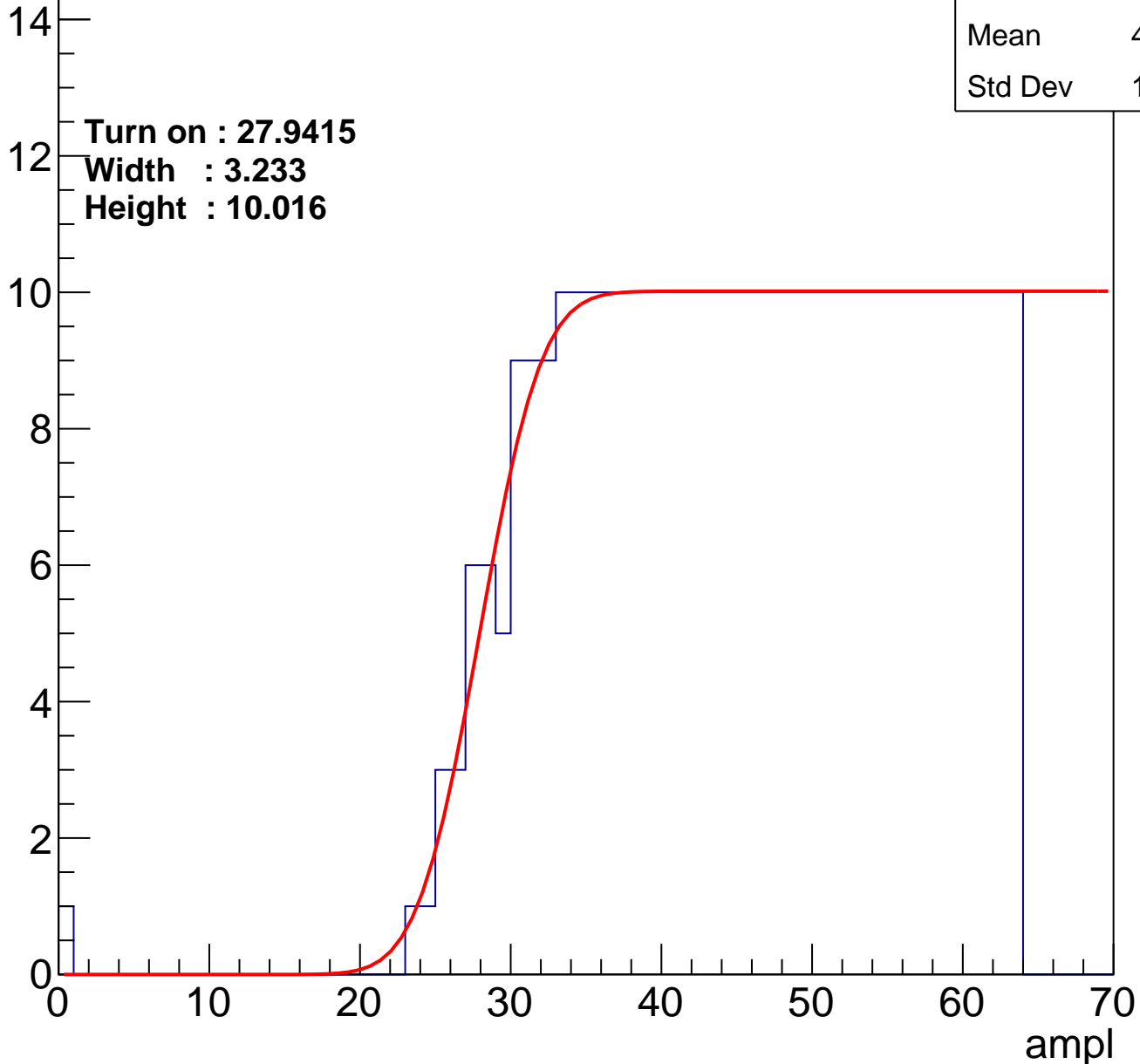
Entries	363
Mean	45.16
Std Dev	10.89

Turn on : 27.9415

Width : 3.233

Height : 10.016

Entry



B0L000S, U5-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 27.3663

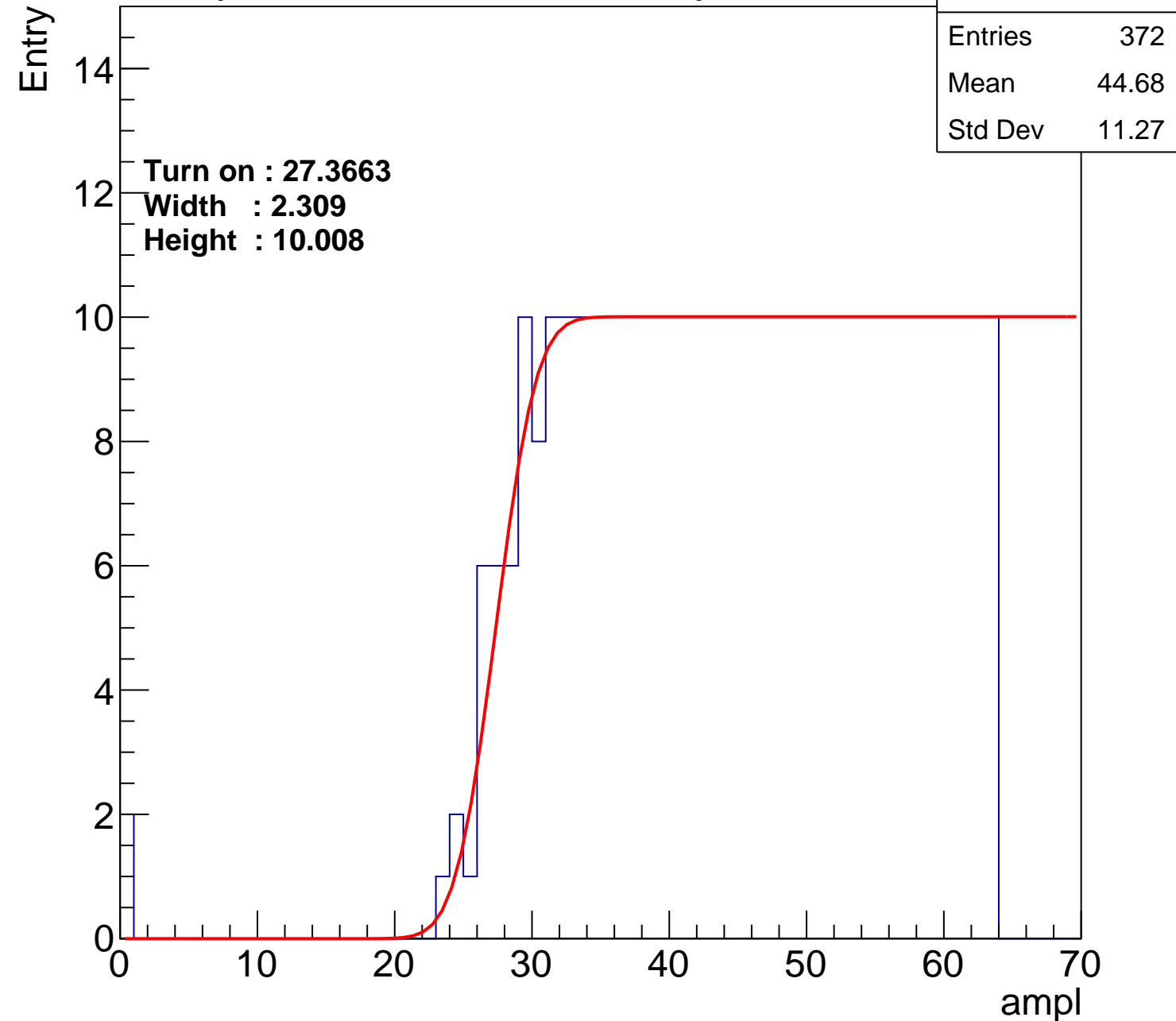
Width : 2.309

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch102

calib_packv5_042523_0143.root, FC#5, port B1

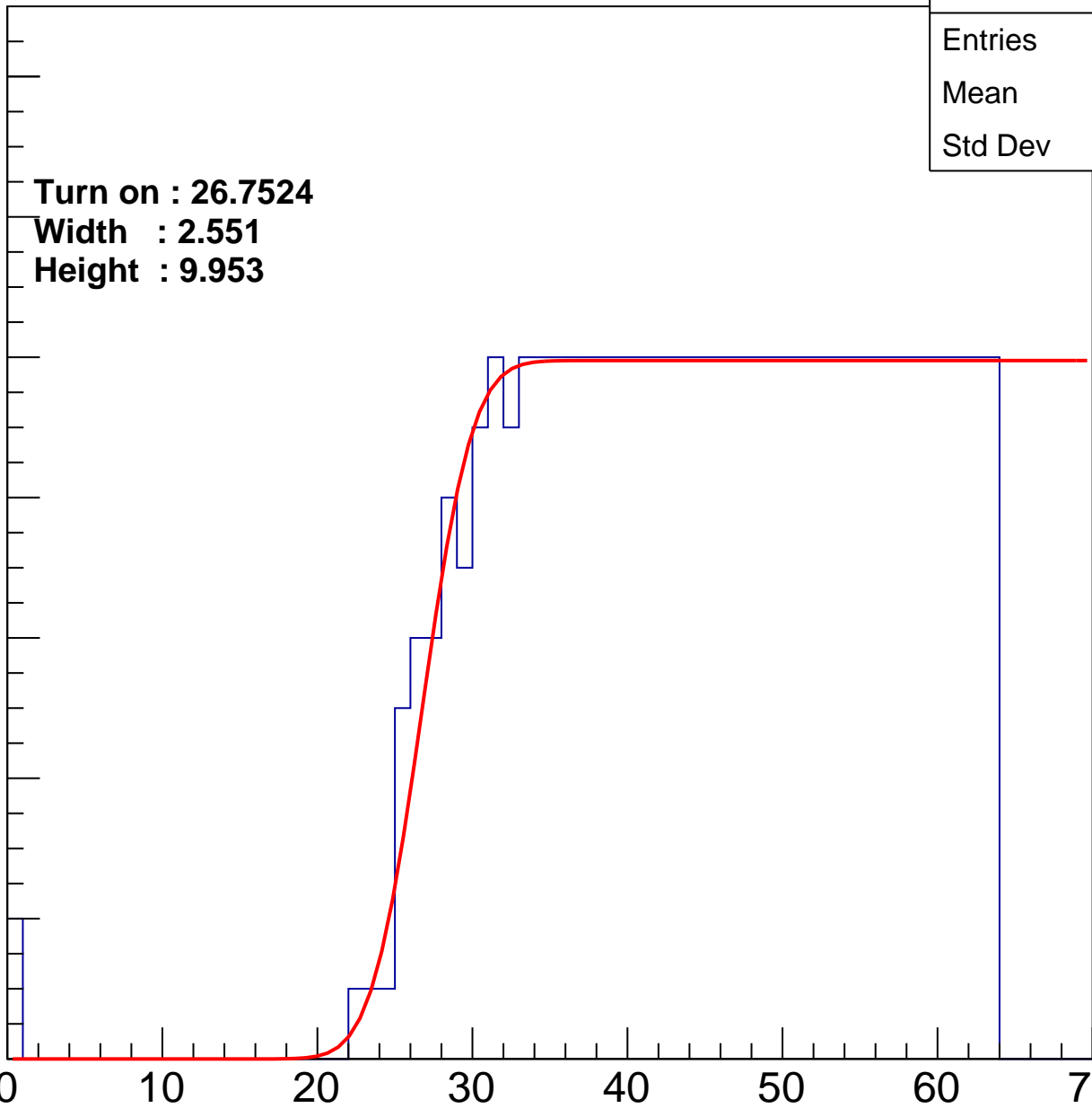
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7524
Width : 2.551
Height : 9.953

Entries	375
Mean	44.5
Std Dev	11.4

ampl



B0L000S, U5-ch103

calib_packv5_042523_0143.root, FC#5, port B1

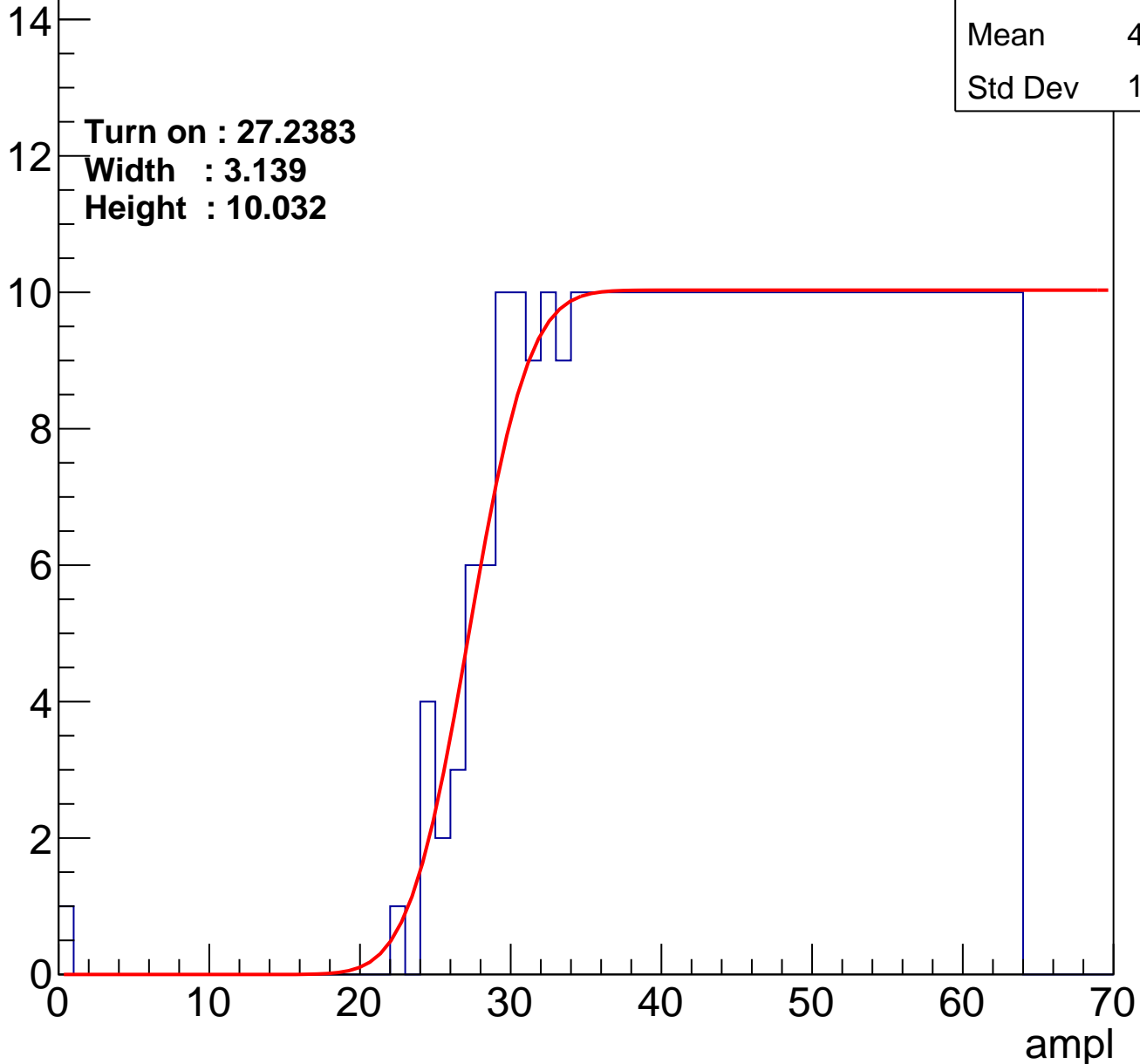
Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.2383

Width : 3.139

Height : 10.032

Entry



B0L000S, U5-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	44.99
Std Dev	11.35

Turn on : 28.7426

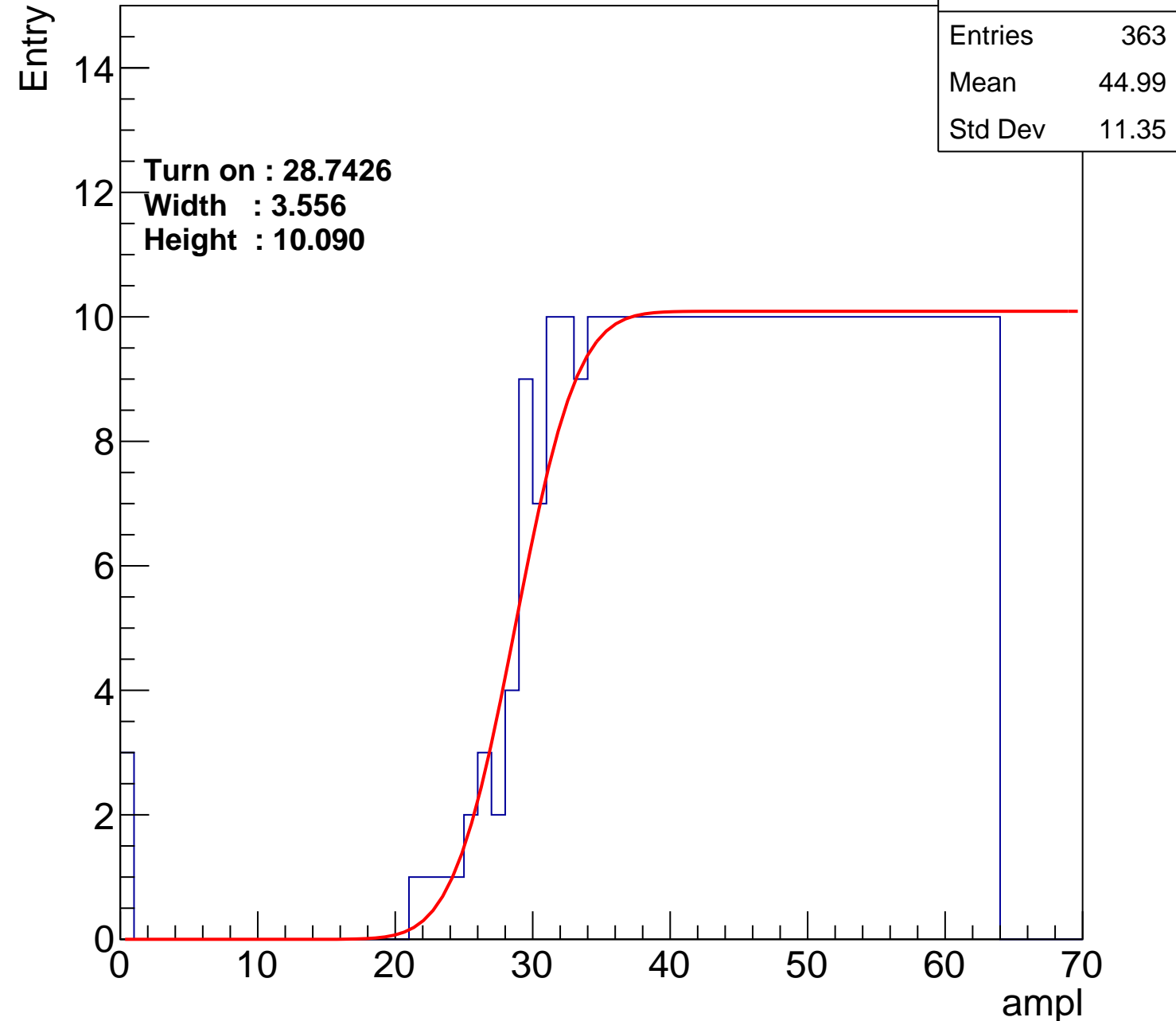
Width : 3.556

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch105

calib_packv5_042523_0143.root, FC#5, port B1

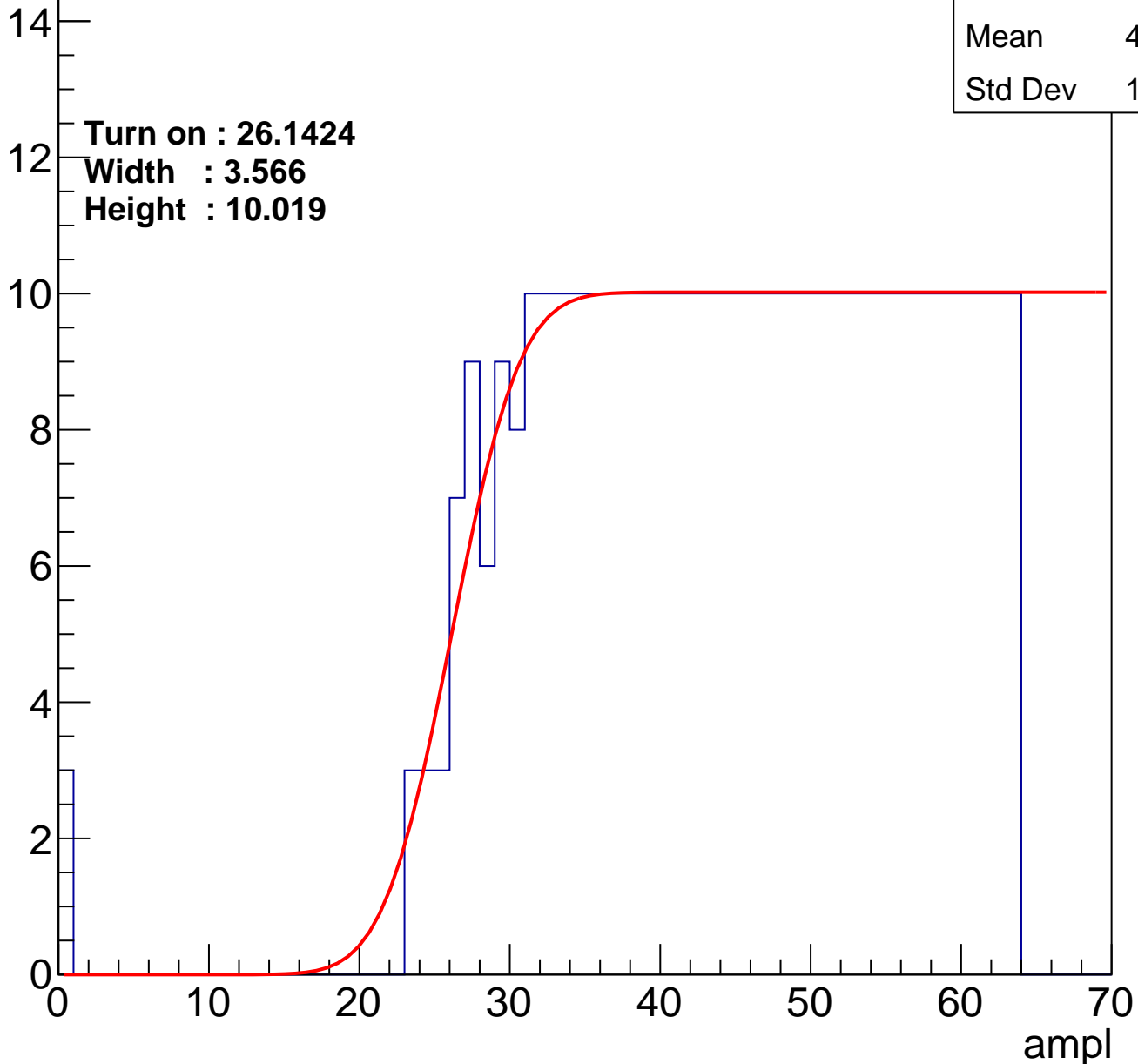
Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.1424

Width : 3.566

Height : 10.019

Entry



B0L000S, U5-ch106

calib_packv5_042523_0143.root, FC#5, port B1

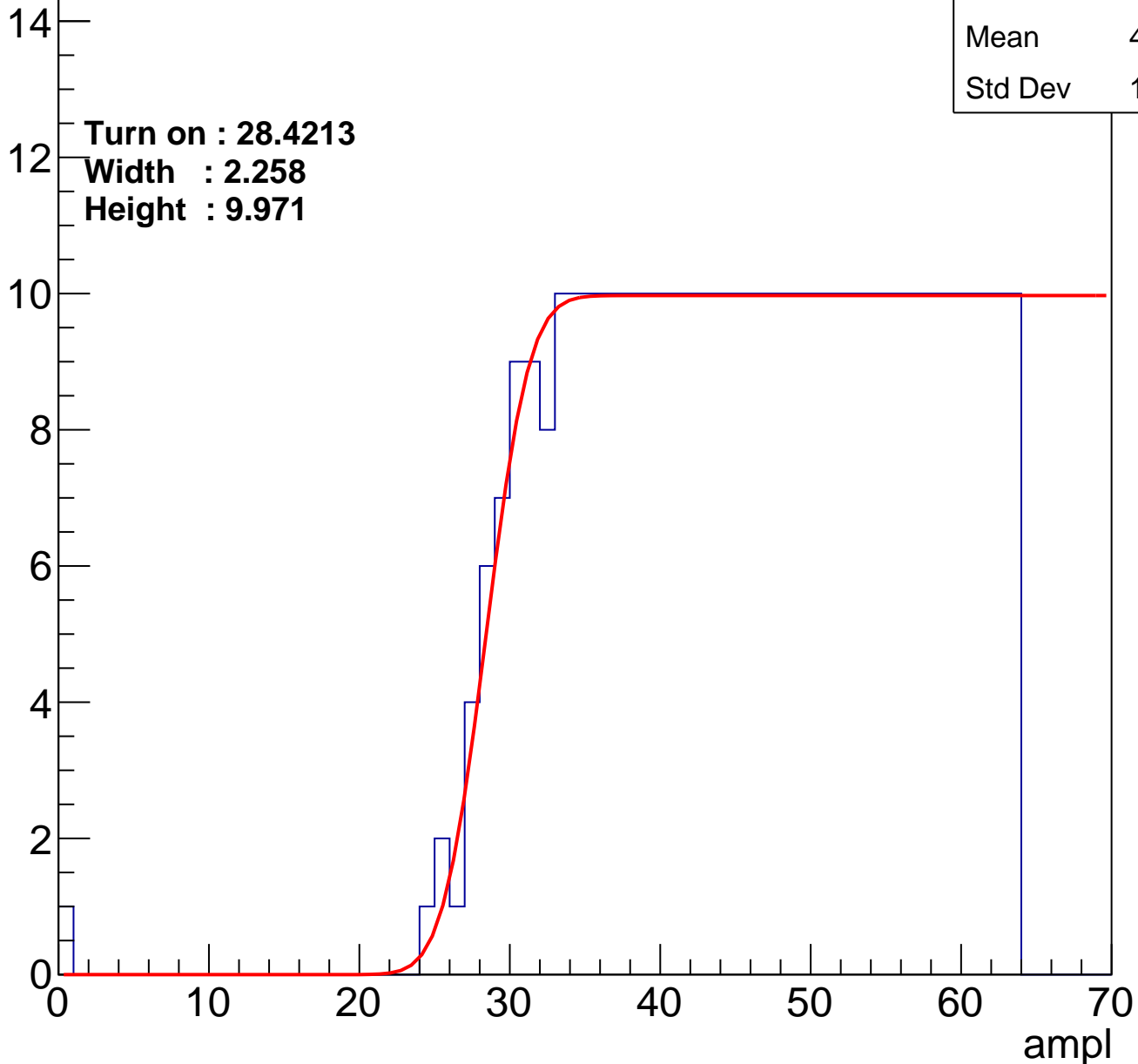
Entries	358
Mean	45.43
Std Dev	10.72

Turn on : 28.4213

Width : 2.258

Height : 9.971

Entry



B0L000S, U5-ch107

calib_packv5_042523_0143.root, FC#5, port B1

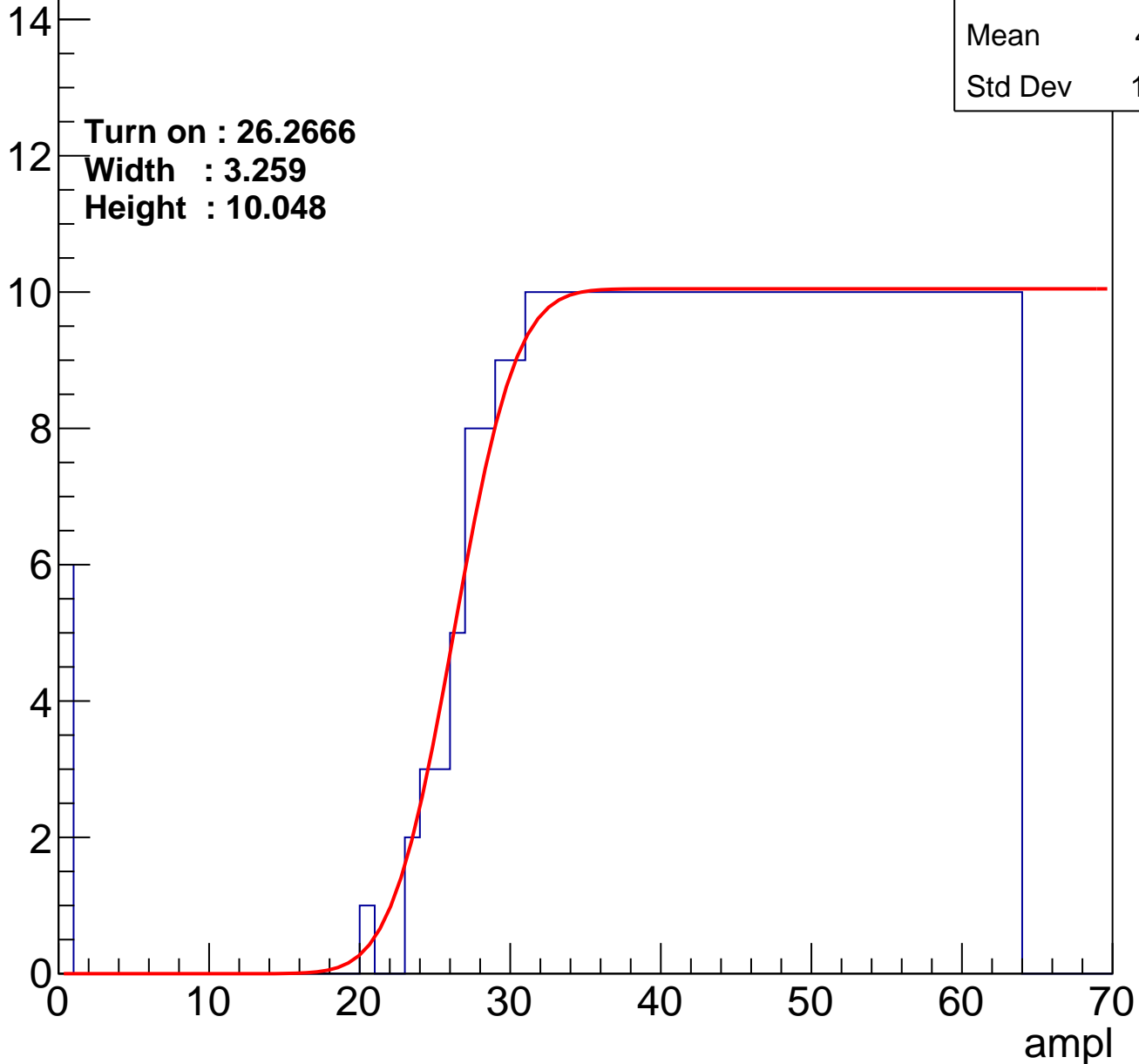
Entries	384
Mean	43.81
Std Dev	12.29

Turn on : 26.2666

Width : 3.259

Height : 10.048

Entry



B0L000S, U5-ch108

calib_packv5_042523_0143.root, FC#5, port B1

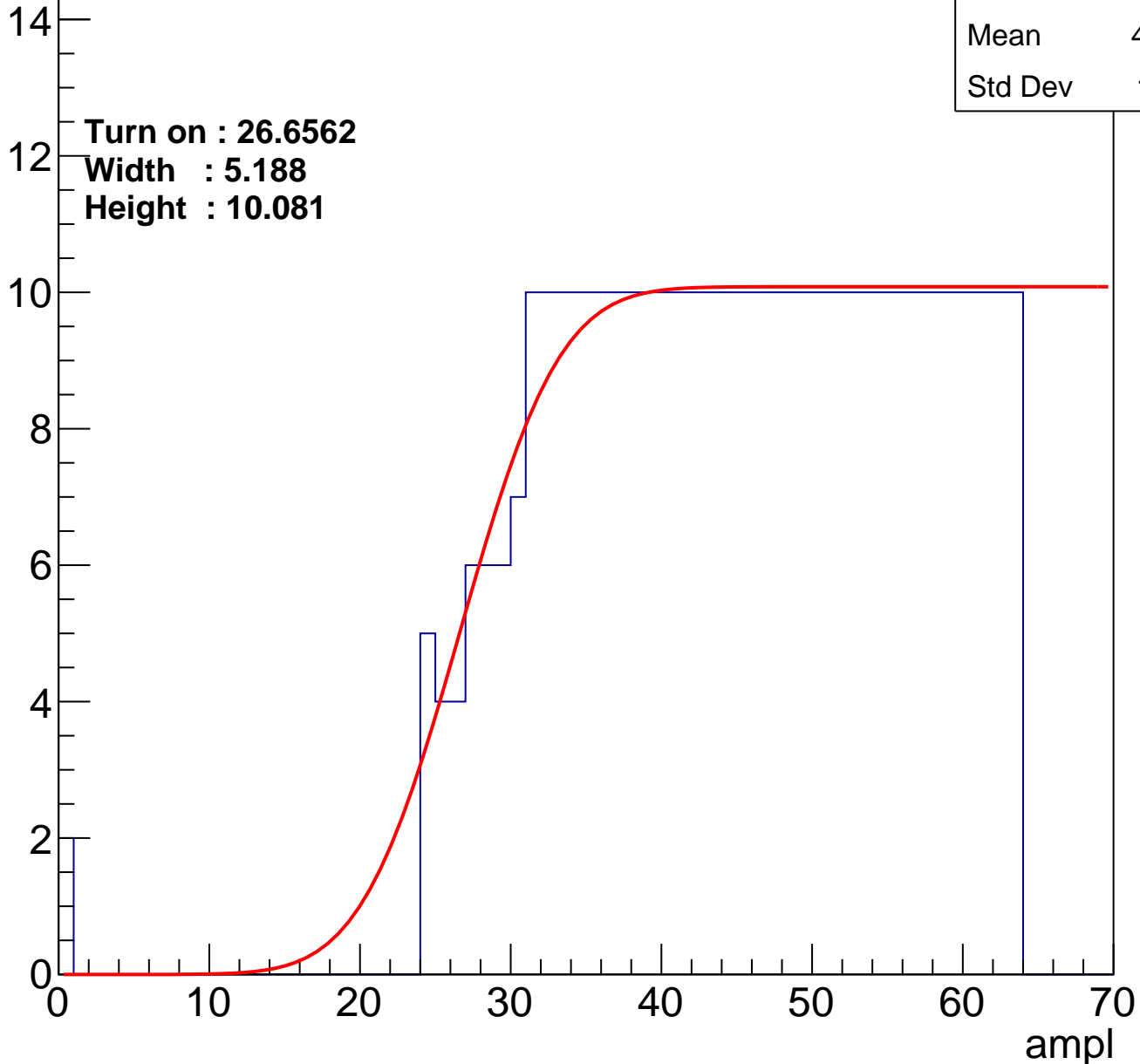
Entries	370
Mean	44.72
Std Dev	11.31

Turn on : 26.6562

Width : 5.188

Height : 10.081

Entry



B0L000S, U5-ch109

calib_packv5_042523_0143.root, FC#5, port B1

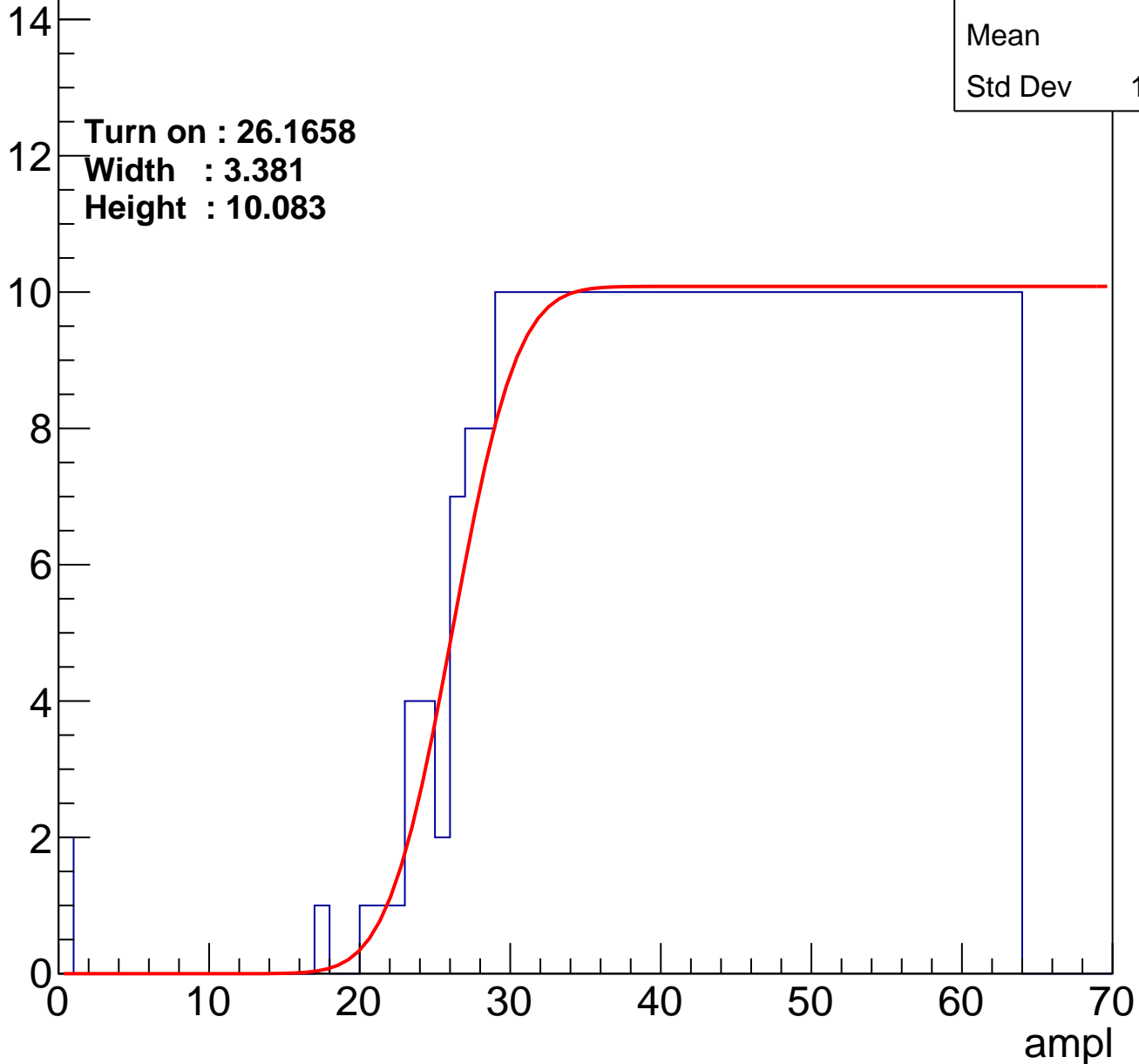
Entries	389
Mean	43.8
Std Dev	11.78

Turn on : 26.1658

Width : 3.381

Height : 10.083

Entry



B0L000S, U5-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	390
Mean	43.82
Std Dev	11.63

Turn on : 25.6195

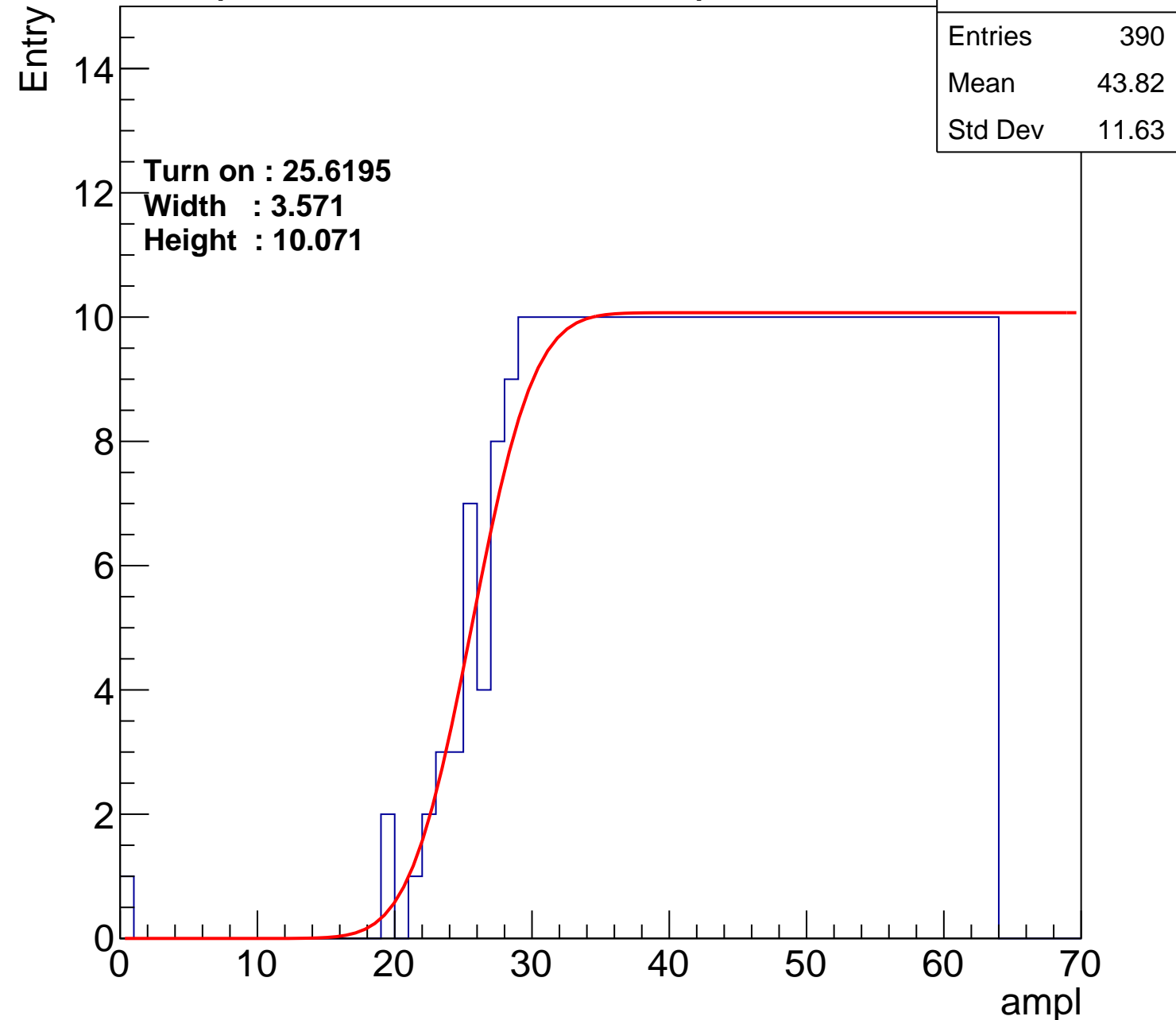
Width : 3.571

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.83
Std Dev	10.55

Turn on : 29.1184

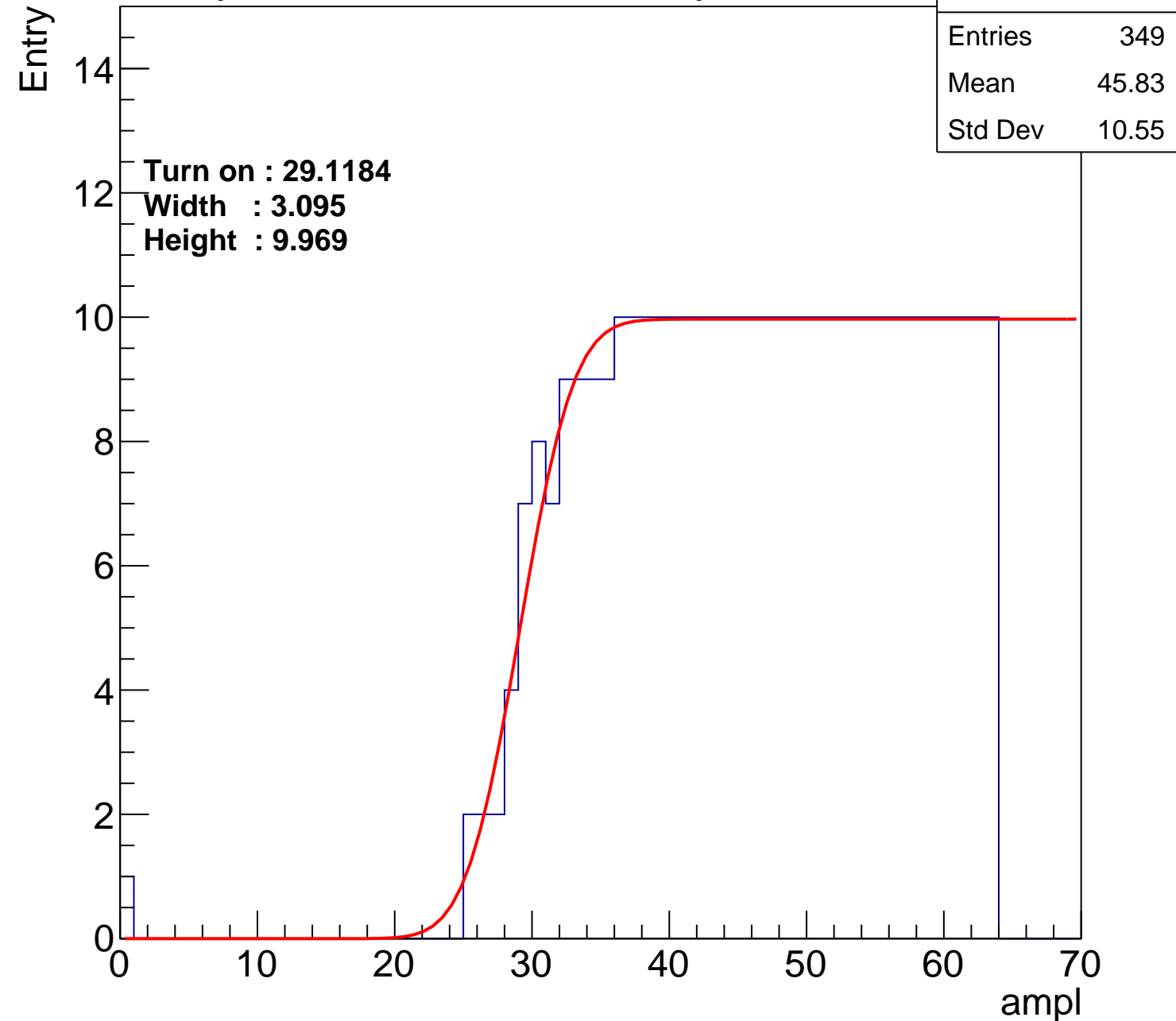
Width : 3.095

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch112

calib_packv5_042523_0143.root, FC#5, port B1

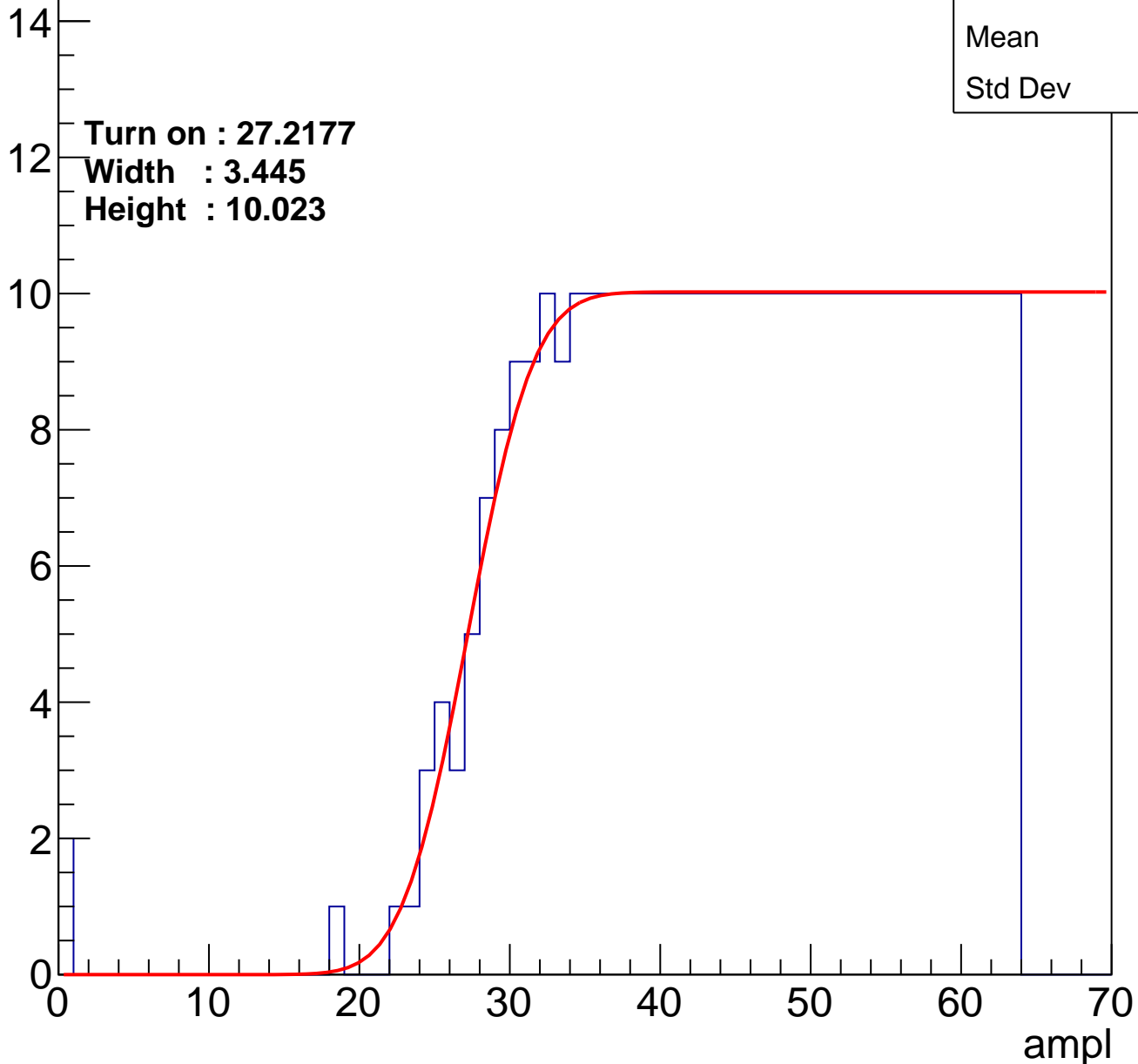
Entries	372
Mean	44.6
Std Dev	11.4

Turn on : 27.2177

Width : 3.445

Height : 10.023

Entry



B0L000S, U5-ch113

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.63
Std Dev	11.8

Turn on : 27.6037

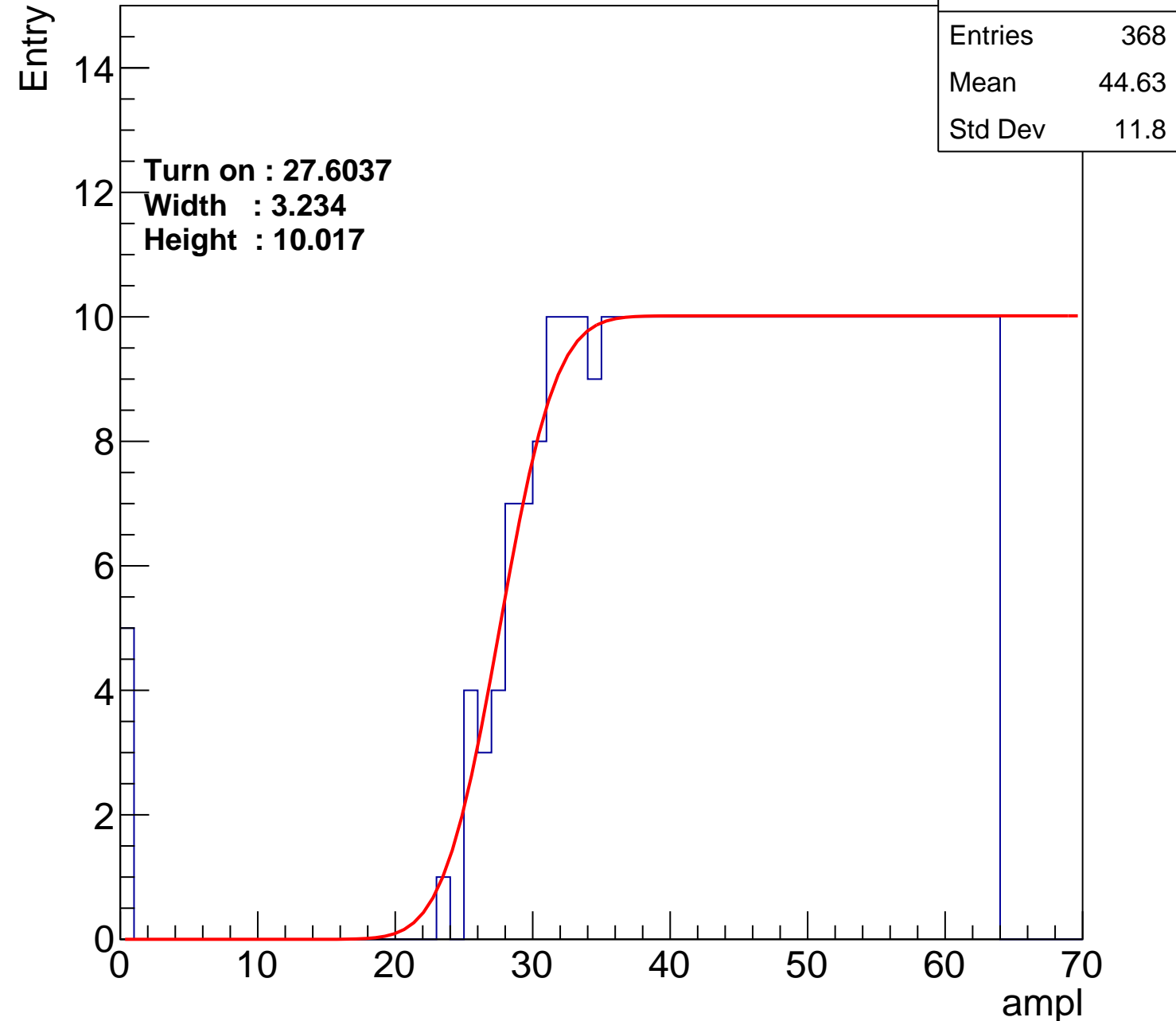
Width : 3.234

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch114

calib_packv5_042523_0143.root, FC#5, port B1

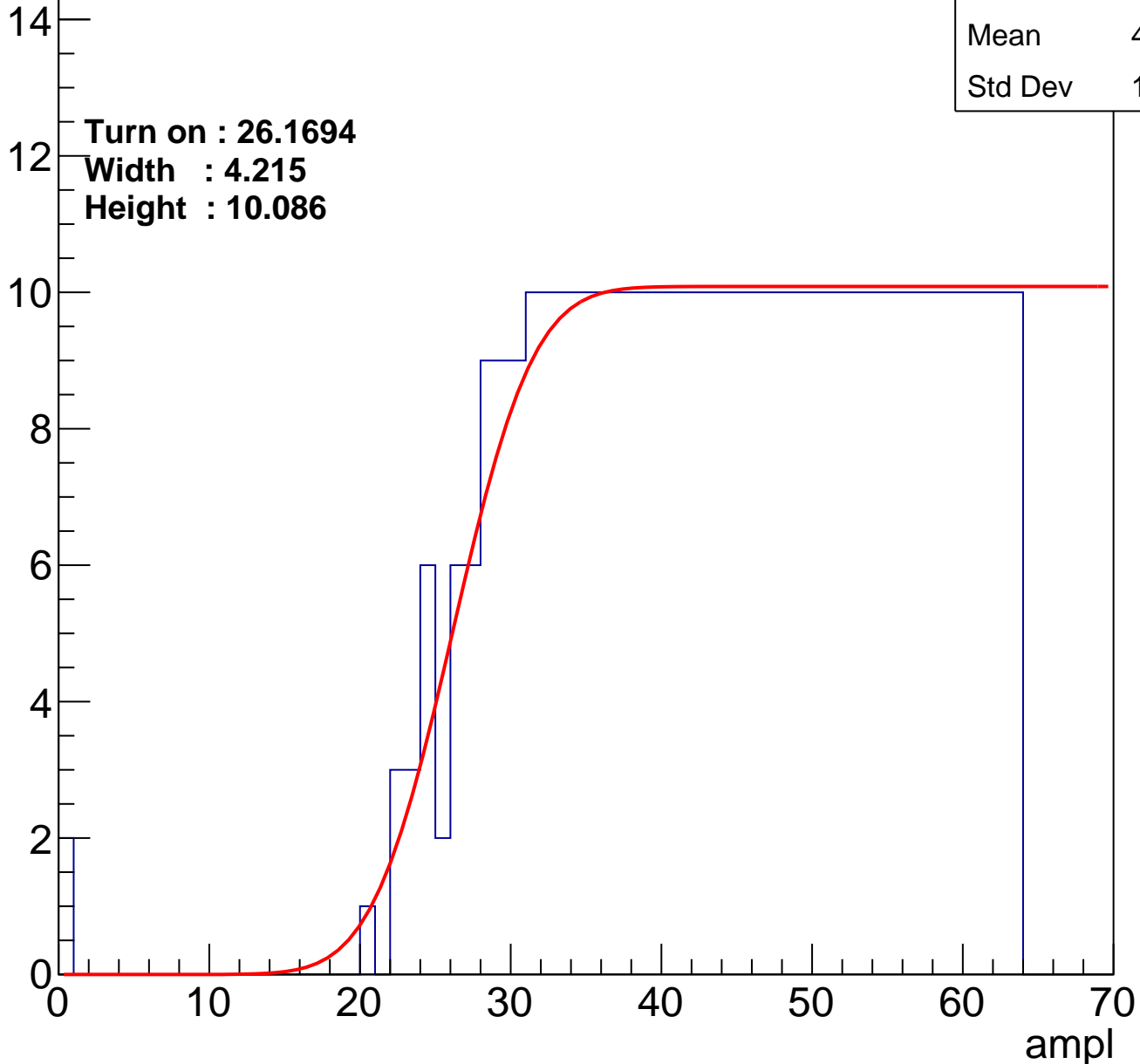
Entries	386
Mean	43.94
Std Dev	11.72

Turn on : 26.1694

Width : 4.215

Height : 10.086

Entry



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.5451
Width : 2.953
Height : 10.005



calib_packv5_042523_0143.root, FC#5, port B1

calib_packv5_042523_0143.root, FC#5, port B1

Turn on : 27.7894
Width : 2.933
Height : 10.034



B0L000S, U5-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.06
Std Dev	11.66

Turn on : 25.6912

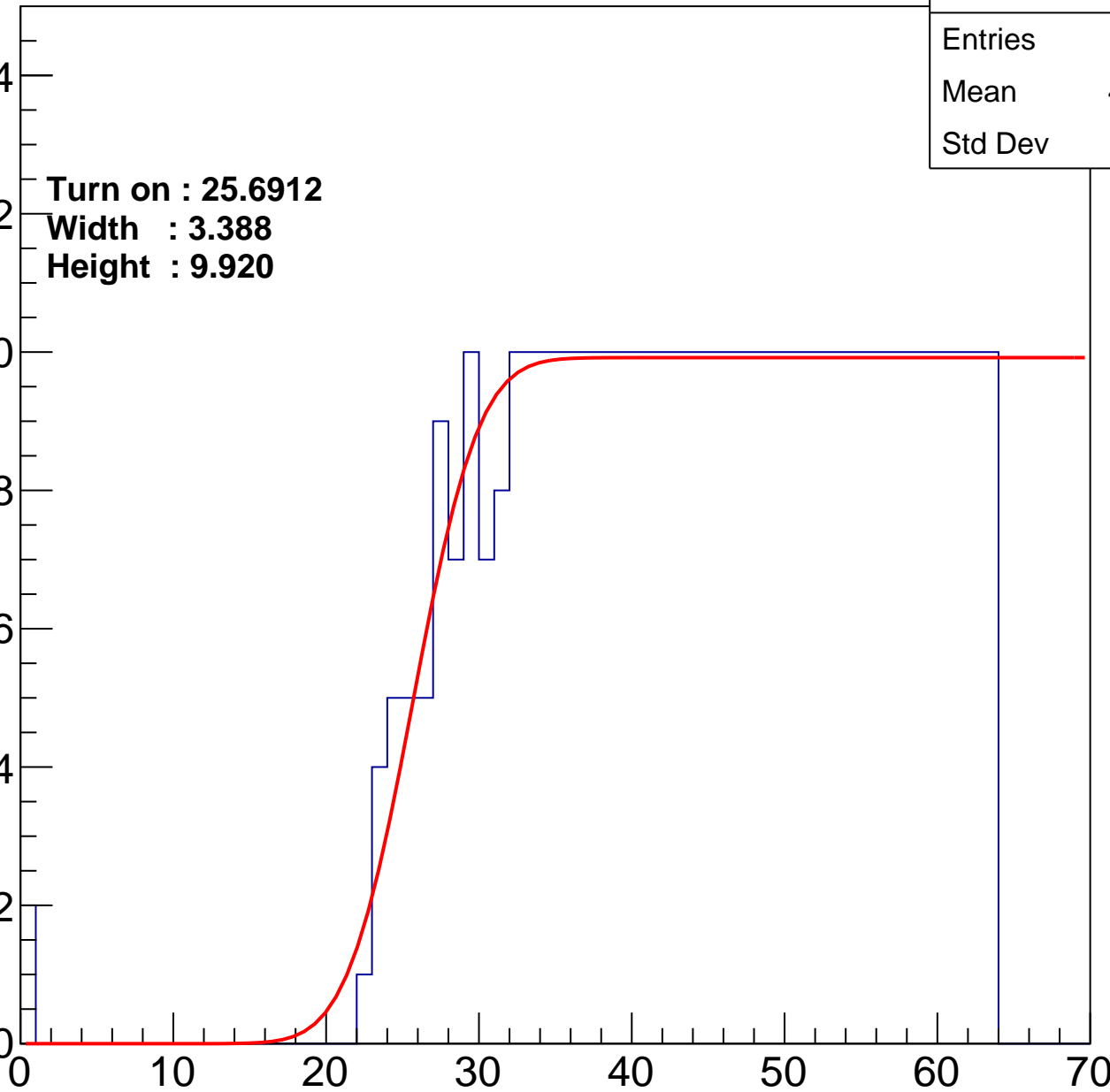
Width : 3.388

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.98
Std Dev	10.96

Turn on : 28.0071

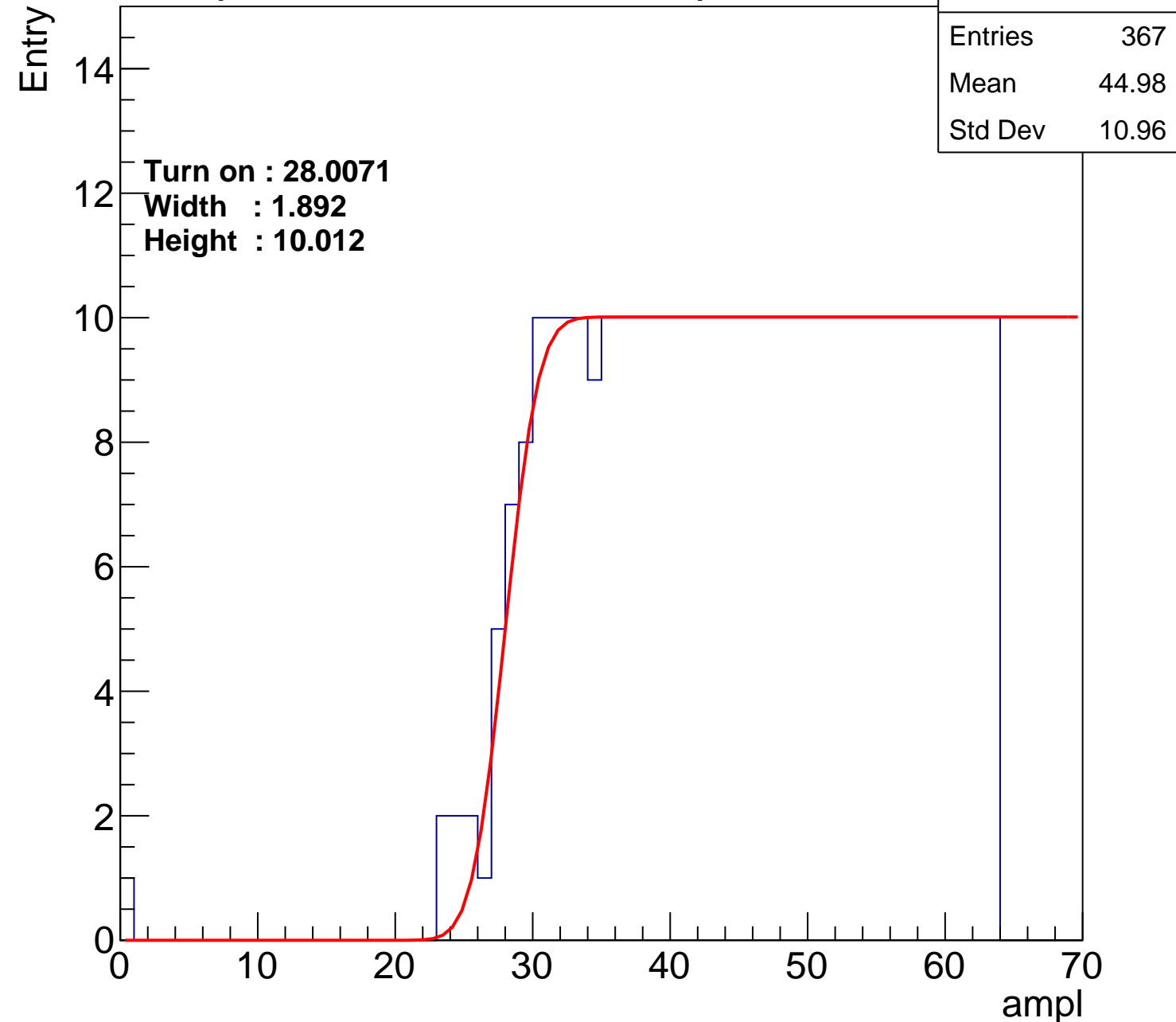
Width : 1.892

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.42
Std Dev	11.6

Turn on : 27.1381

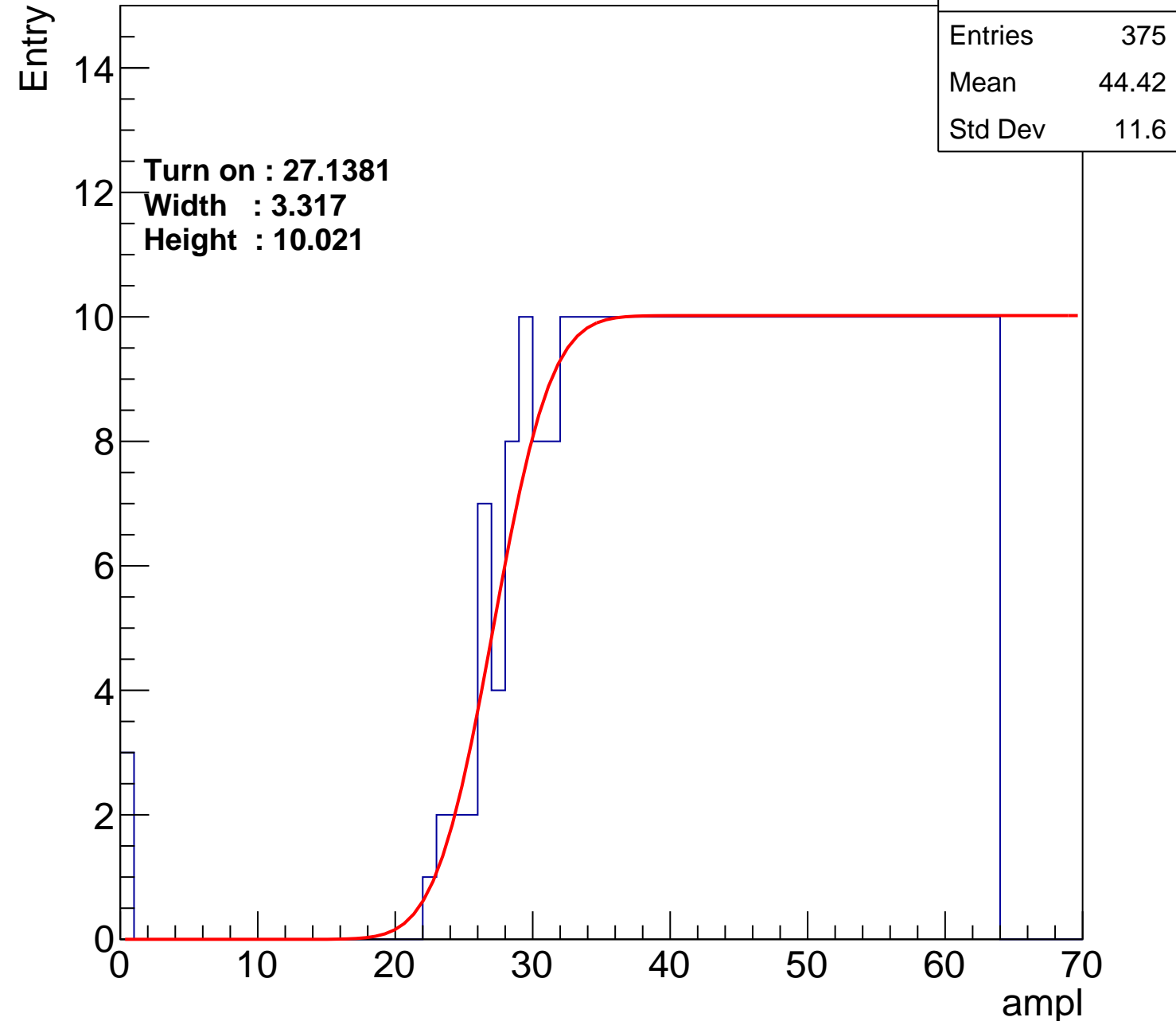
Width : 3.317

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	371
Mean	44.73
Std Dev	11.26

Turn on : 27.4553

Width : 2.883

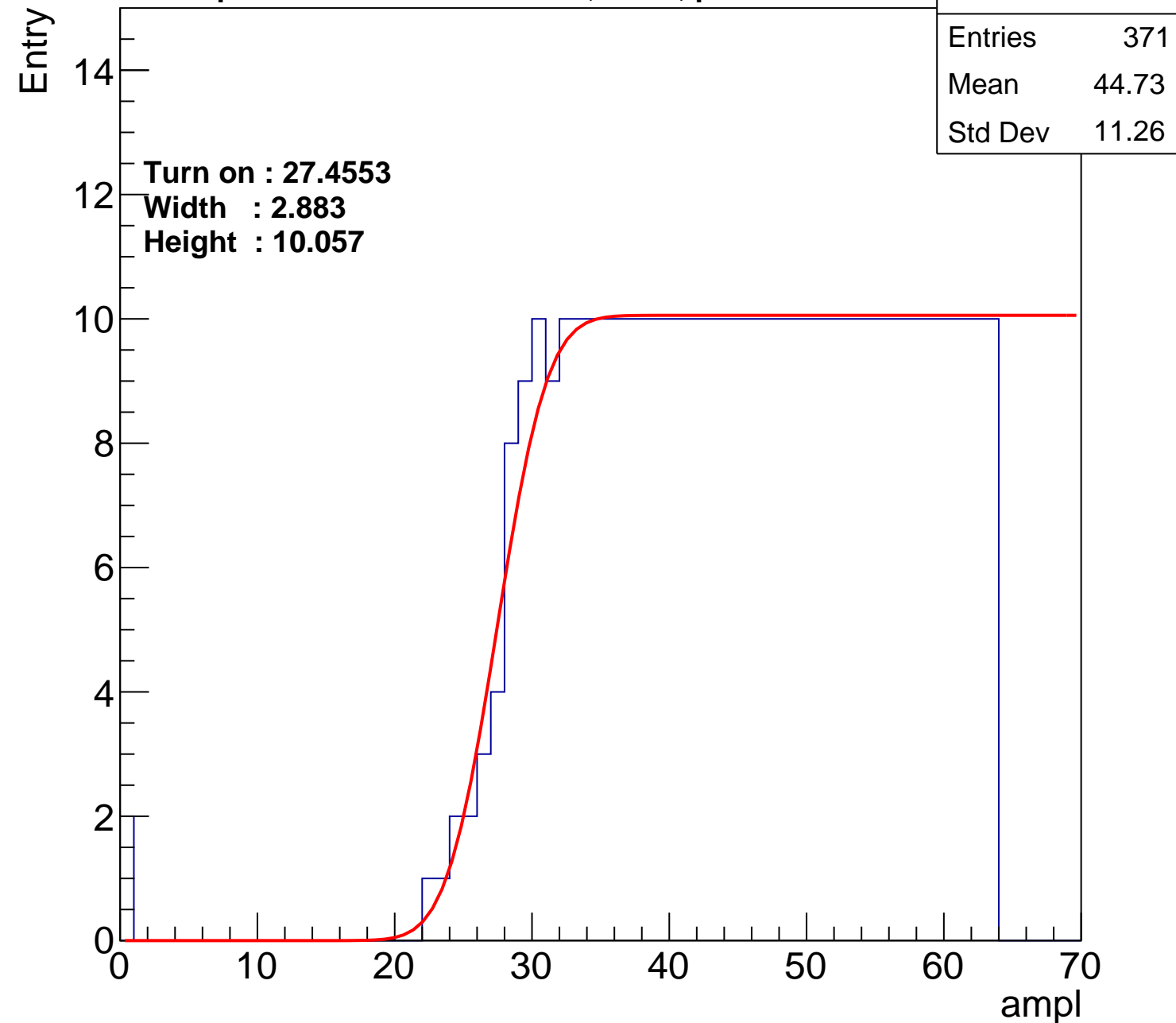
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L000S, U5-ch121

calib_packv5_042523_0143.root, FC#5, port B1

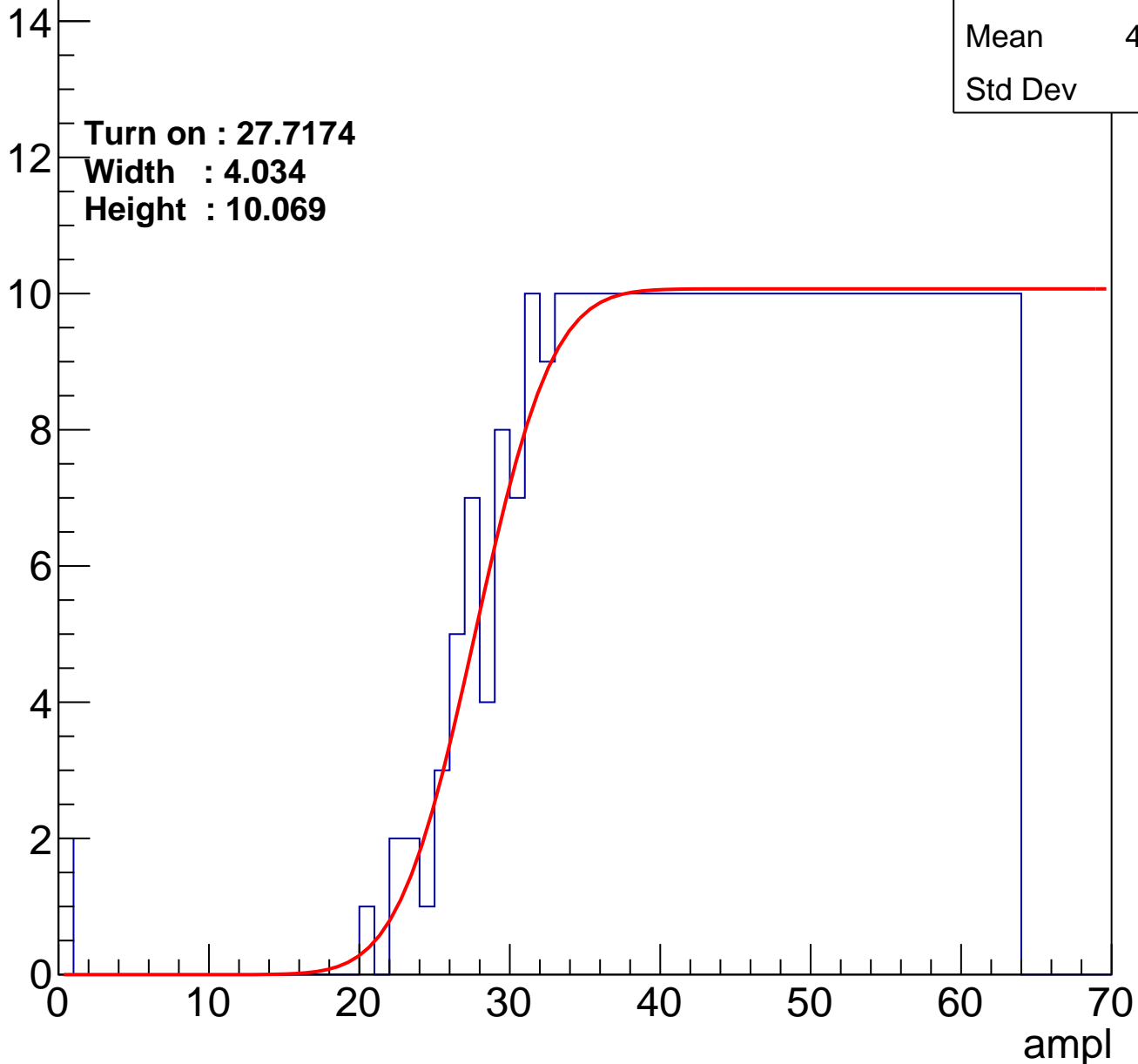
Entries	371
Mean	44.64
Std Dev	11.4

Turn on : 27.7174

Width : 4.034

Height : 10.069

Entry



B0L000S, U5-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.42
Std Dev	11.61

Turn on : 26.9709

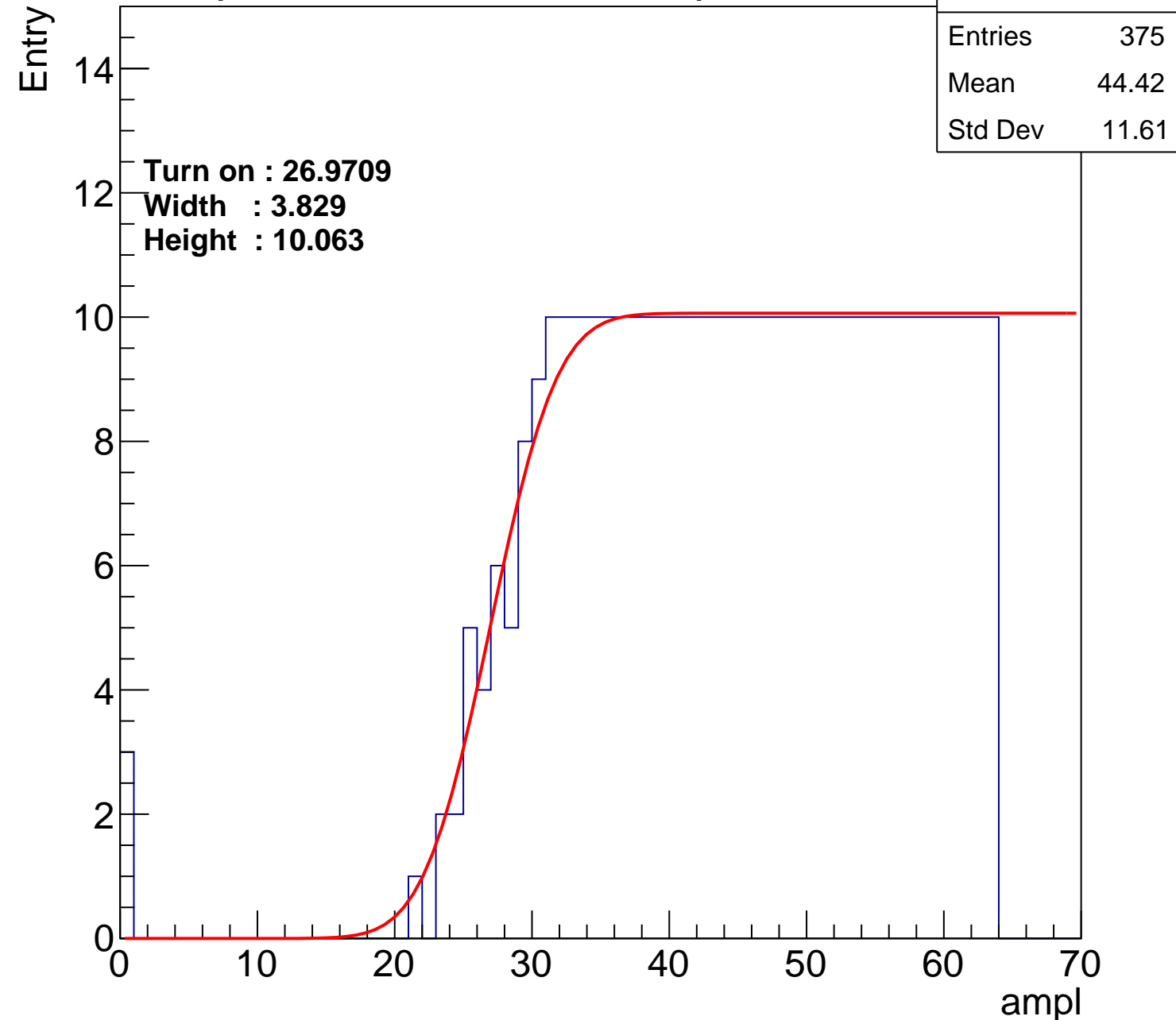
Width : 3.829

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch123

calib_packv5_042523_0143.root, FC#5, port B1

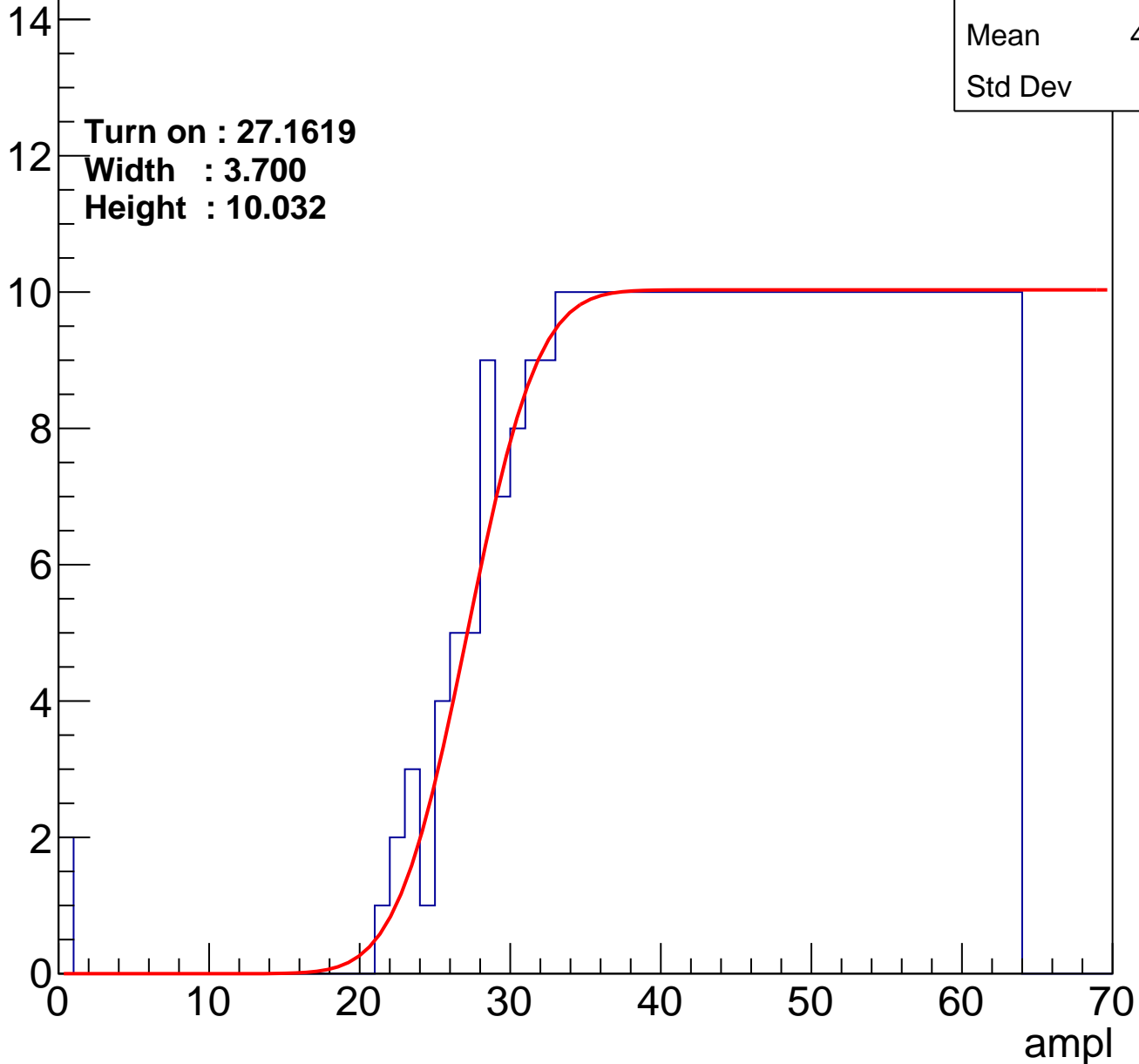
Entries	375
Mean	44.44
Std Dev	11.5

Turn on : 27.1619

Width : 3.700

Height : 10.032

Entry



B0L000S, U5-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.56
Std Dev	11.42

Turn on : 27.5924

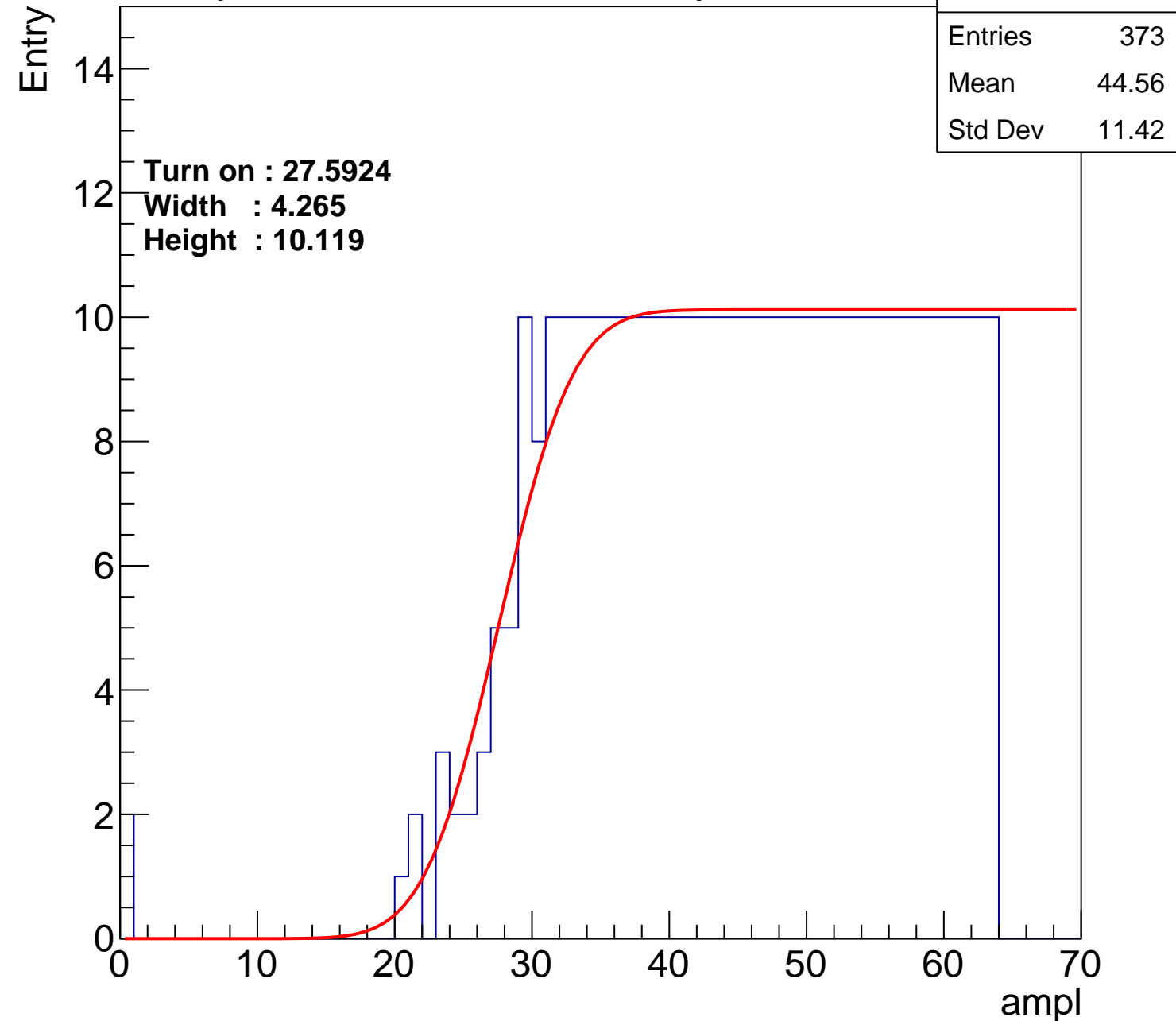
Width : 4.265

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U5-ch125

calib_packv5_042523_0143.root, FC#5, port B1

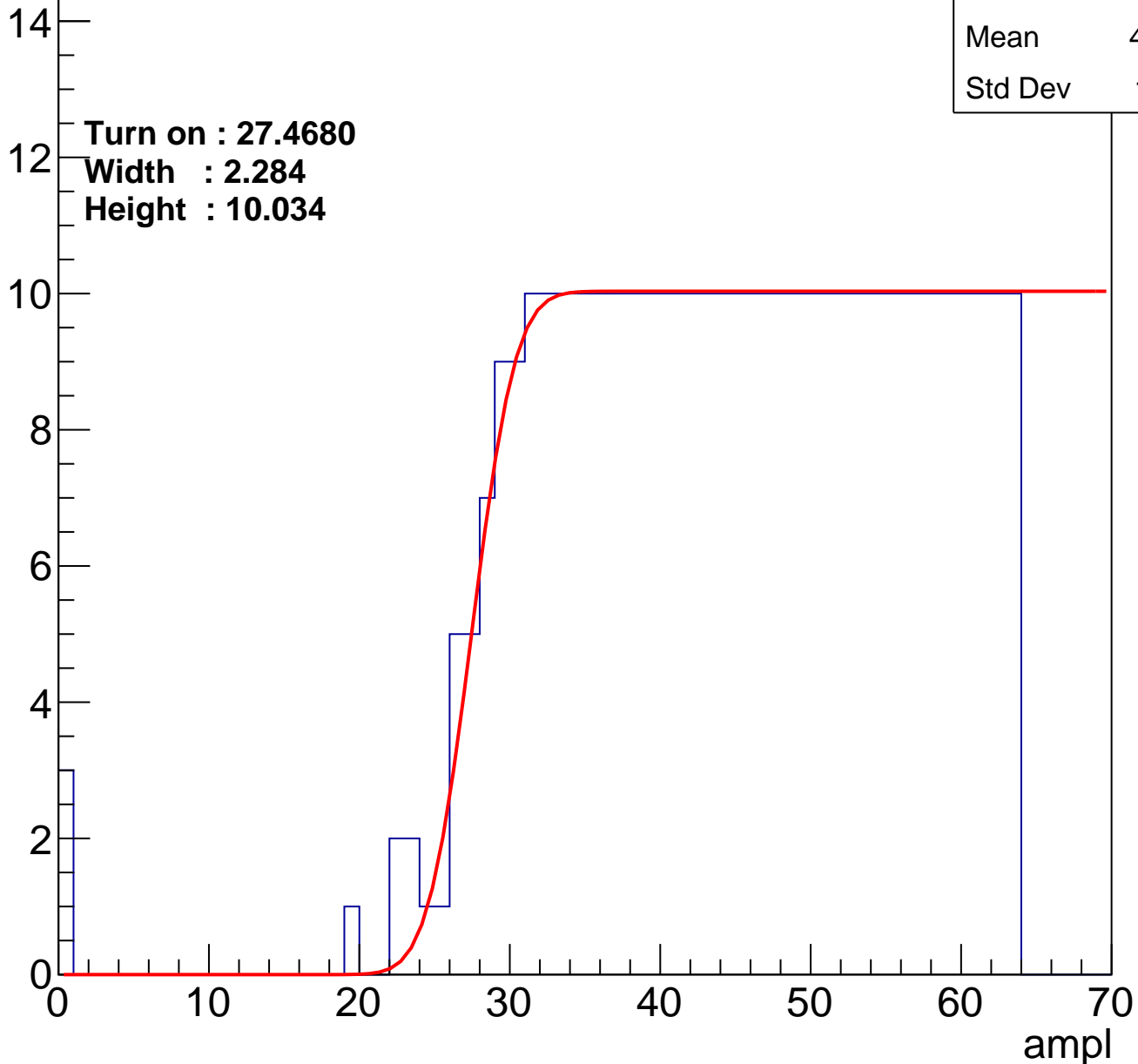
Entries	375
Mean	44.43
Std Dev	11.61

Turn on : 27.4680

Width : 2.284

Height : 10.034

Entry



B0L000S, U5-ch126

calib_packv5_042523_0143.root, FC#5, port B1

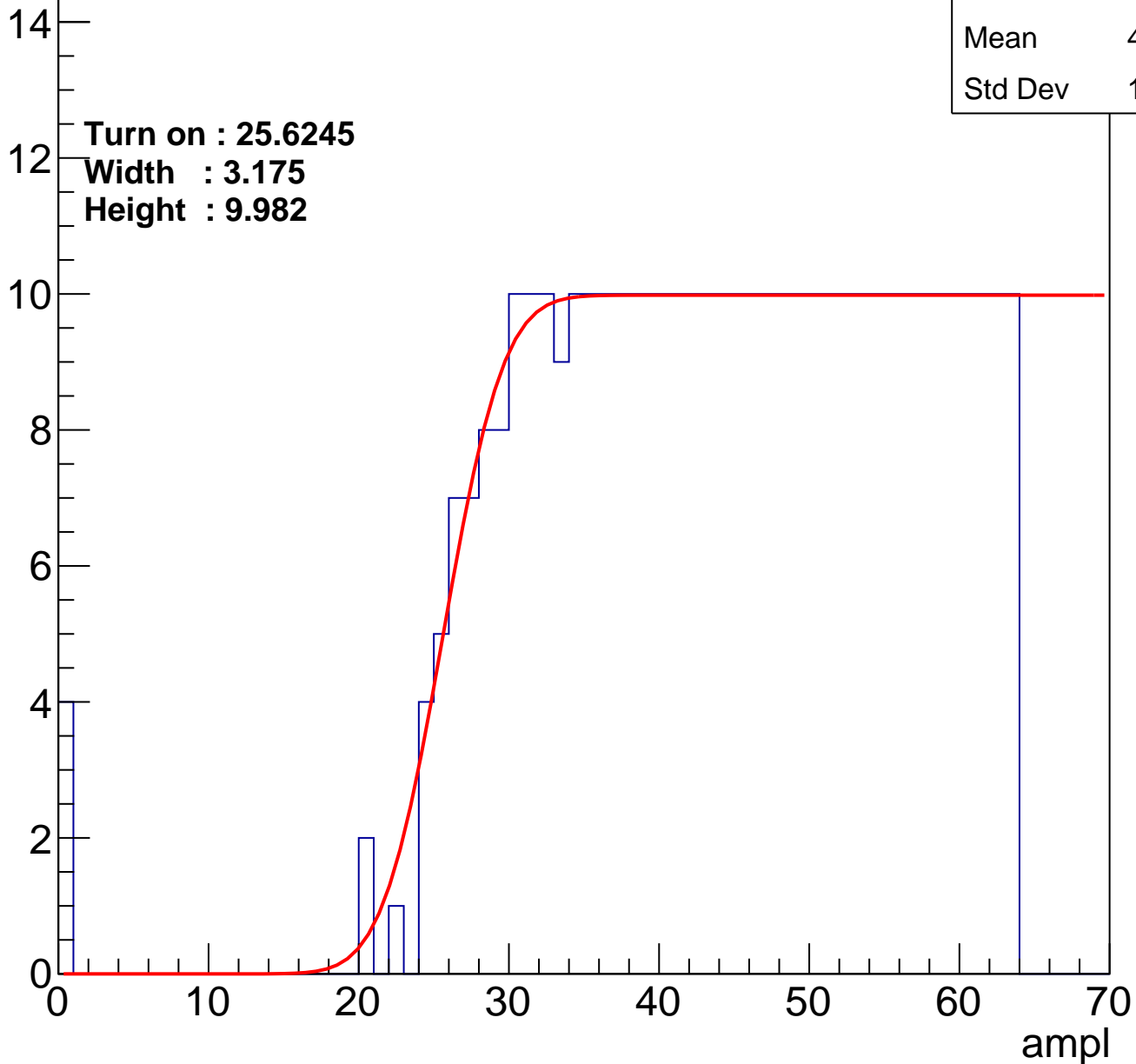
Entries	385
Mean	43.86
Std Dev	12.02

Turn on : 25.6245

Width : 3.175

Height : 9.982

Entry



B0L000S, U5-ch127

calib_packv5_042523_0143.root, FC#5, port B1

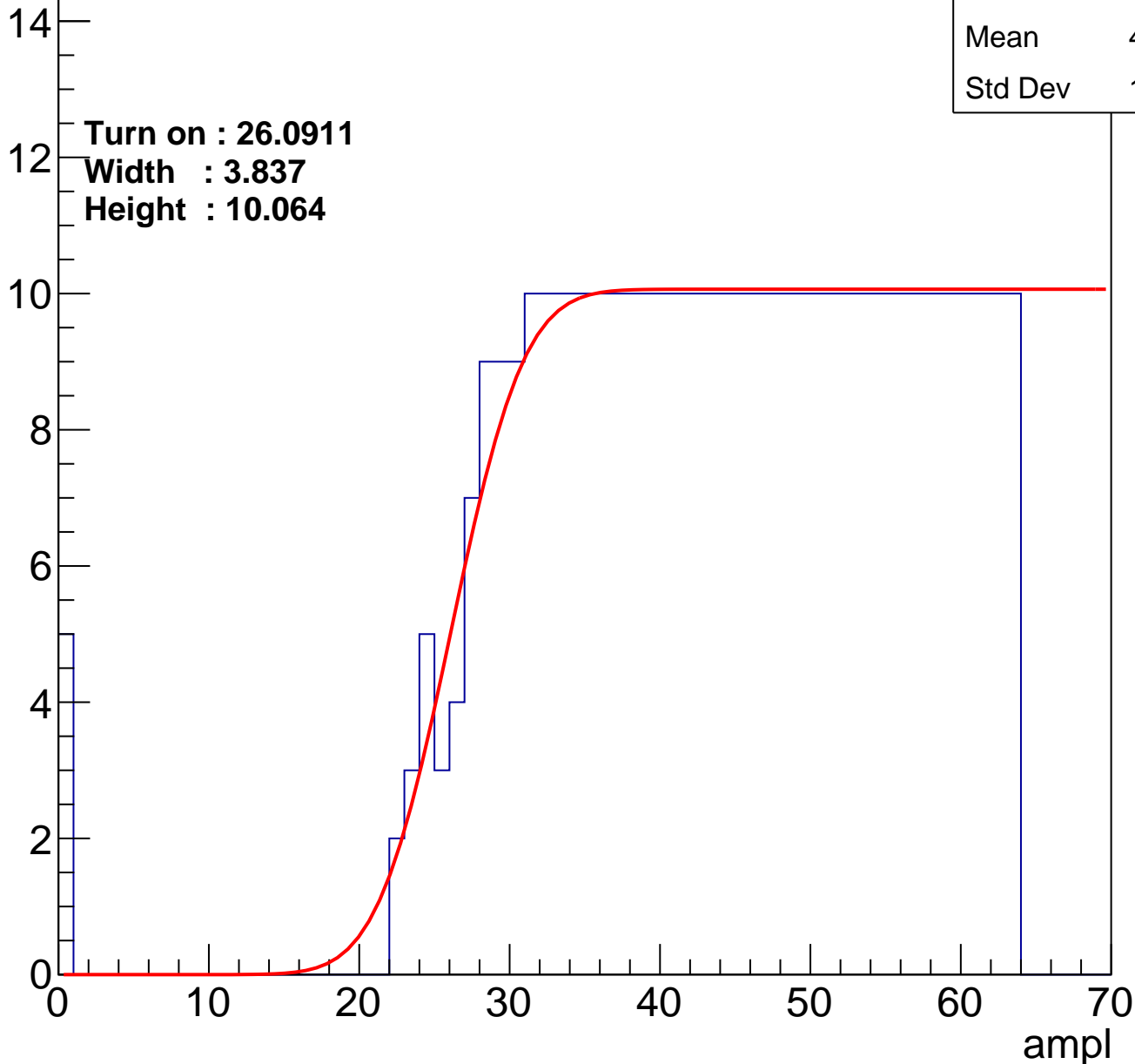
Entries	386
Mean	43.77
Std Dev	12.19

Turn on : 26.0911

Width : 3.837

Height : 10.064

Entry



B0L000S, U5-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	386
Mean	43.77
Std Dev	12.19

Turn on : 26.0911

Width : 3.837

Height : 10.064

Entry

