

B1L003S, U16-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.27
Std Dev	11.67

Turn on : 26.4164

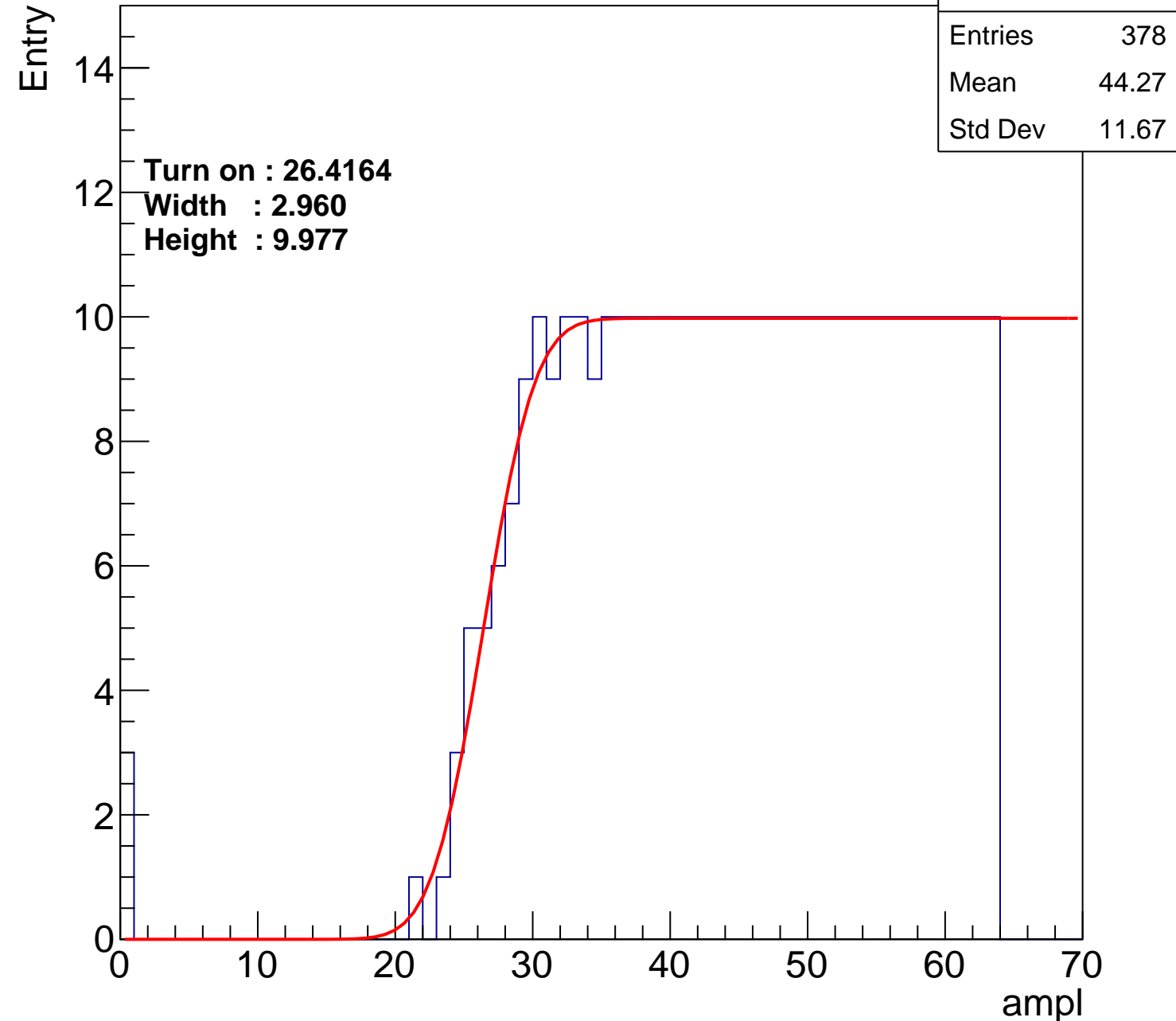
Width : 2.960

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.44
Std Dev	11.58

Turn on : 27.0140

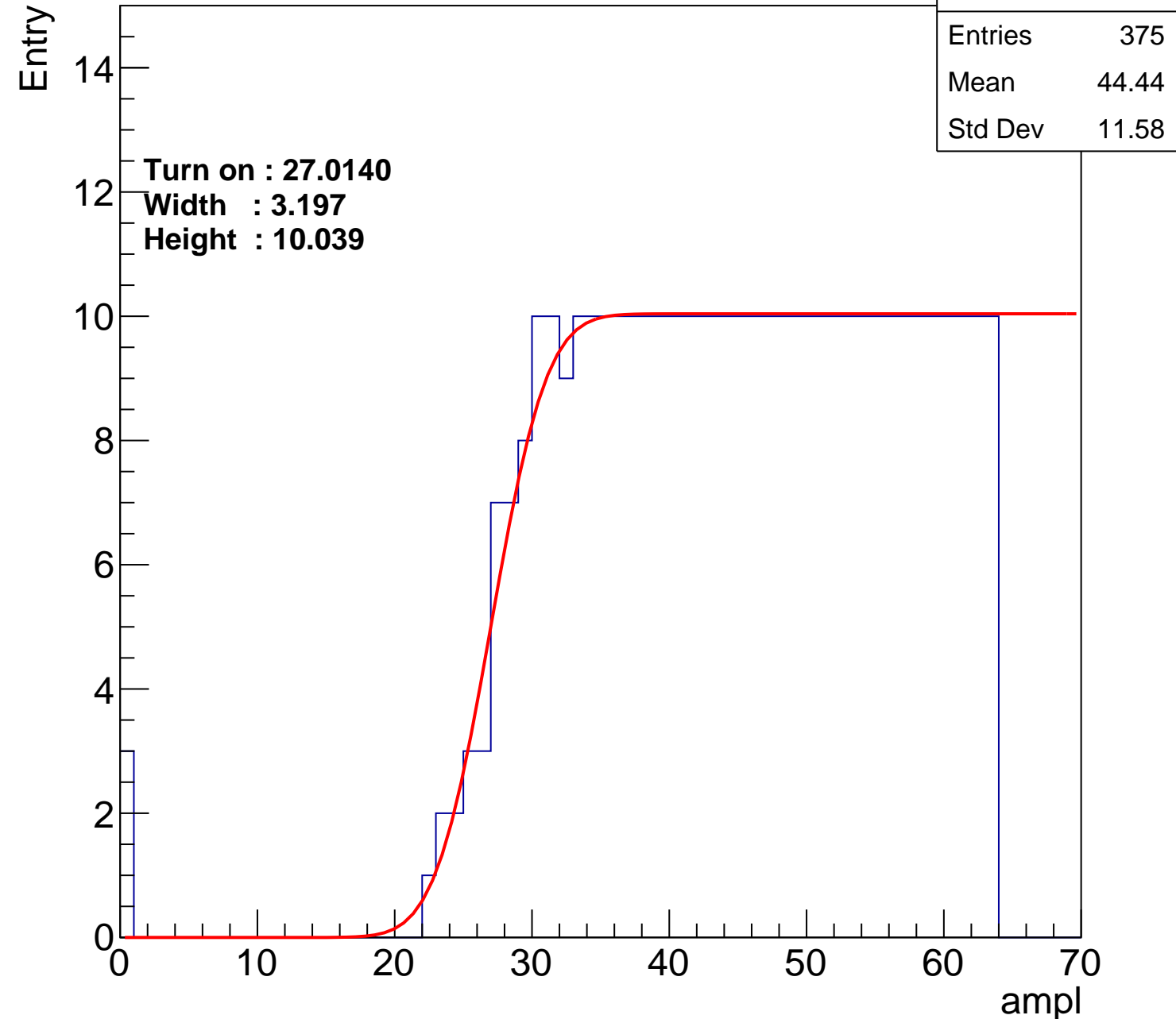
Width : 3.197

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 27.0479

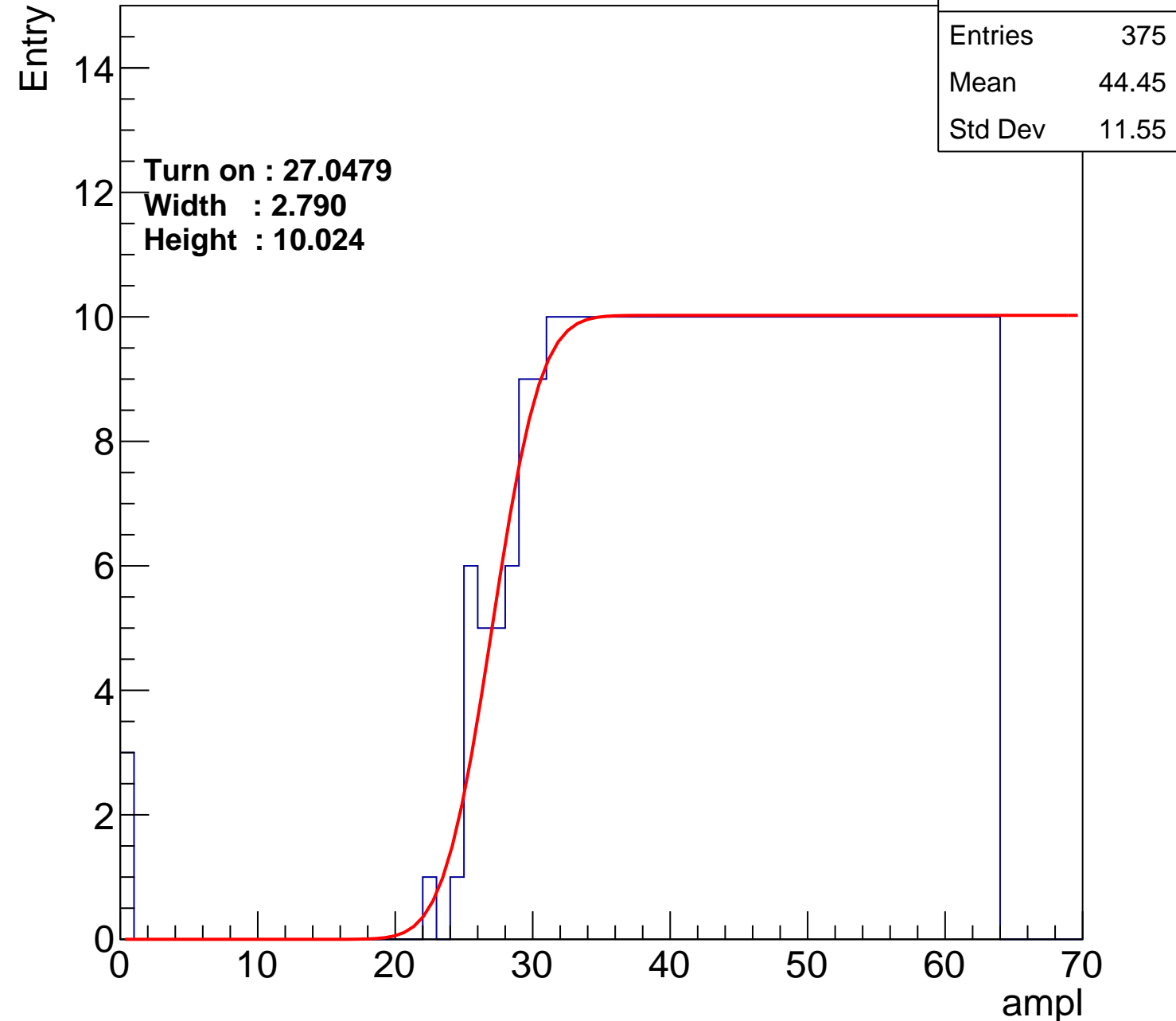
Width : 2.790

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.07
Std Dev	11.91

Turn on : 26.4877

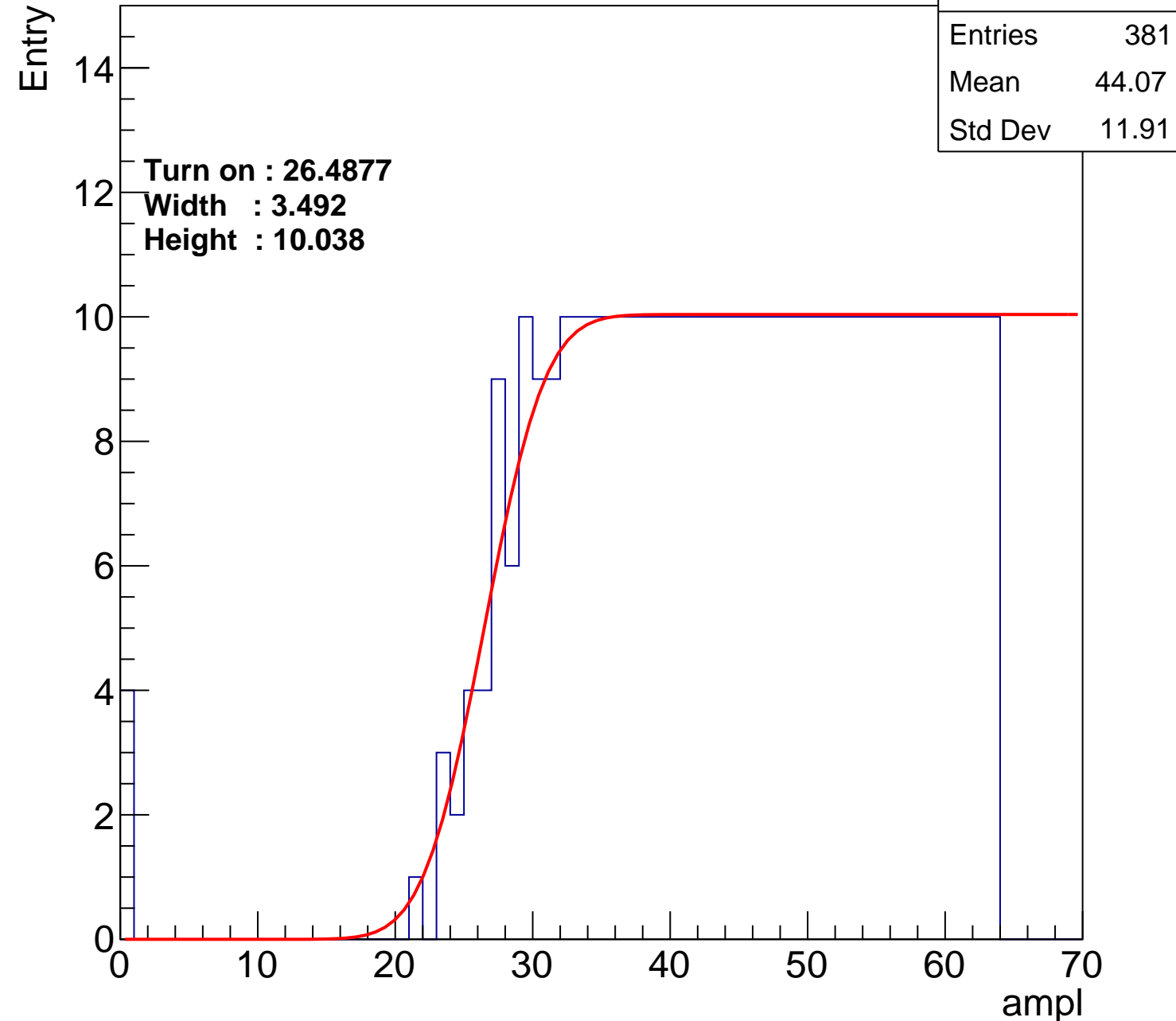
Width : 3.492

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	350
Mean	45.81
Std Dev	10.53

Turn on : 29.5956

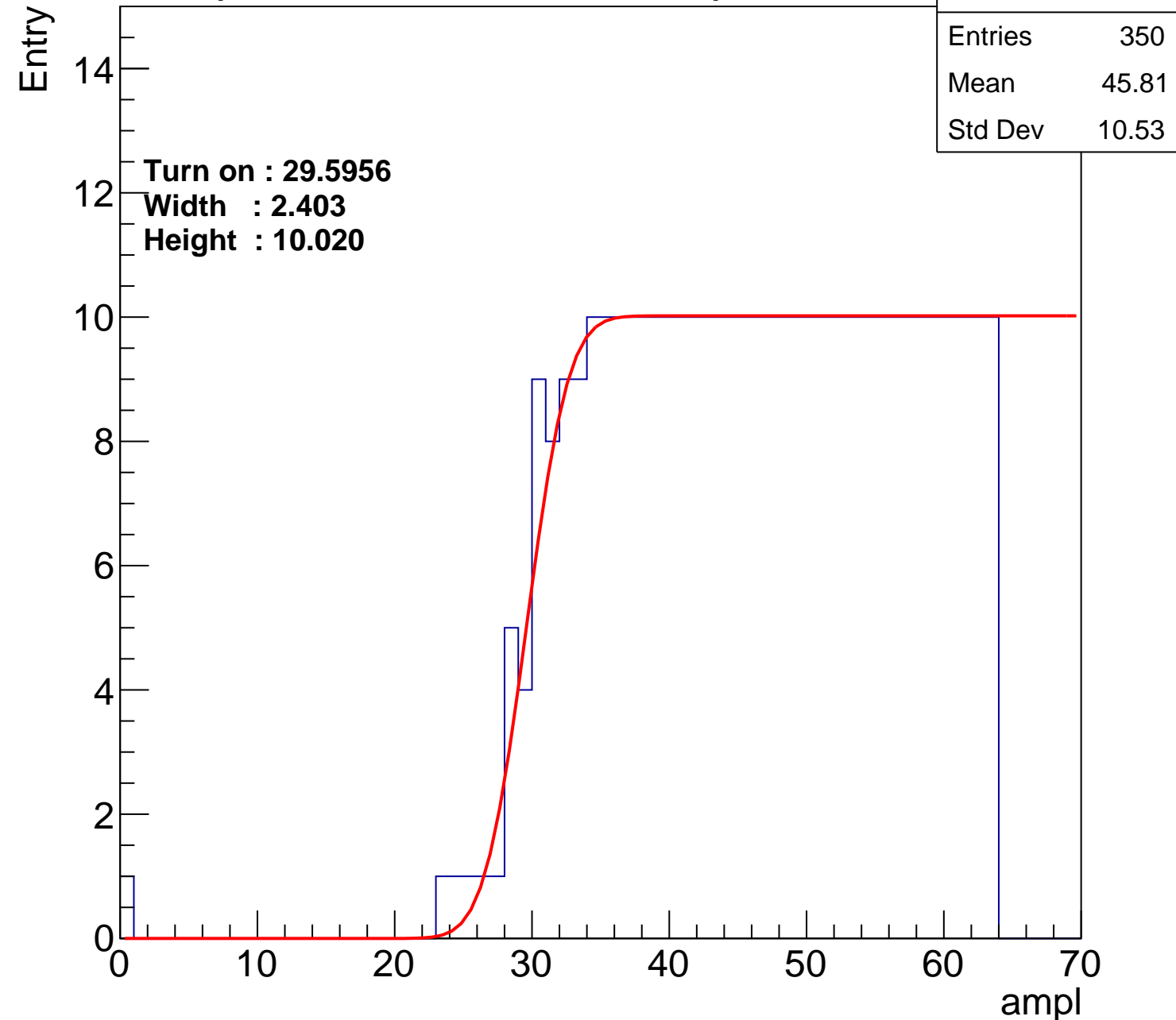
Width : 2.403

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.04
Std Dev	10.99

Turn on : 28.0983

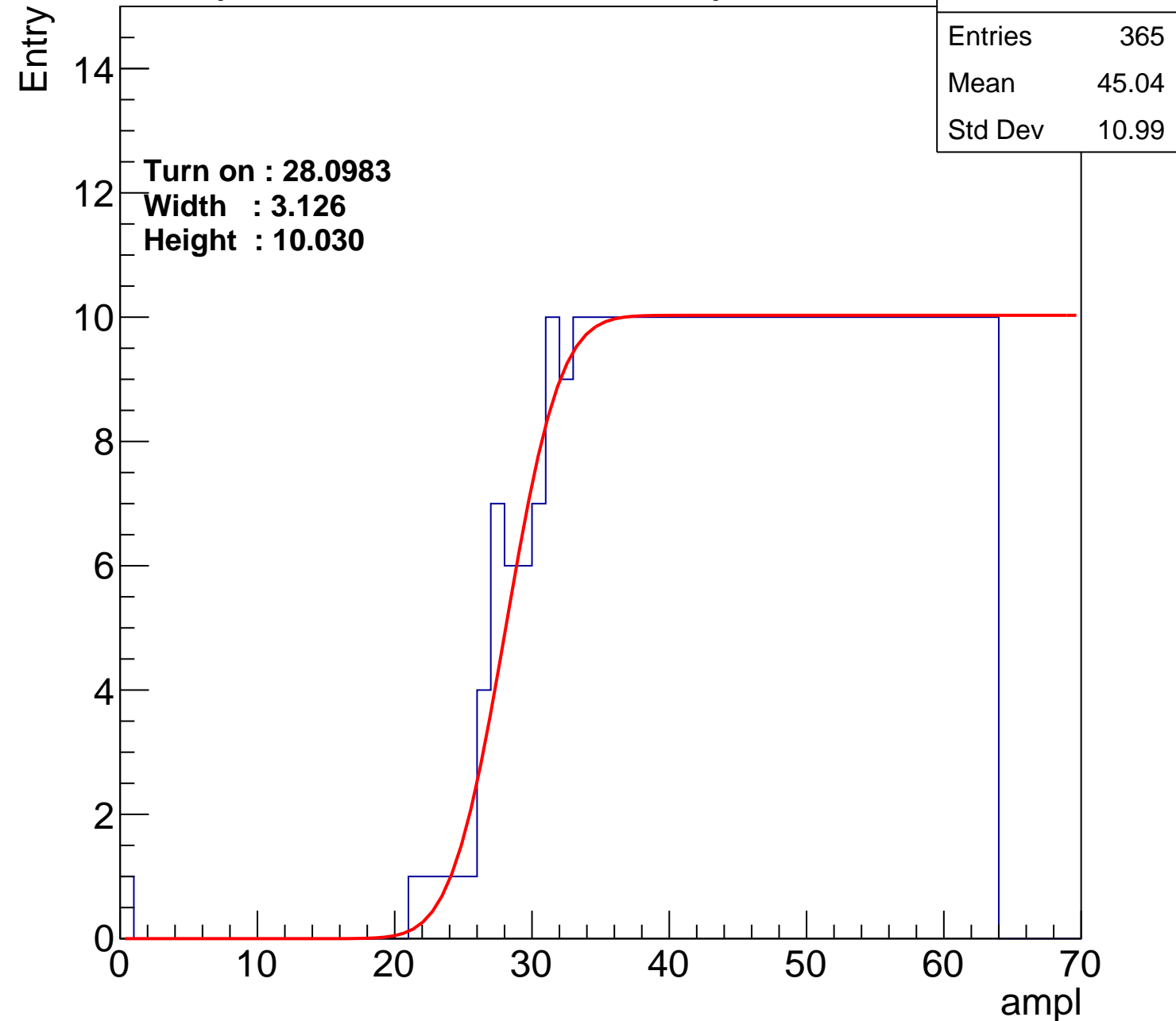
Width : 3.126

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.63
Std Dev	12.53

Turn on : 26.3531

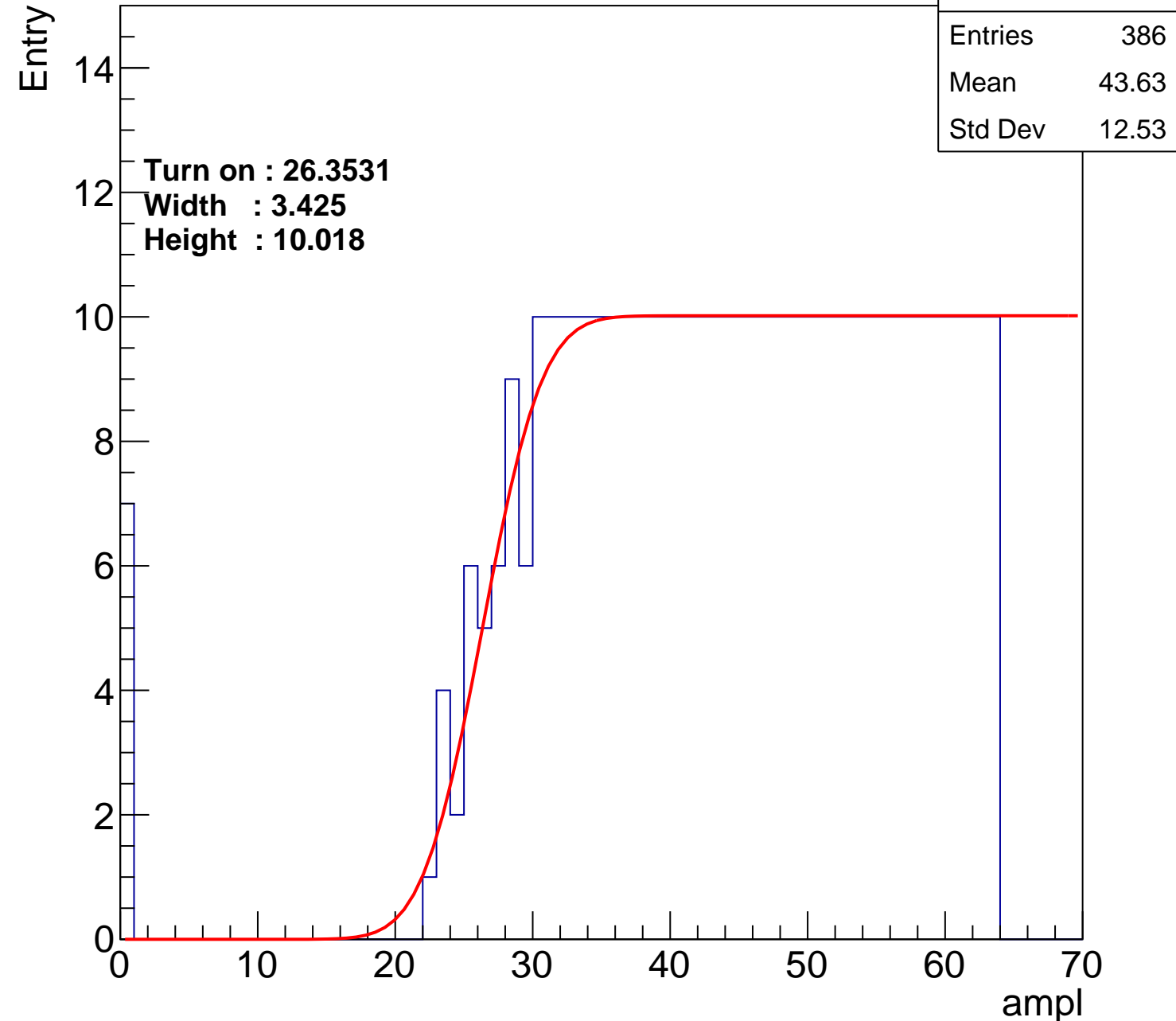
Width : 3.425

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.82
Std Dev	11.4

Turn on : 27.6561

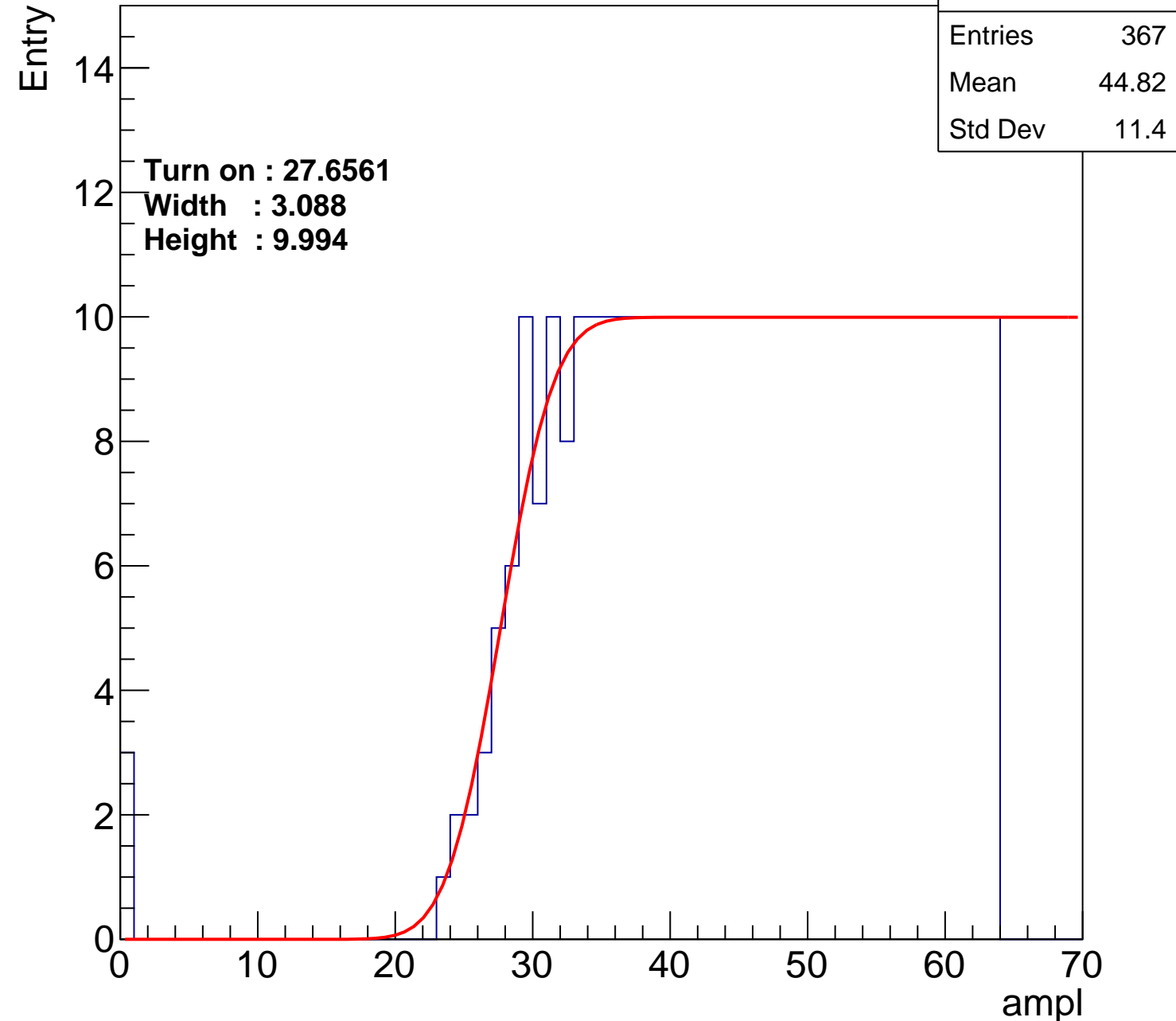
Width : 3.088

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch8

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.16
Std Dev	11.77

Turn on : 29.0454

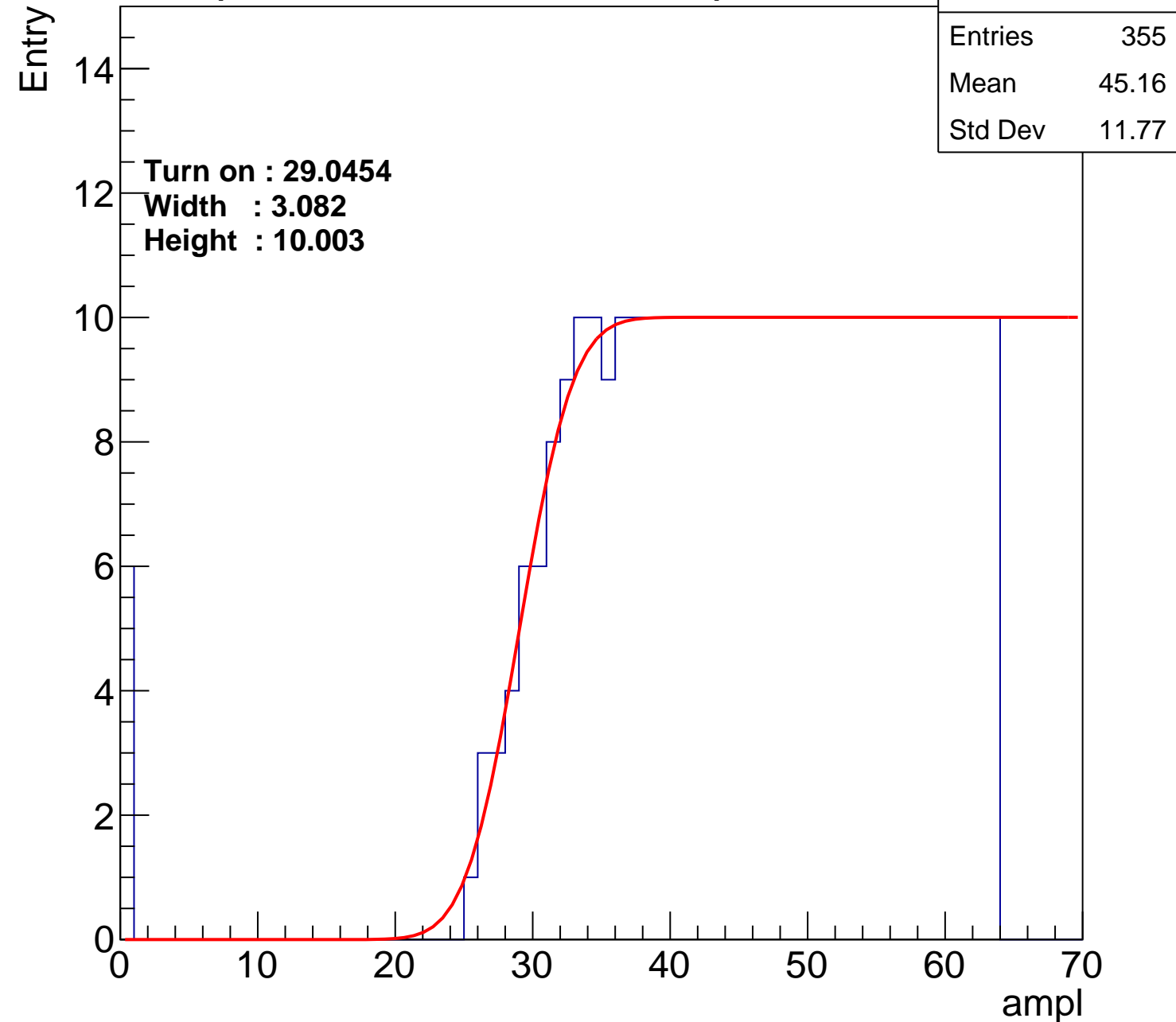
Width : 3.082

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.85
Std Dev	11.08

Turn on : 27.8748

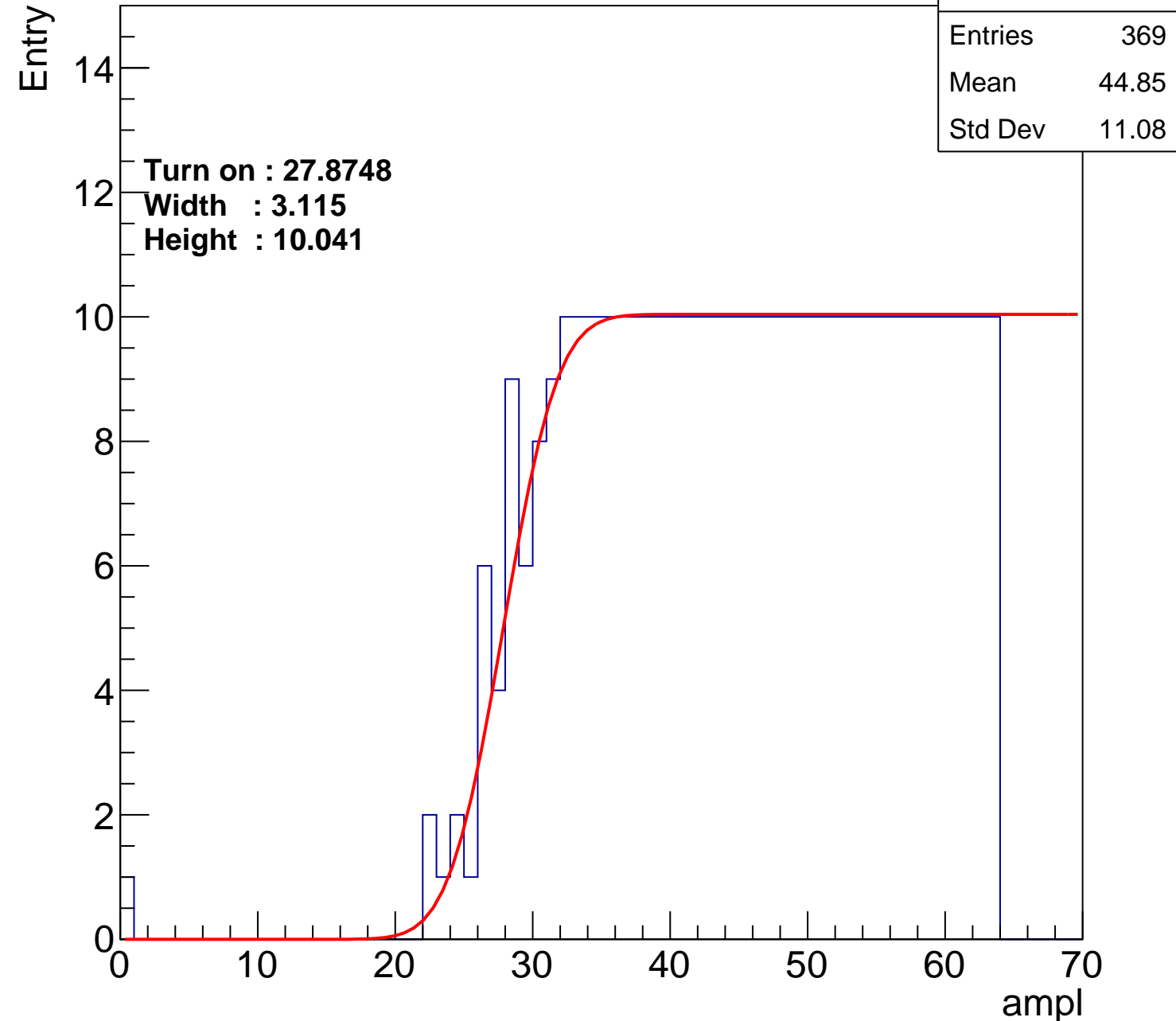
Width : 3.115

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	352
Mean	45.45
Std Dev	11.3

Turn on : 29.3954

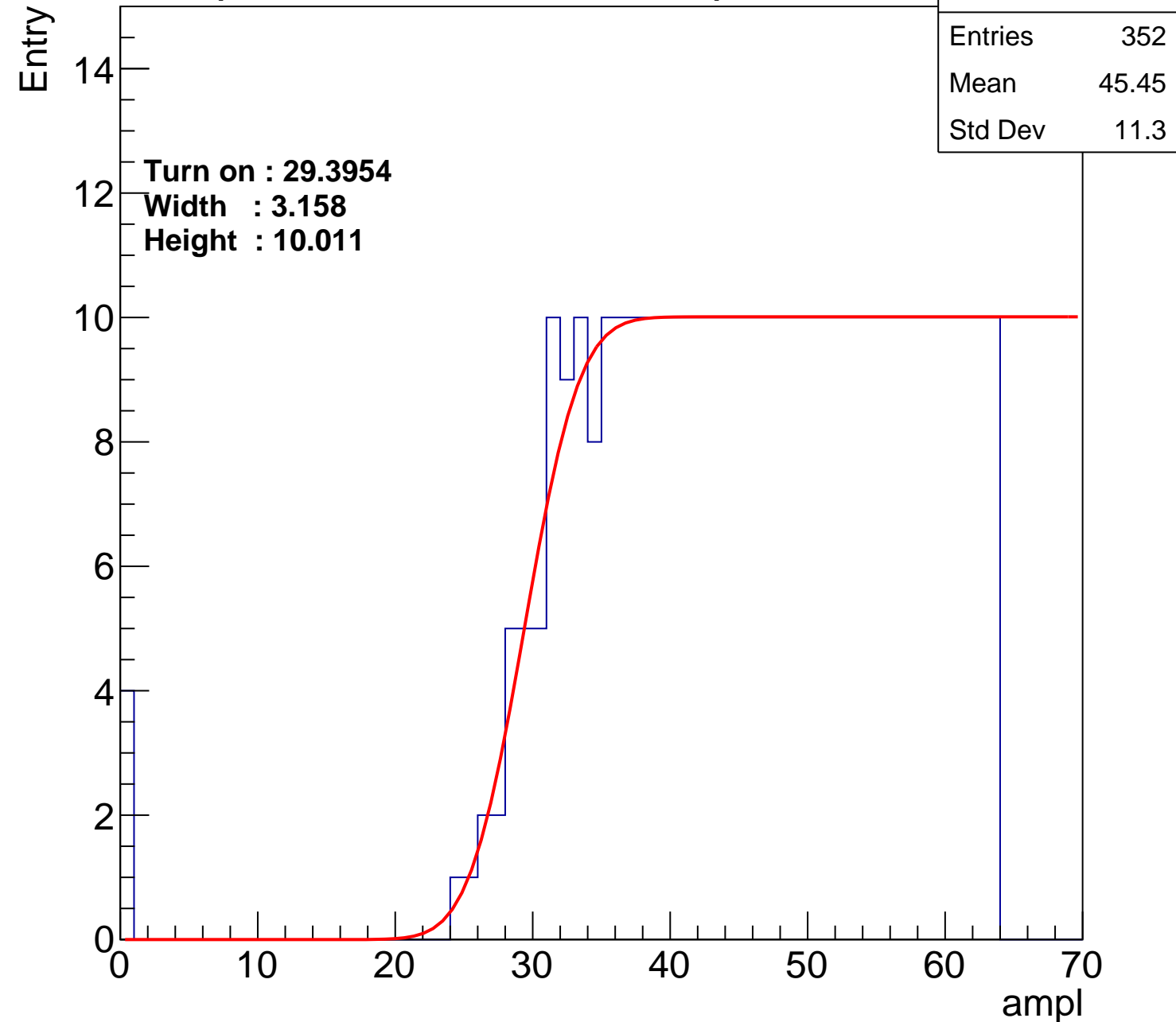
Width : 3.158

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.59
Std Dev	10.68

Turn on : 29.2512

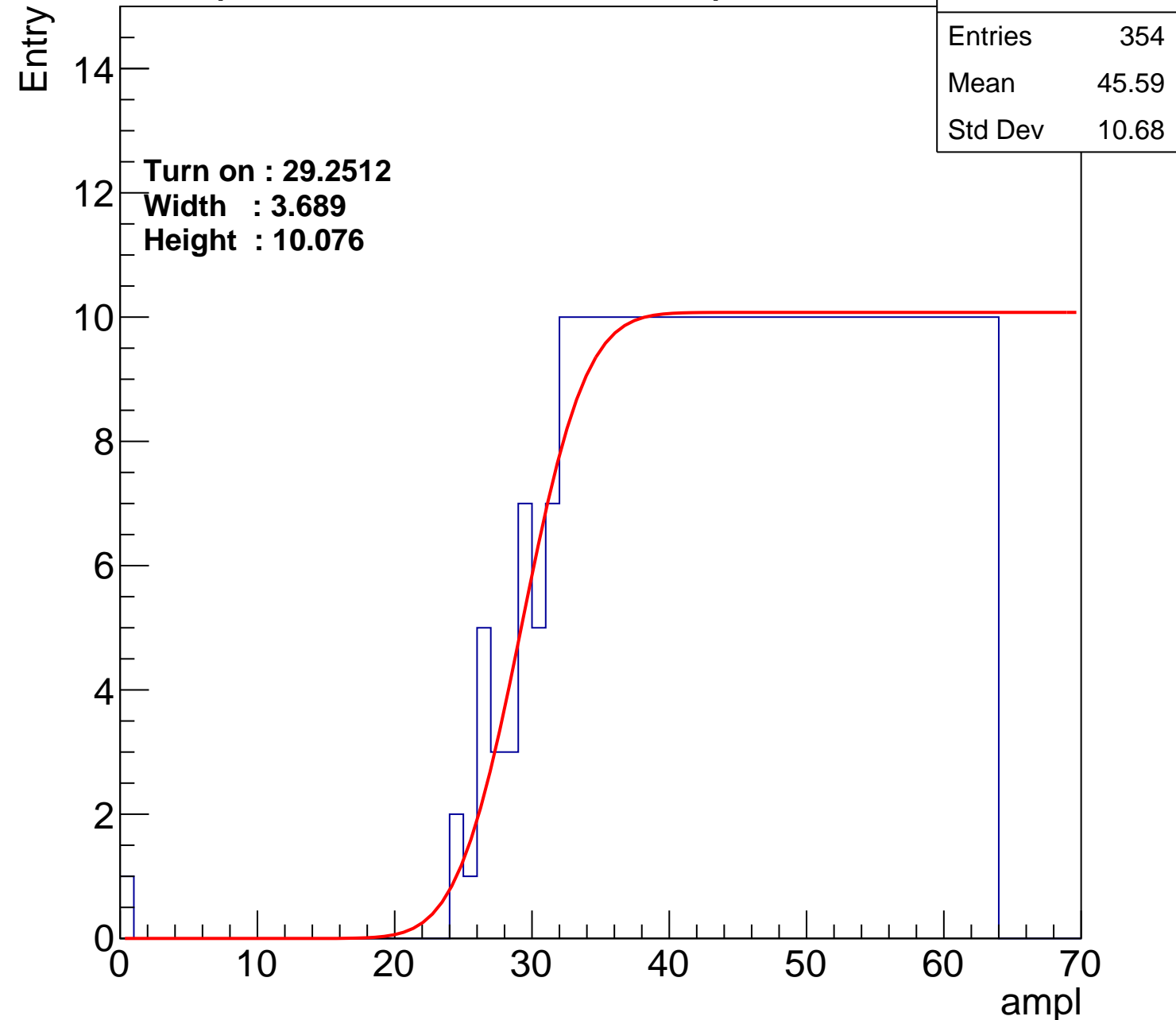
Width : 3.689

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.37
Std Dev	11.63

Turn on : 27.0324

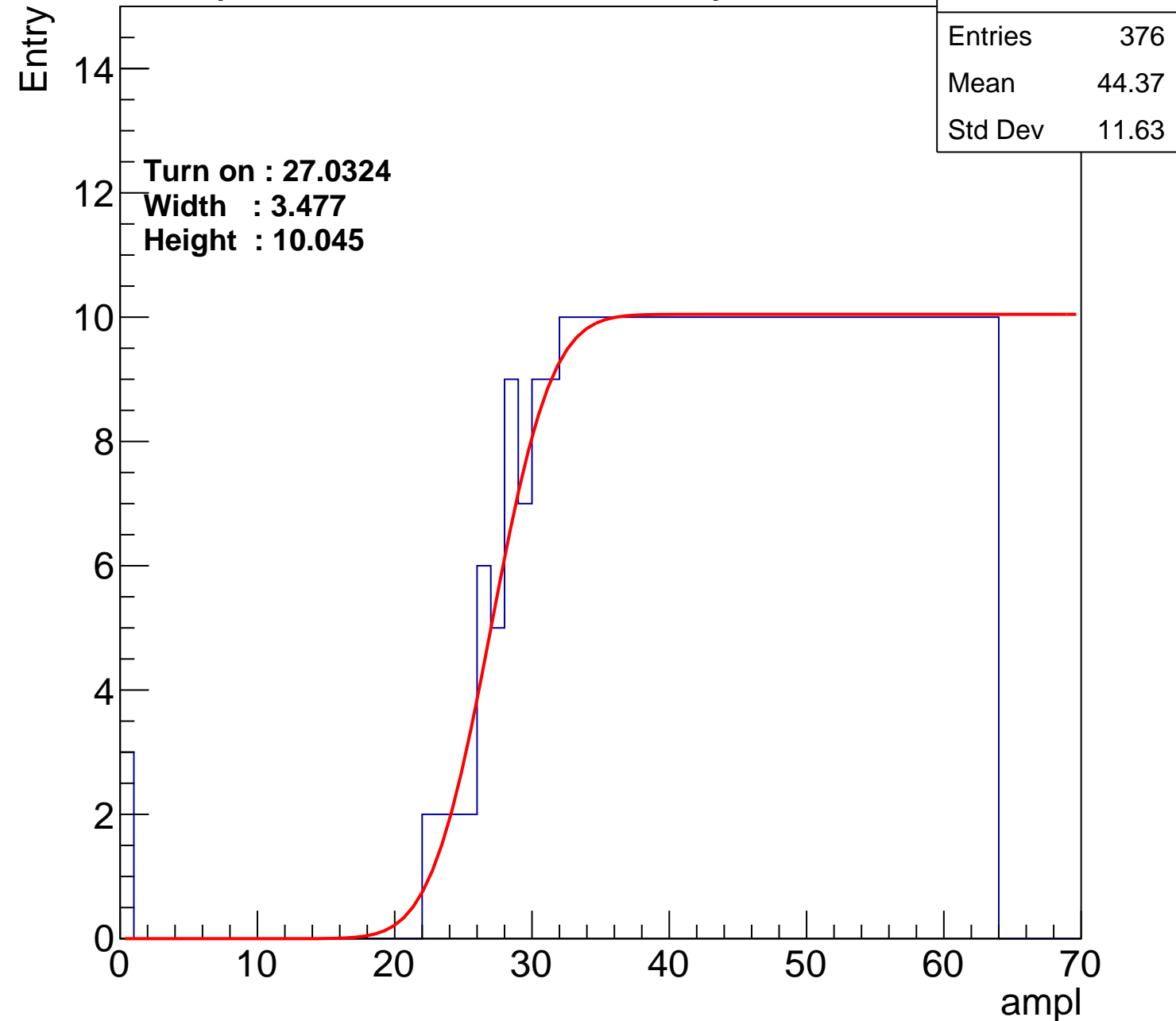
Width : 3.477

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch13

calib_packv5_042523_0143.root, FC#13, port D2

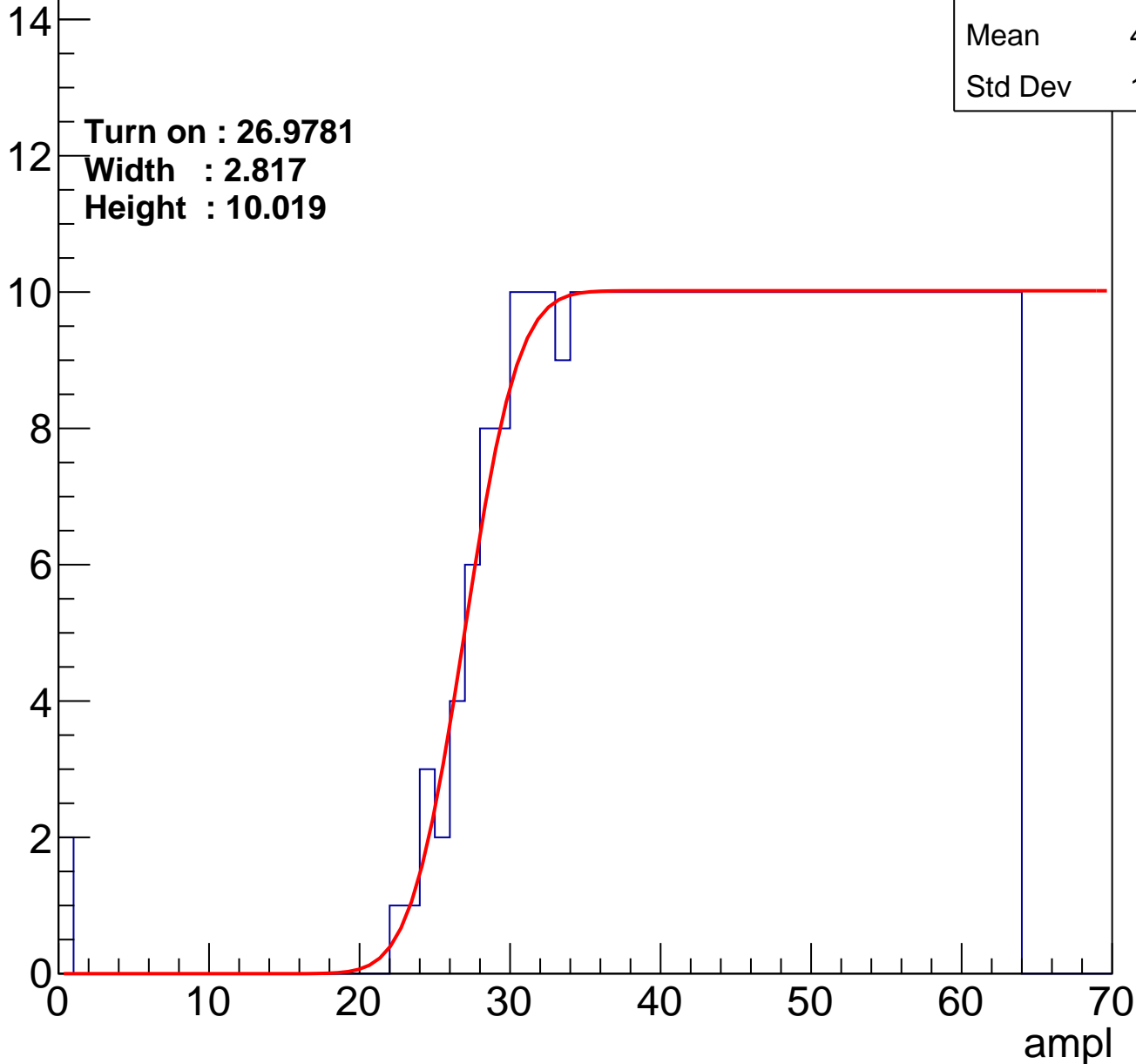
Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.9781

Width : 2.817

Height : 10.019

Entry



B1L003S, U16-ch14

calib_packv5_042523_0143.root, FC#13, port D2

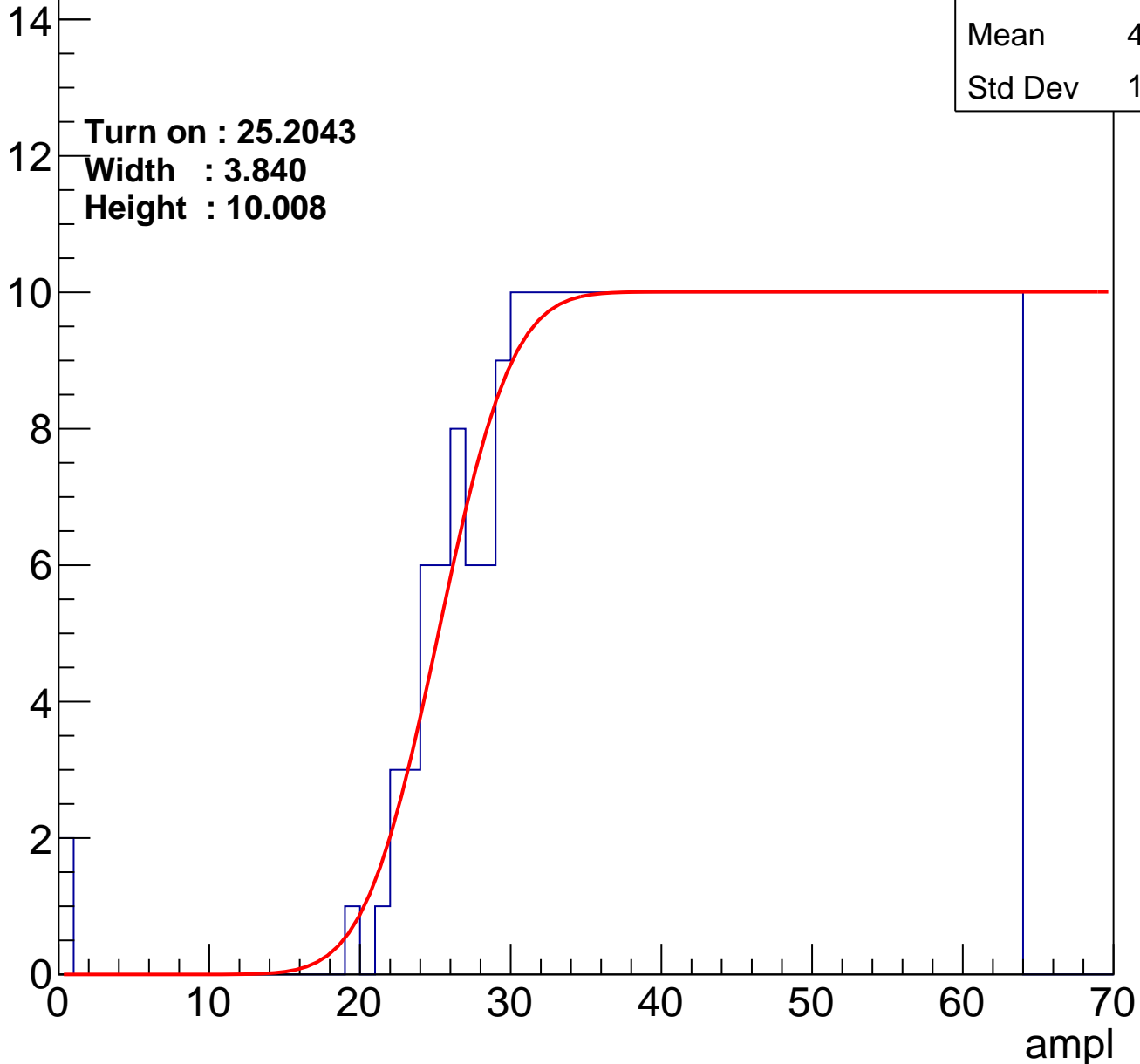
Entries	391
Mean	43.68
Std Dev	11.87

Turn on : 25.2043

Width : 3.840

Height : 10.008

Entry



B1L003S, U16-ch15

calib_packv5_042523_0143.root, FC#13, port D2

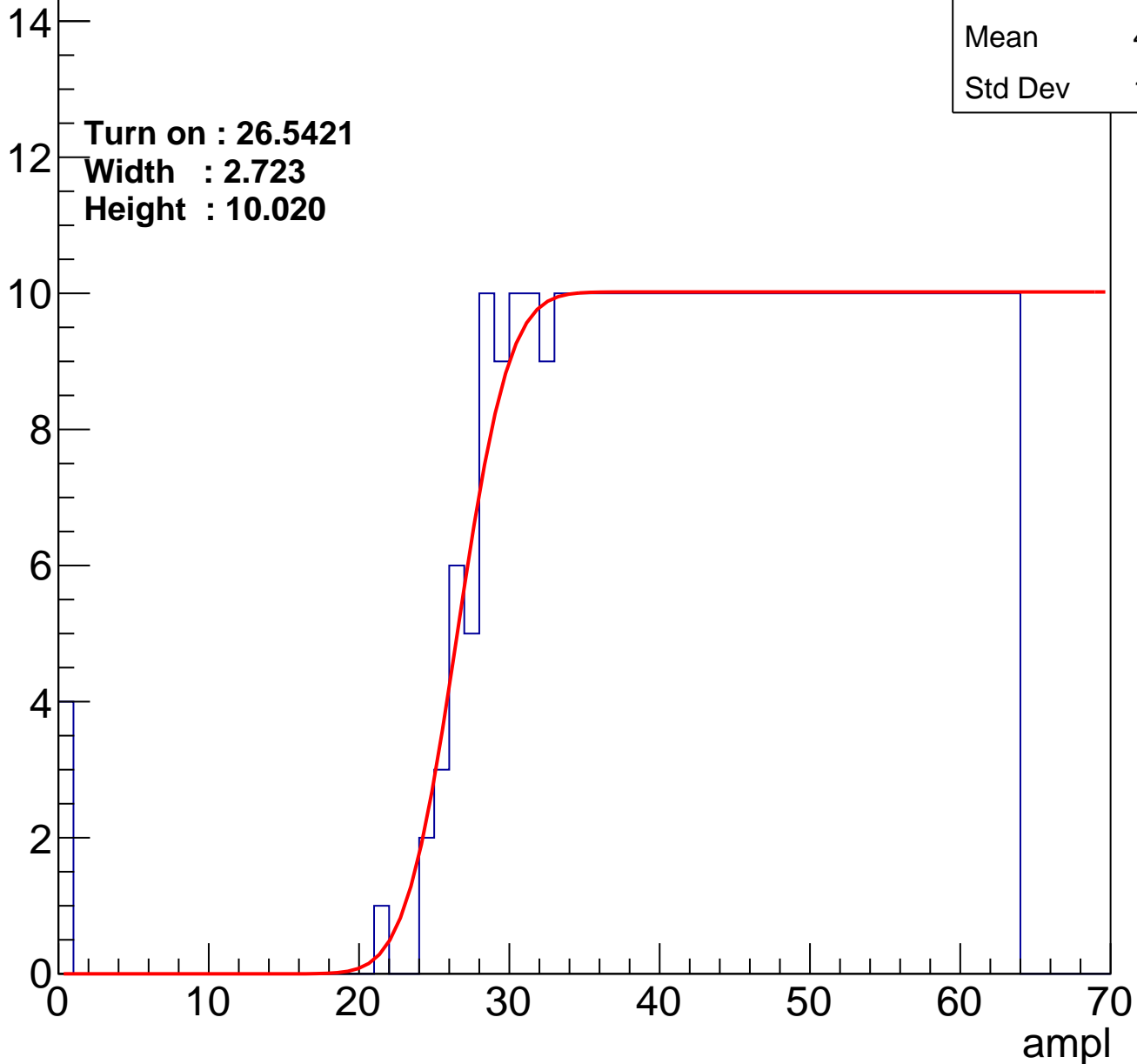
Entries	379
Mean	44.21
Std Dev	11.81

Turn on : 26.5421

Width : 2.723

Height : 10.020

Entry



B1L003S, U16-ch16

calib_packv5_042523_0143.root, FC#13, port D2

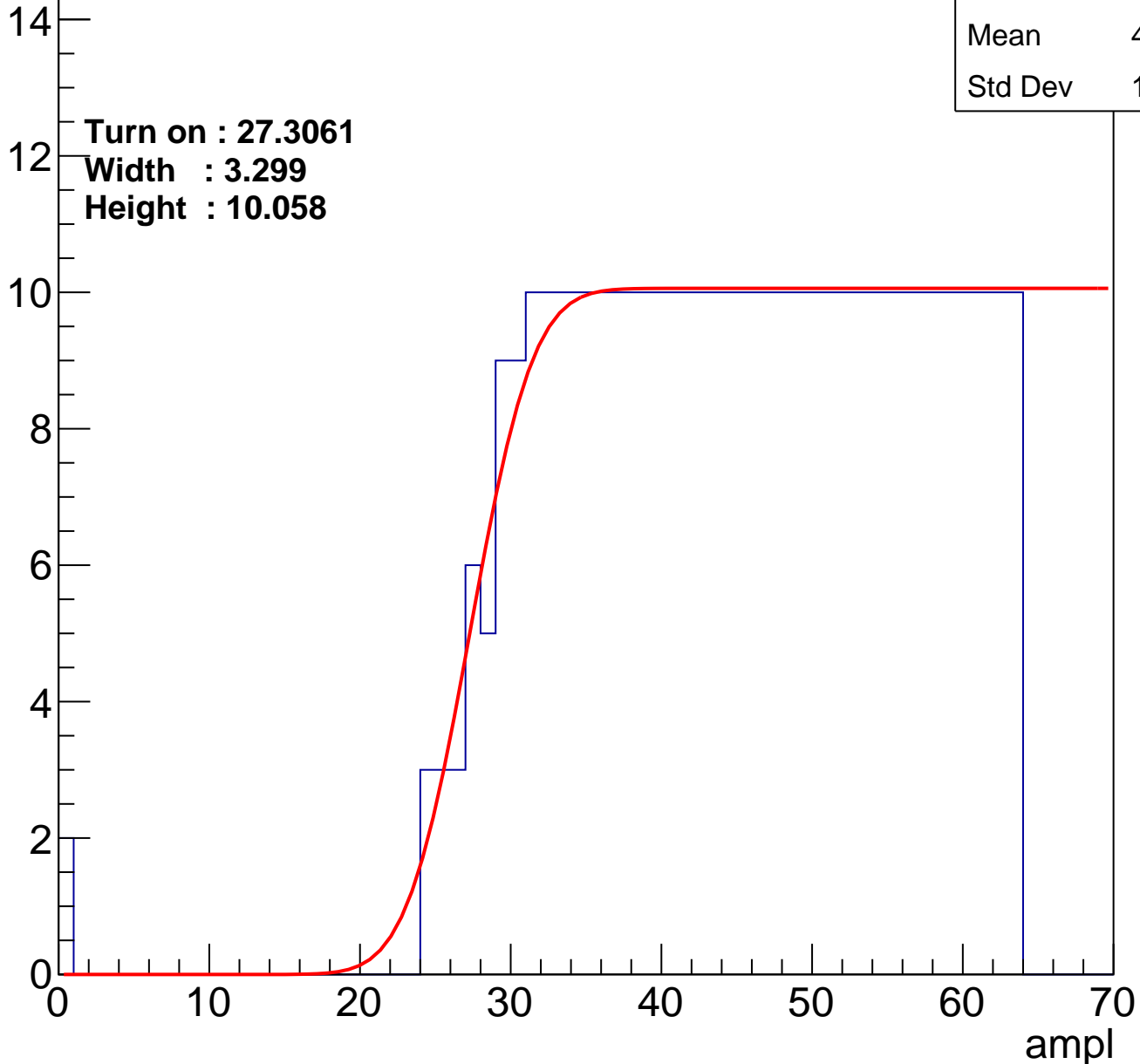
Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.3061

Width : 3.299

Height : 10.058

Entry



B1L003S, U16-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.68
Std Dev	11.15

Turn on : 26.9917

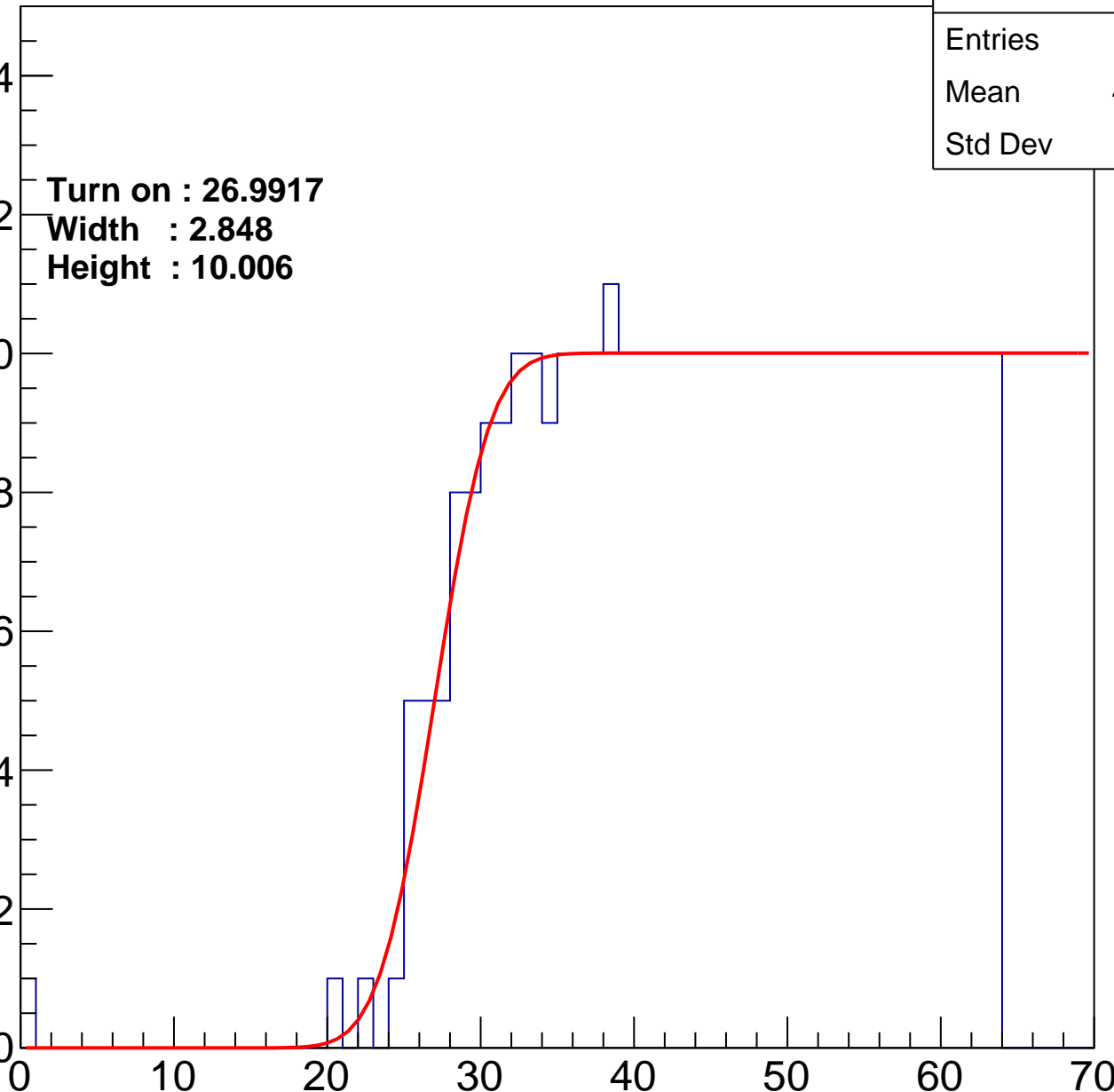
Width : 2.848

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch18

calib_packv5_042523_0143.root, FC#13, port D2

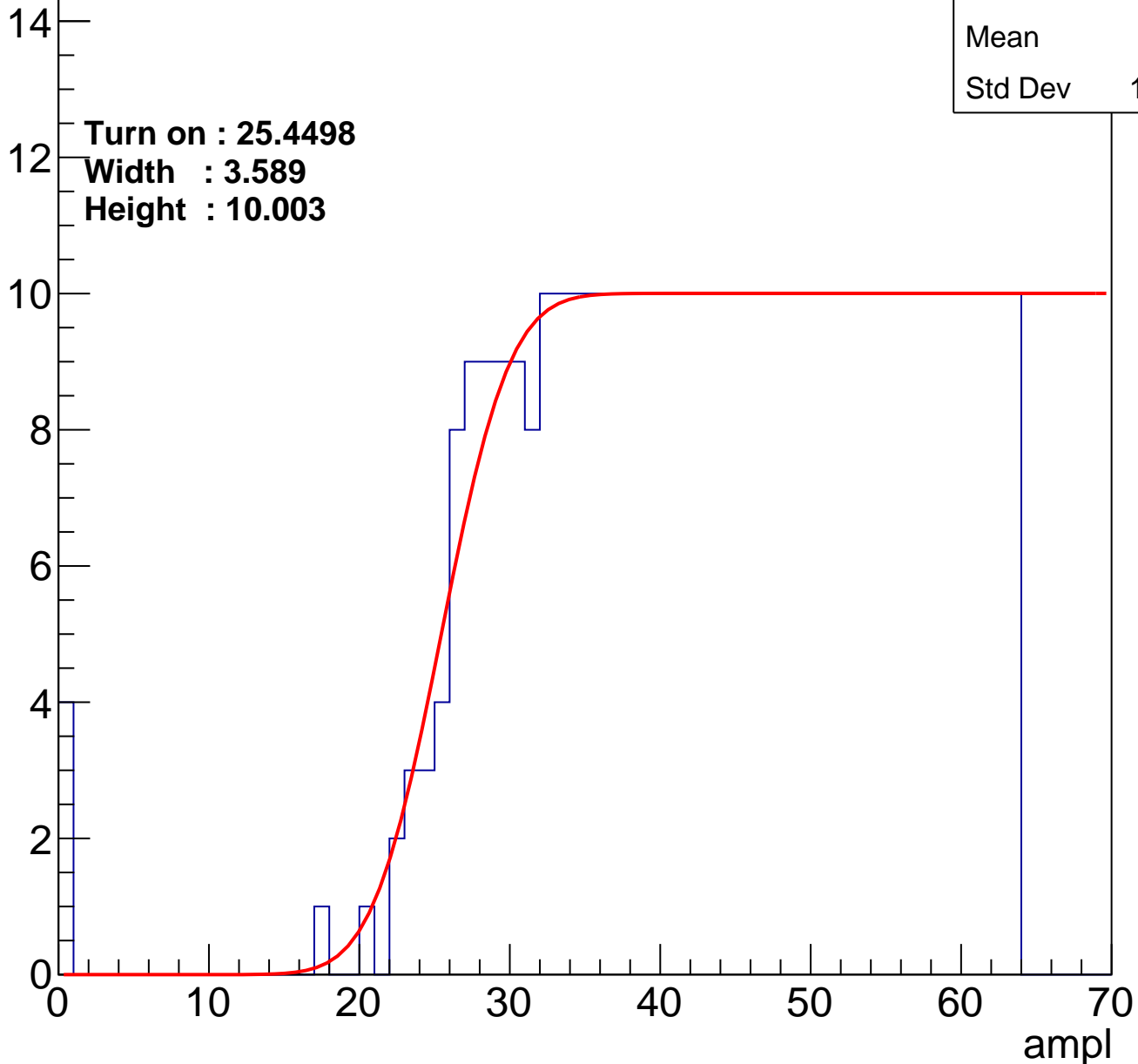
Entries	390
Mean	43.6
Std Dev	12.17

Turn on : 25.4498

Width : 3.589

Height : 10.003

Entry



B1L003S, U16-ch19

calib_packv5_042523_0143.root, FC#13, port D2

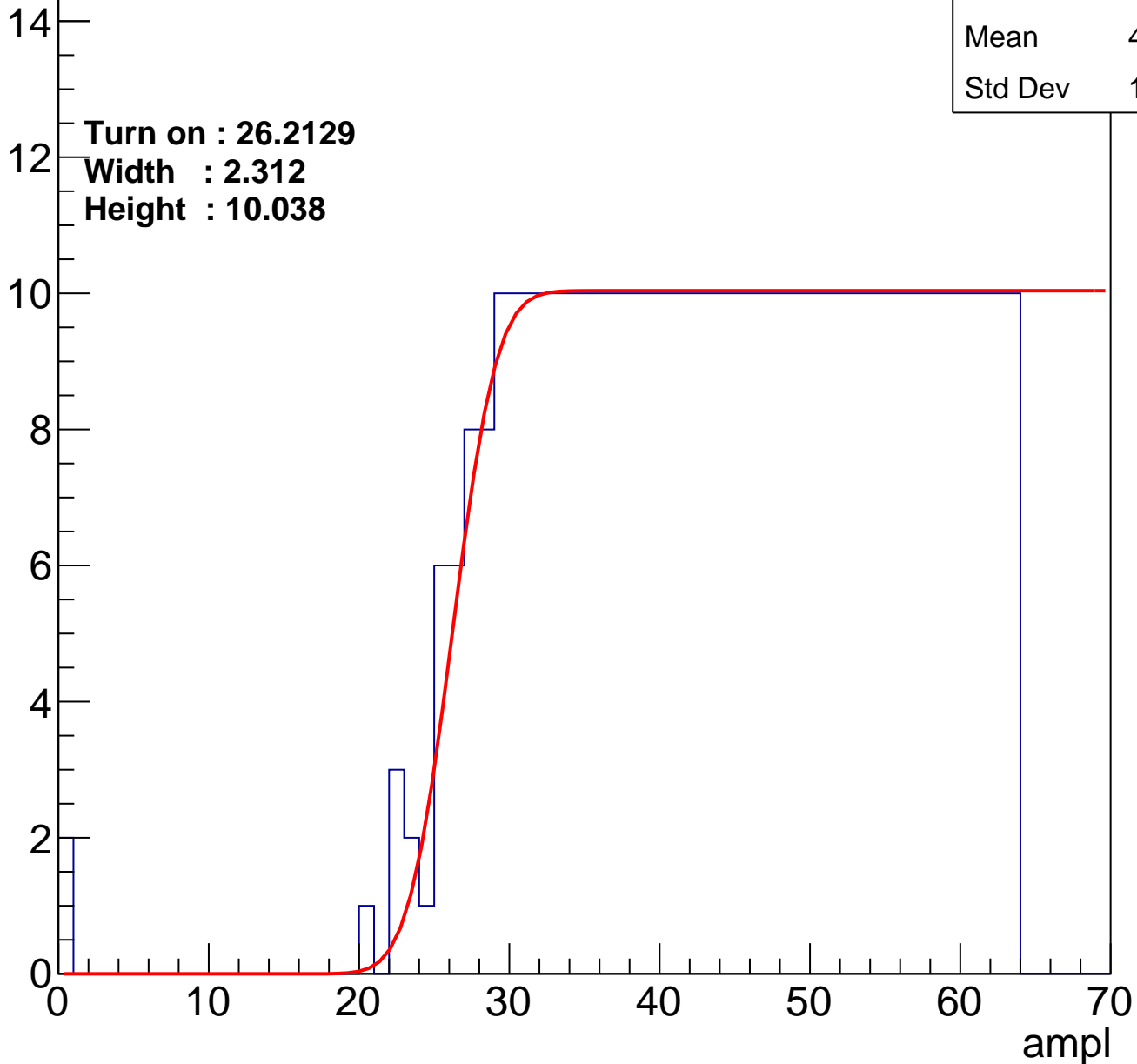
Entries	387
Mean	43.93
Std Dev	11.67

Turn on : 26.2129

Width : 2.312

Height : 10.038

Entry



B1L003S, U16-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.92
Std Dev	11.68

Turn on : 25.1399

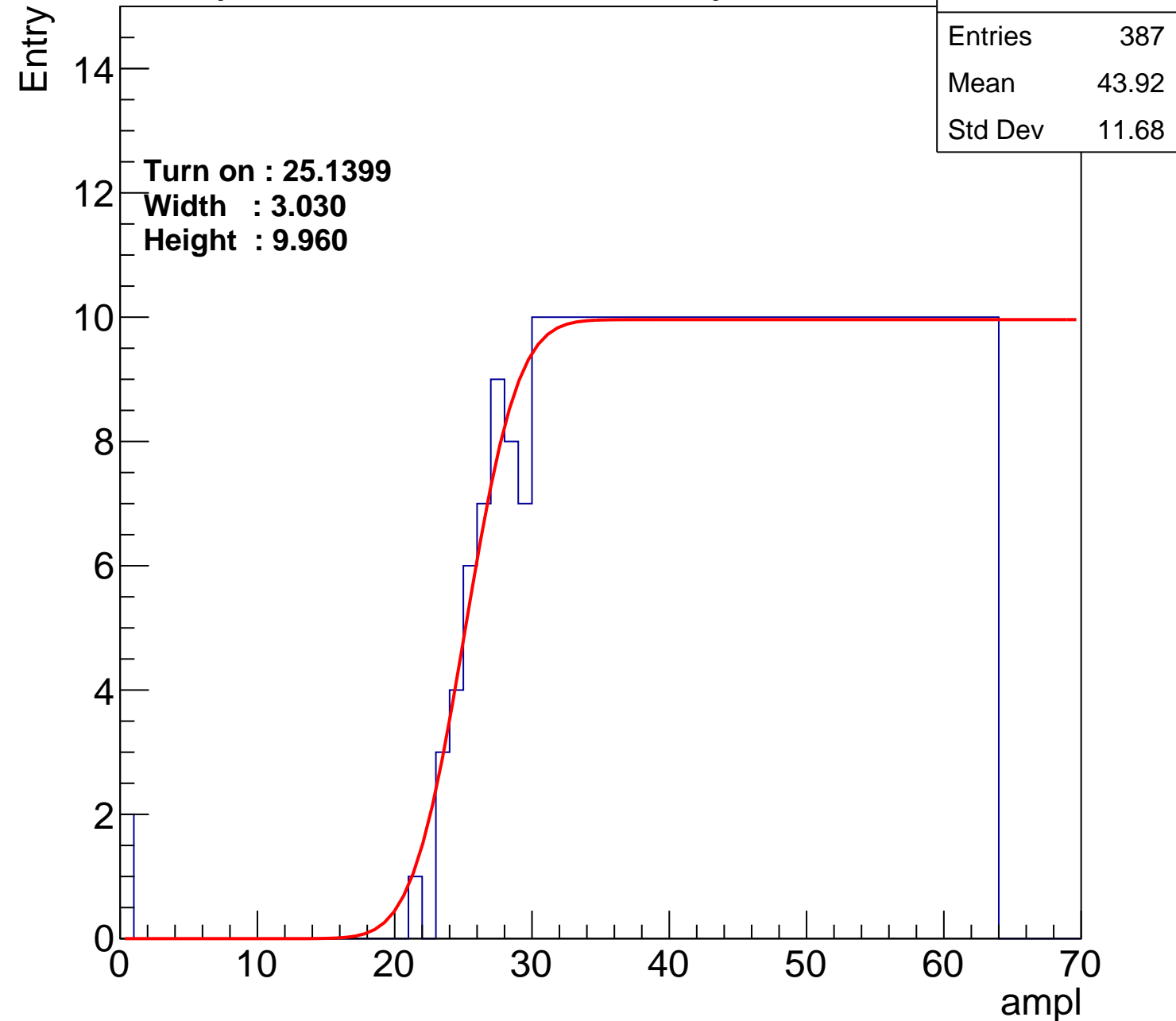
Width : 3.030

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.57
Std Dev	11.2

Turn on : 26.1163

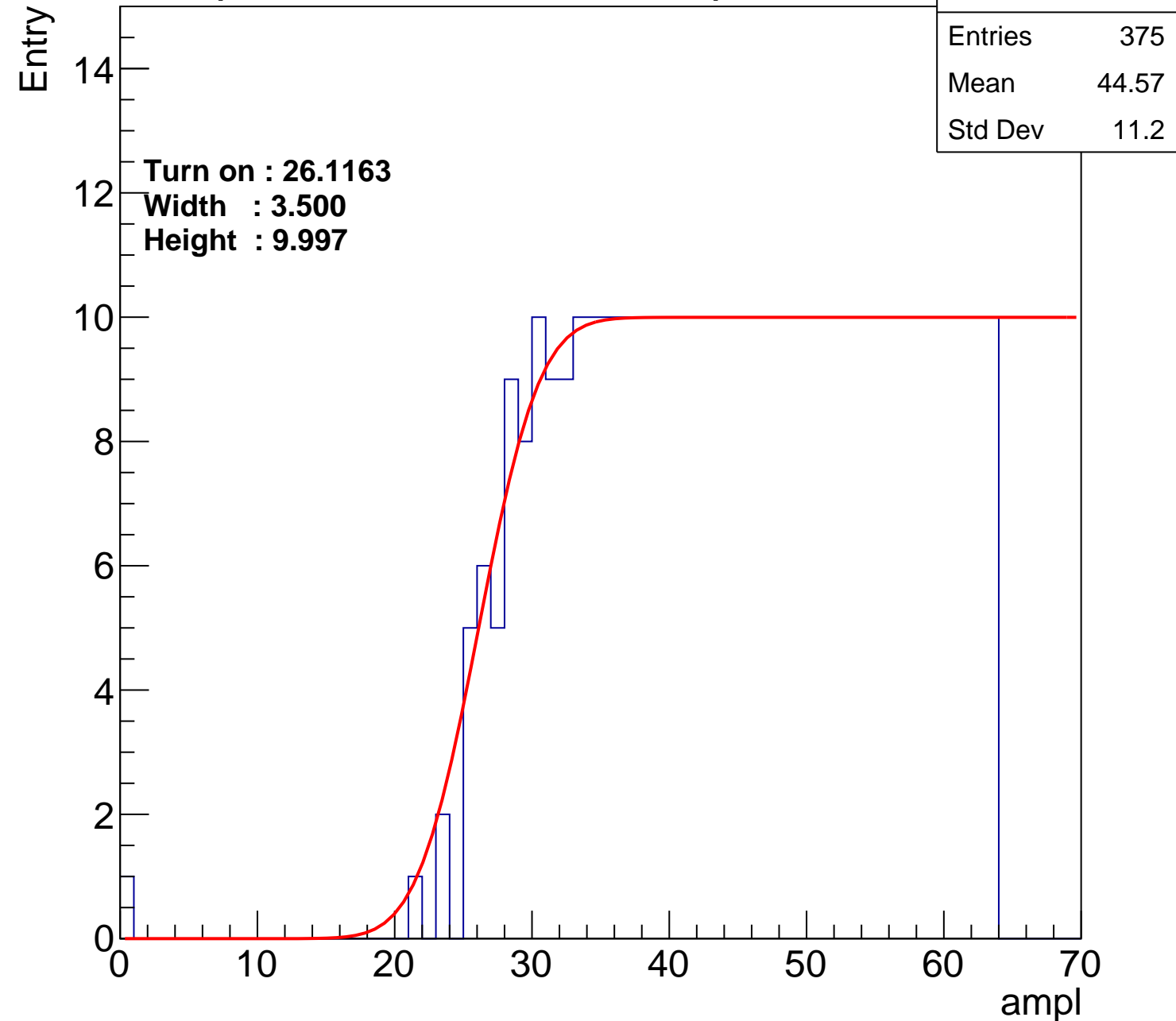
Width : 3.500

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch22

calib_packv5_042523_0143.root, FC#13, port D2

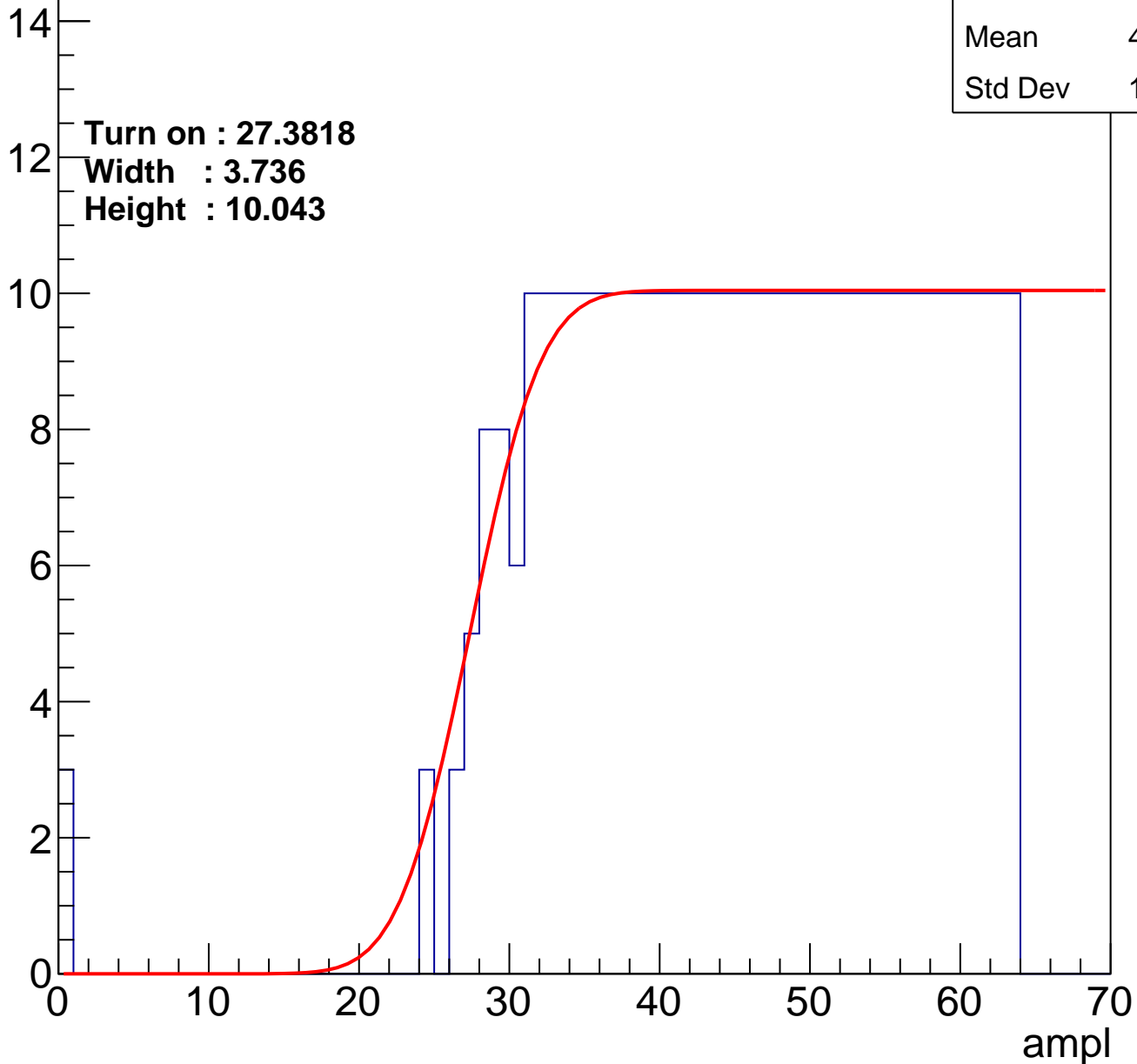
Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.3818

Width : 3.736

Height : 10.043

Entry



B1L003S, U16-ch23

calib_packv5_042523_0143.root, FC#13, port D2

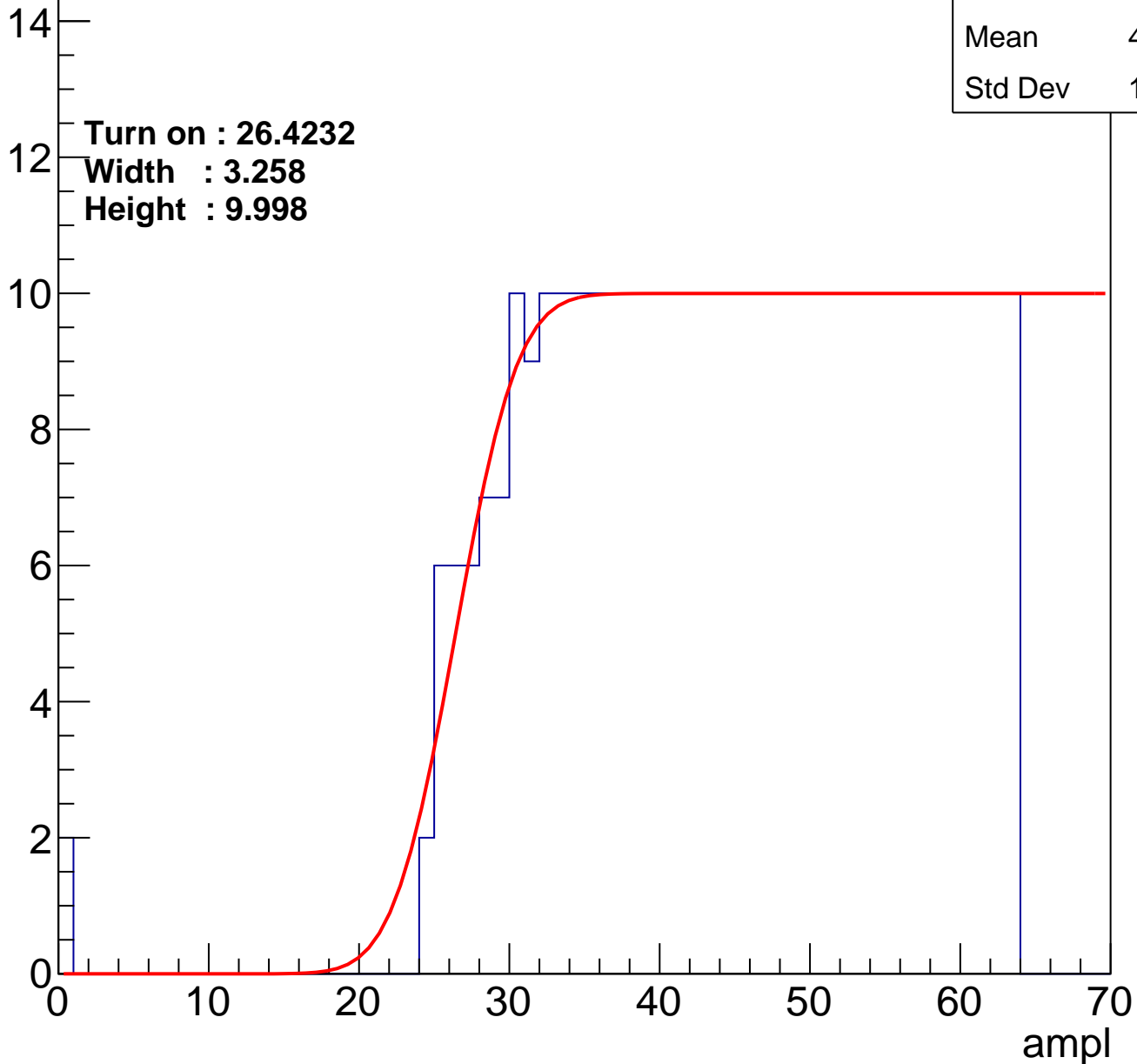
Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.4232

Width : 3.258

Height : 9.998

Entry



B1L003S, U16-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.71
Std Dev	11.99

Turn on : 25.8207

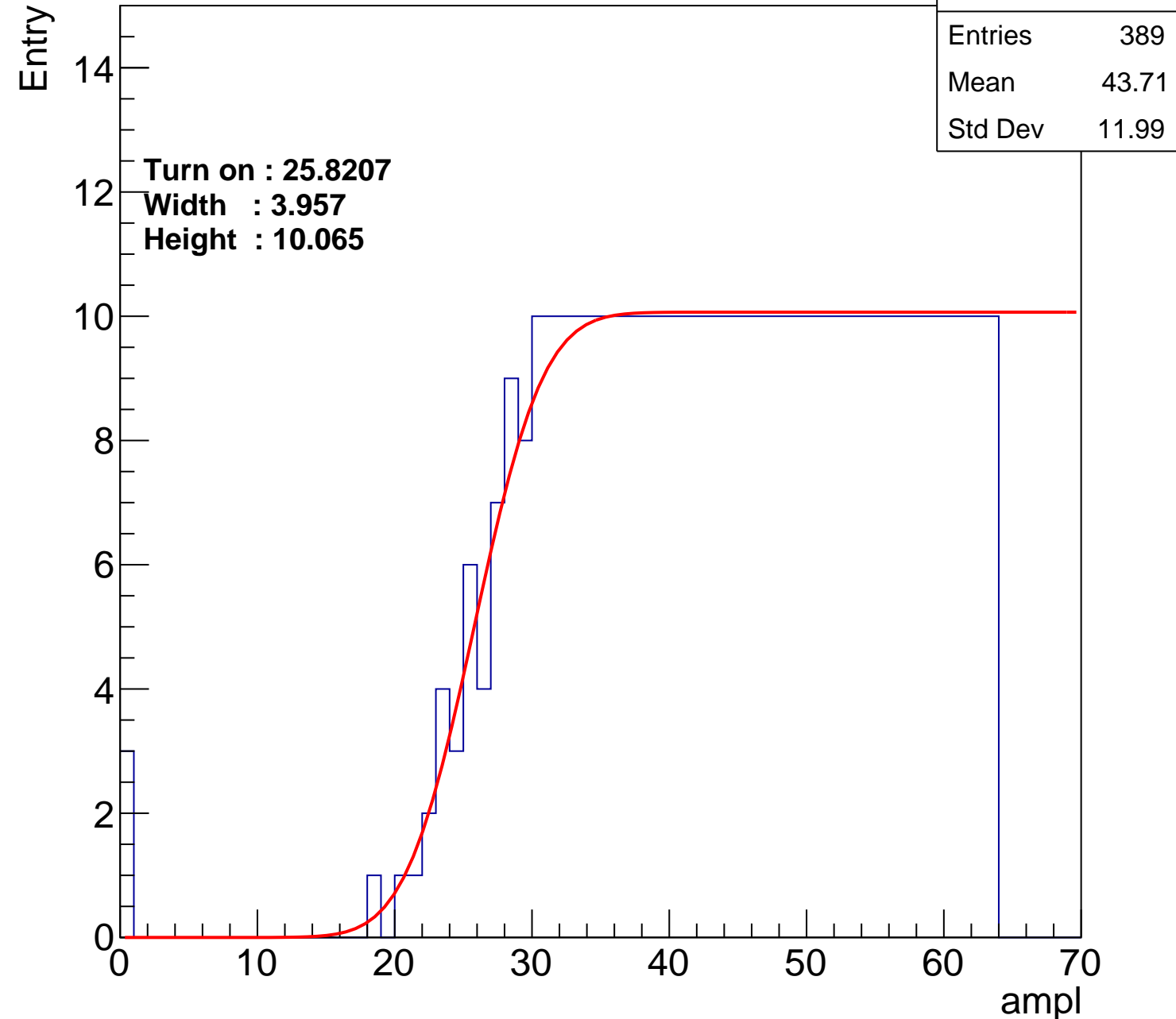
Width : 3.957

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch25

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.24
Std Dev	11.82

Turn on : 27.0942

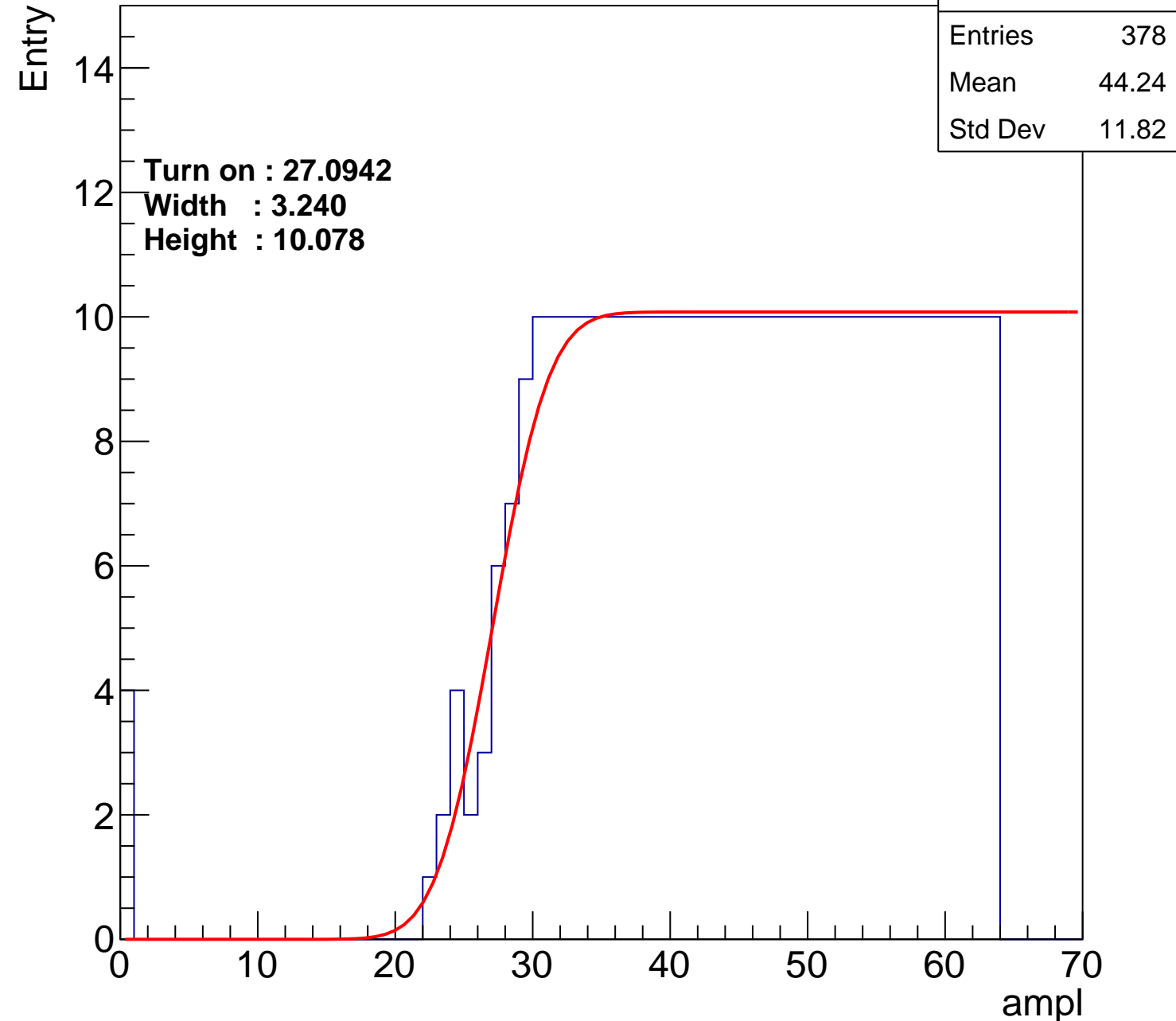
Width : 3.240

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.82
Std Dev	11.75

Turn on : 25.7431

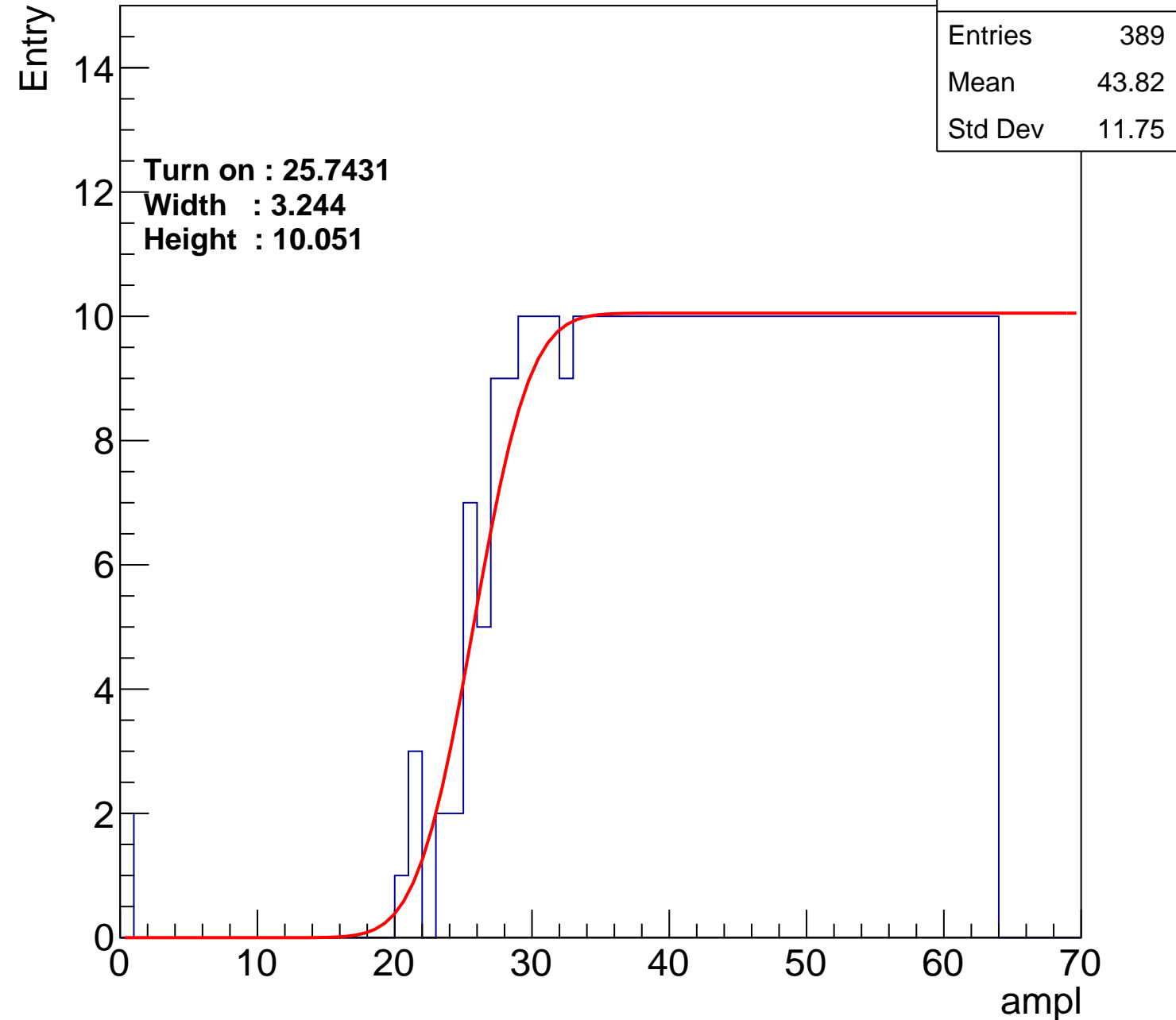
Width : 3.244

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.68
Std Dev	11.59

Turn on : 27.4478

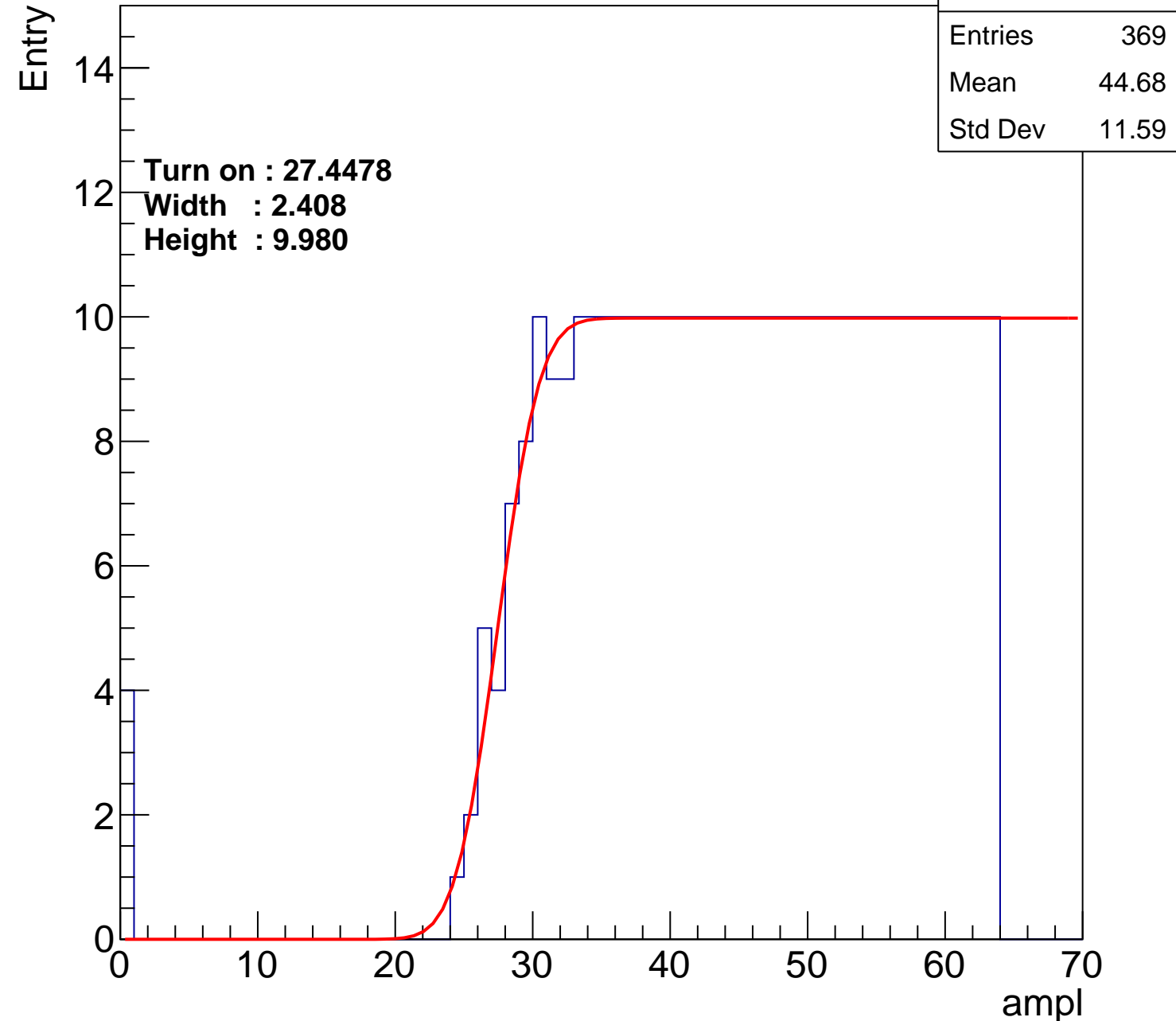
Width : 2.408

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.23
Std Dev	11.64

Turn on : 26.4363

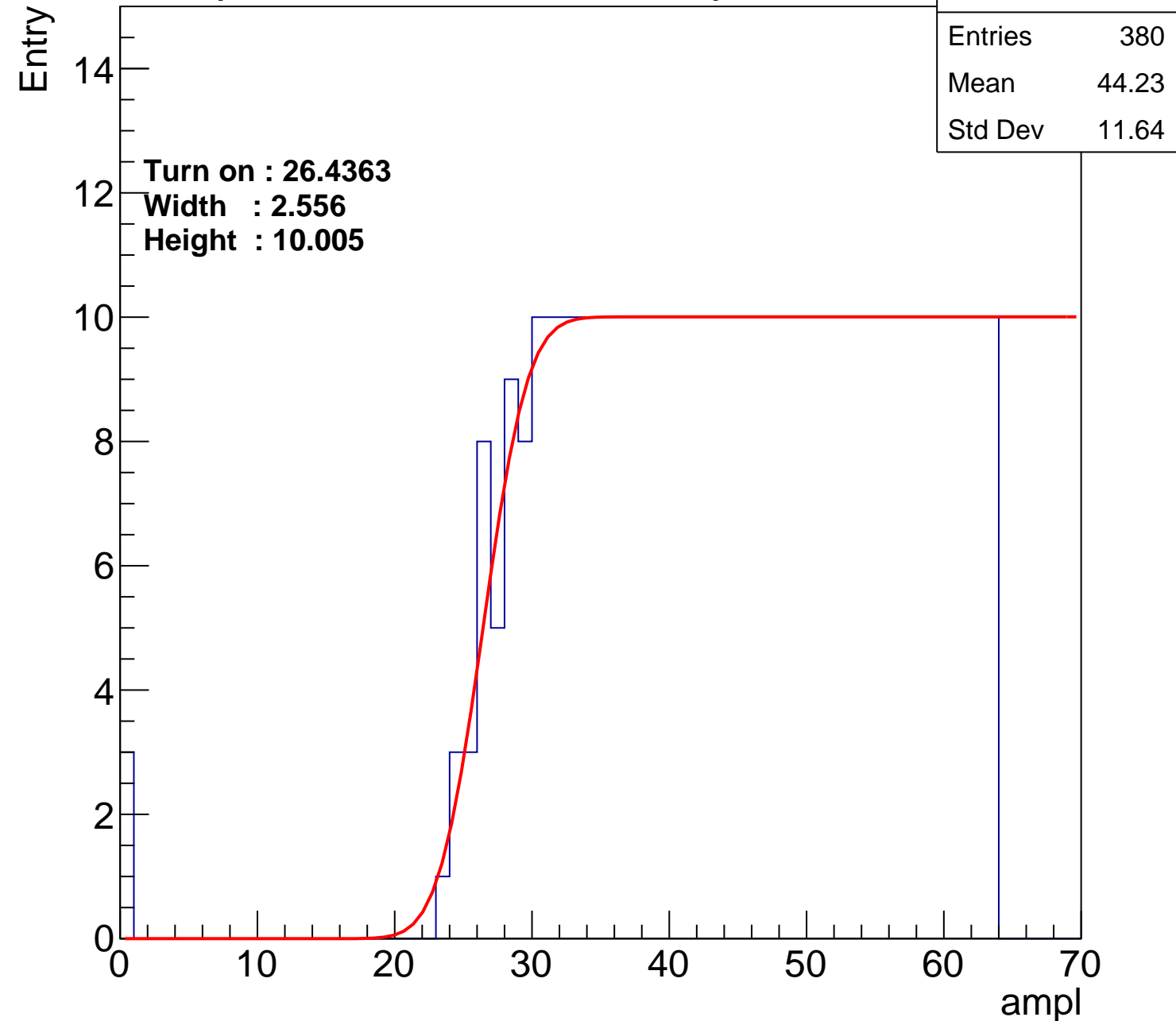
Width : 2.556

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.32
Std Dev	11.15

Turn on : 28.3308

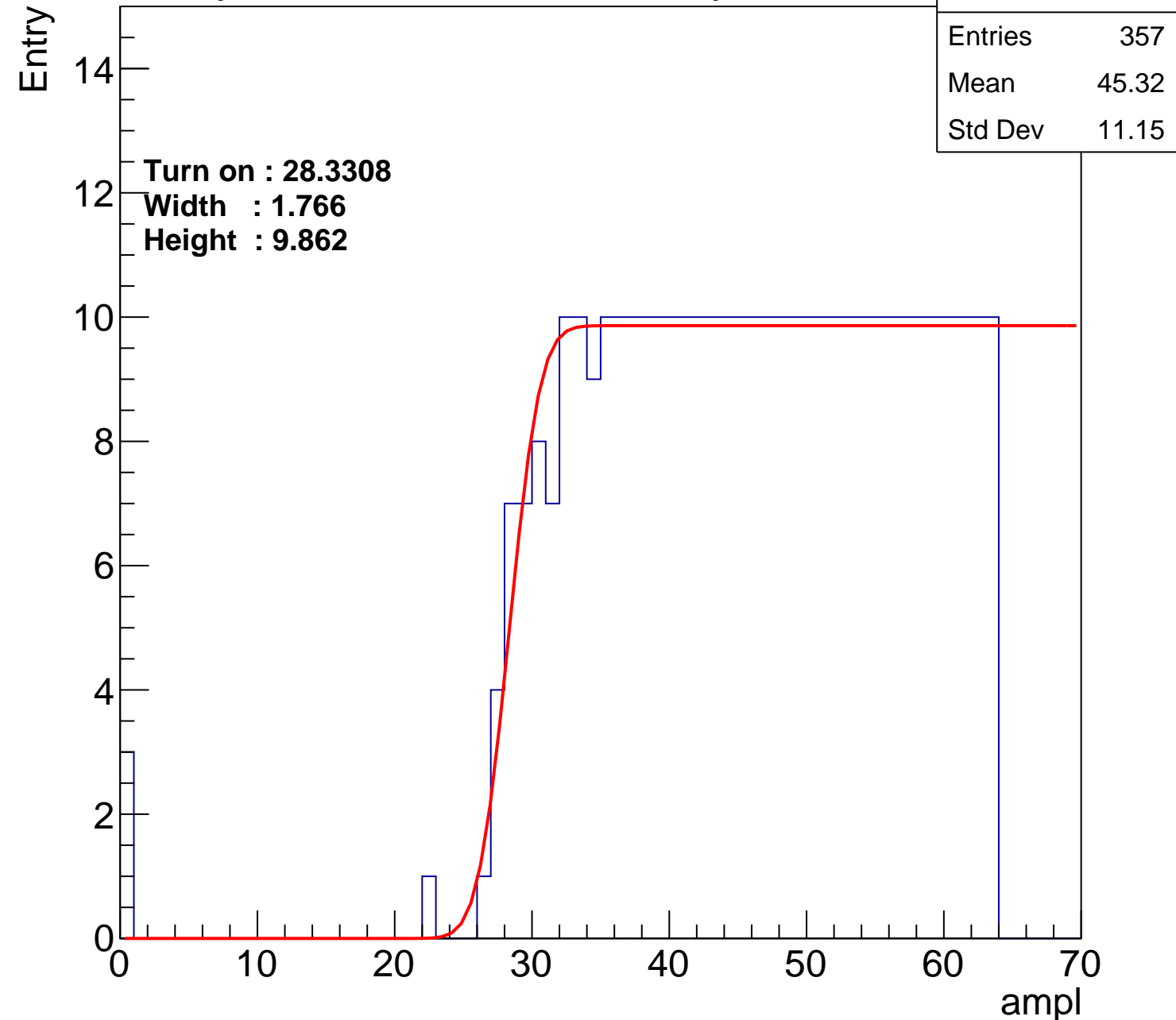
Width : 1.766

Height : 9.862

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch30

calib_packv5_042523_0143.root, FC#13, port D2

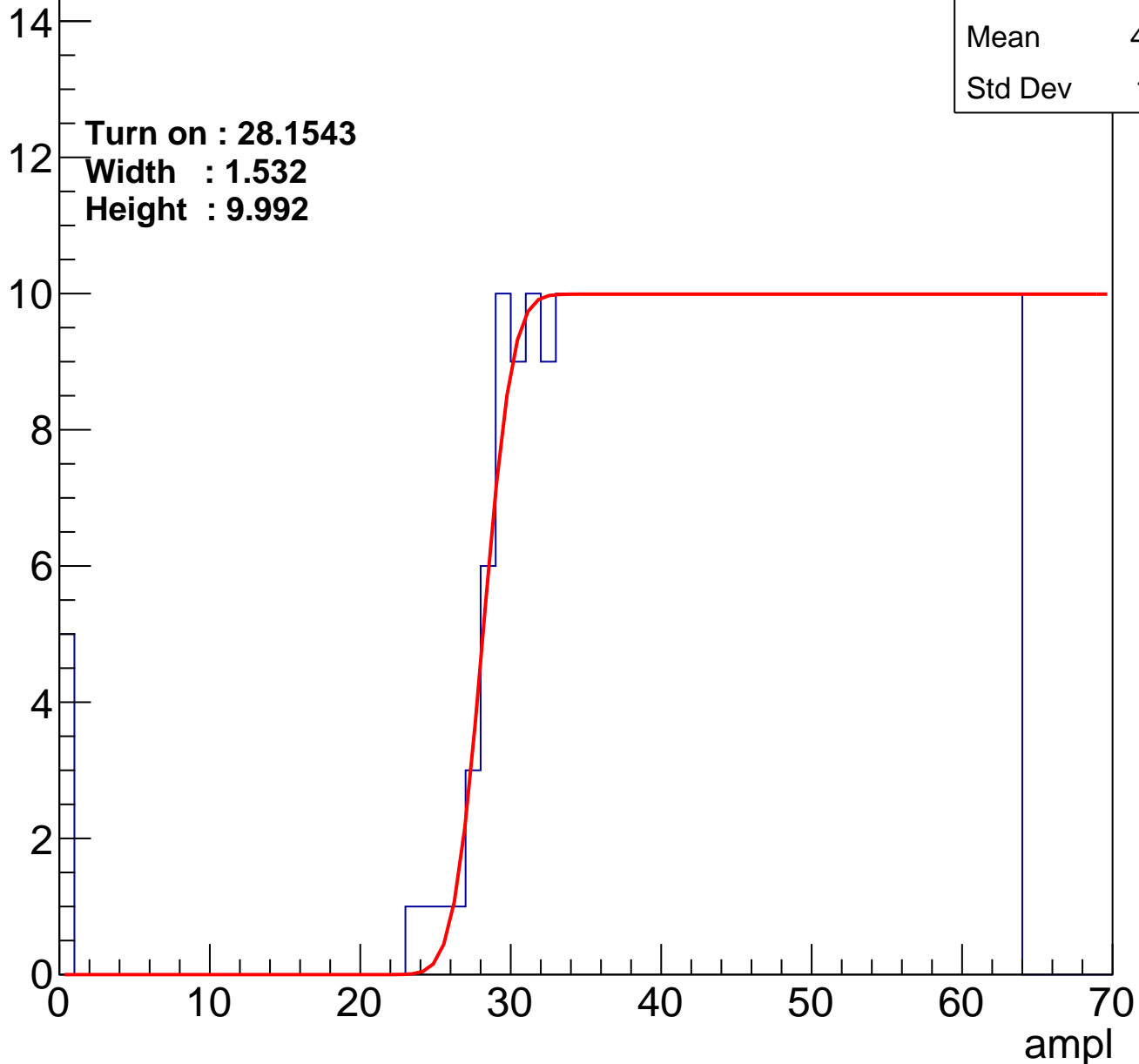
Entries	366
Mean	44.77
Std Dev	11.71

Turn on : 28.1543

Width : 1.532

Height : 9.992

Entry



B1L003S, U16-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	398
Mean	43.21
Std Dev	12.36

Turn on : 25.5465

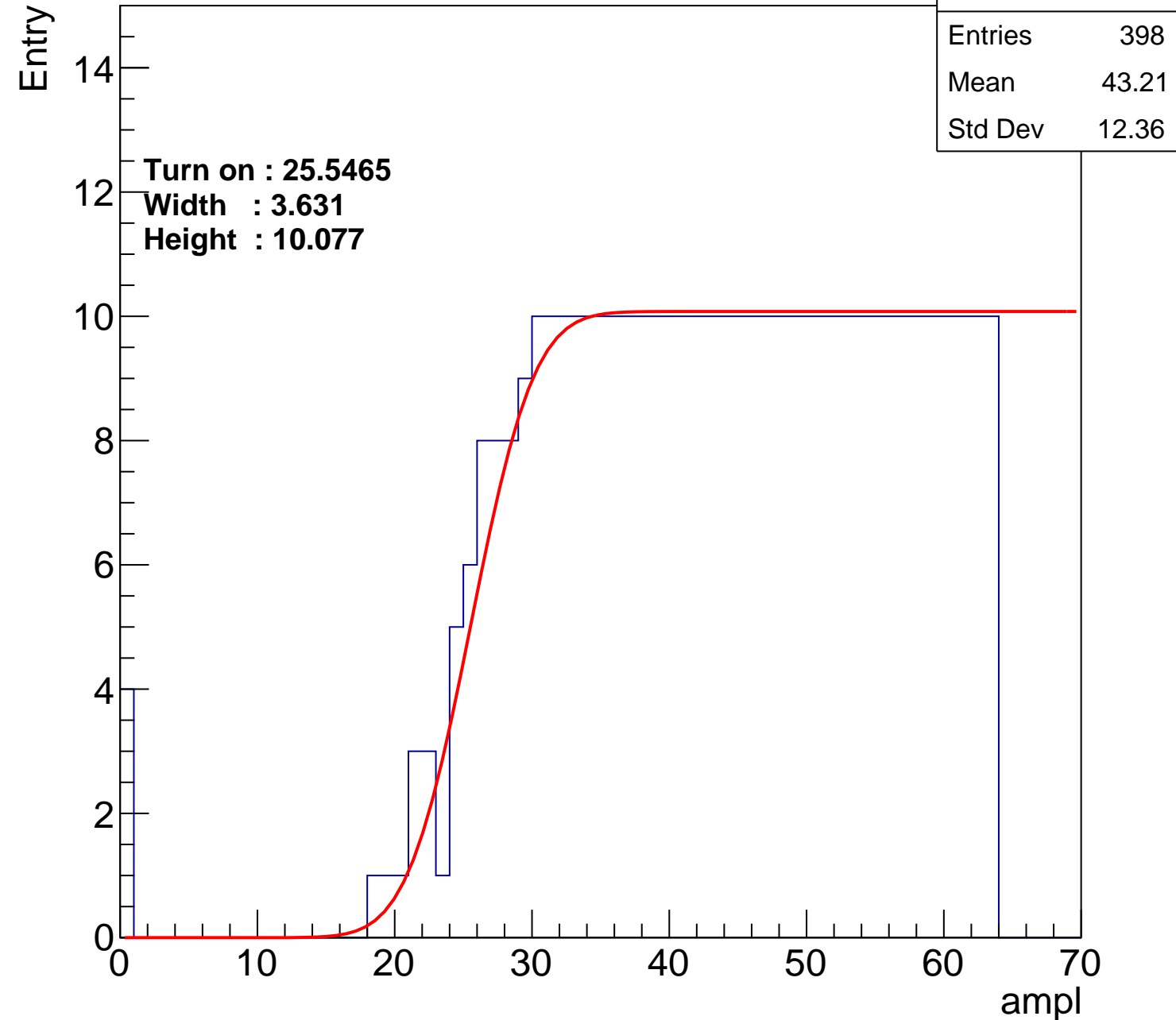
Width : 3.631

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch32

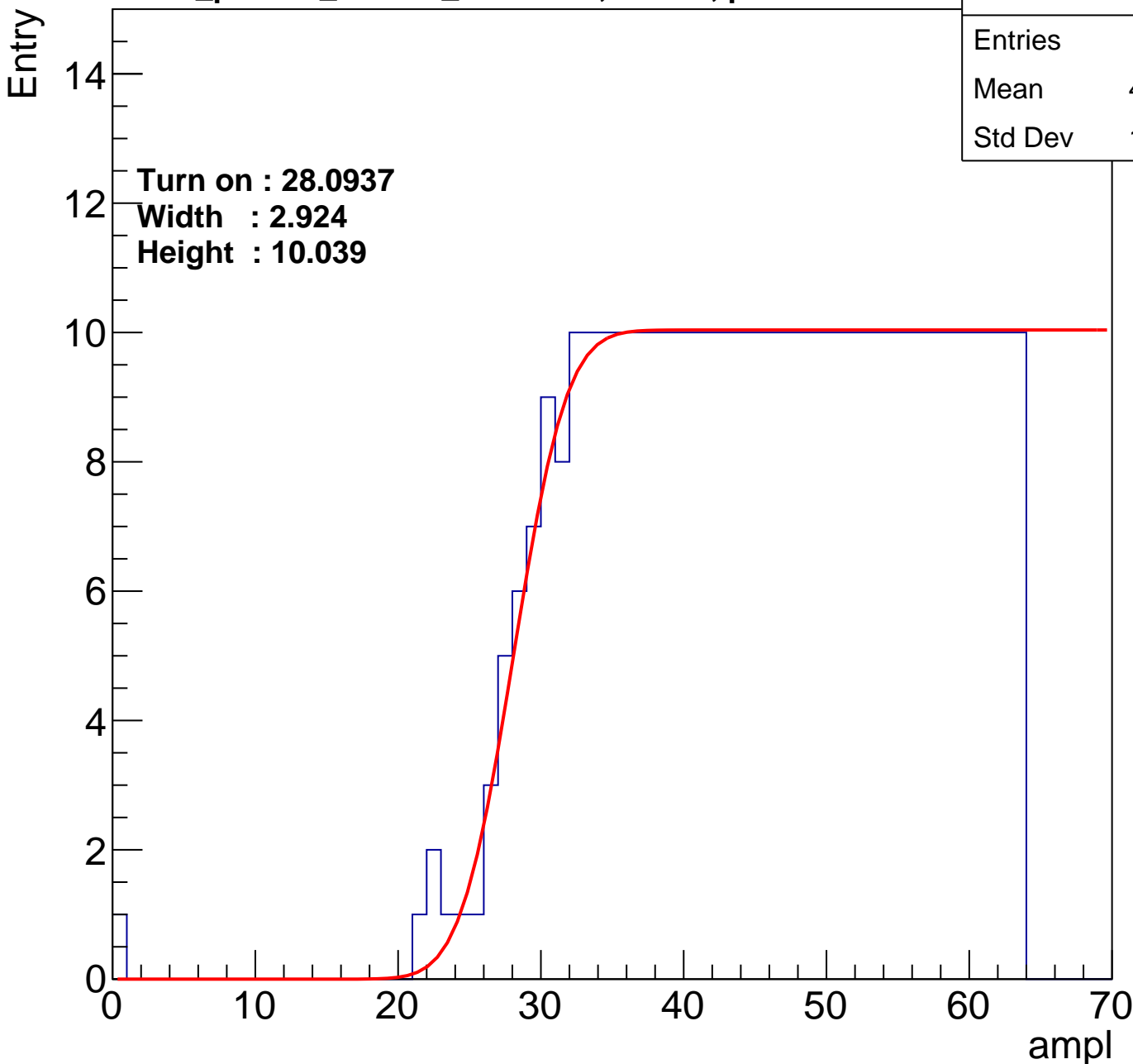
calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.04
Std Dev	10.99

Turn on : 28.0937

Width : 2.924

Height : 10.039



B1L003S, U16-ch33

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.07
Std Dev	11.96

Turn on : 26.9163

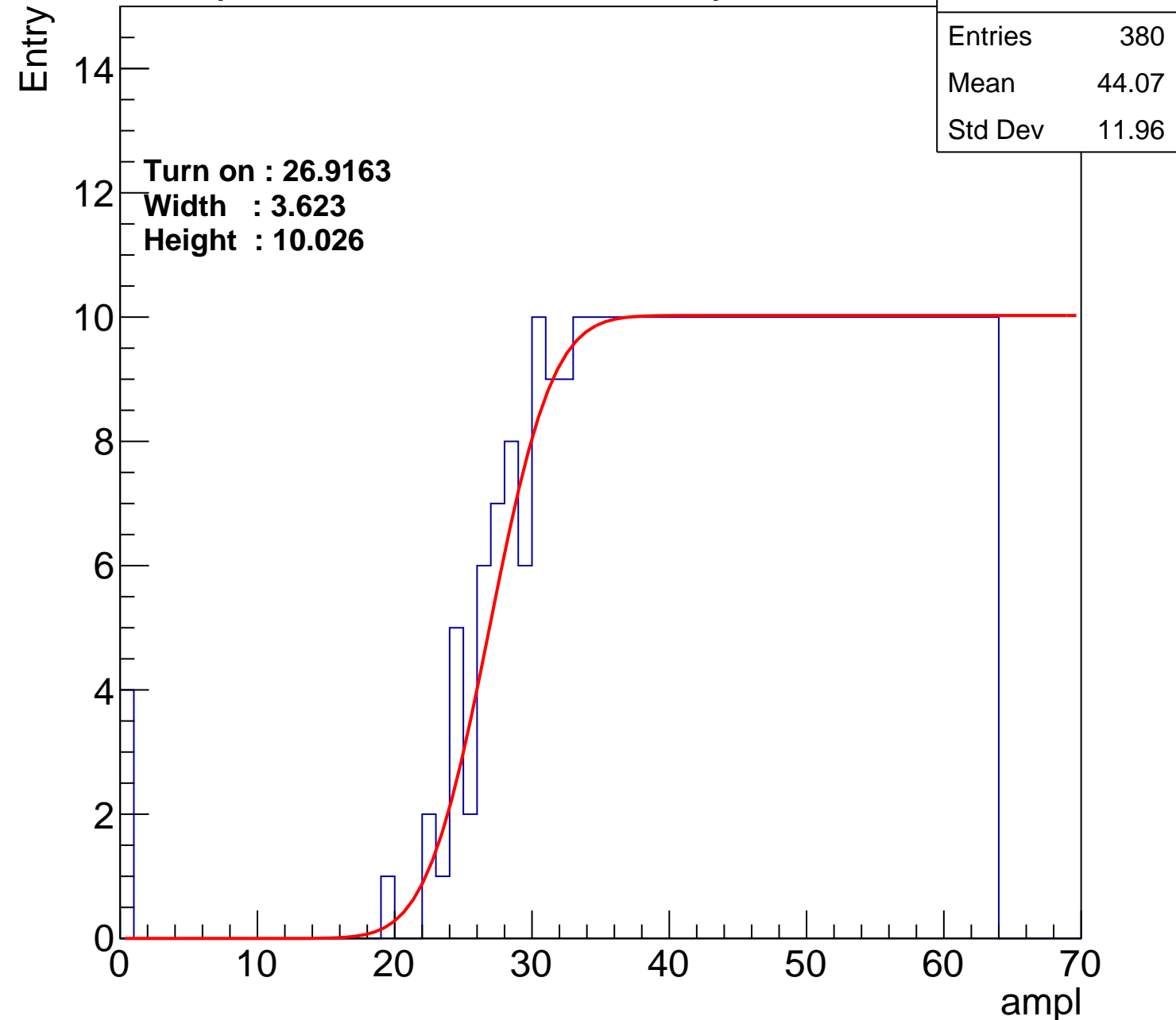
Width : 3.623

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch34

calib_packv5_042523_0143.root, FC#13, port D2

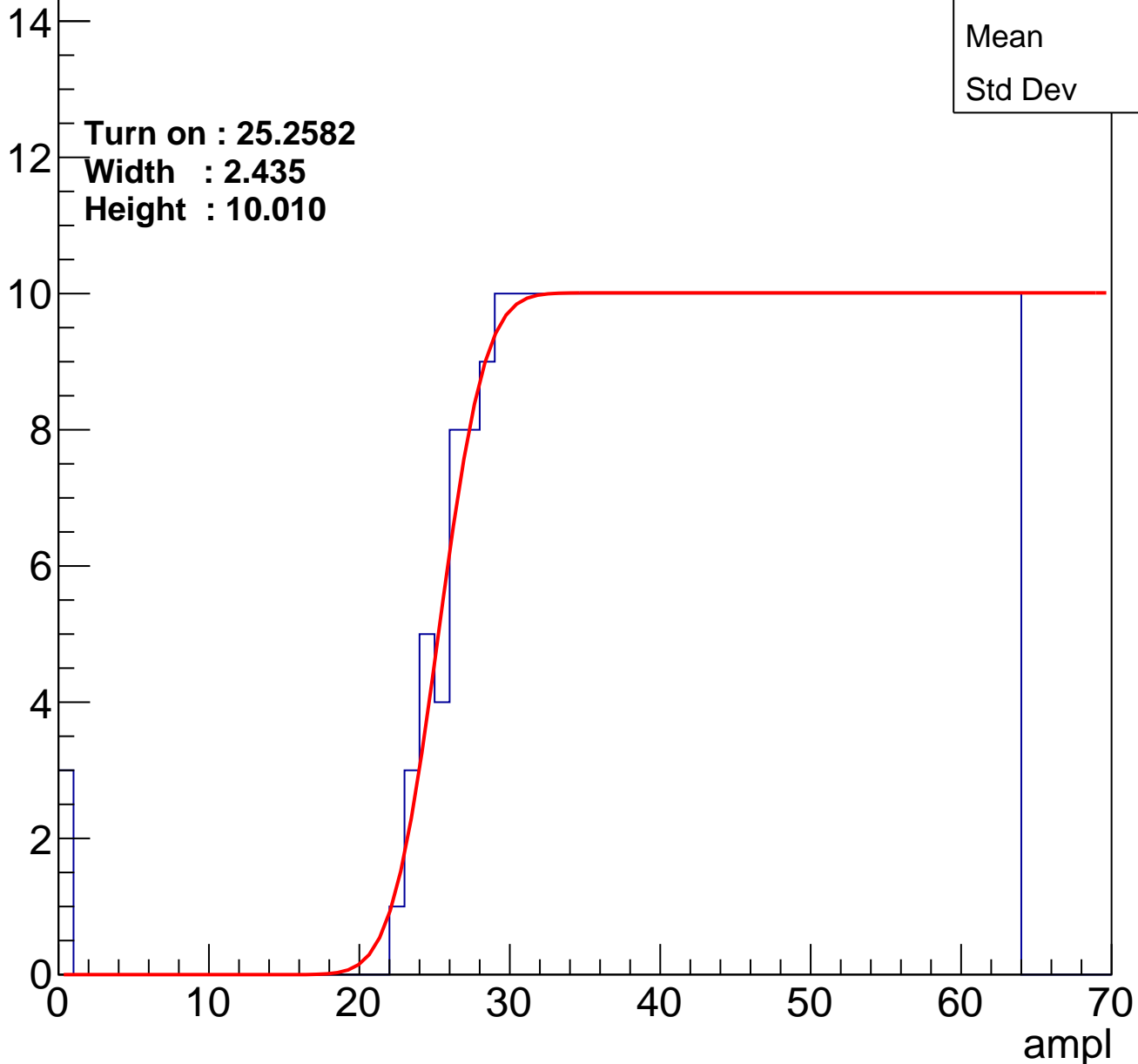
Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.2582

Width : 2.435

Height : 10.010

Entry



B1L003S, U16-ch35

calib_packv5_042523_0143.root, FC#13, port D2

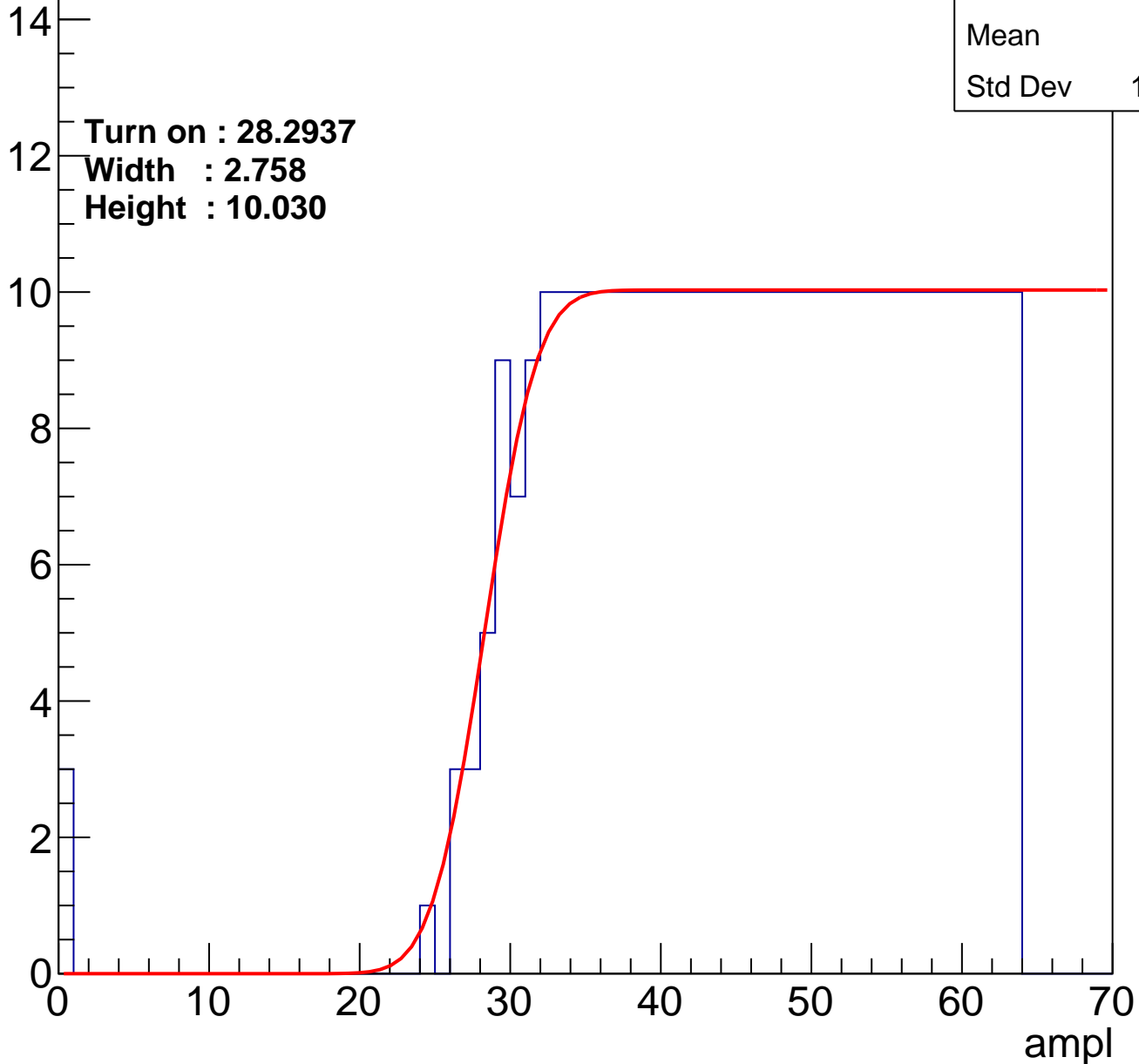
Entries	360
Mean	45.2
Std Dev	11.17

Turn on : 28.2937

Width : 2.758

Height : 10.030

Entry



calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.07
Std Dev	11.07

Mean	45.07
------	-------

Std Dev	11.07
---------	-------

Width : 2.903

Height : 10.068



B1L003S, U16-ch37

calib_packv5_042523_0143.root, FC#13, port D2

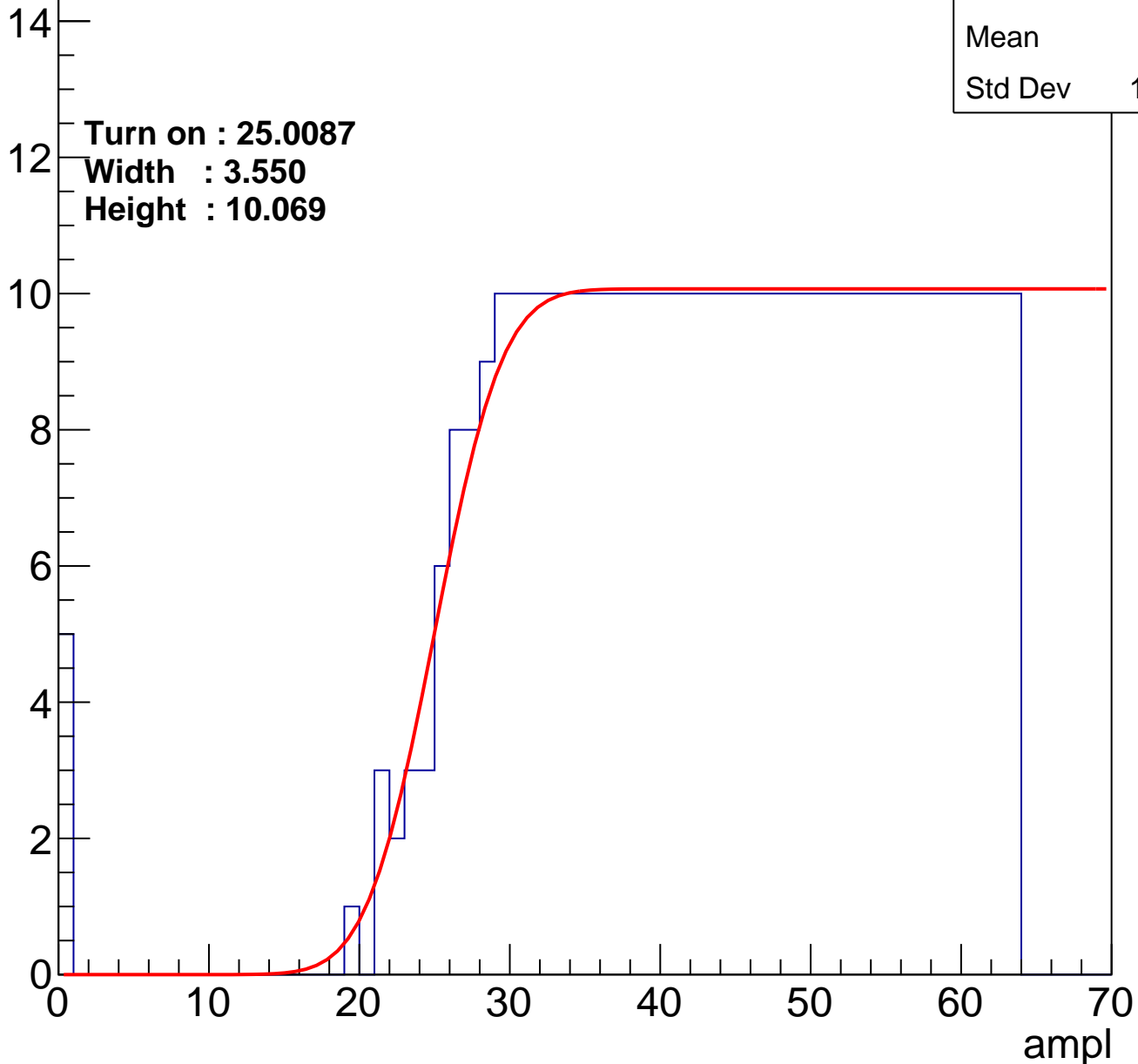
Entries	398
Mean	43.2
Std Dev	12.44

Turn on : 25.0087

Width : 3.550

Height : 10.069

Entry



B1L003S, U16-ch38

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.75
Std Dev	11.64

Turn on : 25.1941

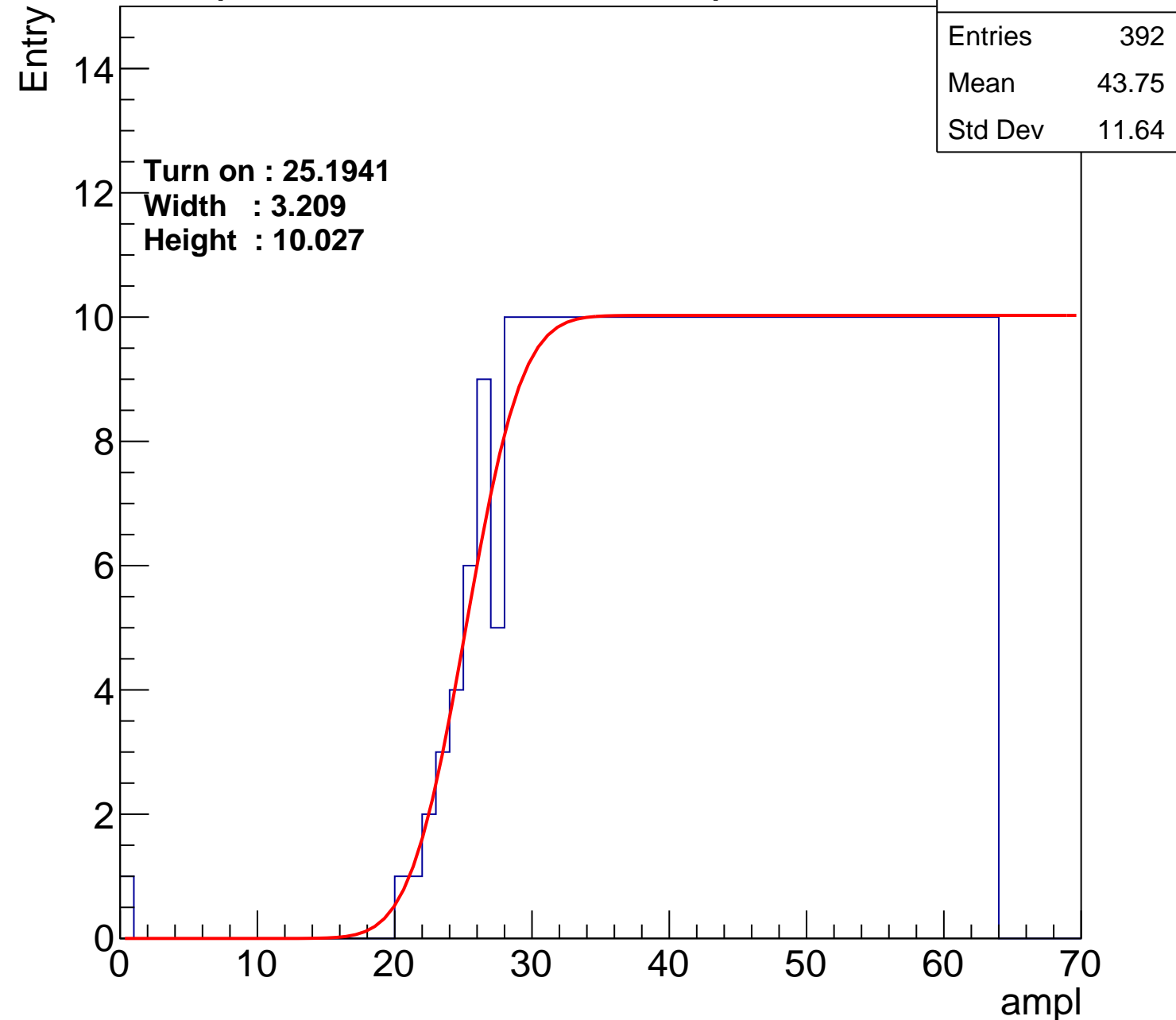
Width : 3.209

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch39

calib_packv5_042523_0143.root, FC#13, port D2

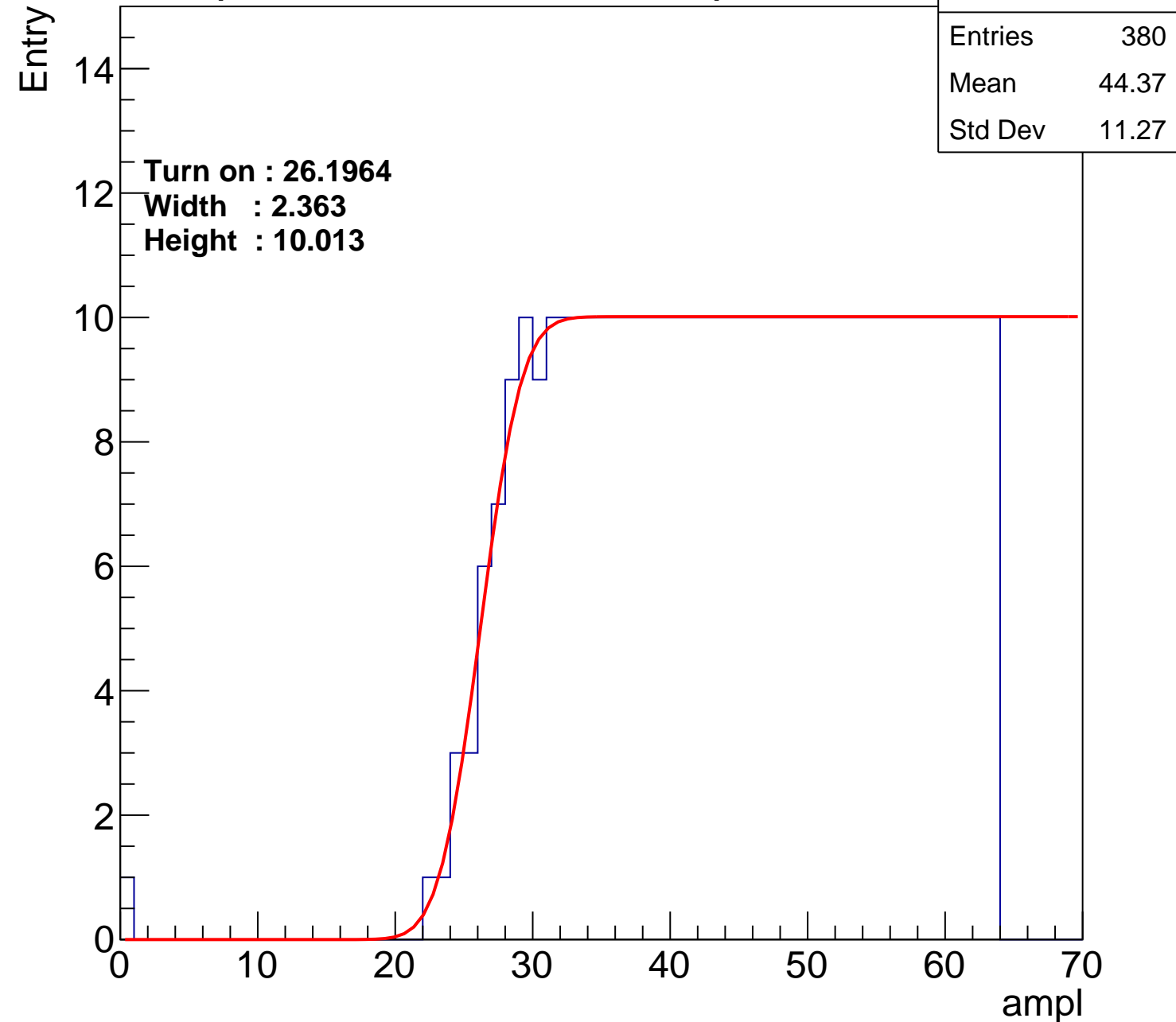
Entry

14
12
10
8
6
4
2
0

Turn on : 26.1964
Width : 2.363
Height : 10.013

Entries	380
Mean	44.37
Std Dev	11.27

ampl



B1L003S, U16-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 27.1678

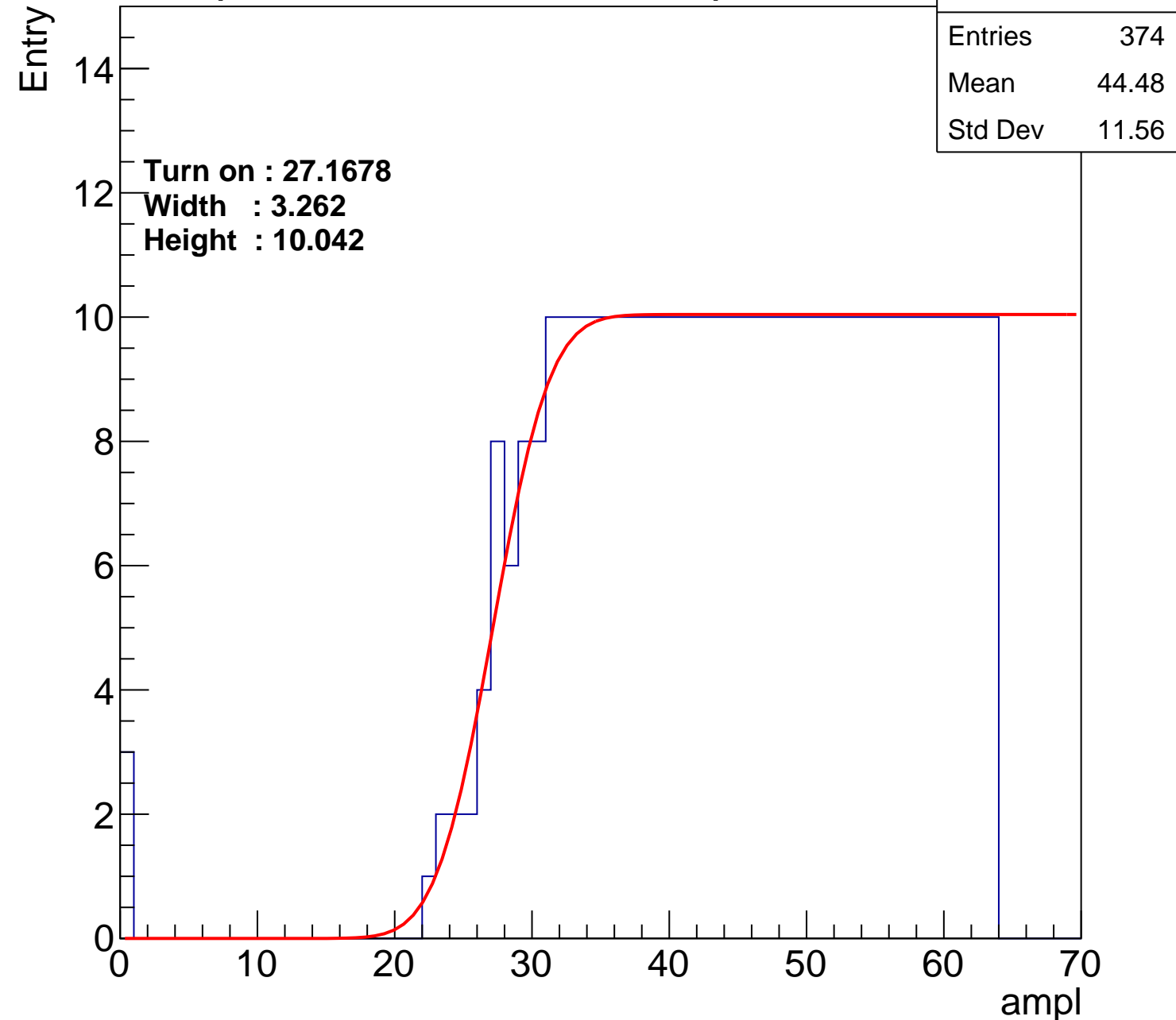
Width : 3.262

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.3
Std Dev	10.99

Turn on : 29.0969

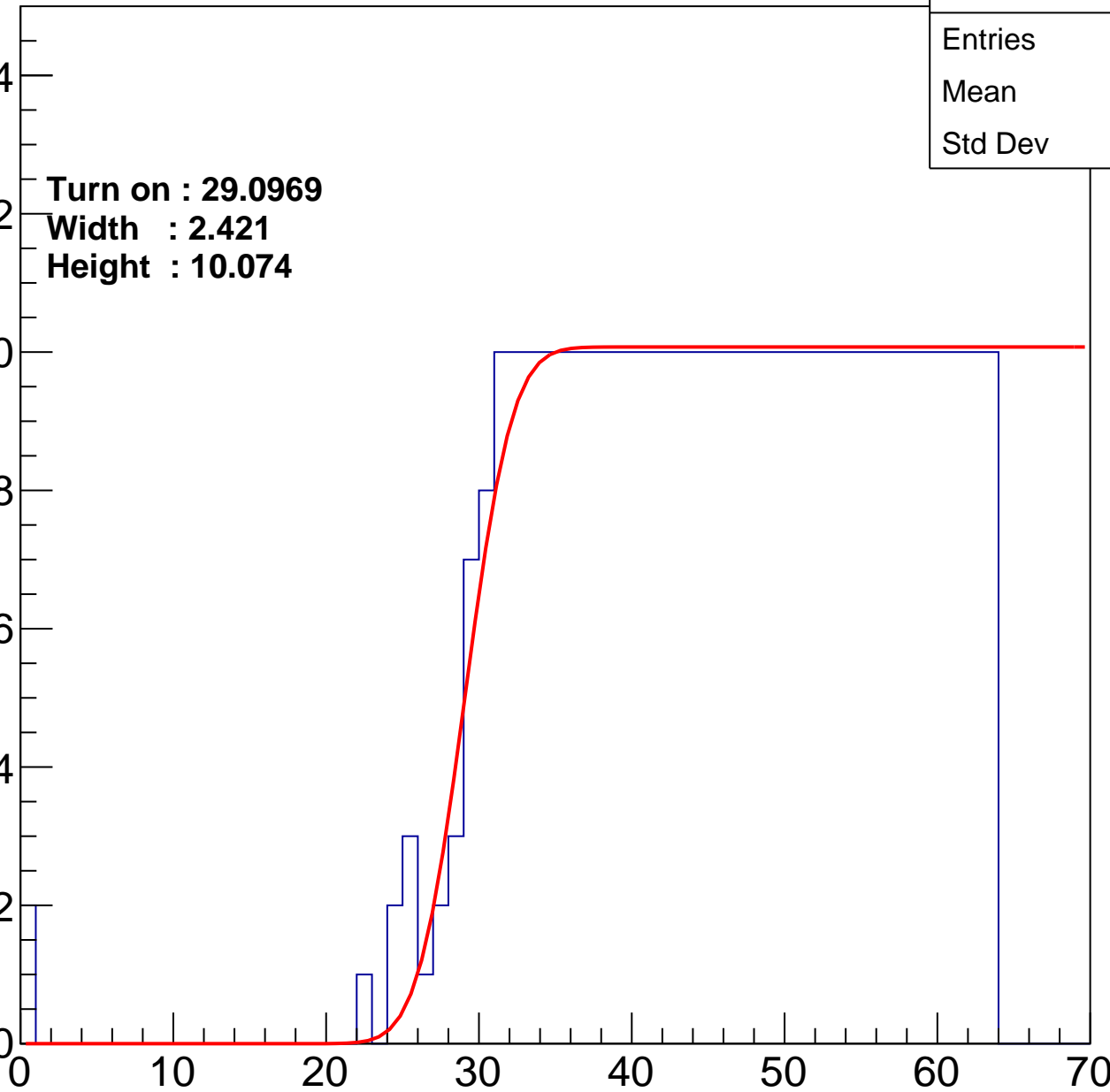
Width : 2.421

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch42

calib_packv5_042523_0143.root, FC#13, port D2

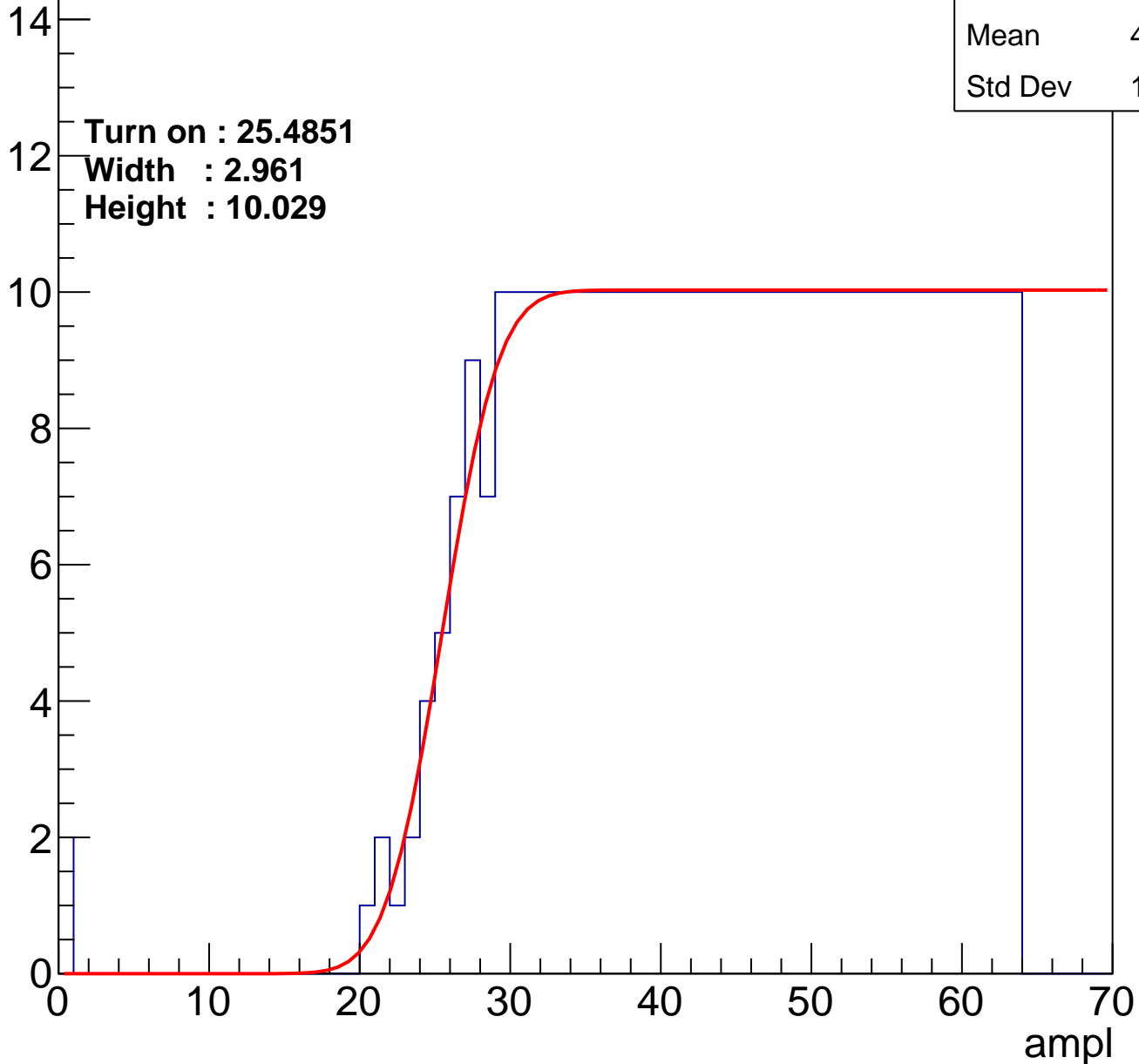
Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.4851

Width : 2.961

Height : 10.029

Entry



B1L003S, U16-ch43

calib_packv5_042523_0143.root, FC#13, port D2

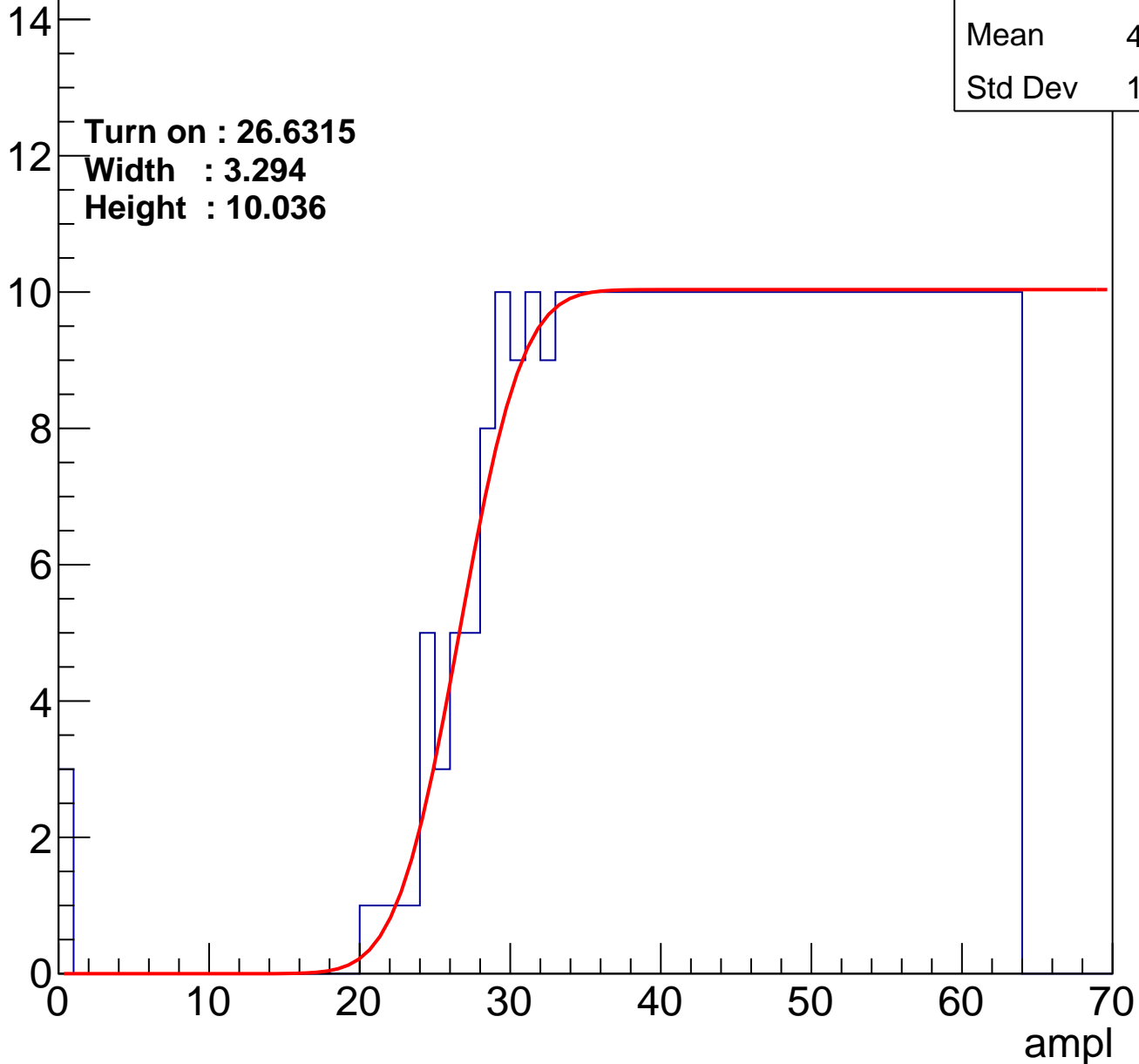
Entries	381
Mean	44.12
Std Dev	11.77

Turn on : 26.6315

Width : 3.294

Height : 10.036

Entry



B1L003S, U16-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	44.91
Std Dev	11.62

Turn on : 28.9419

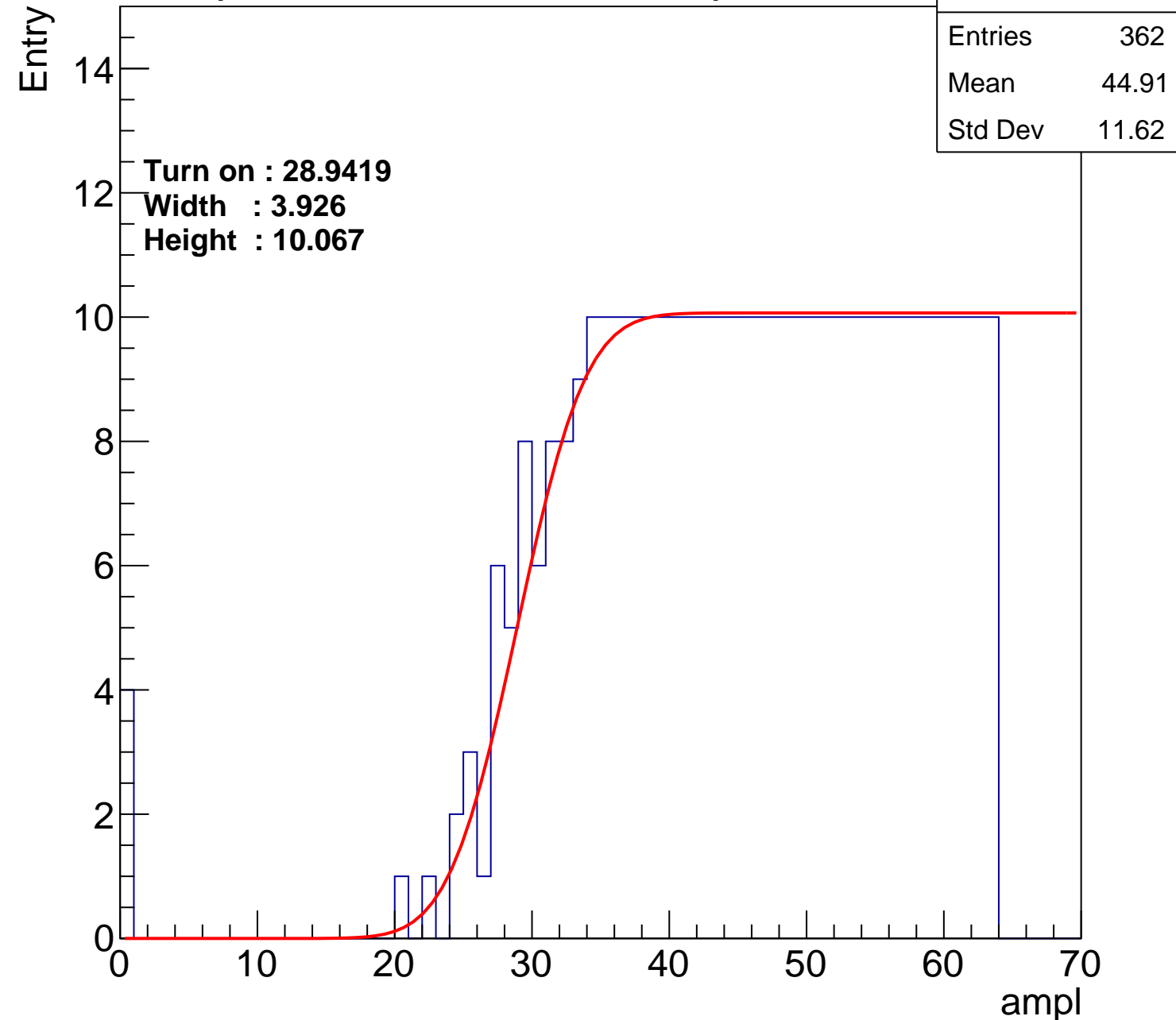
Width : 3.926

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 25.5427

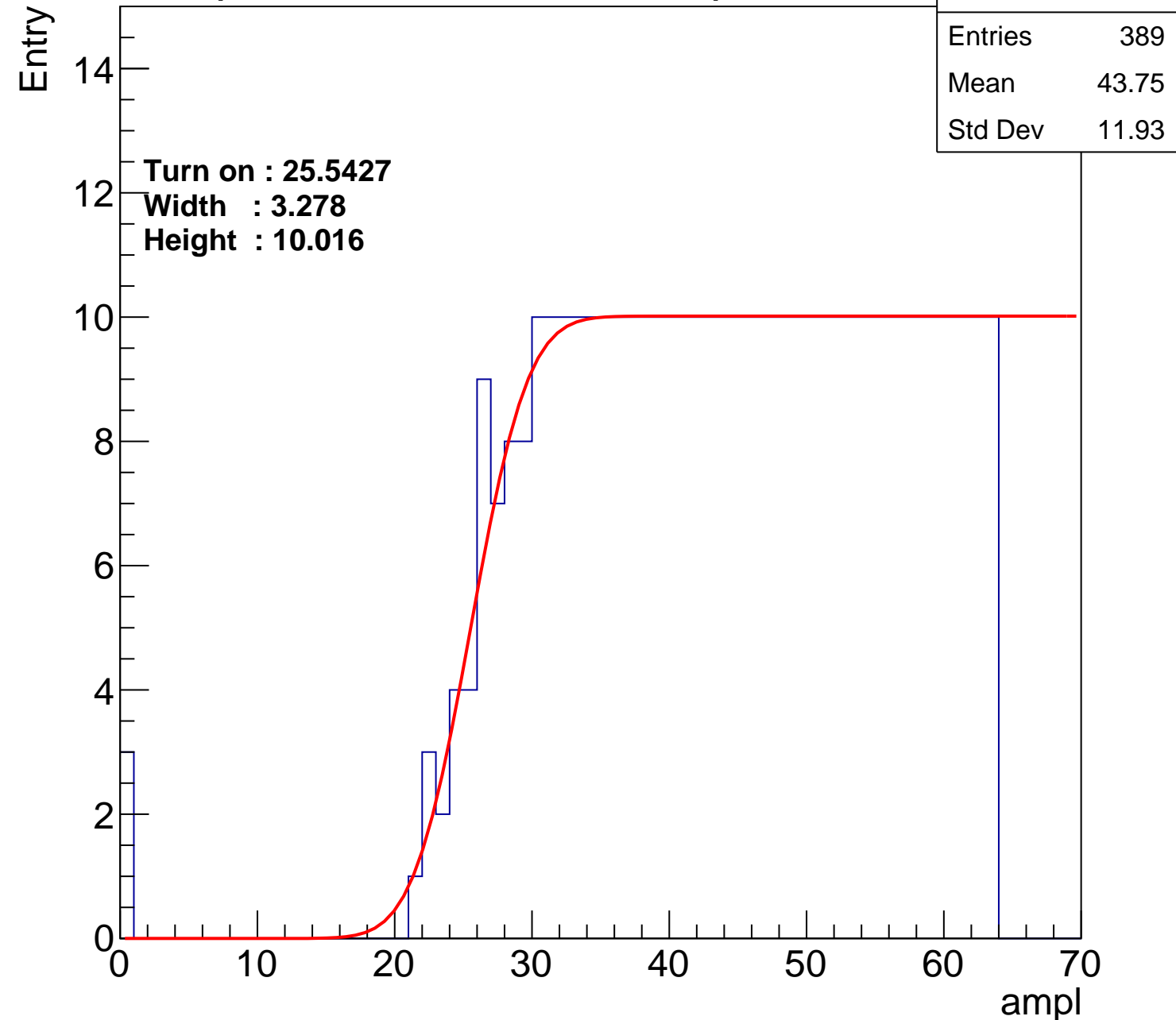
Width : 3.278

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch46

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.78
Std Dev	11.48

Turn on : 28.1093

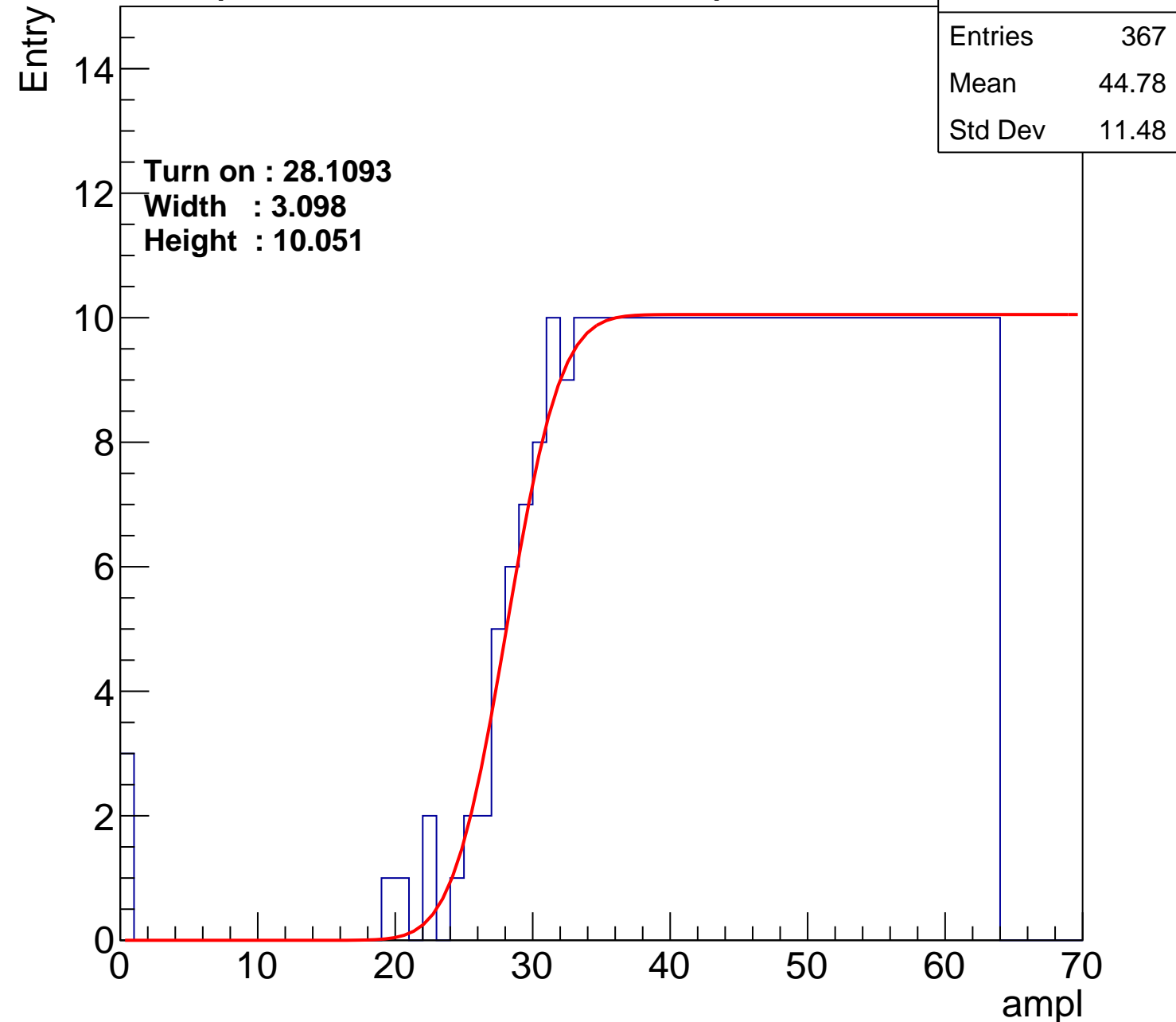
Width : 3.098

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch47

calib_packv5_042523_0143.root, FC#13, port D2

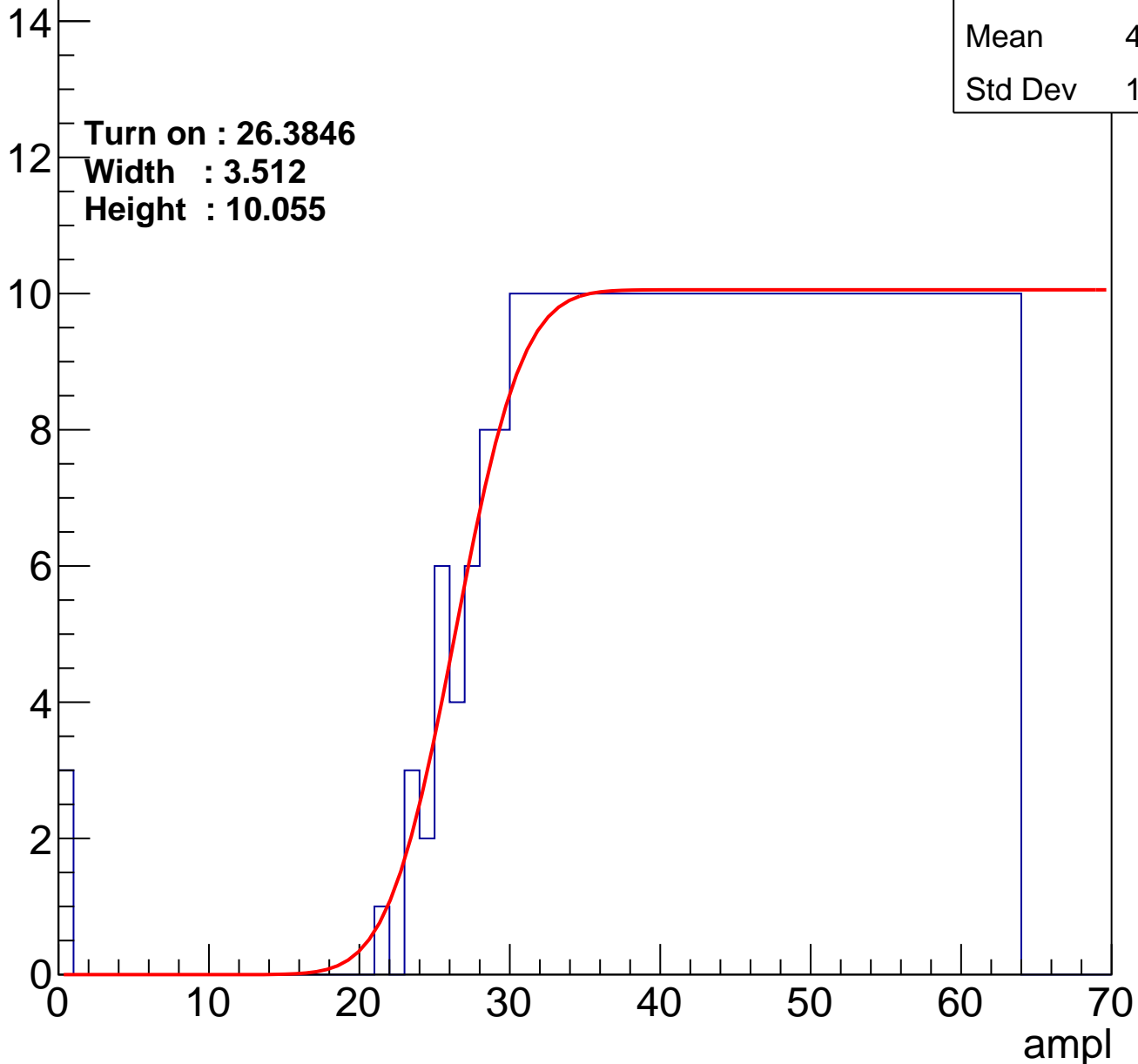
Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.3846

Width : 3.512

Height : 10.055

Entry



B1L003S, U16-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.36
Std Dev	11.64

Turn on : 26.4121

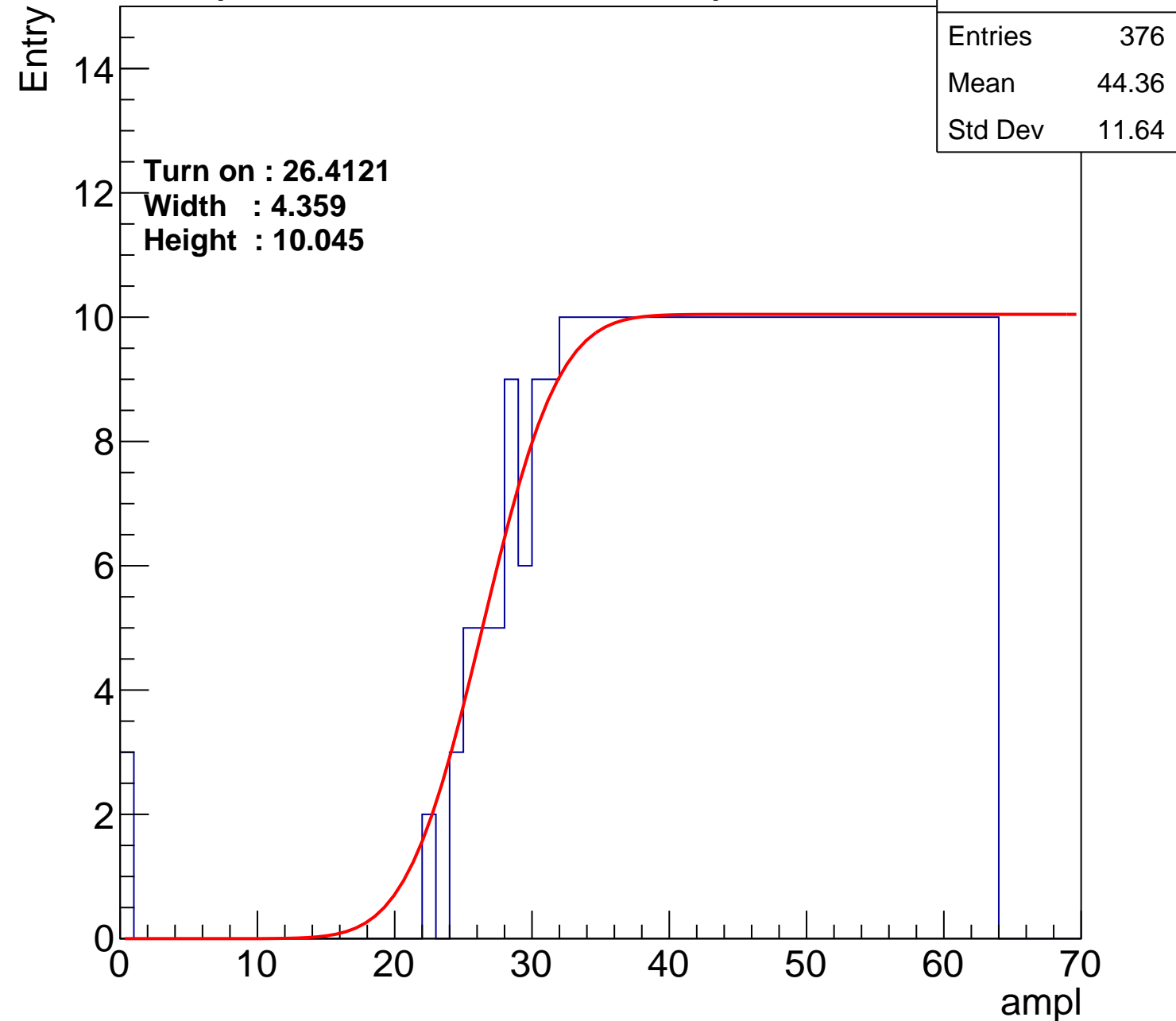
Width : 4.359

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.47
Std Dev	10.97

Turn on : 29.1983

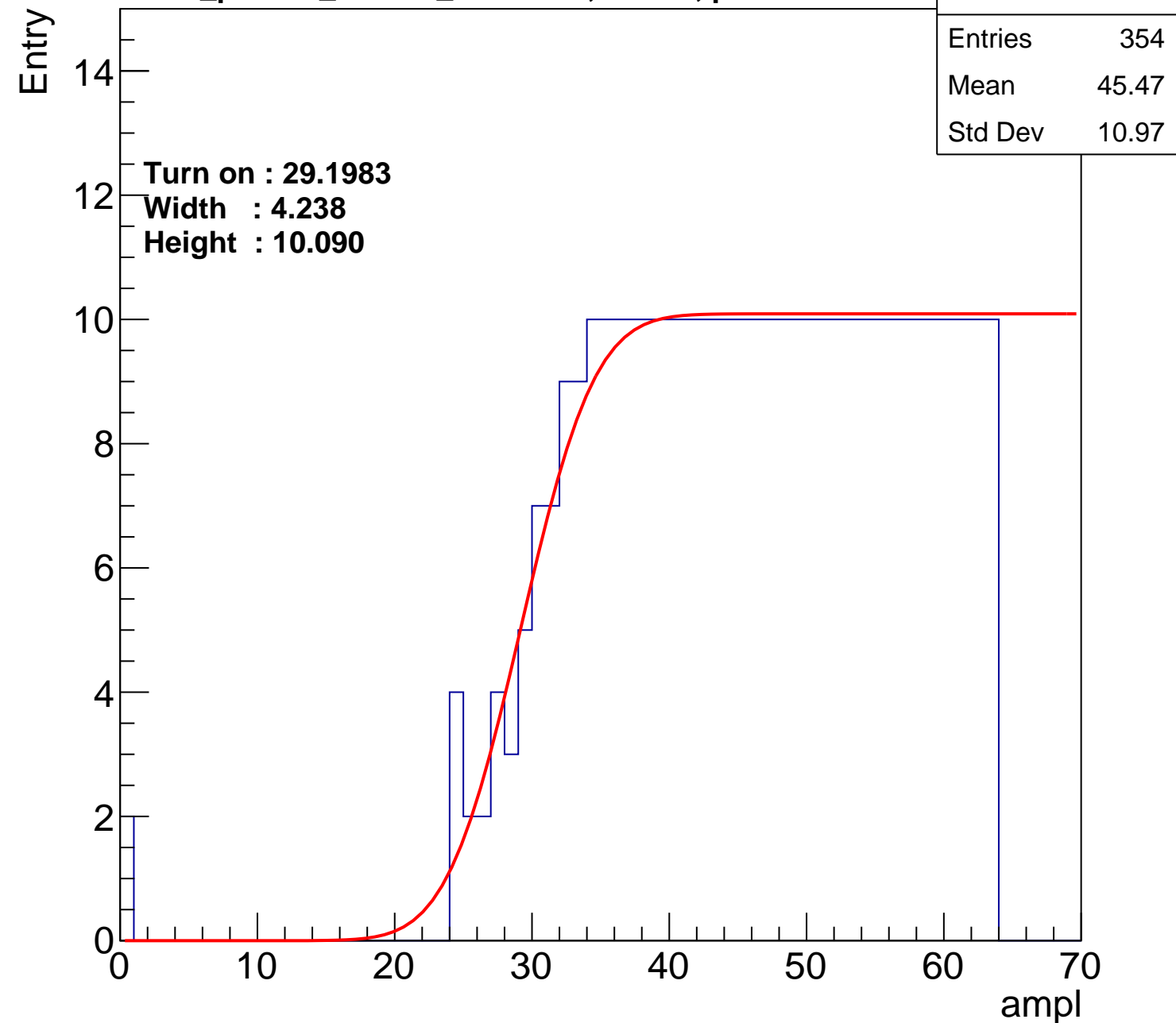
Width : 4.238

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch50

calib_packv5_042523_0143.root, FC#13, port D2

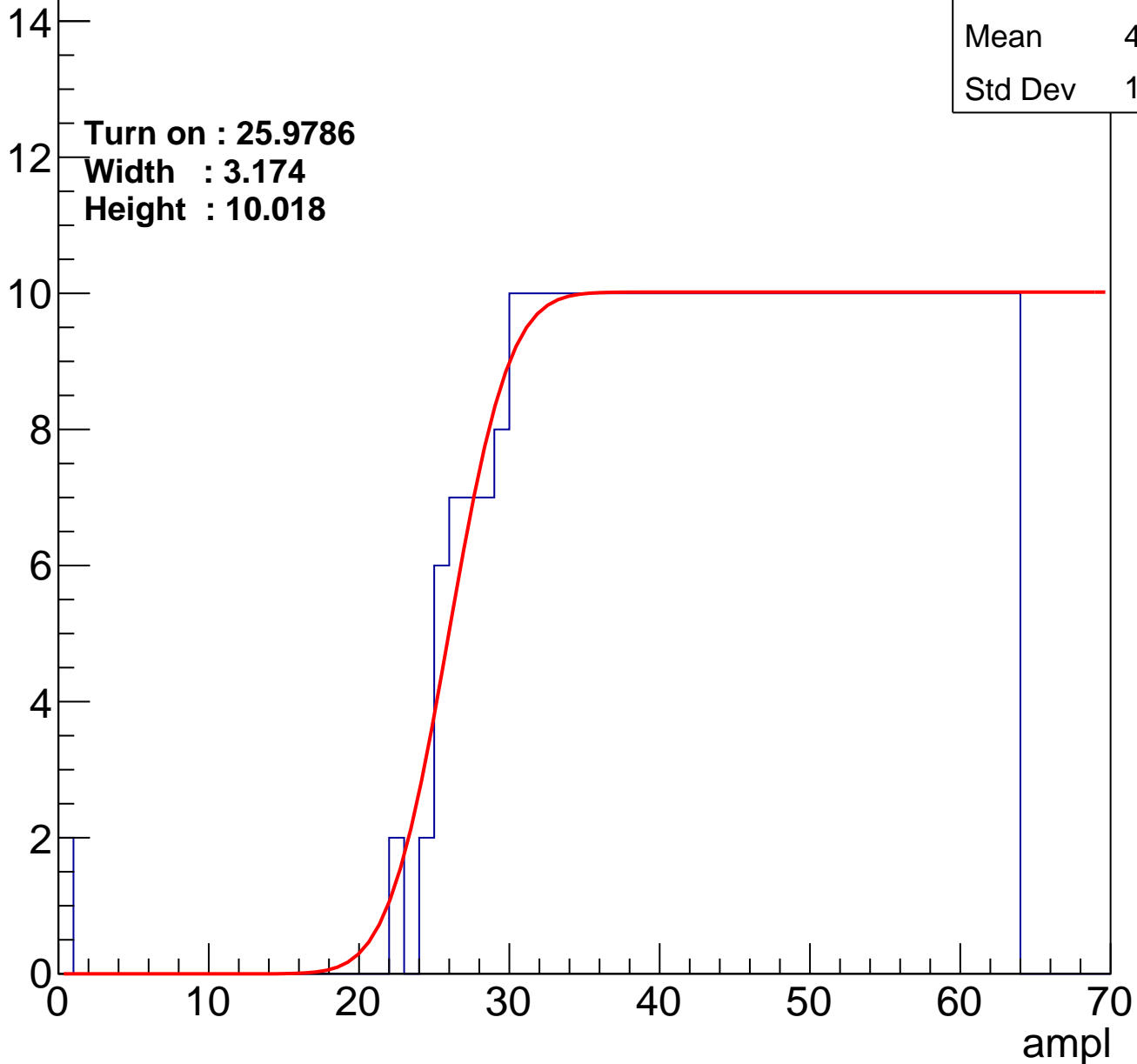
Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 25.9786

Width : 3.174

Height : 10.018

Entry



B1L003S, U16-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	44.94
Std Dev	11.66

Turn on : 28.7596

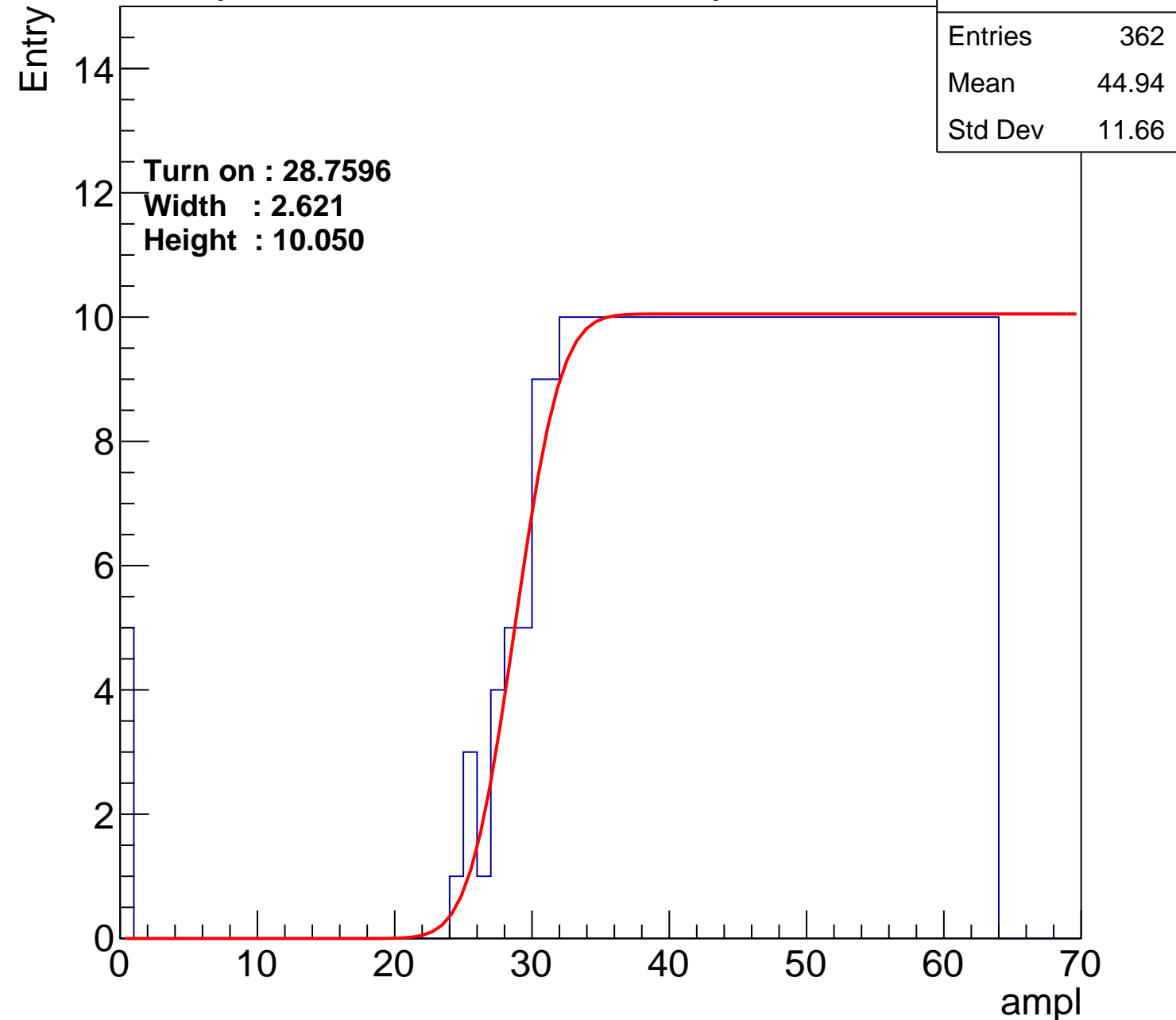
Width : 2.621

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.7235

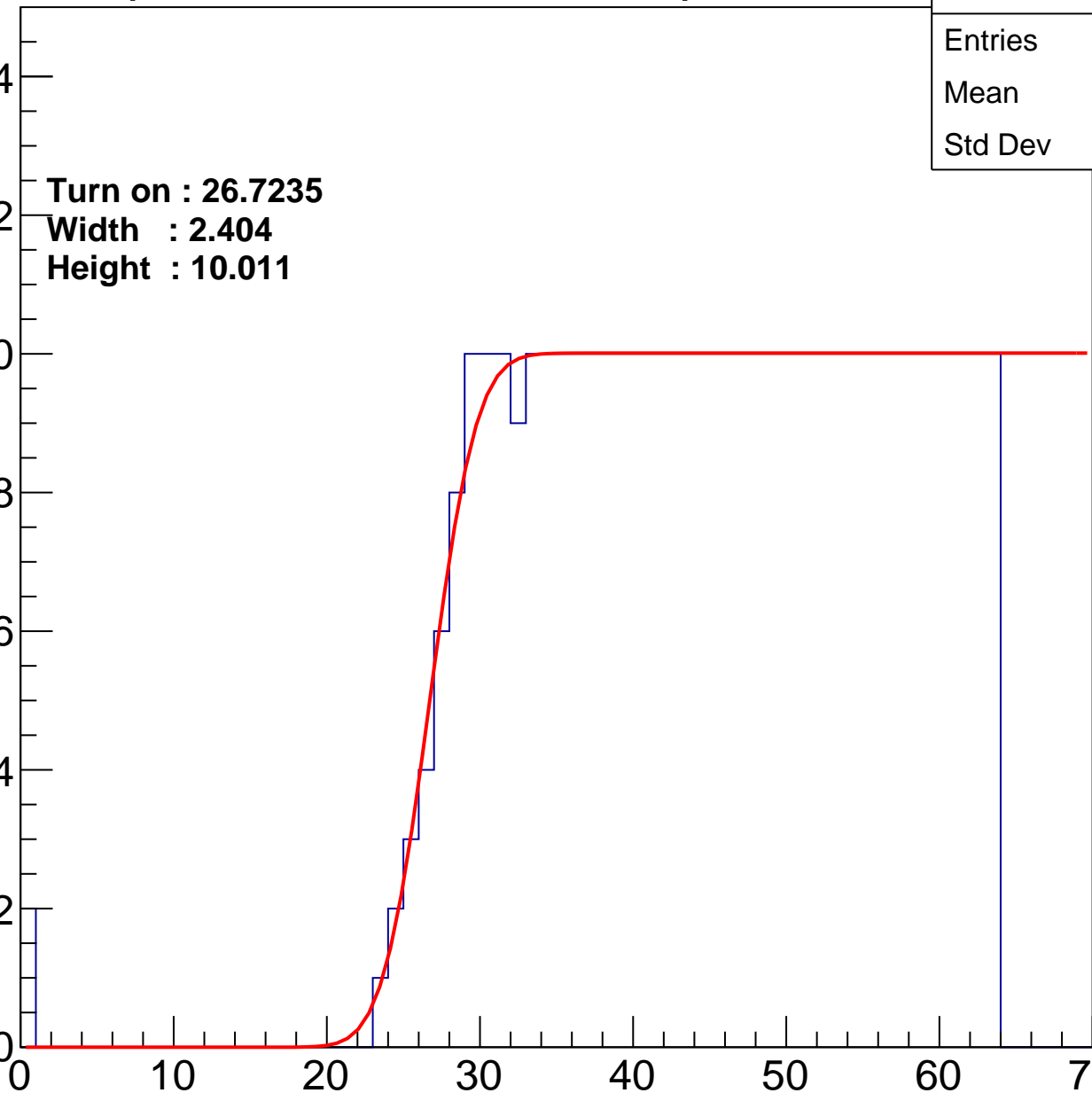
Width : 2.404

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch53

calib_packv5_042523_0143.root, FC#13, port D2

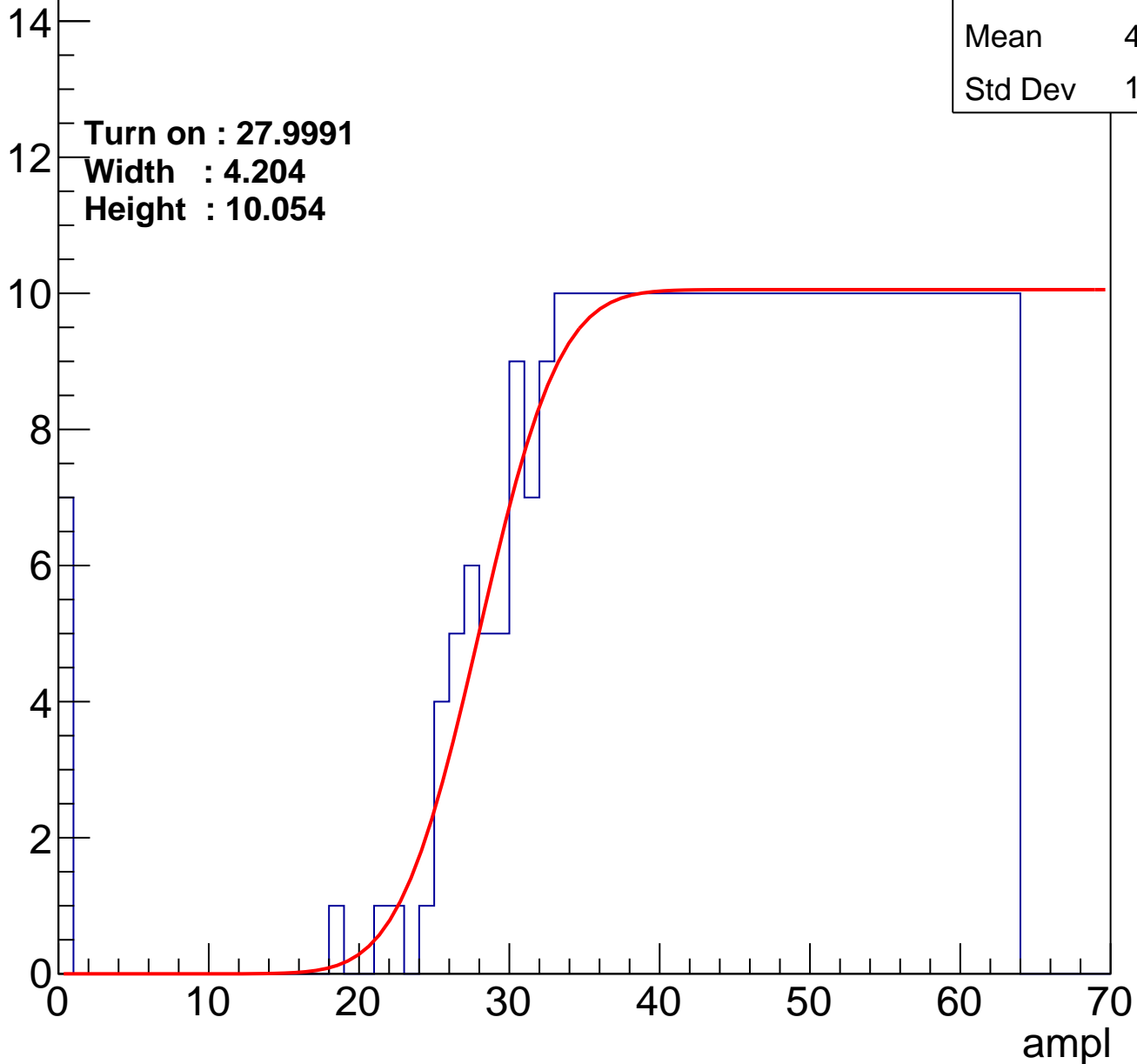
Entries	371
Mean	44.25
Std Dev	12.38

Turn on : 27.9991

Width : 4.204

Height : 10.054

Entry



B1L003S, U16-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 27.4480

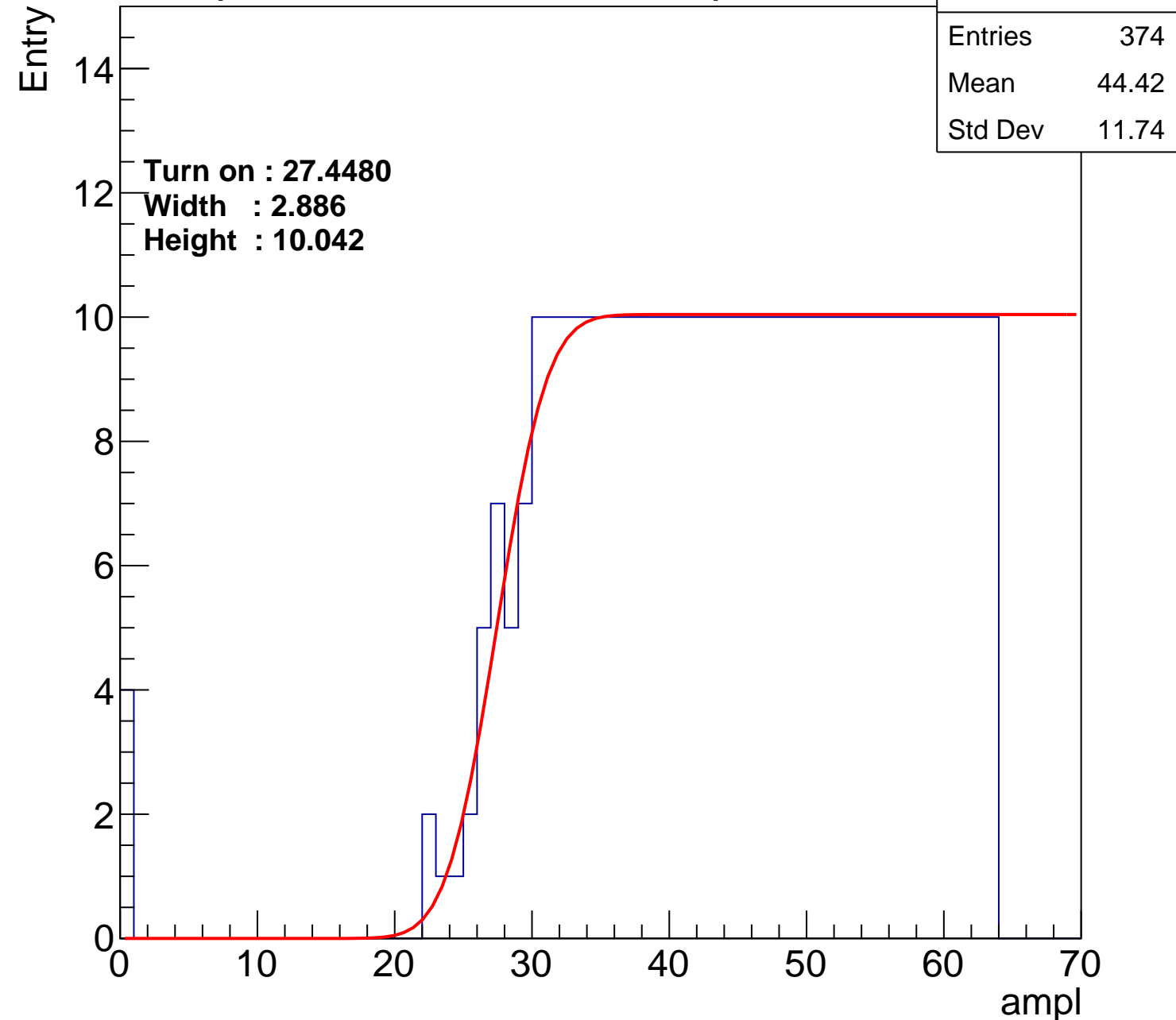
Width : 2.886

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch55

calib_packv5_042523_0143.root, FC#13, port D2

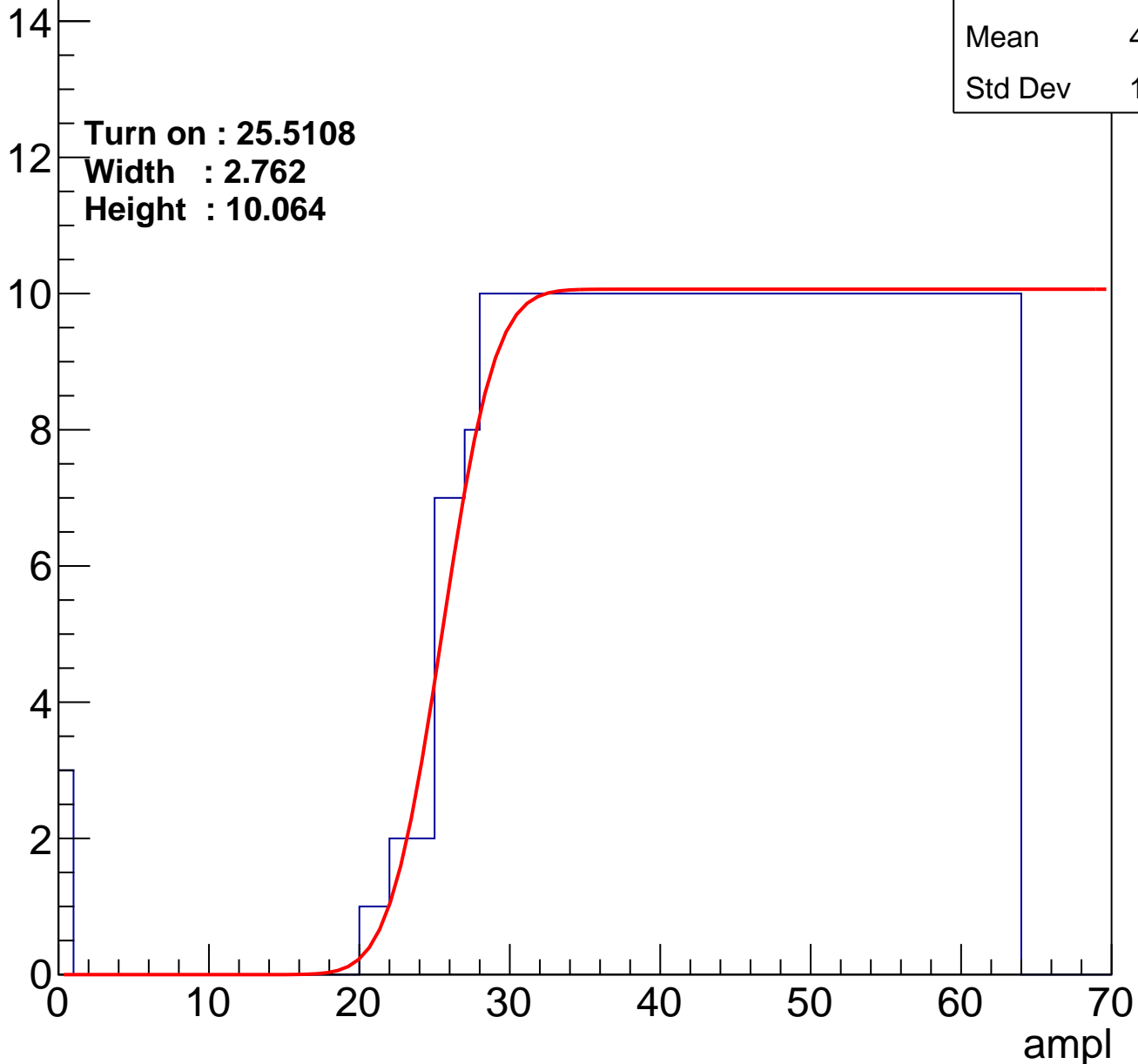
Entries	393
Mean	43.59
Std Dev	11.97

Turn on : 25.5108

Width : 2.762

Height : 10.064

Entry



B1L003S, U16-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.52
Std Dev	11.3

Turn on : 26.8837

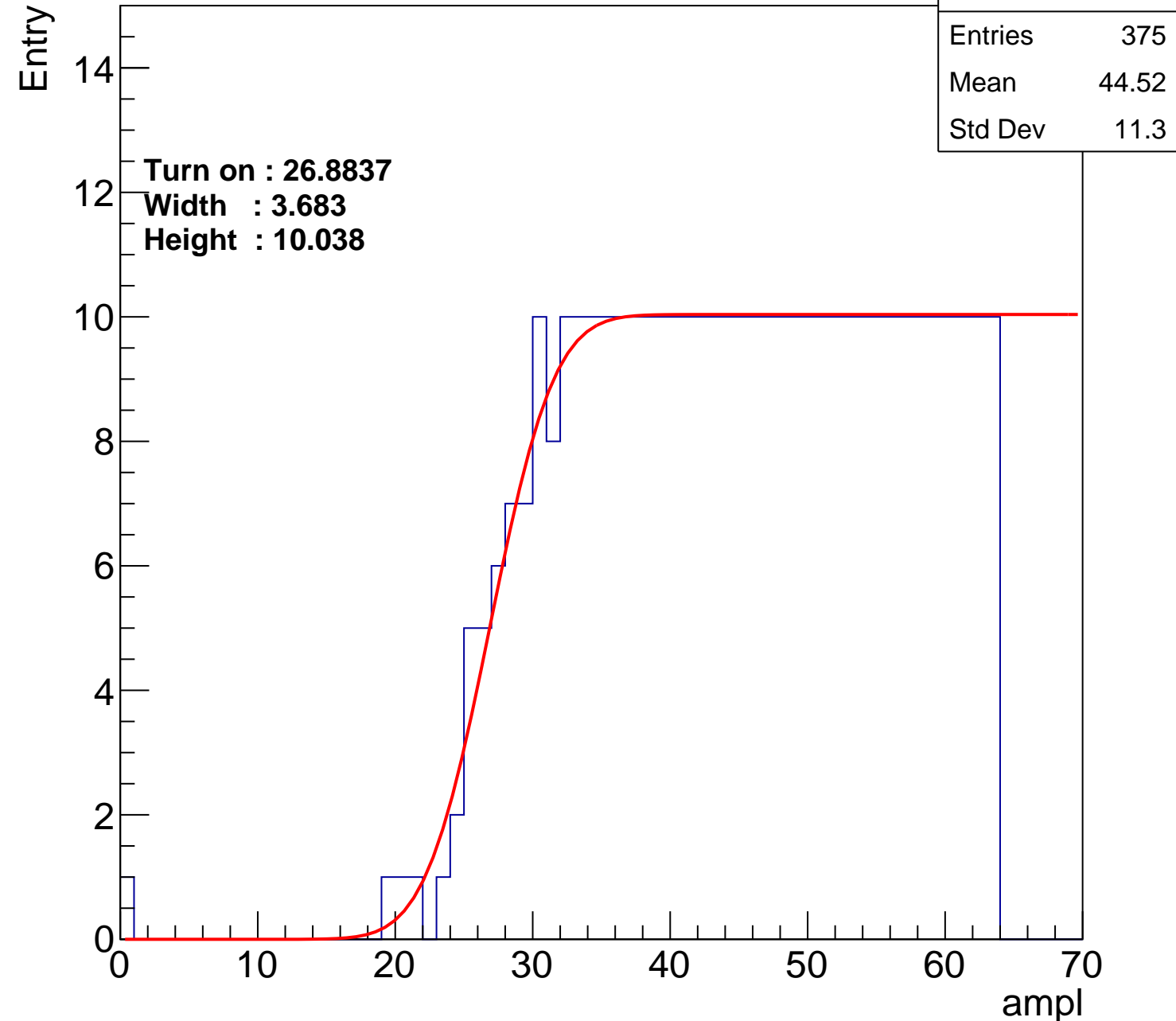
Width : 3.683

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44
Std Dev	11.92

Turn on : 26.2902

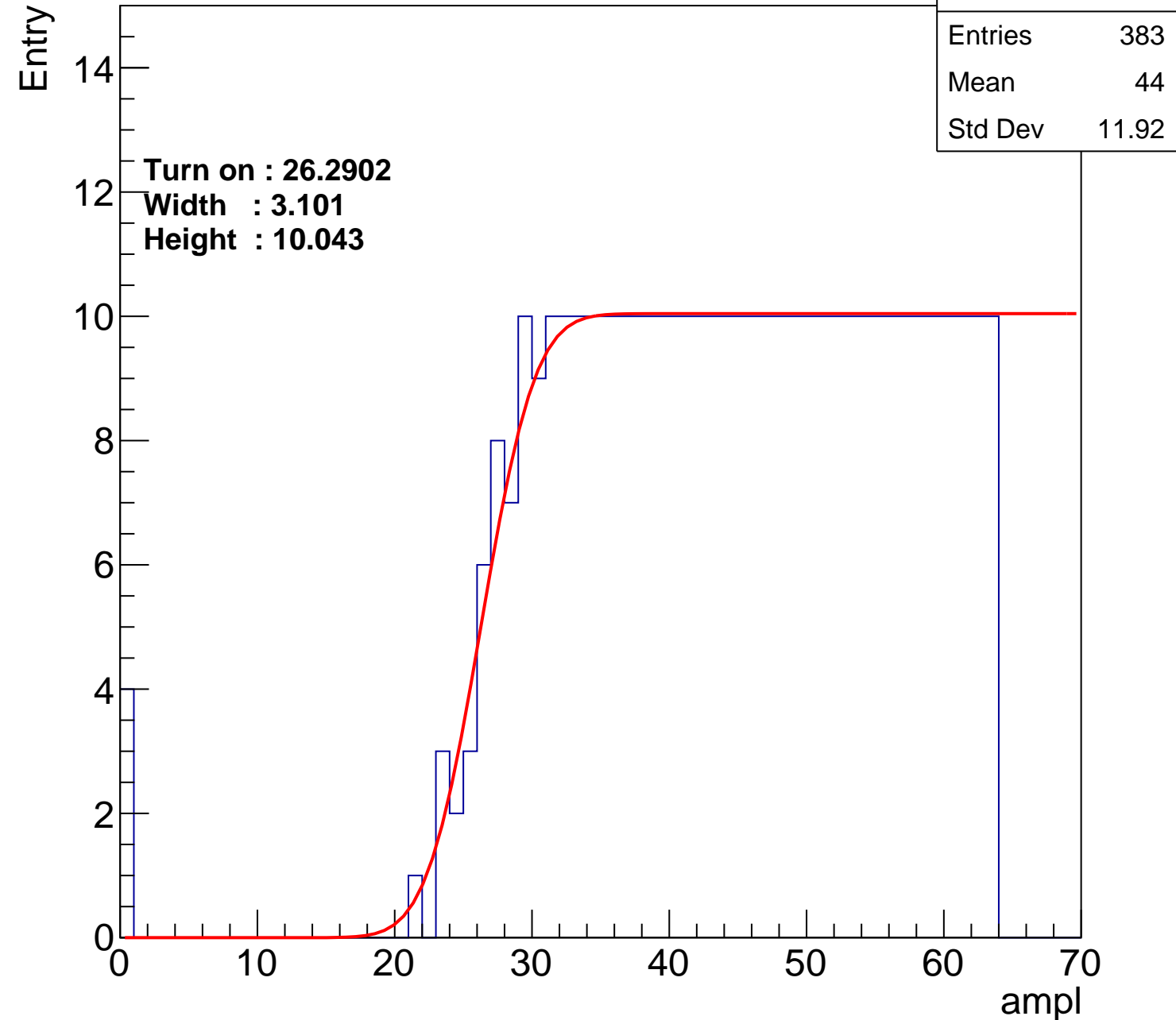
Width : 3.101

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.92
Std Dev	11.31

Turn on : 28.1033

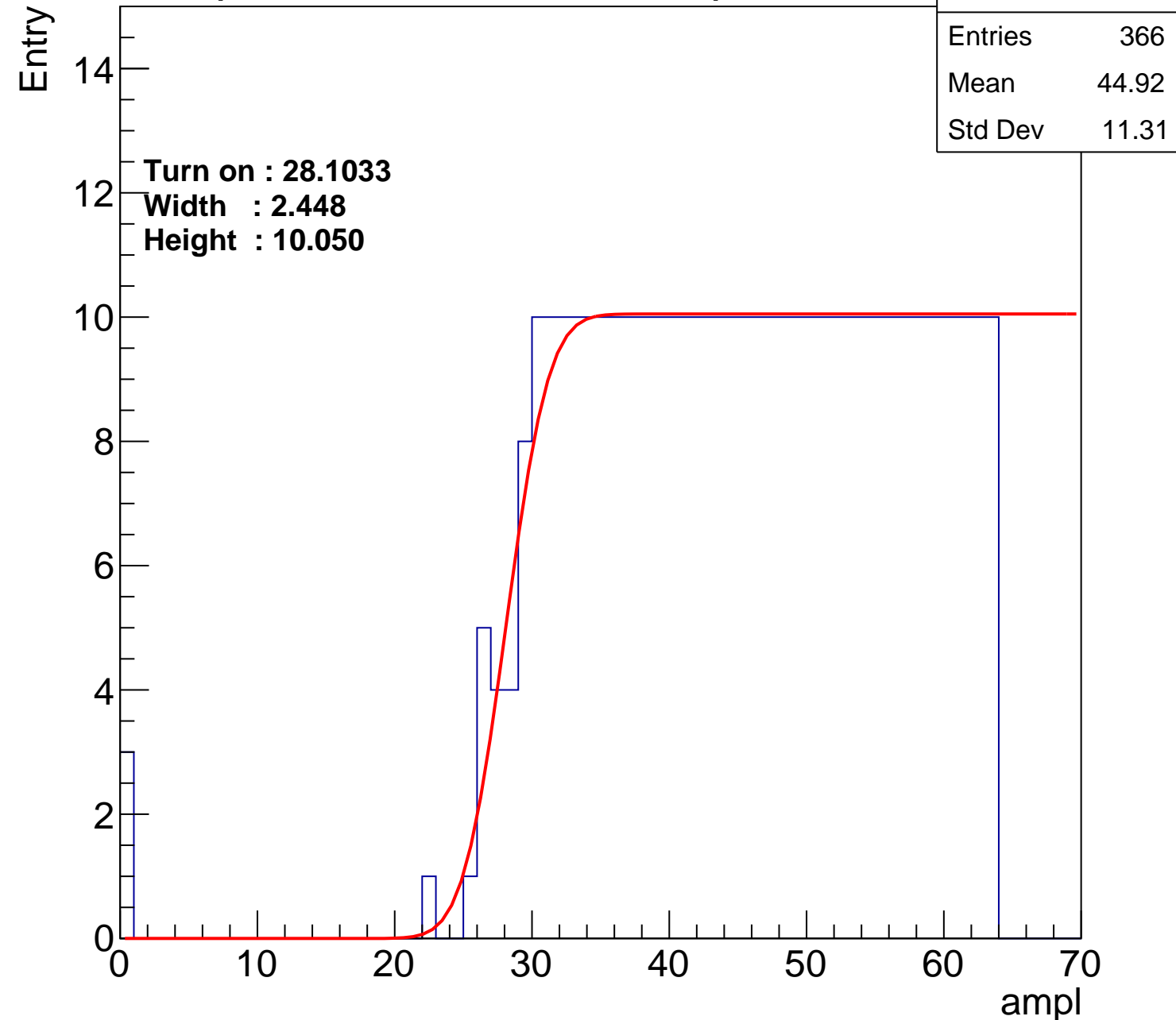
Width : 2.448

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch59

calib_packv5_042523_0143.root, FC#13, port D2

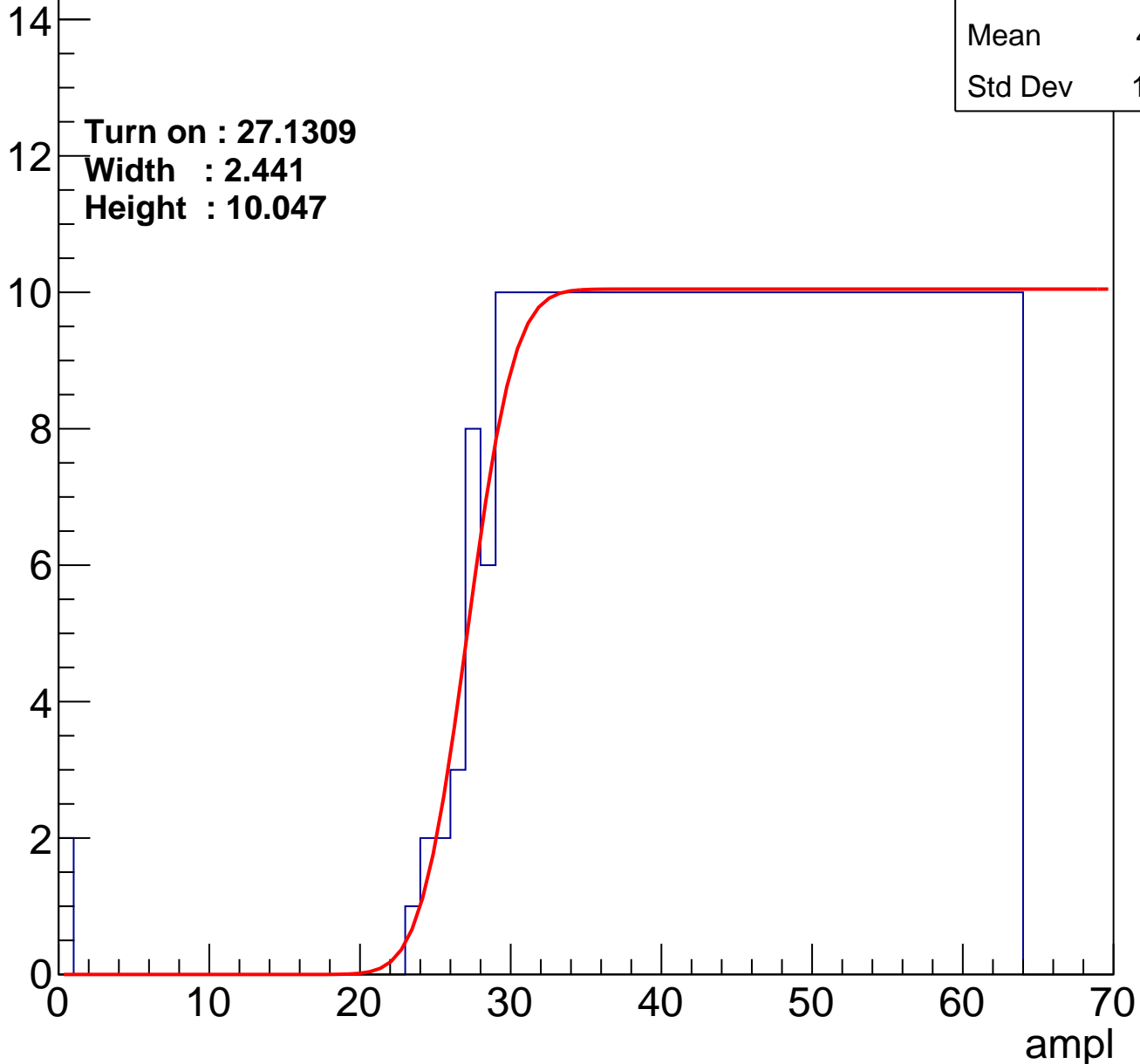
Entries	374
Mean	44.61
Std Dev	11.28

Turn on : 27.1309

Width : 2.441

Height : 10.047

Entry



B1L003S, U16-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.97
Std Dev	11.78

Turn on : 25.6472

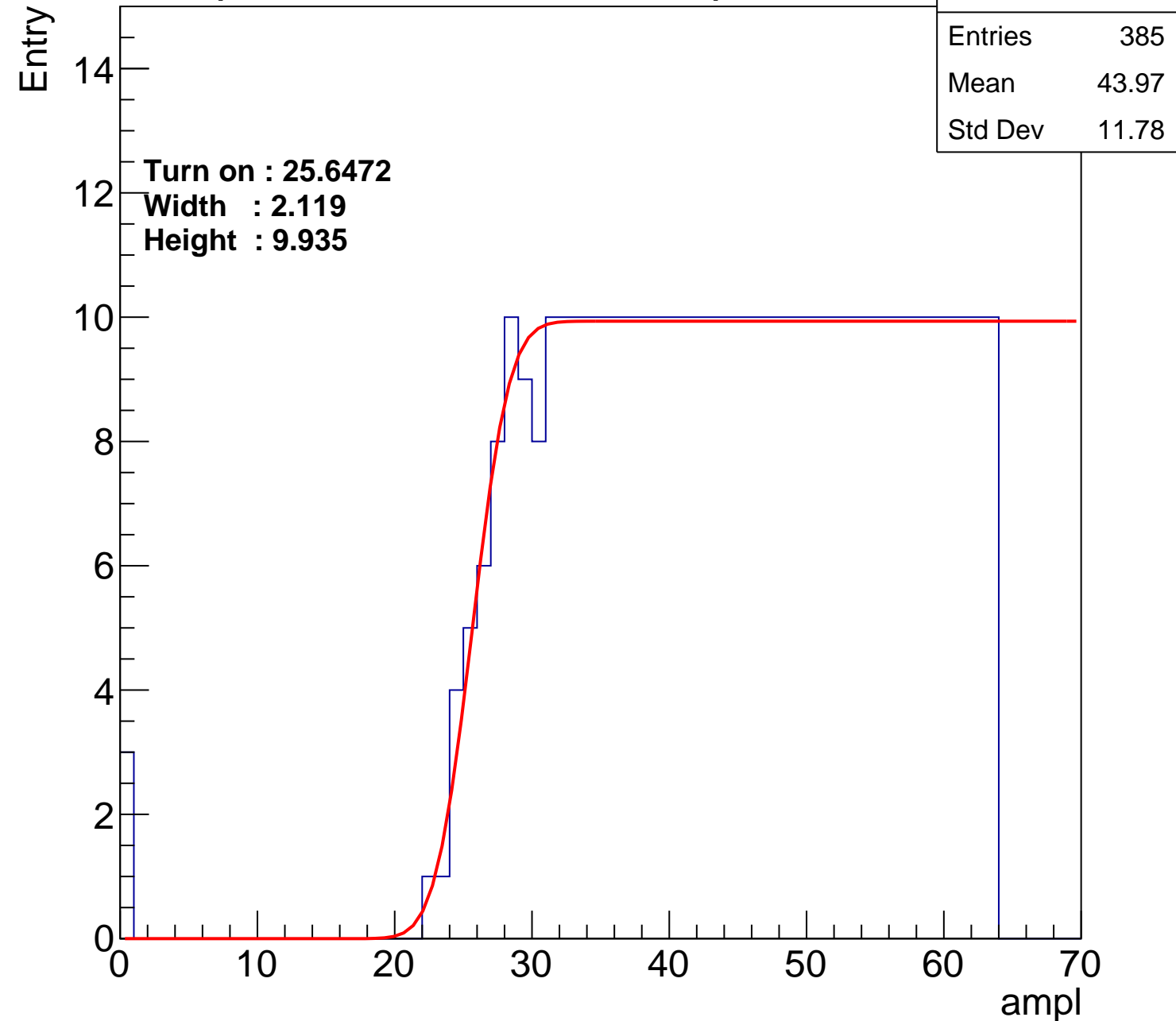
Width : 2.119

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.25
Std Dev	11.54

Turn on : 27.0651

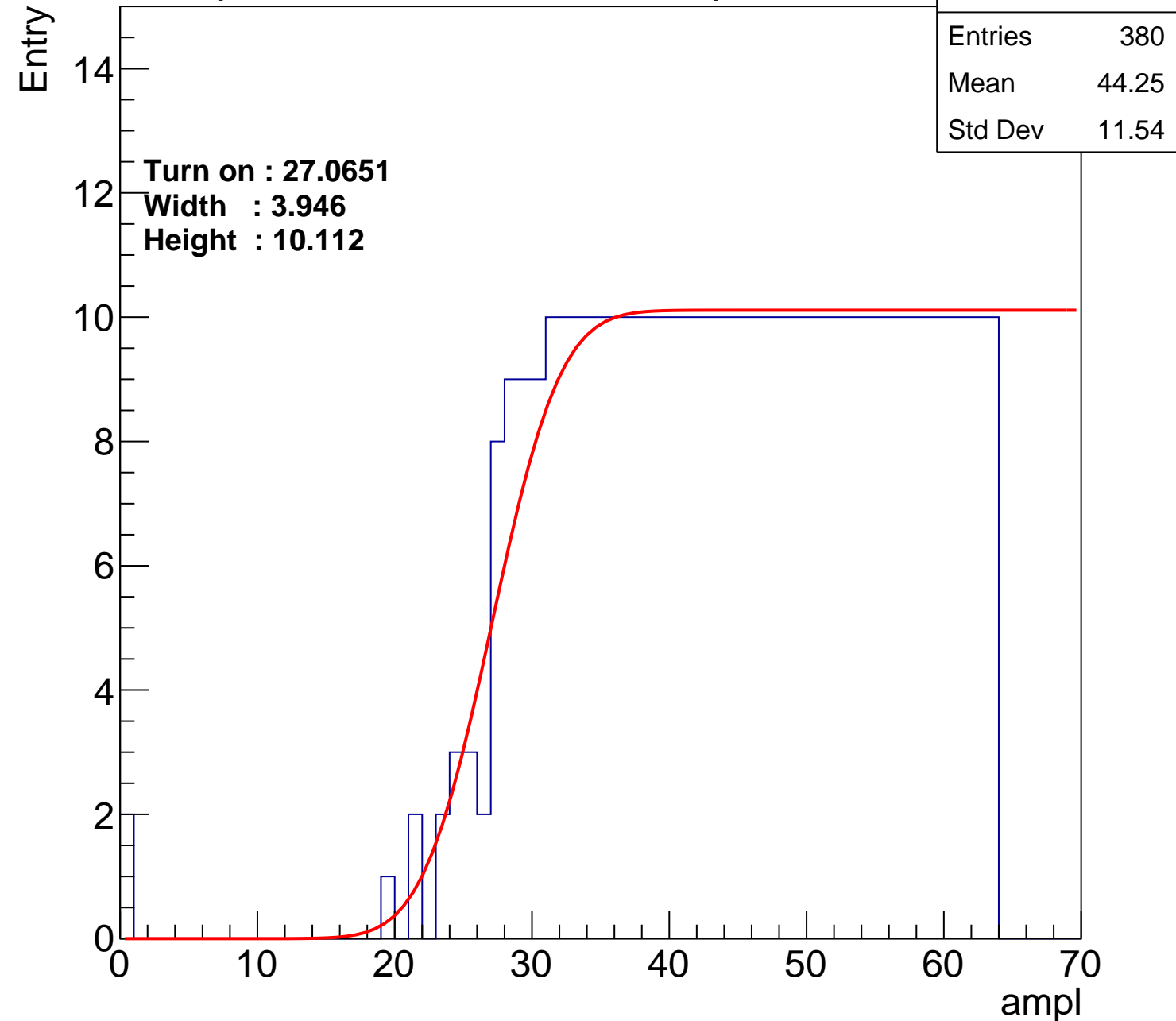
Width : 3.946

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.63
Std Dev	11.36

Turn on : 27.6202

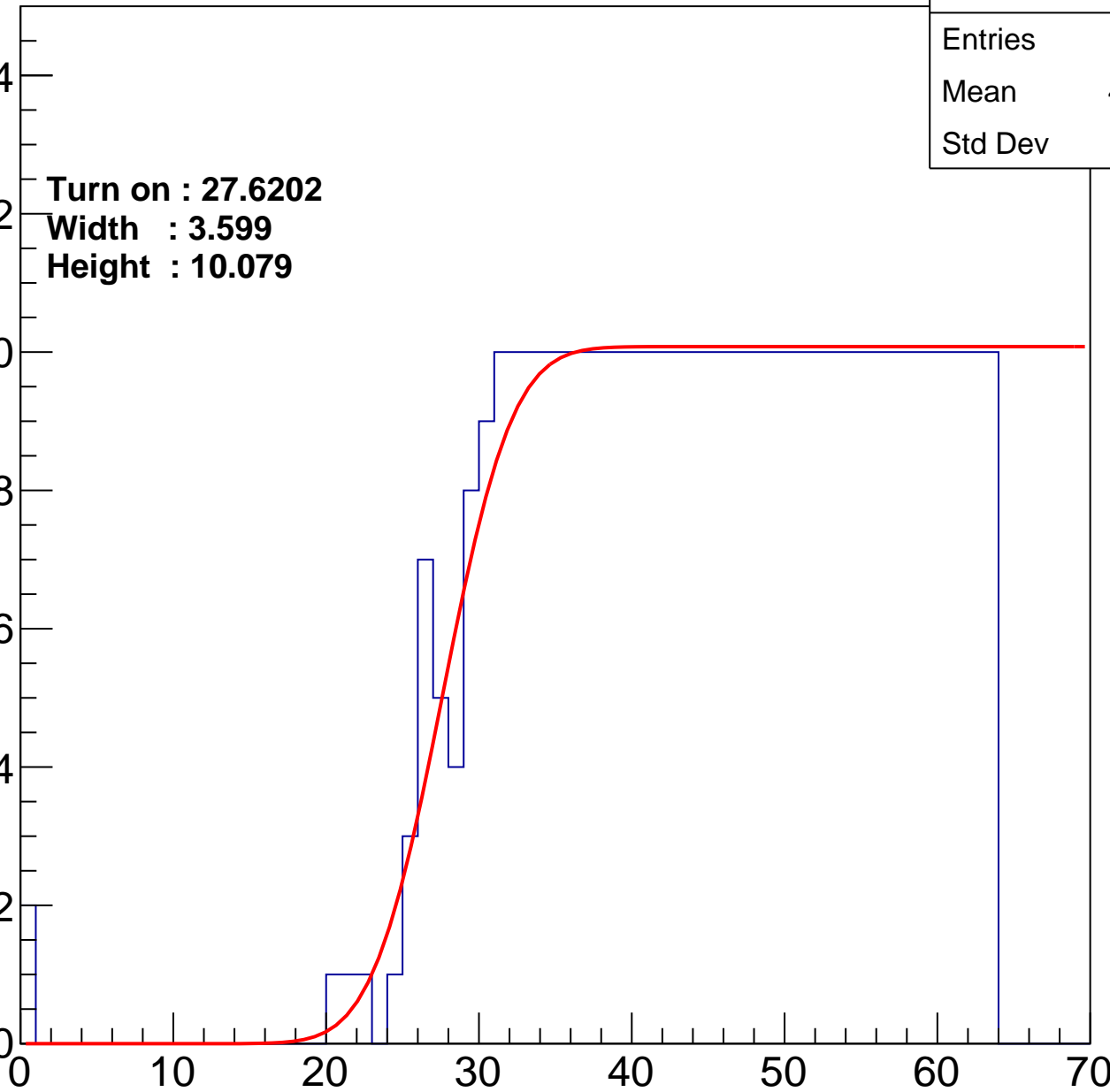
Width : 3.599

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch63

calib_packv5_042523_0143.root, FC#13, port D2

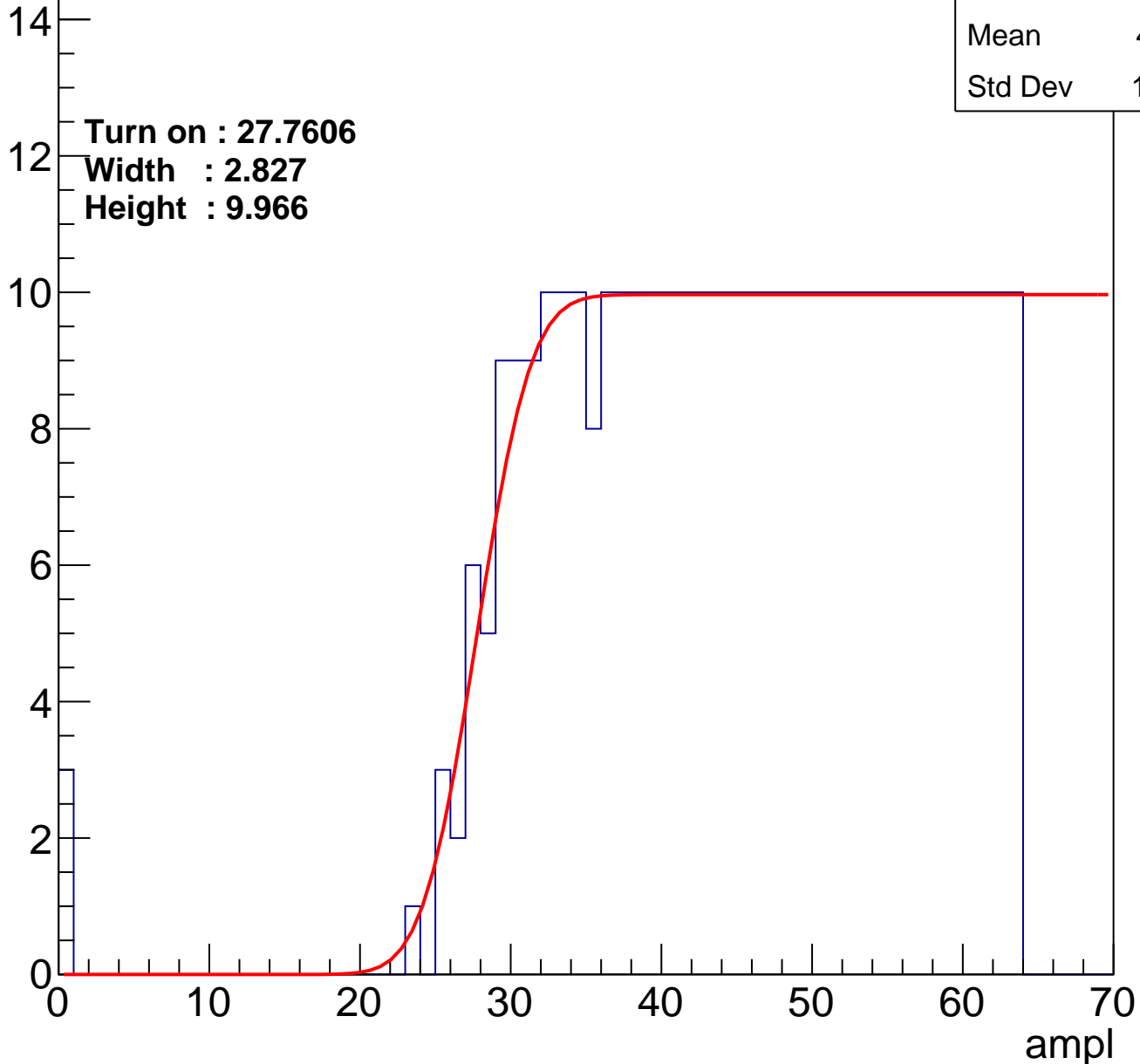
Entries	365
Mean	44.91
Std Dev	11.35

Turn on : 27.7606

Width : 2.827

Height : 9.966

Entry



B1L003S, U16-ch64

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.54
Std Dev	11.76

Turn on : 27.7969

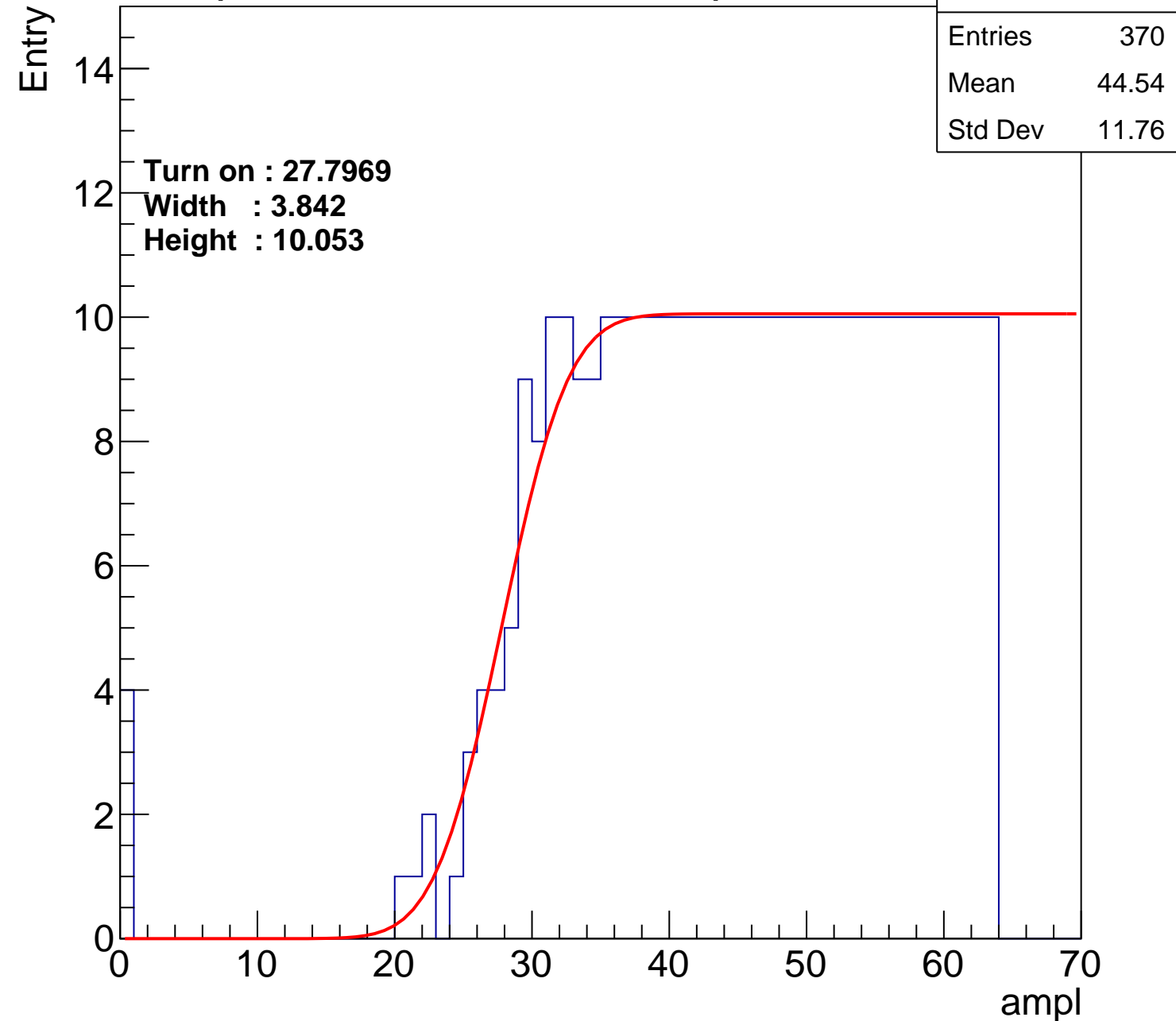
Width : 3.842

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch65

calib_packv5_042523_0143.root, FC#13, port D2

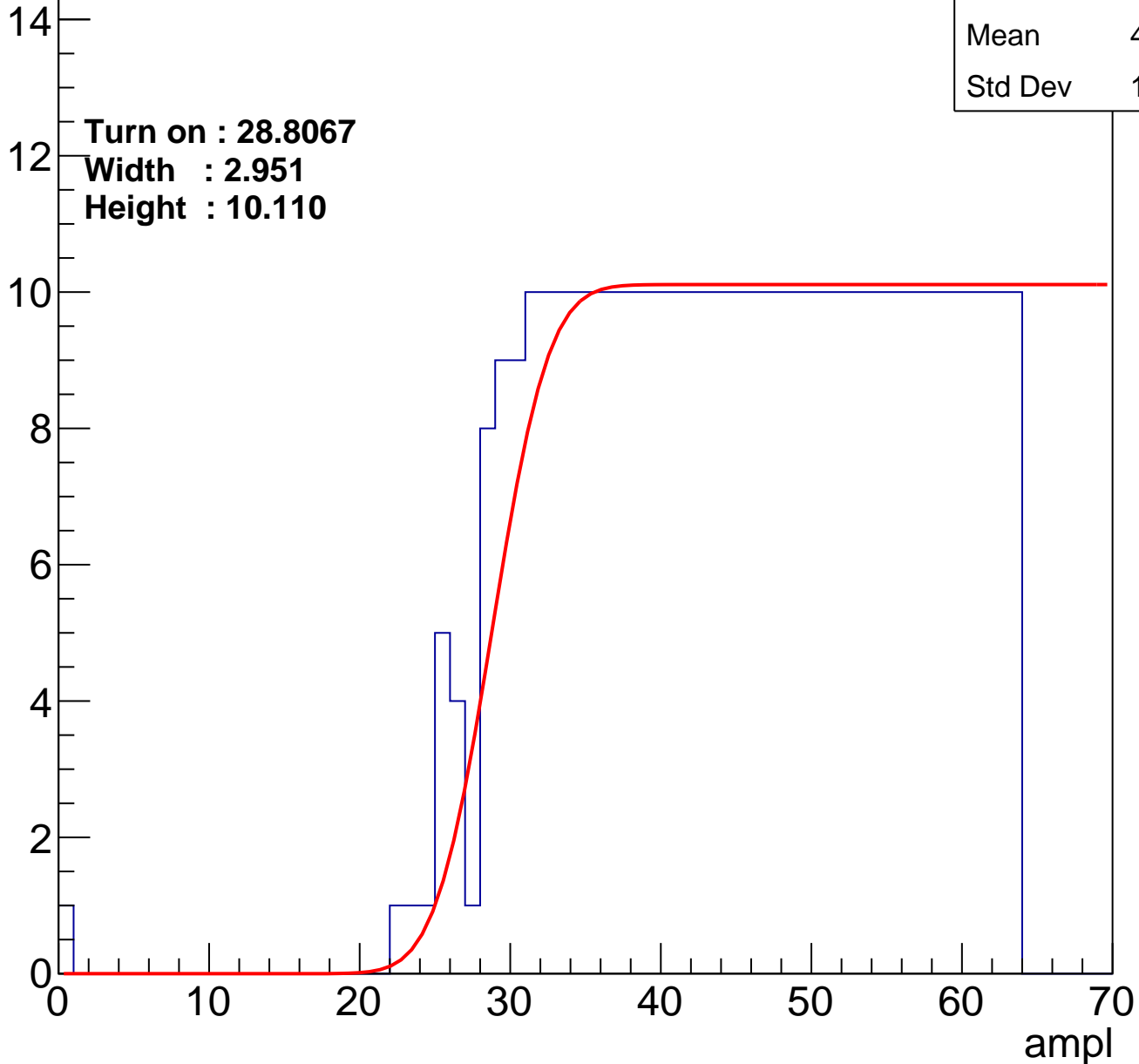
Entries	370
Mean	44.84
Std Dev	11.04

Turn on : 28.8067

Width : 2.951

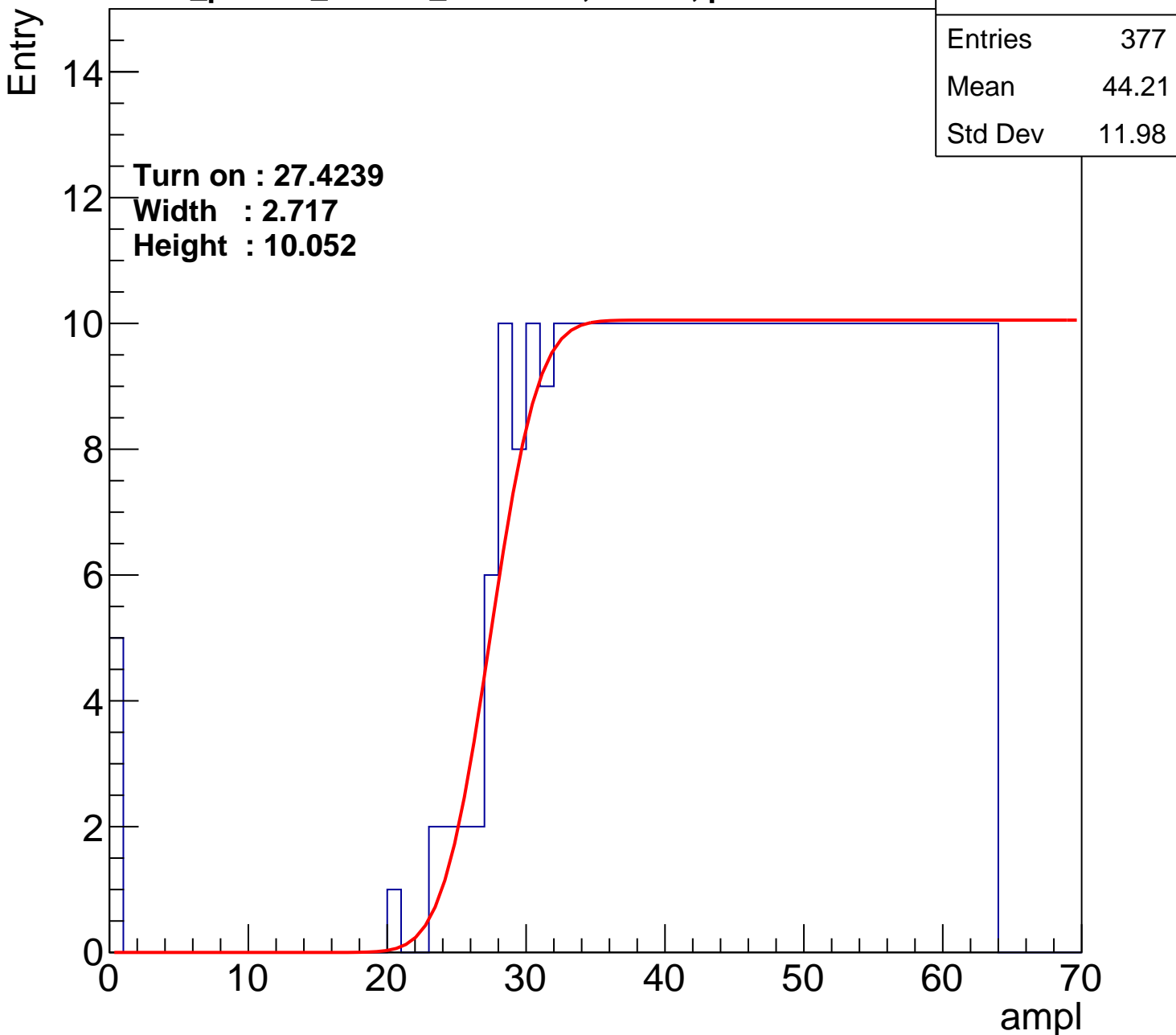
Height : 10.110

Entry



calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 27.4239
Width : 2.717
Height : 10.052



B1L003S, U16-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 25.2709

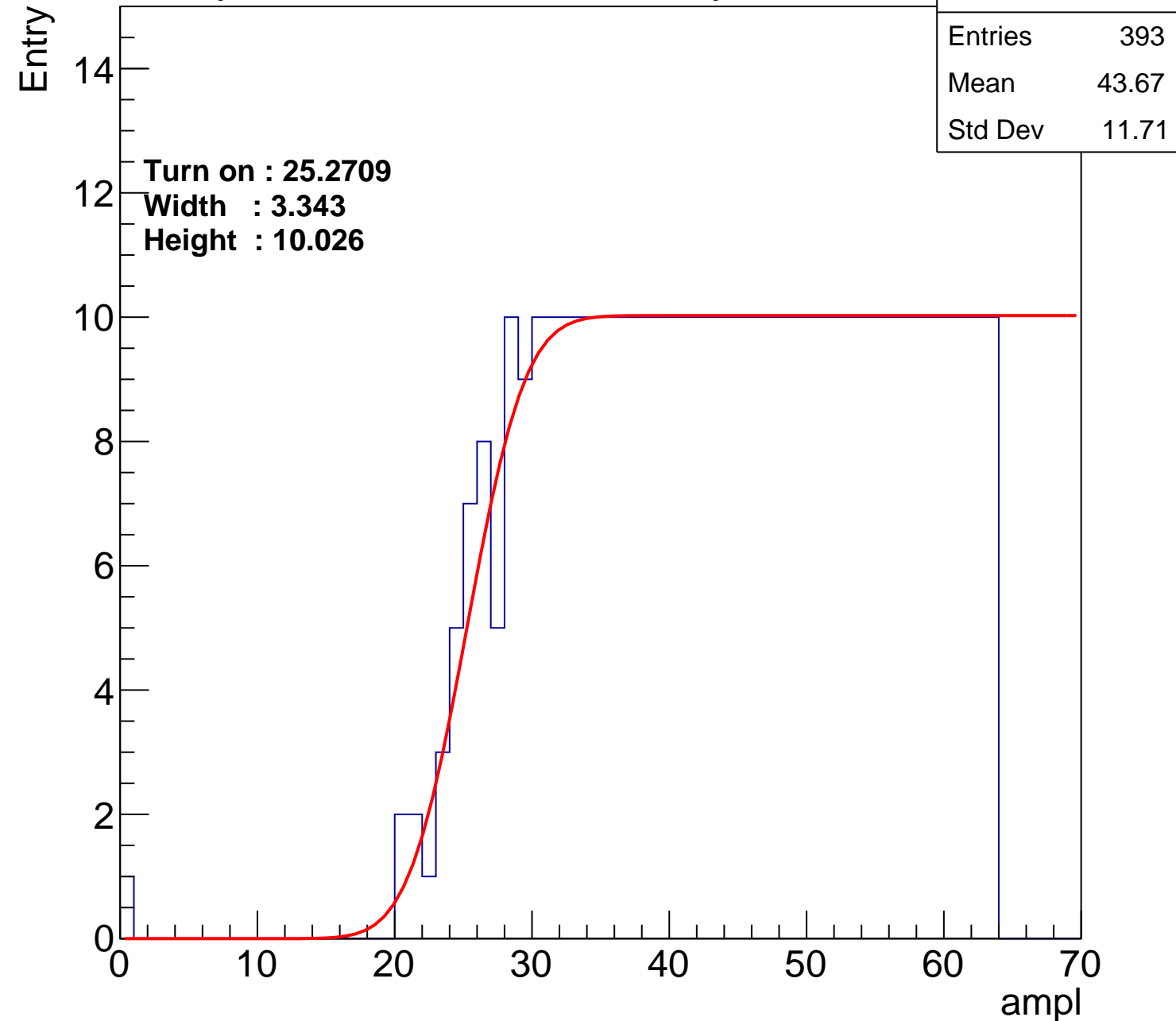
Width : 3.343

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch68

calib_packv5_042523_0143.root, FC#13, port D2

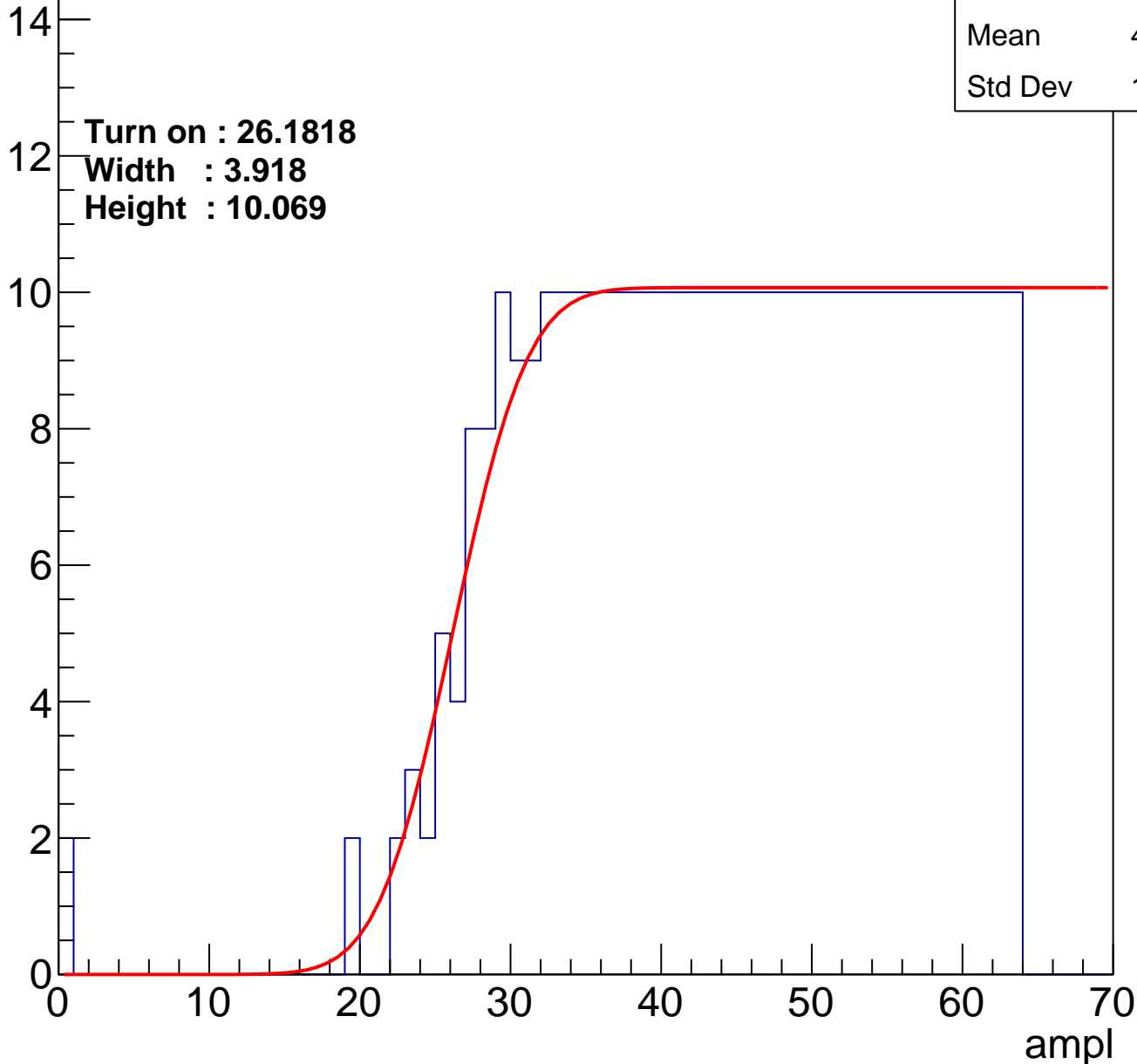
Entries	384
Mean	44.03
Std Dev	11.68

Turn on : 26.1818

Width : 3.918

Height : 10.069

Entry



B1L003S, U16-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.27
Std Dev	11.03

Turn on : 29.2843

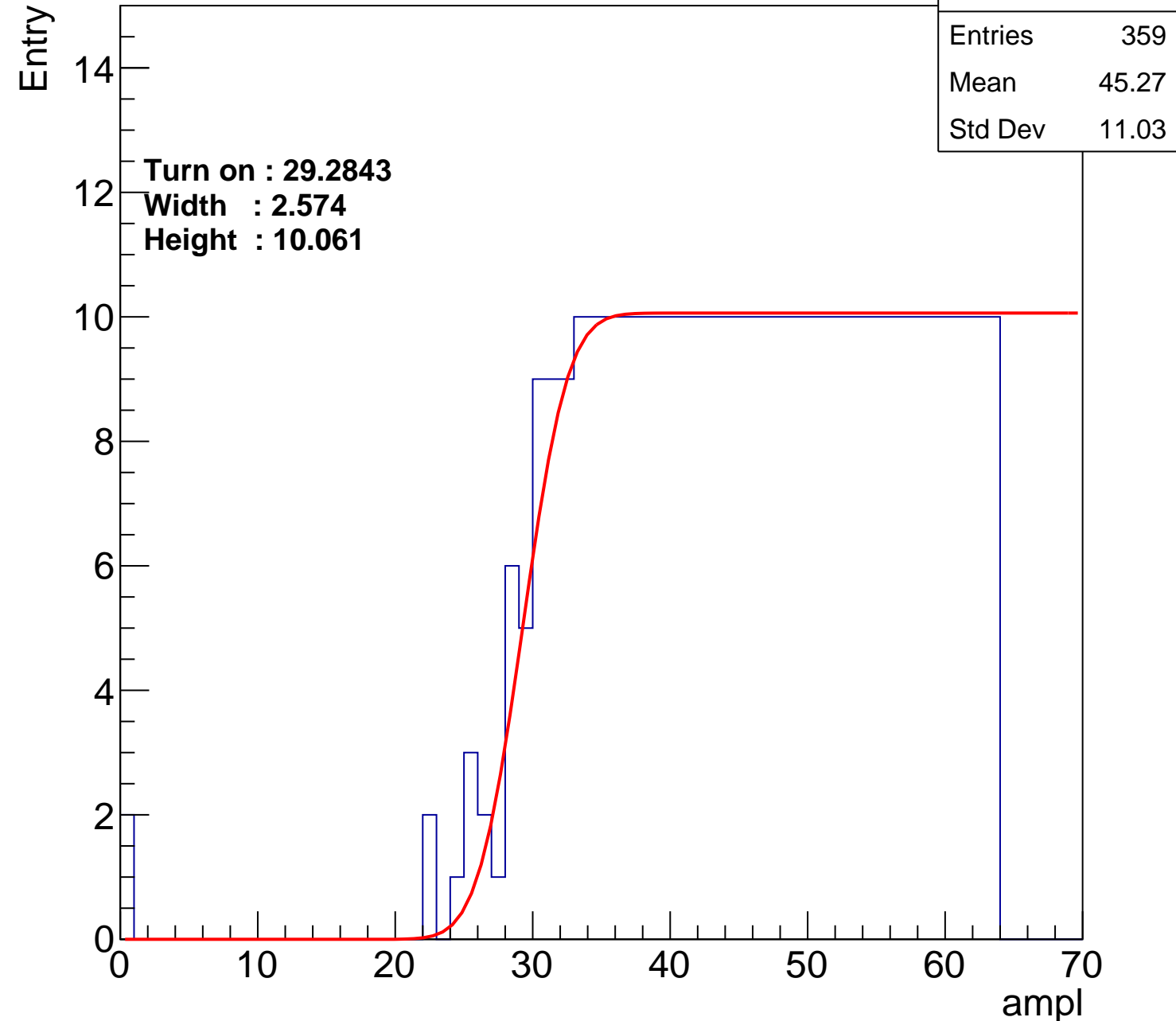
Width : 2.574

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch70

calib_packv5_042523_0143.root, FC#13, port D2

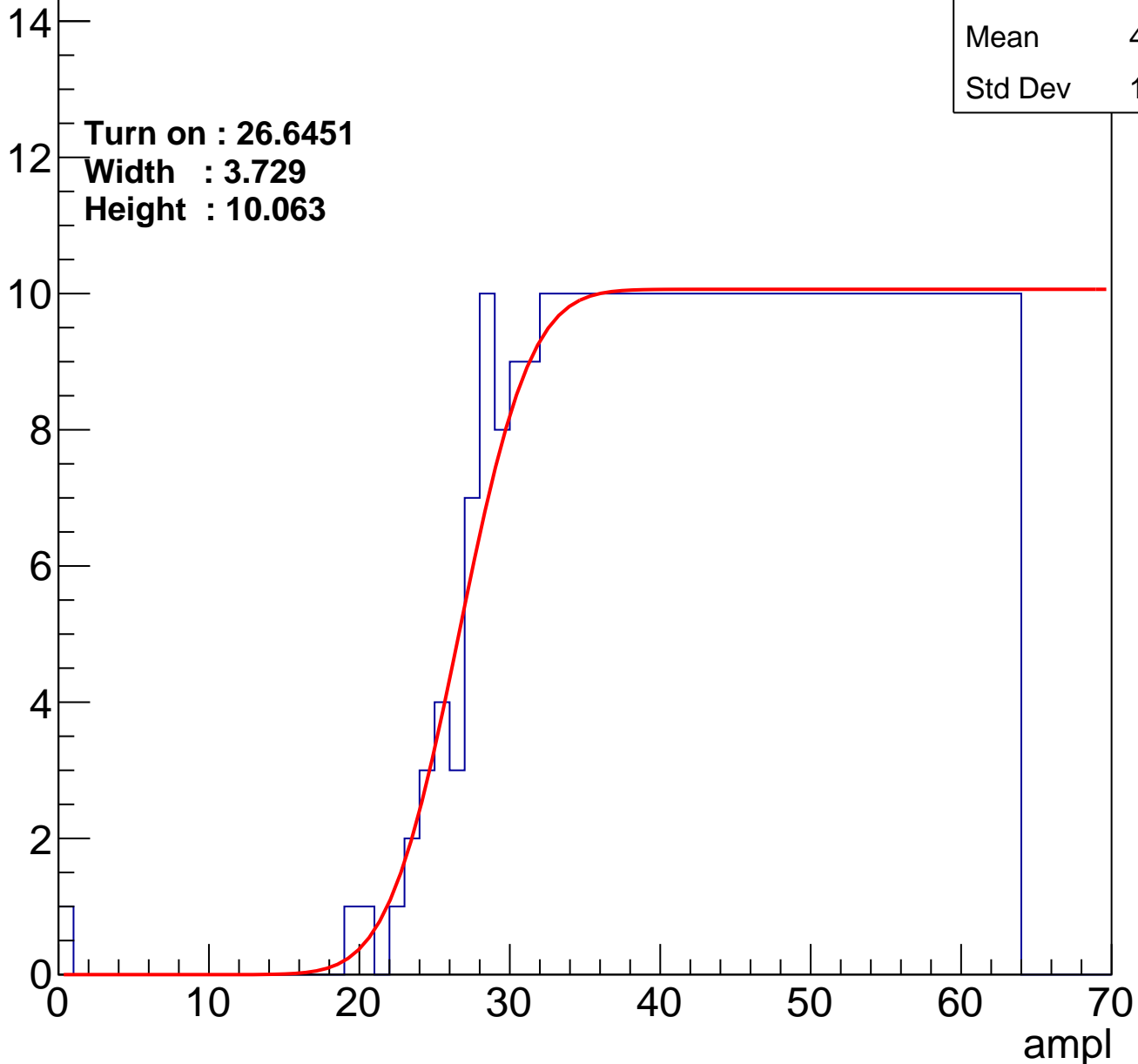
Entries	379
Mean	44.35
Std Dev	11.36

Turn on : 26.6451

Width : 3.729

Height : 10.063

Entry



B1L003S, U16-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.15
Std Dev	11.89

Turn on : 26.3248

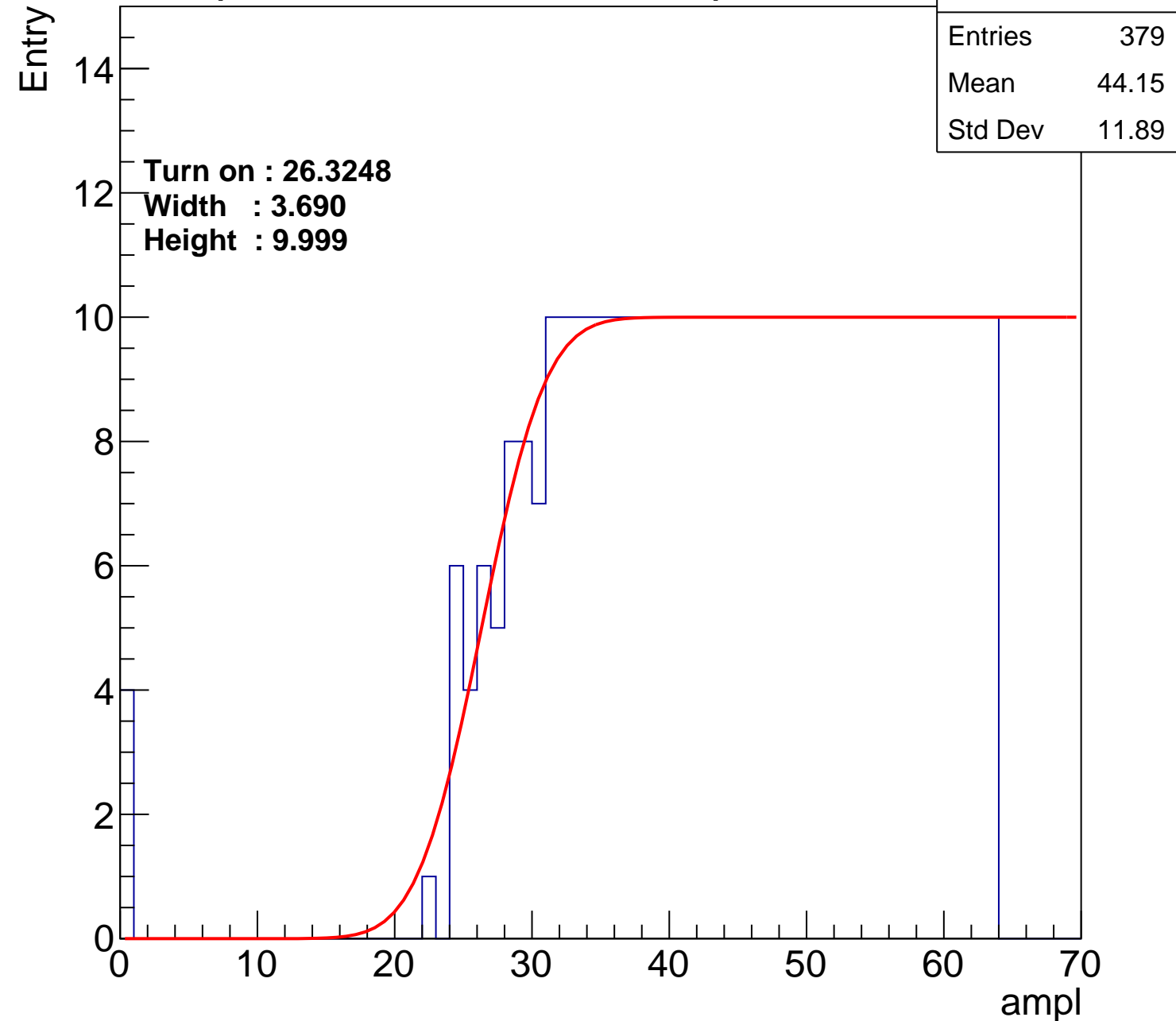
Width : 3.690

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch72

calib_packv5_042523_0143.root, FC#13, port D2

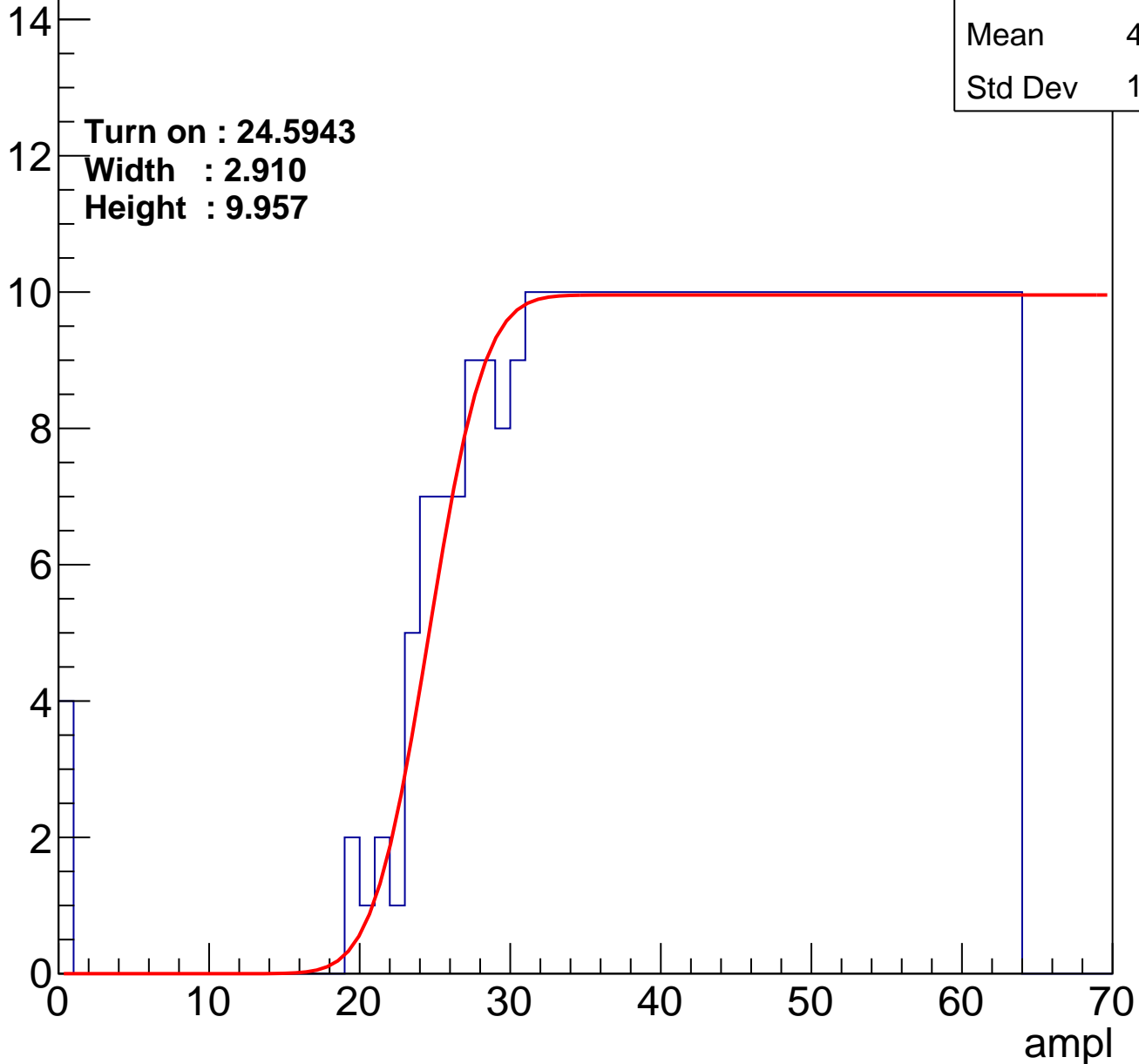
Entries	401
Mean	43.06
Std Dev	12.43

Turn on : 24.5943

Width : 2.910

Height : 9.957

Entry



B1L003S, U16-ch73

calib_packv5_042523_0143.root, FC#13, port D2

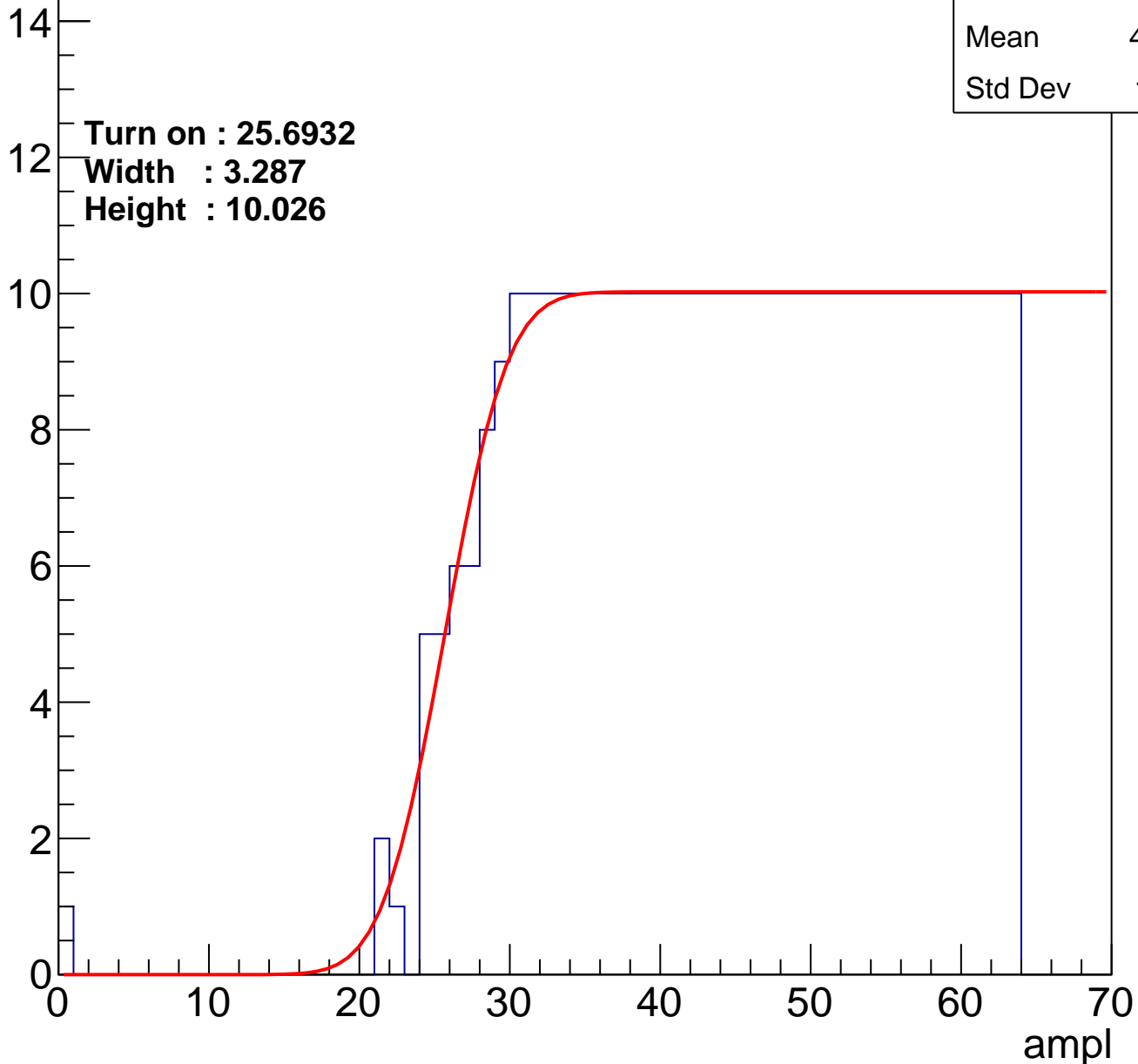
Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 25.6932

Width : 3.287

Height : 10.026

Entry



B1L003S, U16-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.88
Std Dev	12.29

Turn on : 27.1670

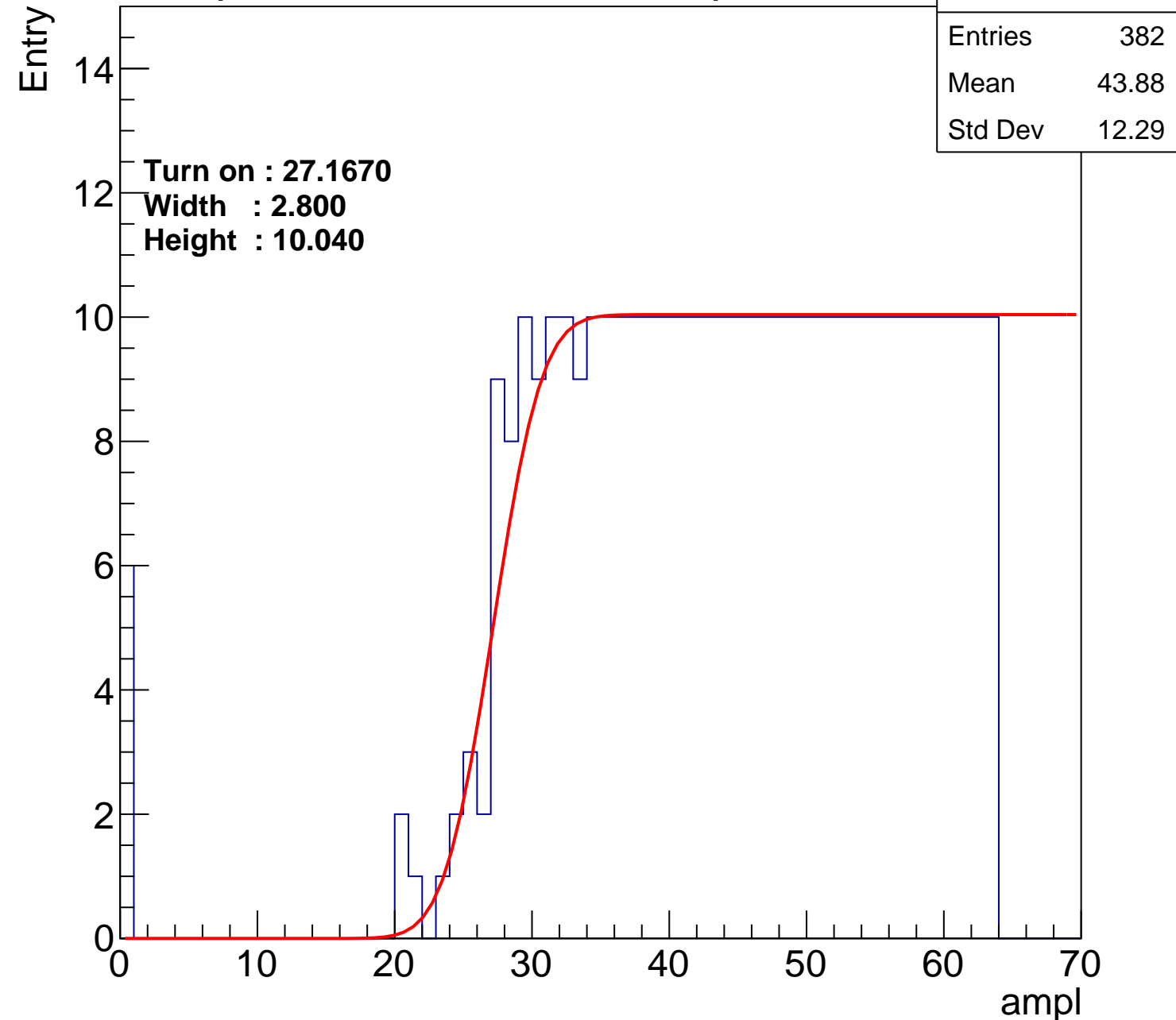
Width : 2.800

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	395
Mean	43.14
Std Dev	12.85

Turn on : 25.6558

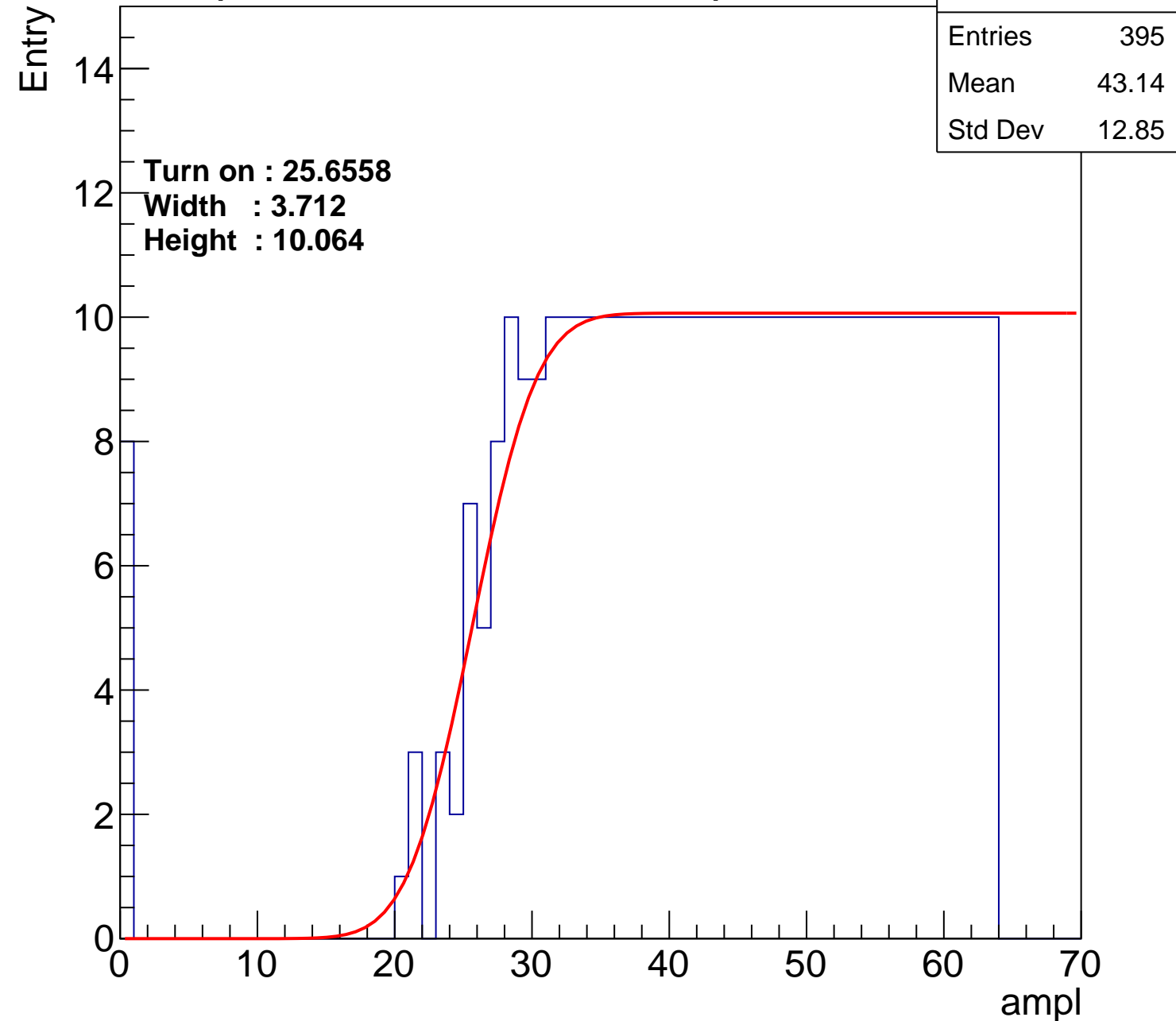
Width : 3.712

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch76

calib_packv5_042523_0143.root, FC#13, port D2

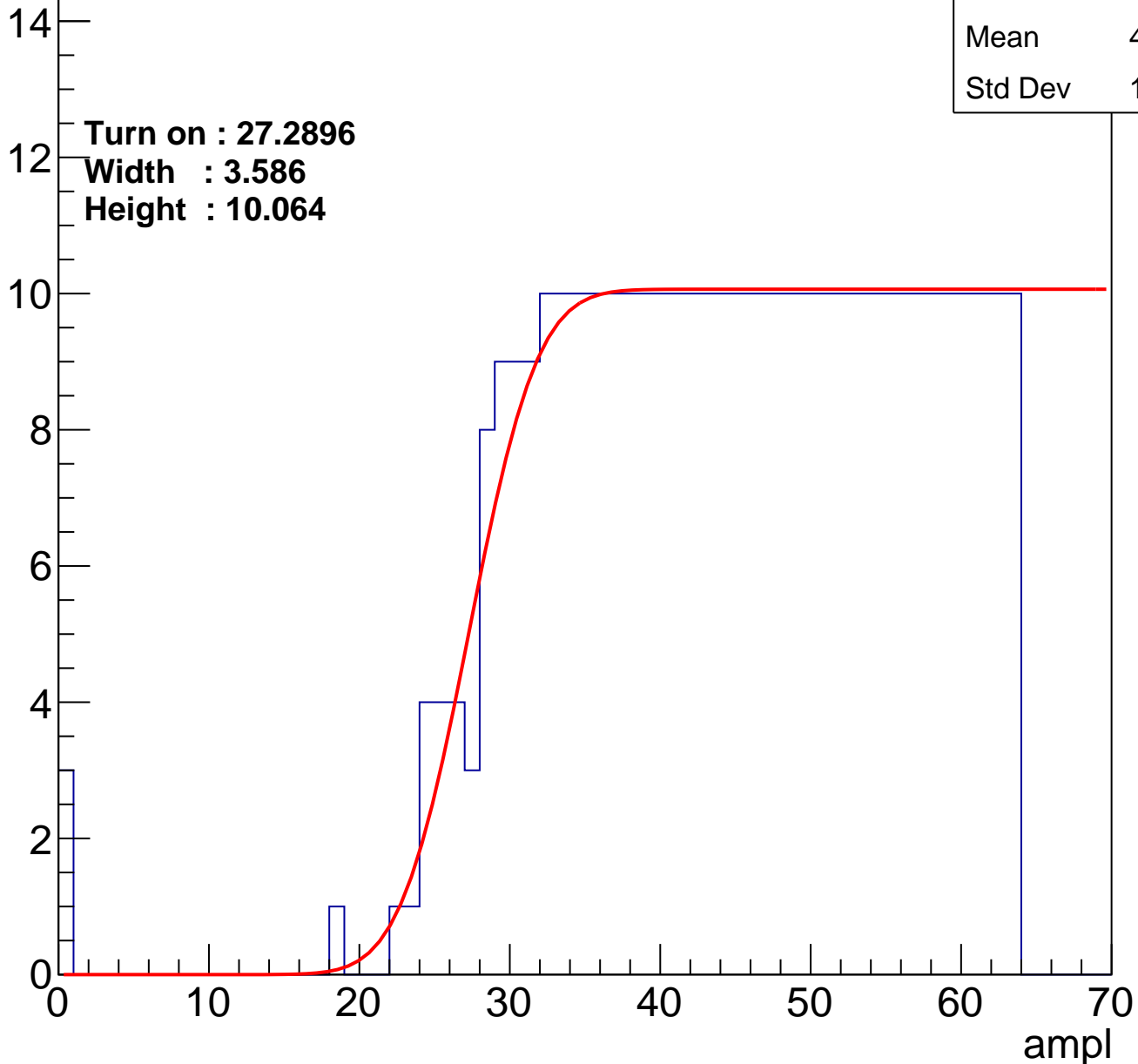
Entries	376
Mean	44.36
Std Dev	11.66

Turn on : 27.2896

Width : 3.586

Height : 10.064

Entry



B1L003S, U16-ch77

calib_packv5_042523_0143.root, FC#13, port D2

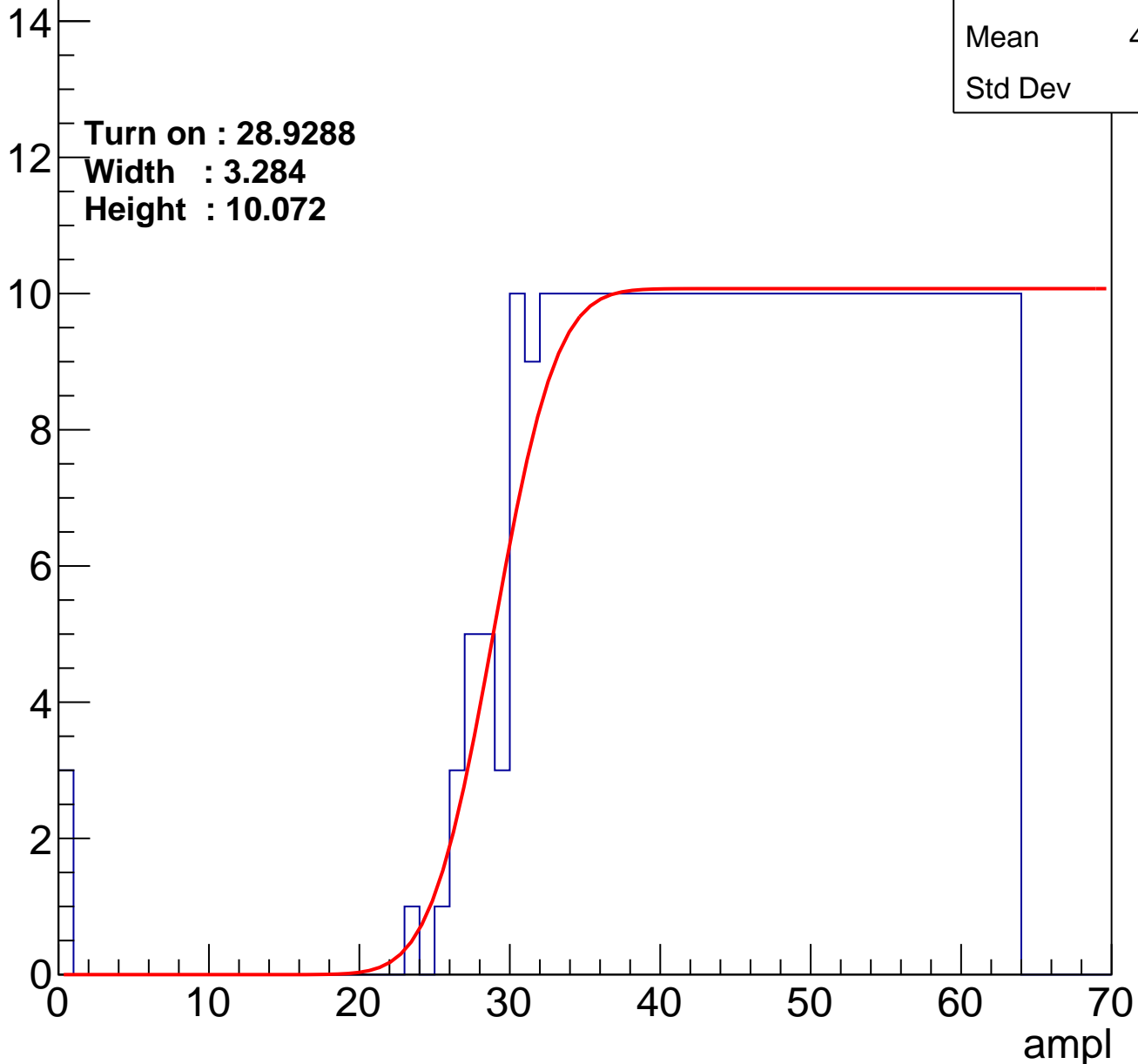
Entries	360
Mean	45.19
Std Dev	11.2

Turn on : 28.9288

Width : 3.284

Height : 10.072

Entry



B1L003S, U16-ch78

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.58
Std Dev	11.99

Turn on : 25.2827

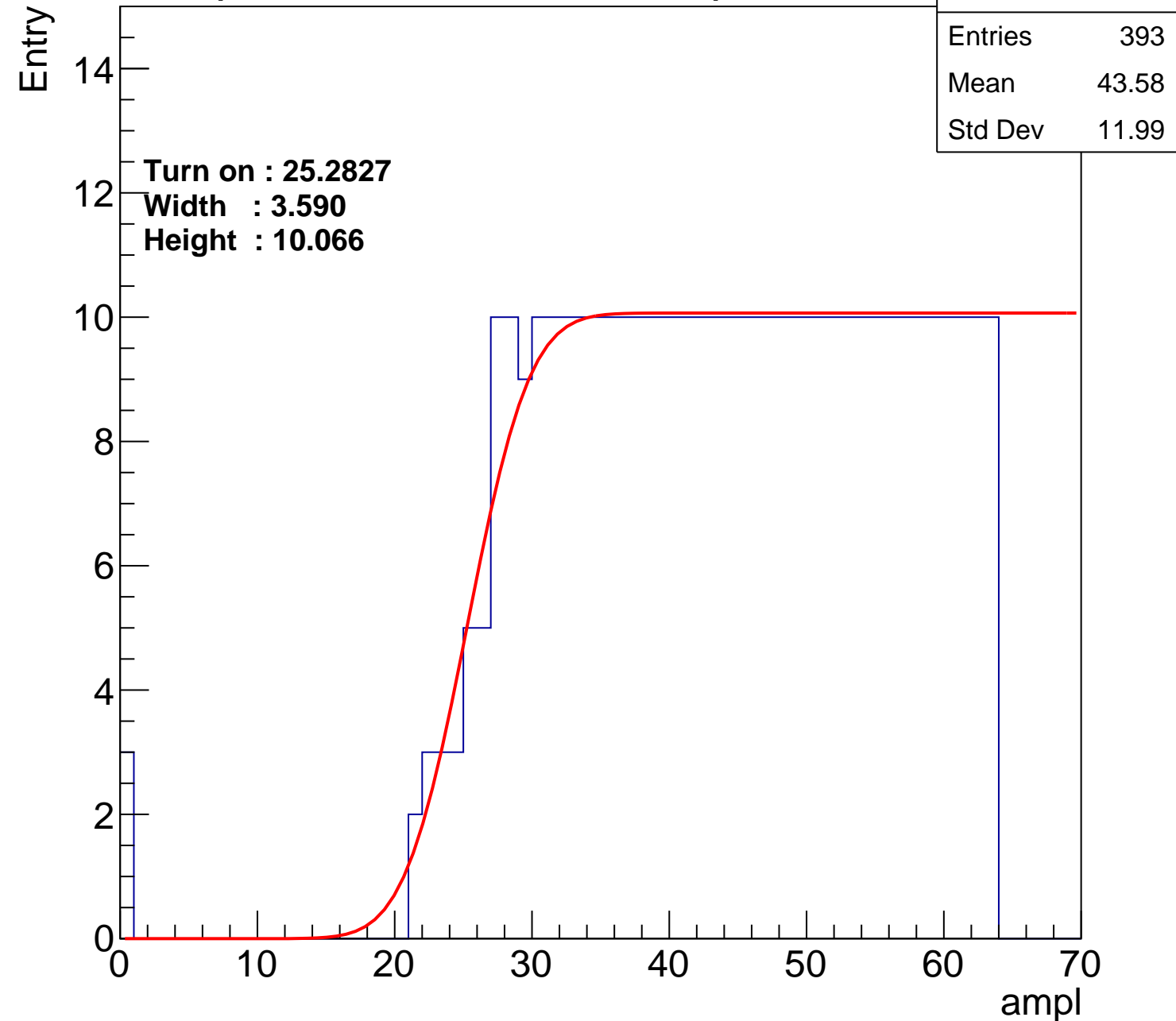
Width : 3.590

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.11
Std Dev	11.79

Turn on : 26.9003

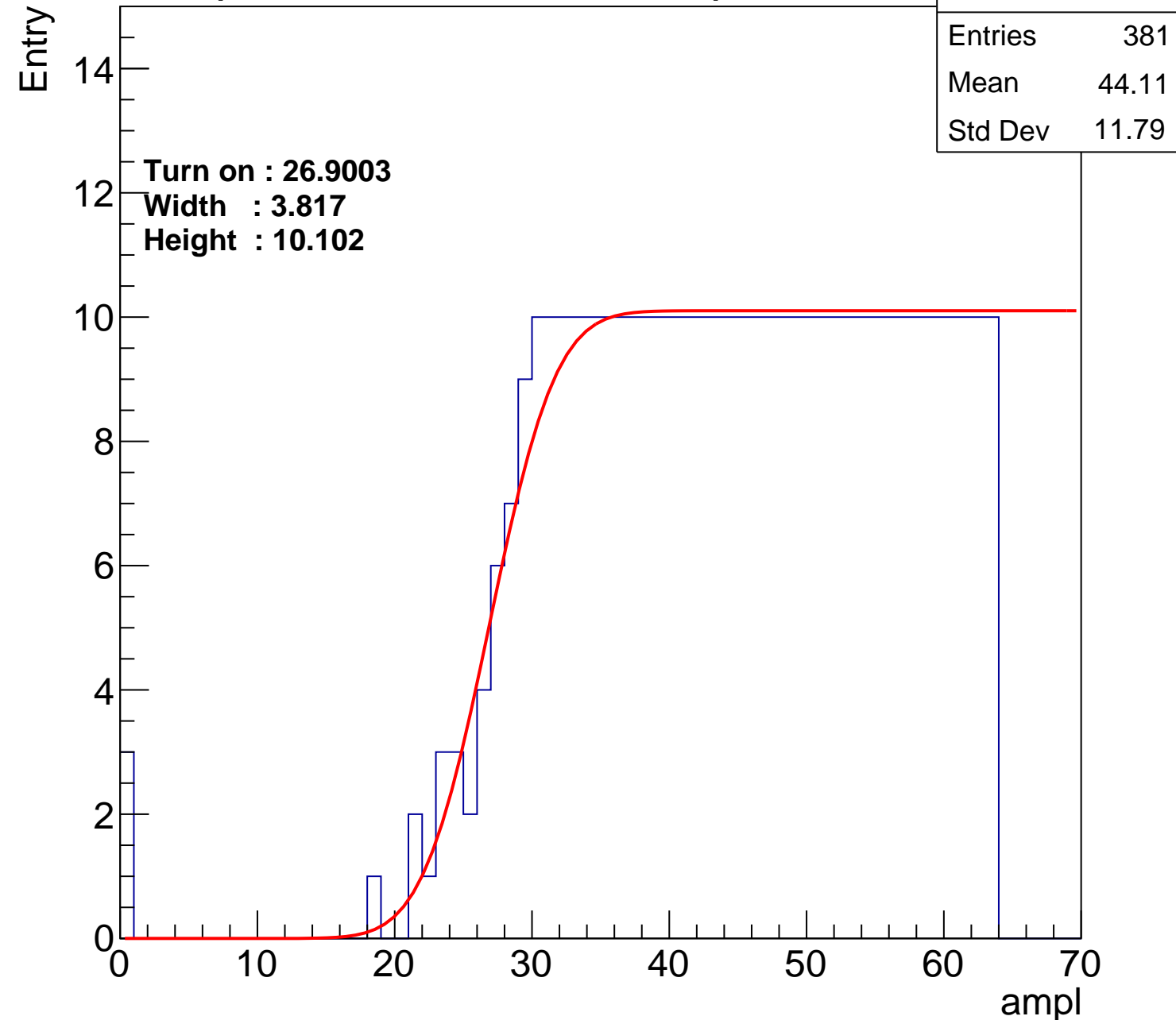
Width : 3.817

Height : 10.102

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.59
Std Dev	10.68

Turn on : 29.6447

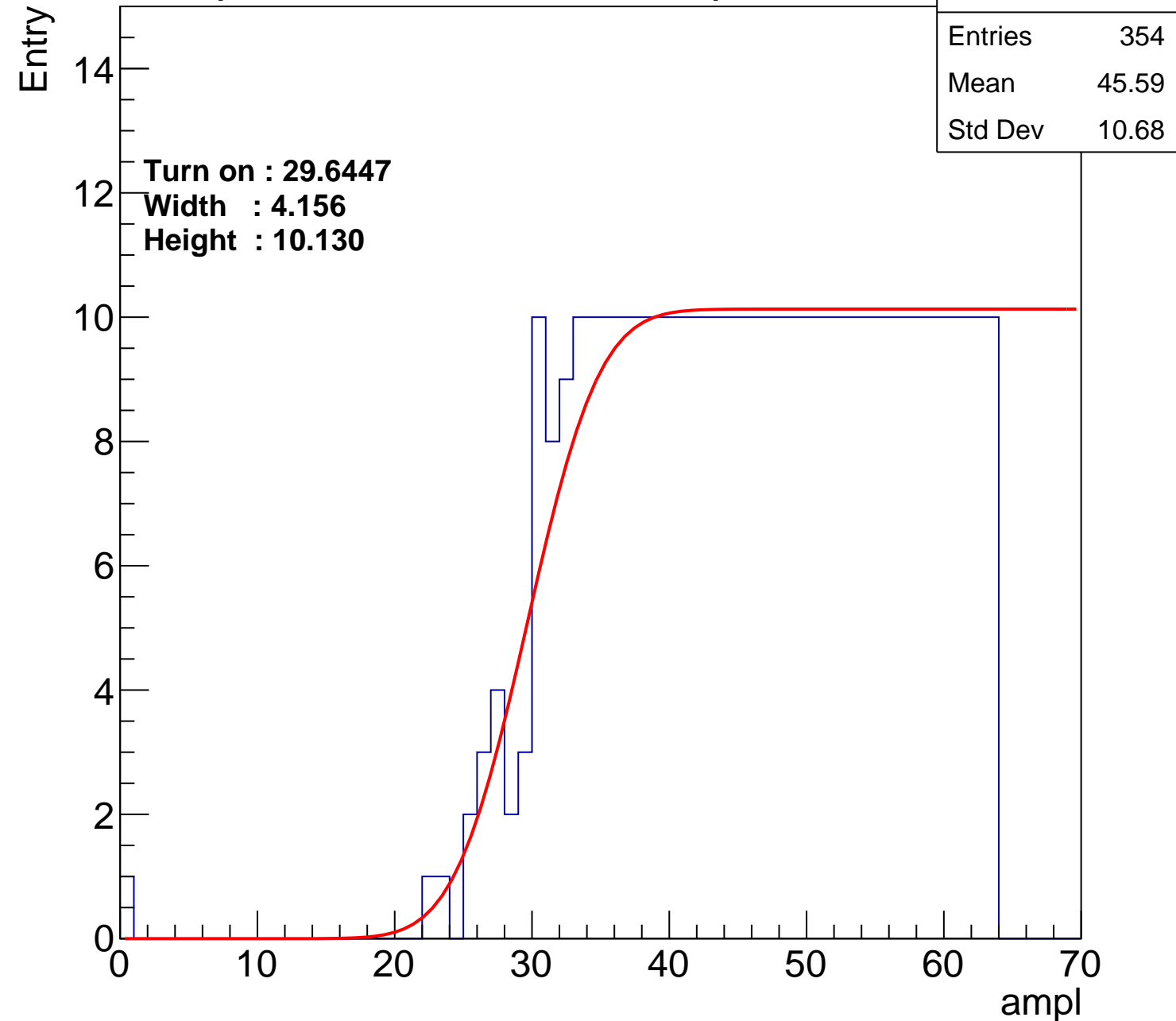
Width : 4.156

Height : 10.130

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch81

calib_packv5_042523_0143.root, FC#13, port D2

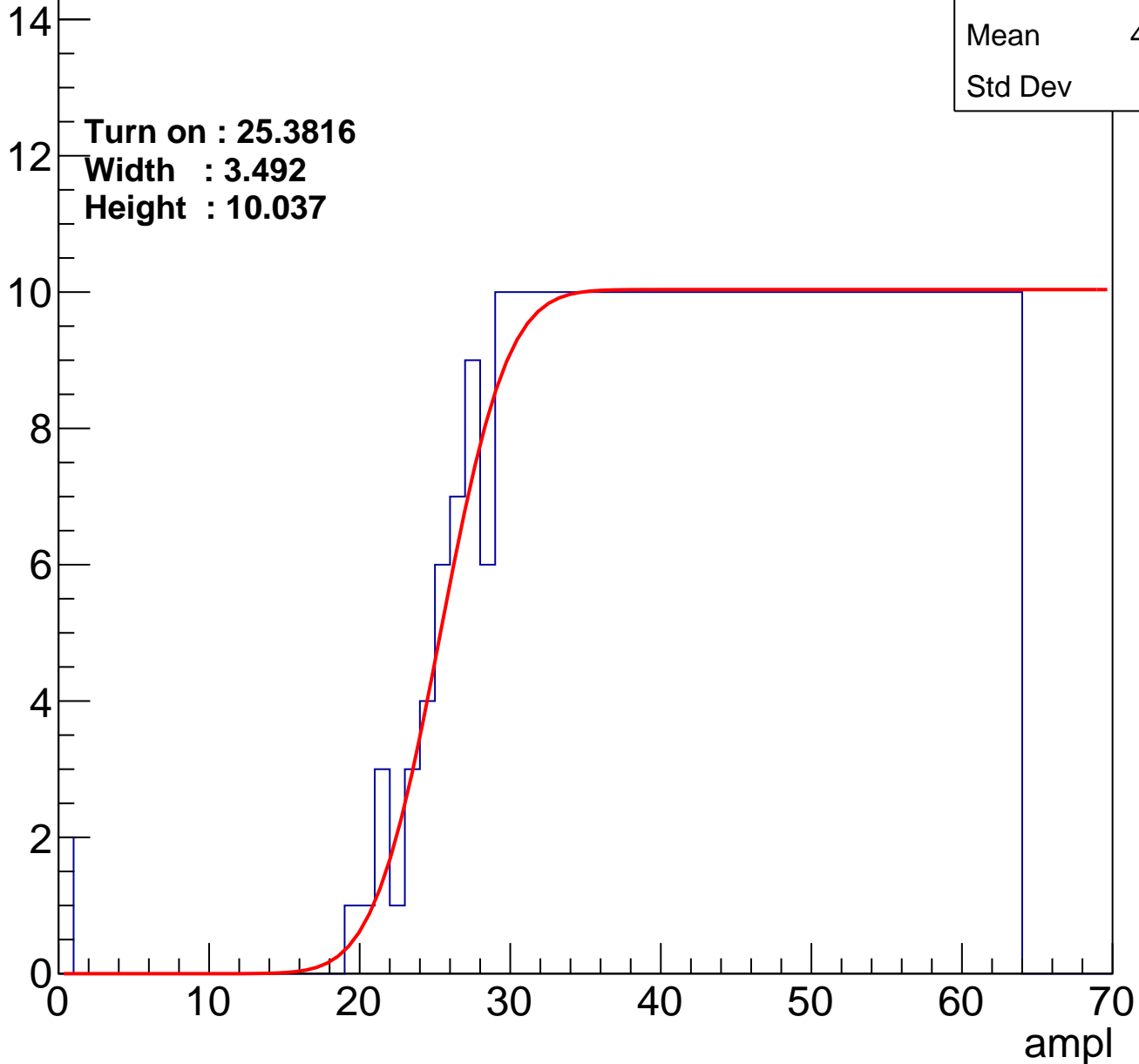
Entries	393
Mean	43.59
Std Dev	11.9

Turn on : 25.3816

Width : 3.492

Height : 10.037

Entry



B1L003S, U16-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	397
Mean	43.21
Std Dev	12.47

Turn on : 24.7749

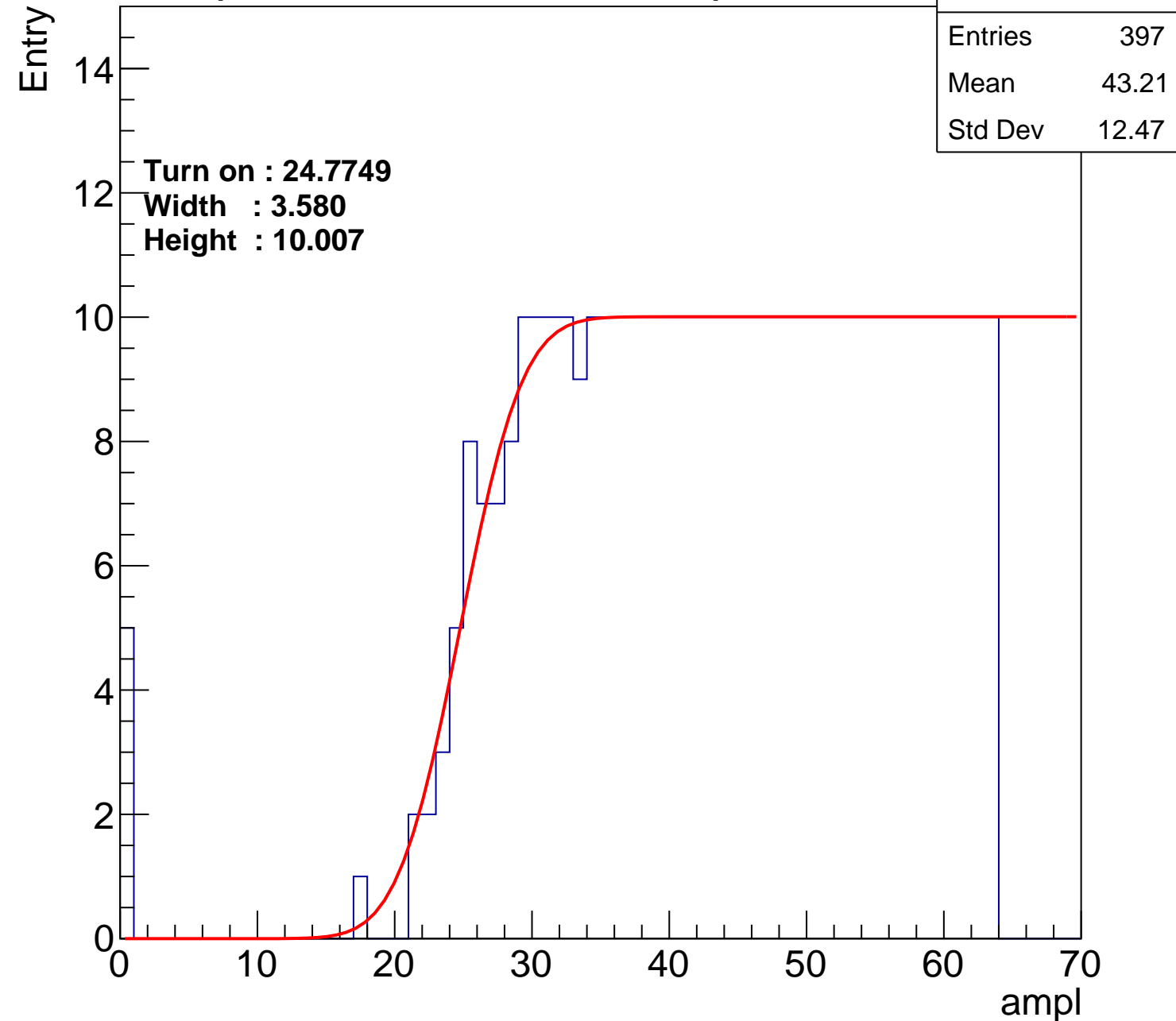
Width : 3.580

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 25.6306

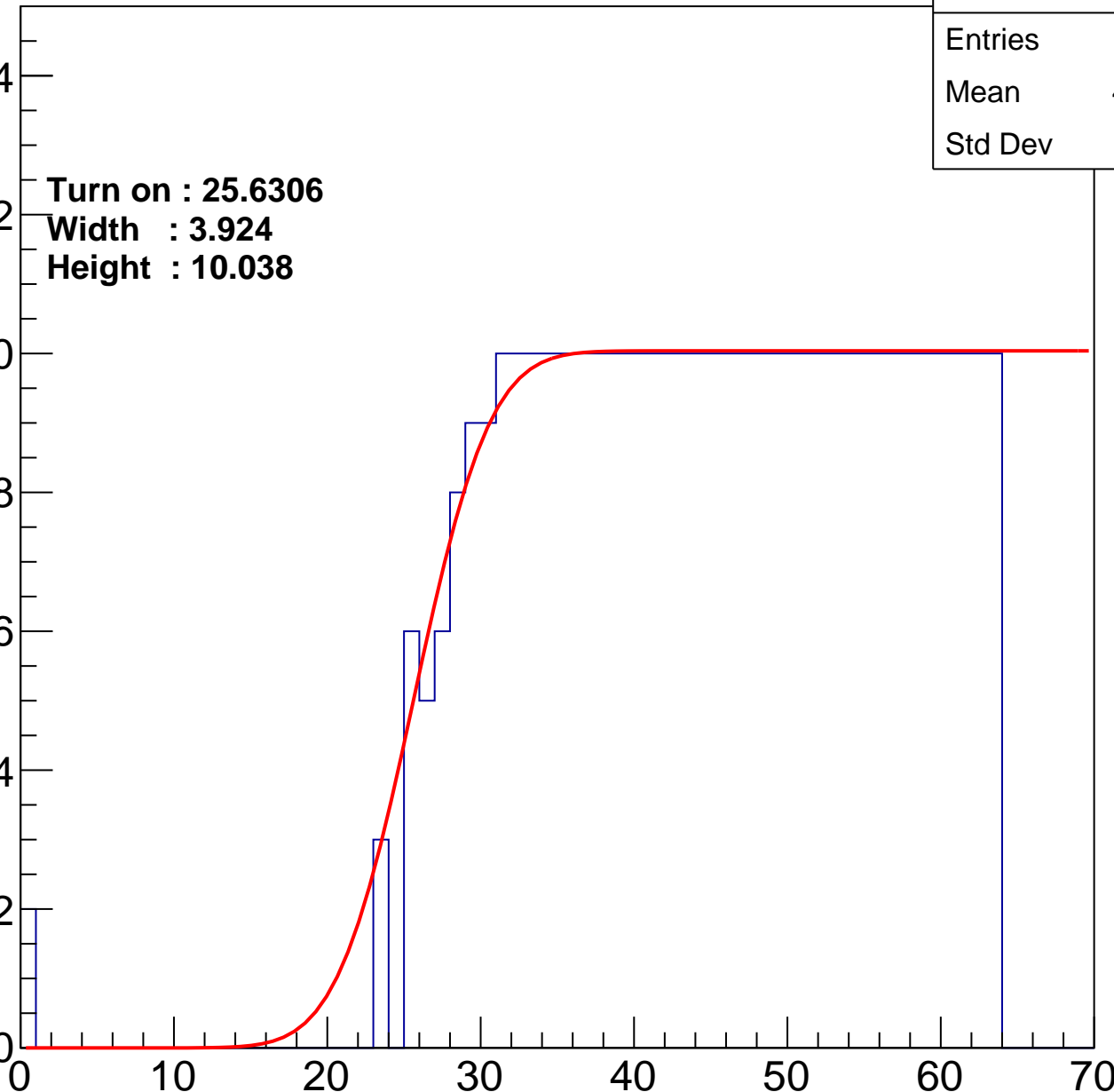
Width : 3.924

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 26.8780

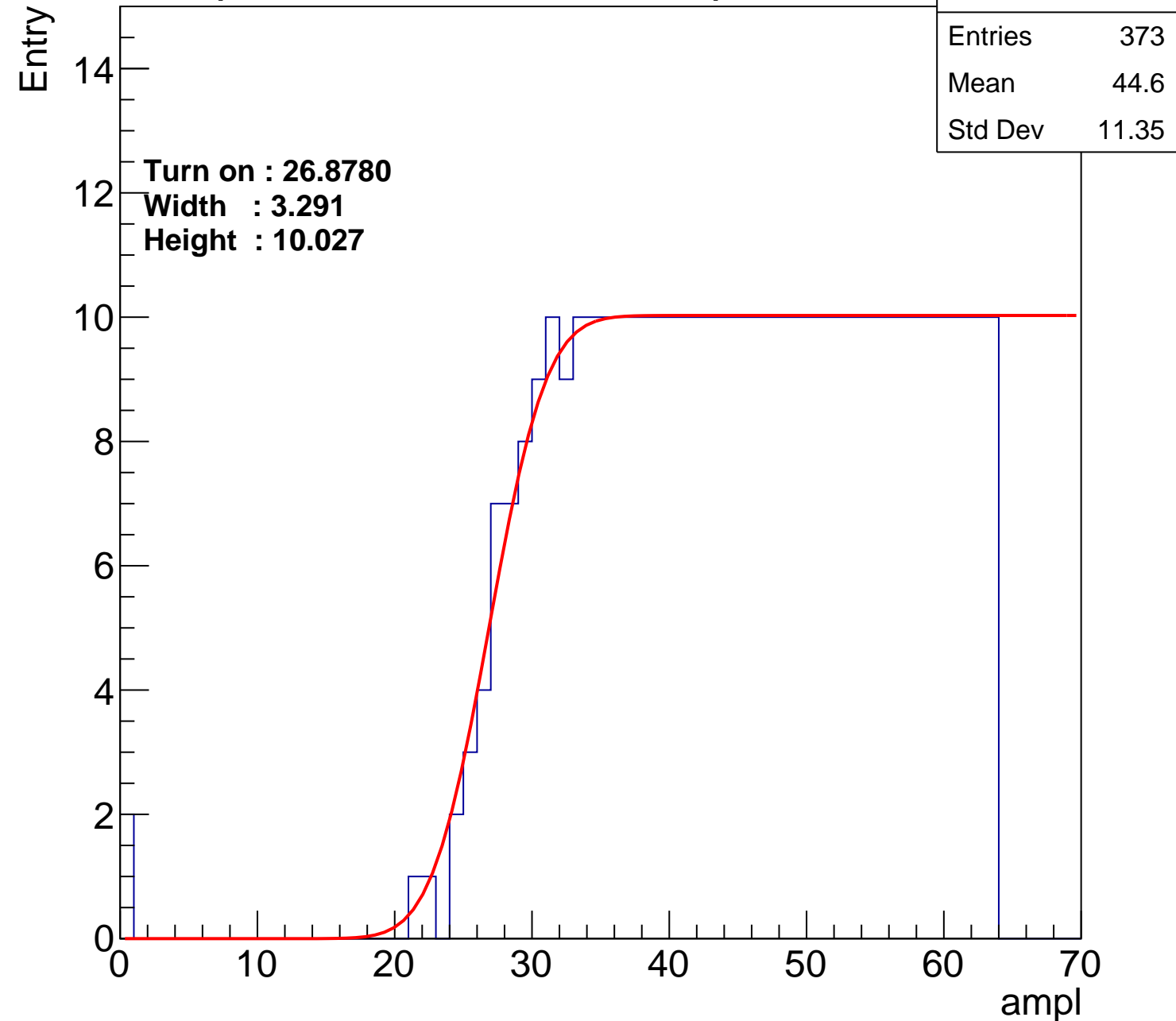
Width : 3.291

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.29
Std Dev	12.08

Turn on : 27.1921

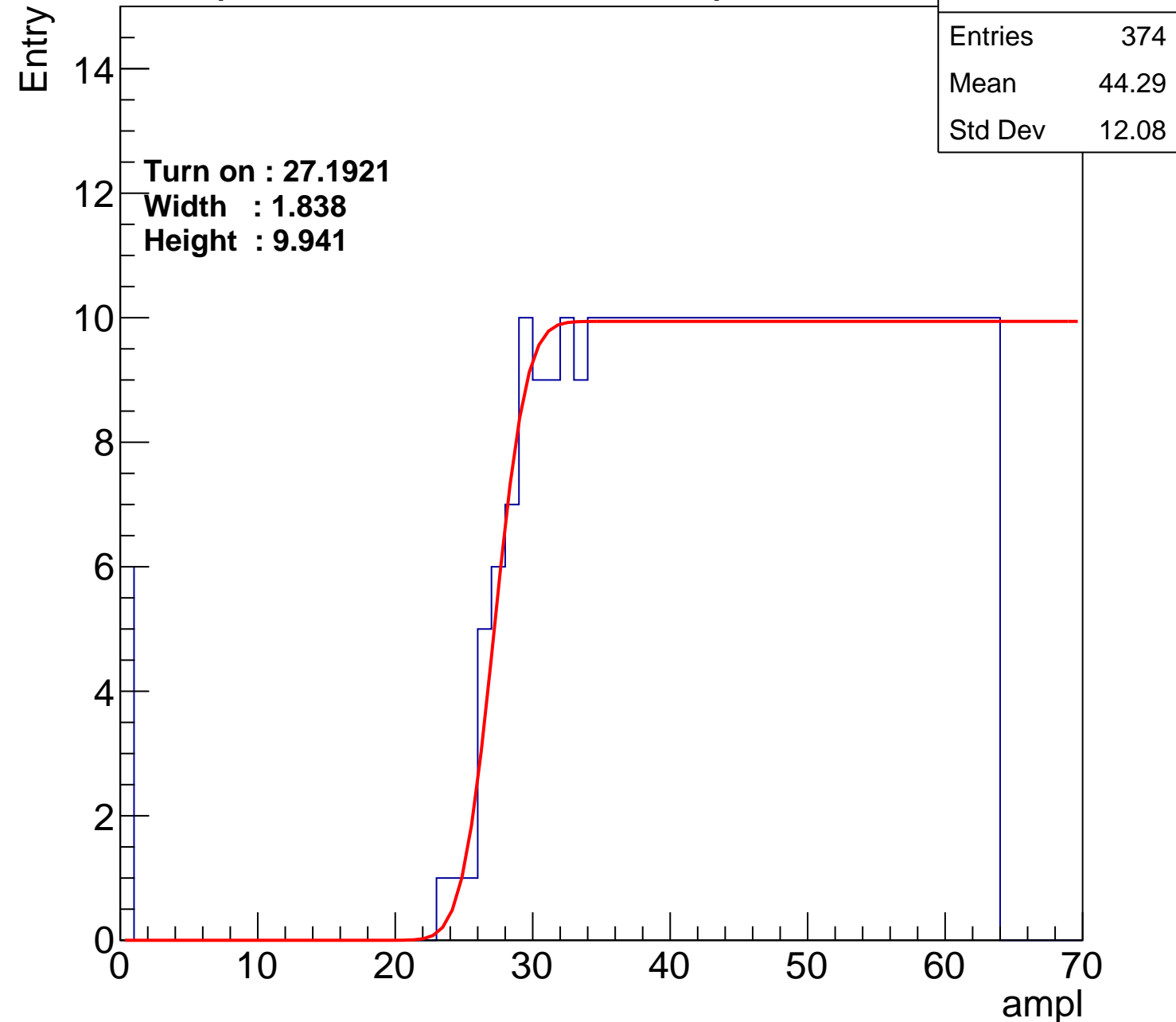
Width : 1.838

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch86

calib_packv5_042523_0143.root, FC#13, port D2

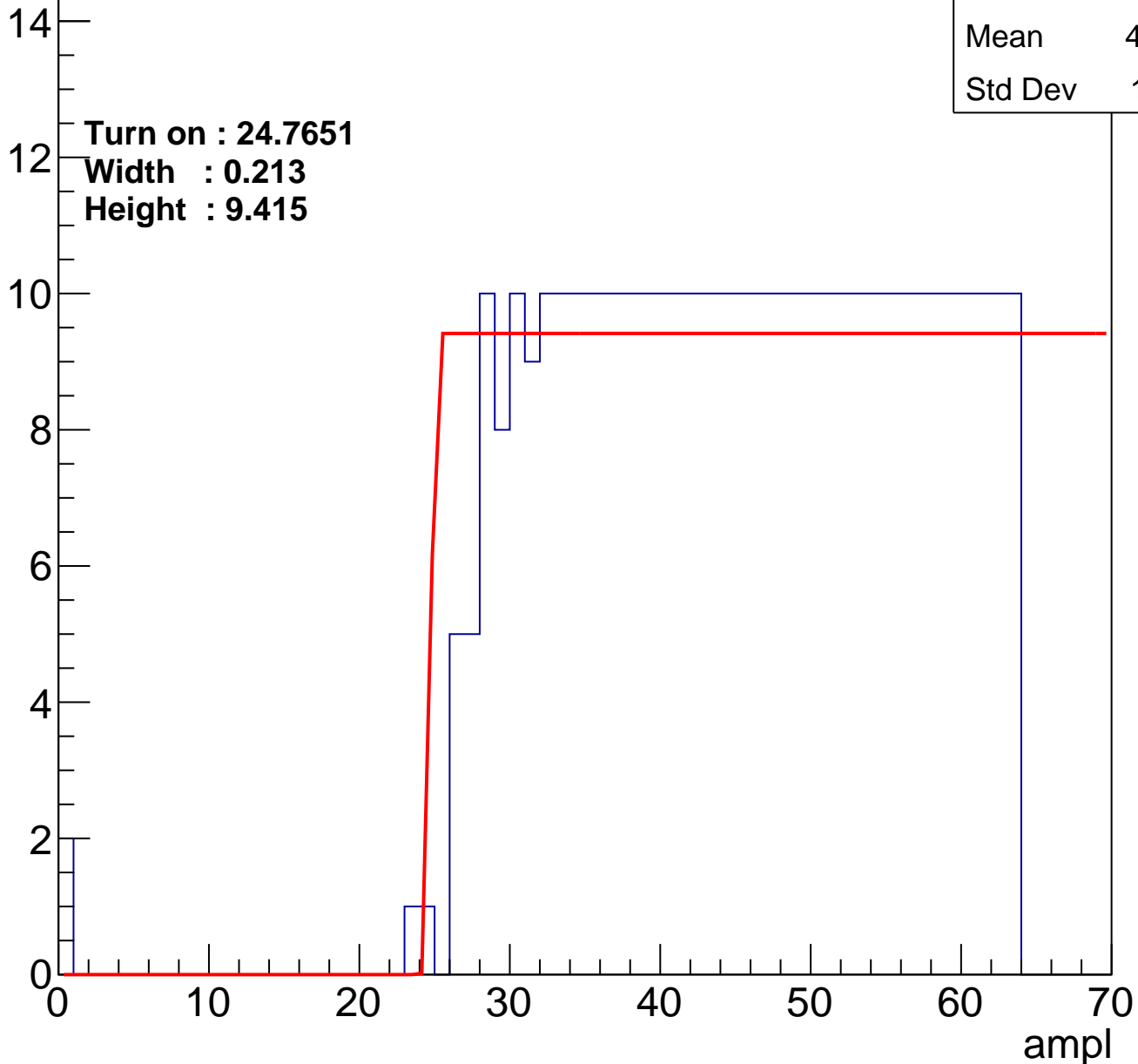
Entries	371
Mean	44.75
Std Dev	11.21

Turn on : 24.7651

Width : 0.213

Height : 9.415

Entry



B1L003S, U16-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.93
Std Dev	11.35

Turn on : 27.9348

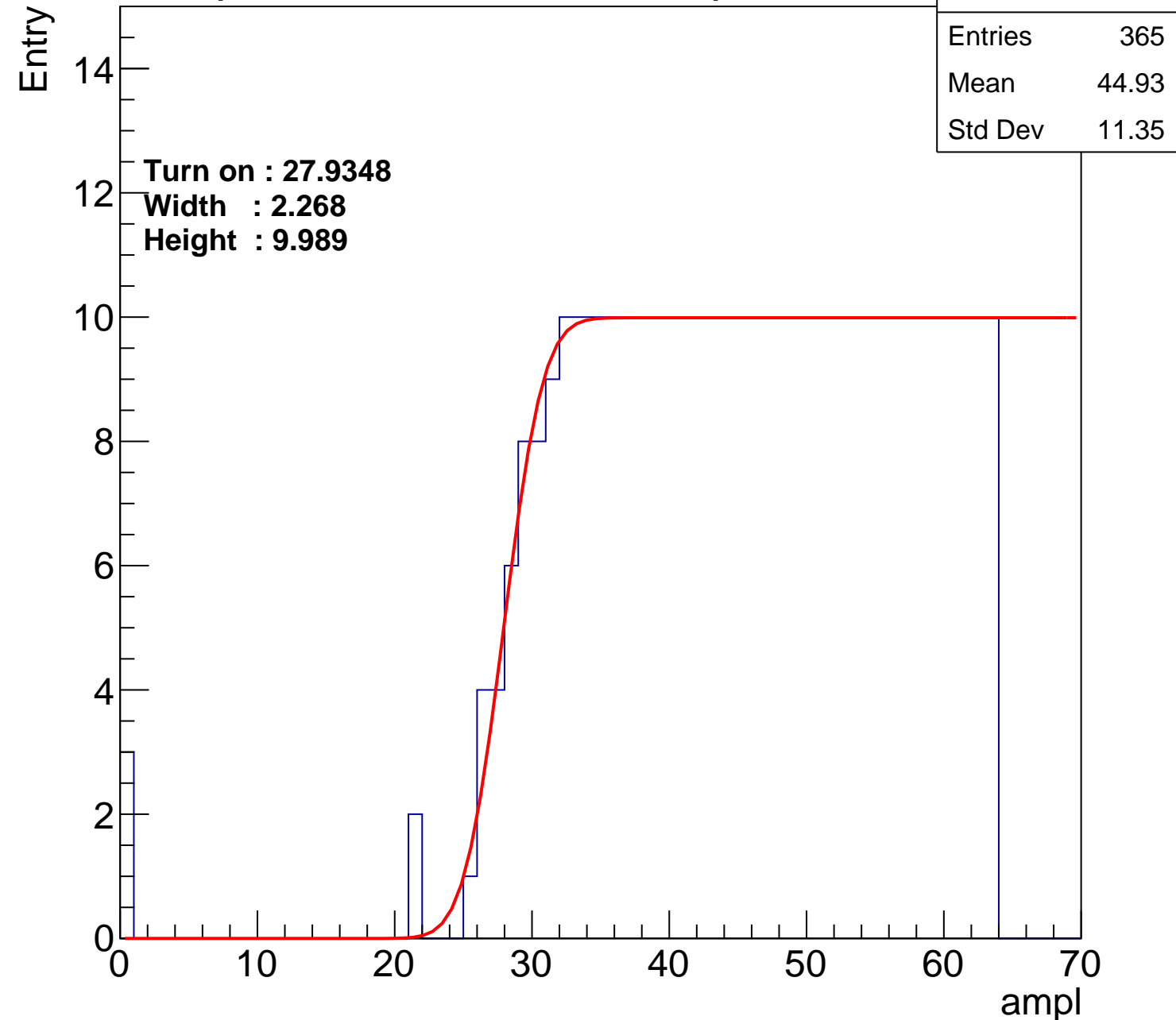
Width : 2.268

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch88

calib_packv5_042523_0143.root, FC#13, port D2

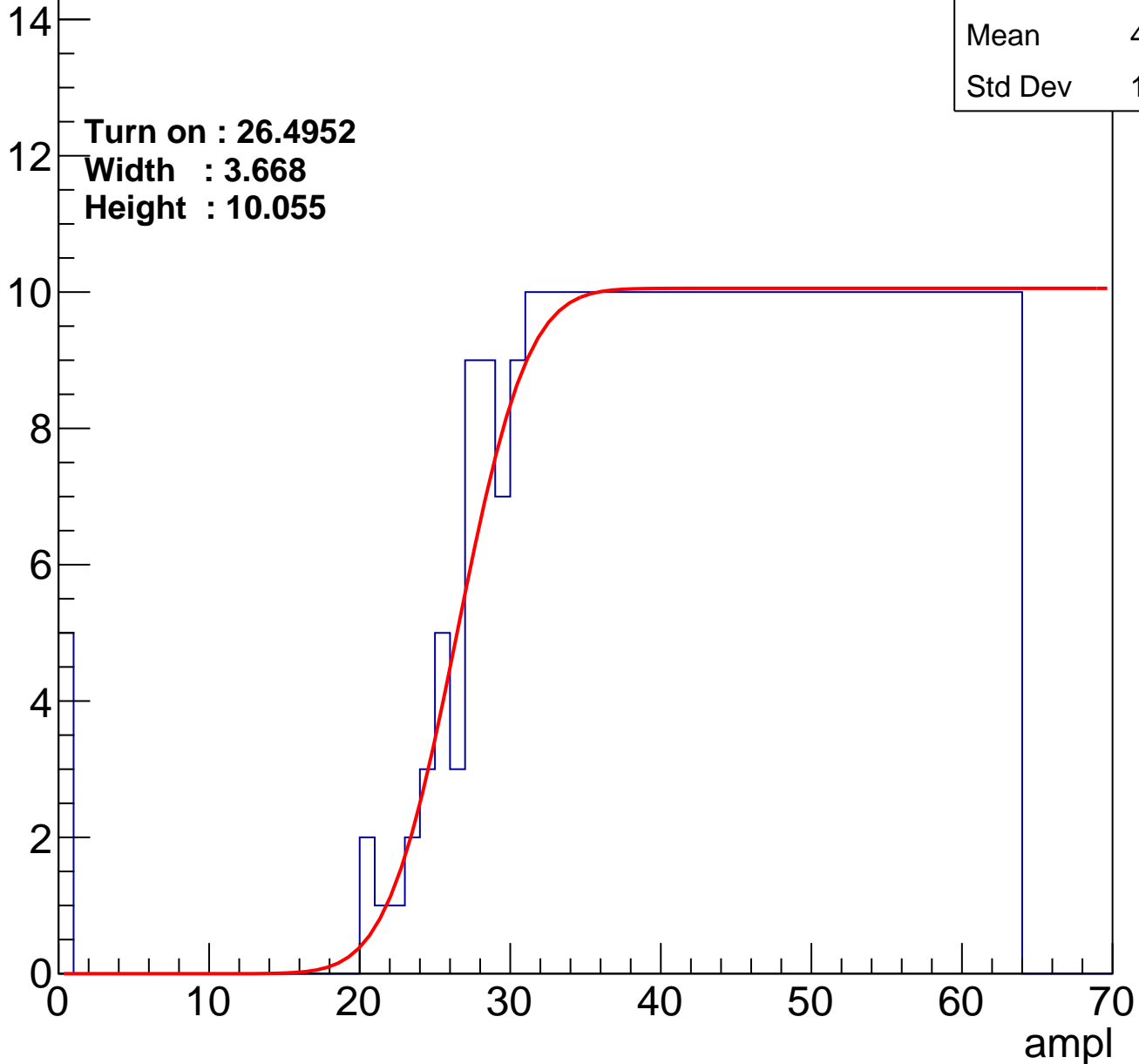
Entries	386
Mean	43.74
Std Dev	12.24

Turn on : 26.4952

Width : 3.668

Height : 10.055

Entry



B1L003S, U16-ch89

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.97
Std Dev	11.86

Turn on : 27.0185

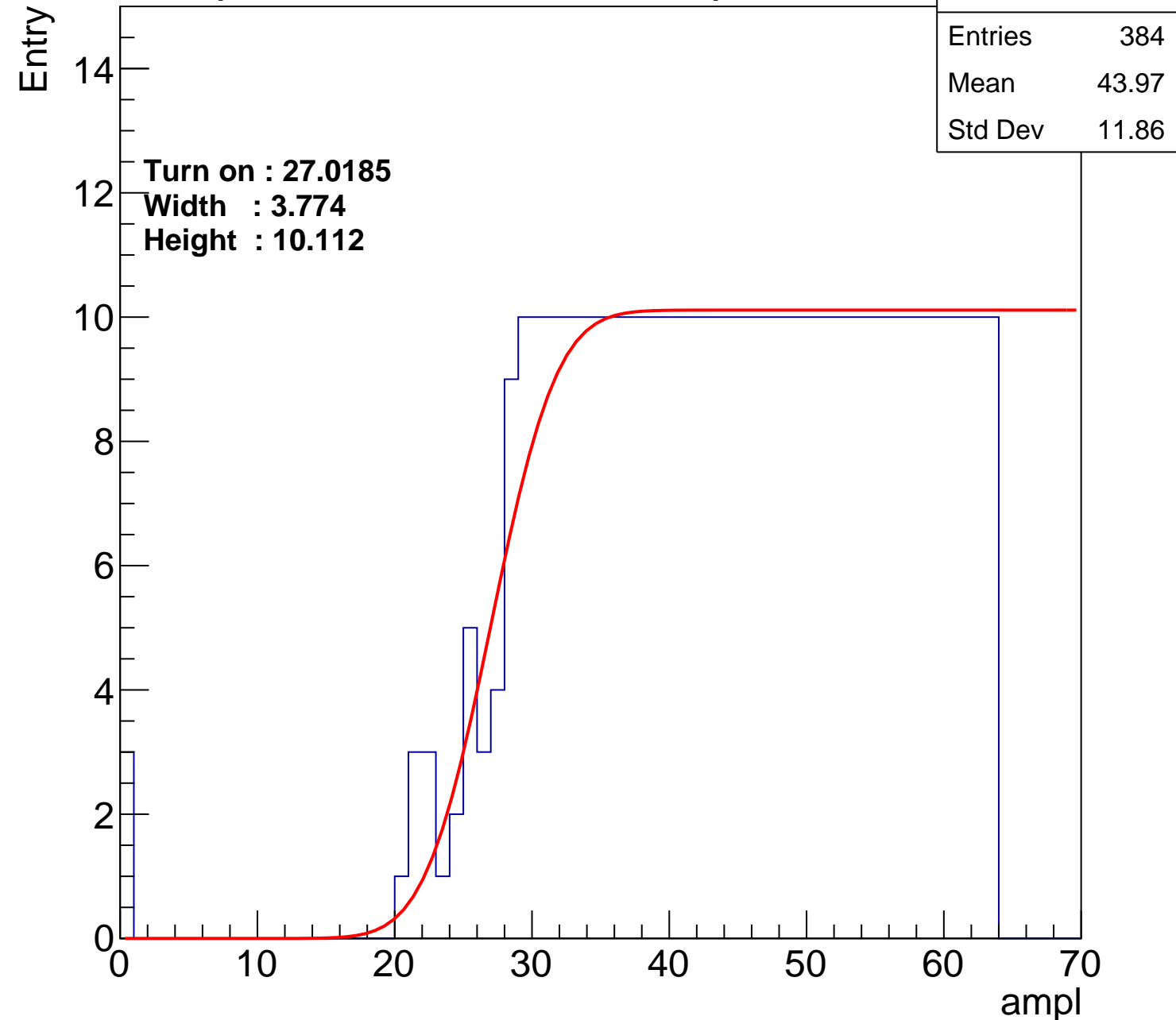
Width : 3.774

Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.63
Std Dev	11.89

Turn on : 25.3237

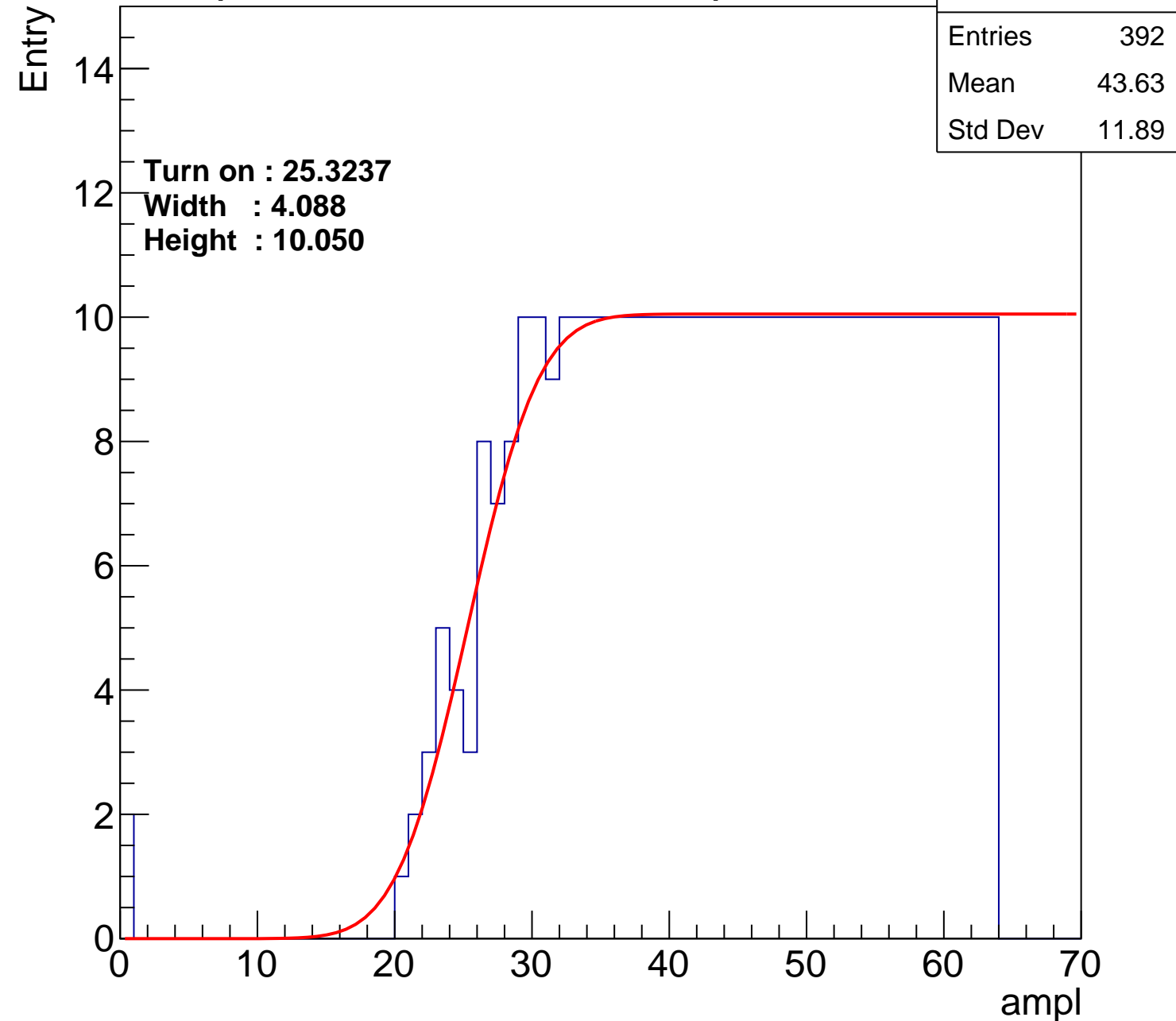
Width : 4.088

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch91

calib_packv5_042523_0143.root, FC#13, port D2

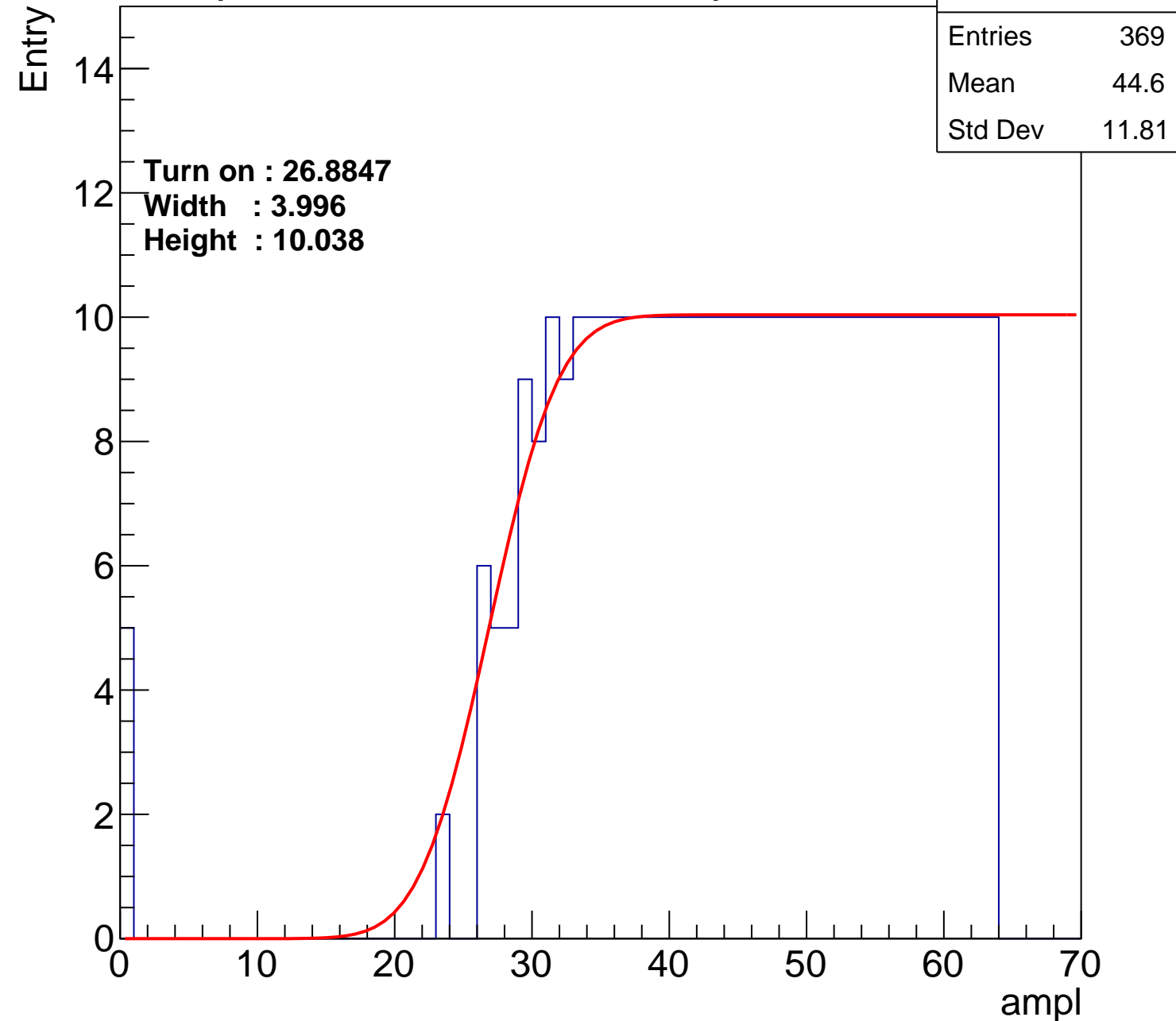
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8847
Width : 3.996
Height : 10.038

Entries	369
Mean	44.6
Std Dev	11.81

ampl



B1L003S, U16-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.07
Std Dev	11.47

Turn on : 28.4021

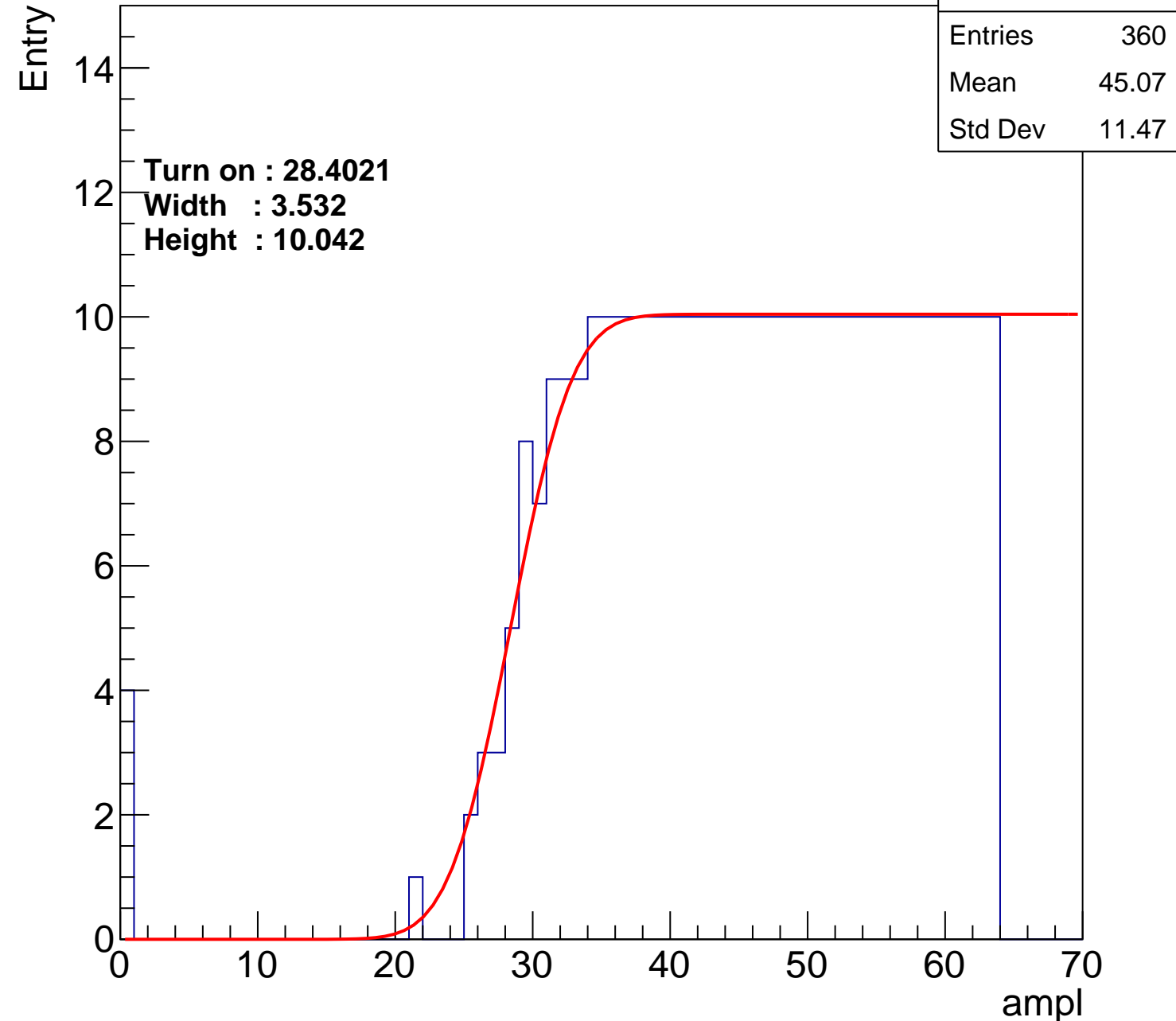
Width : 3.532

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.87
Std Dev	11.57

Turn on : 28.4661

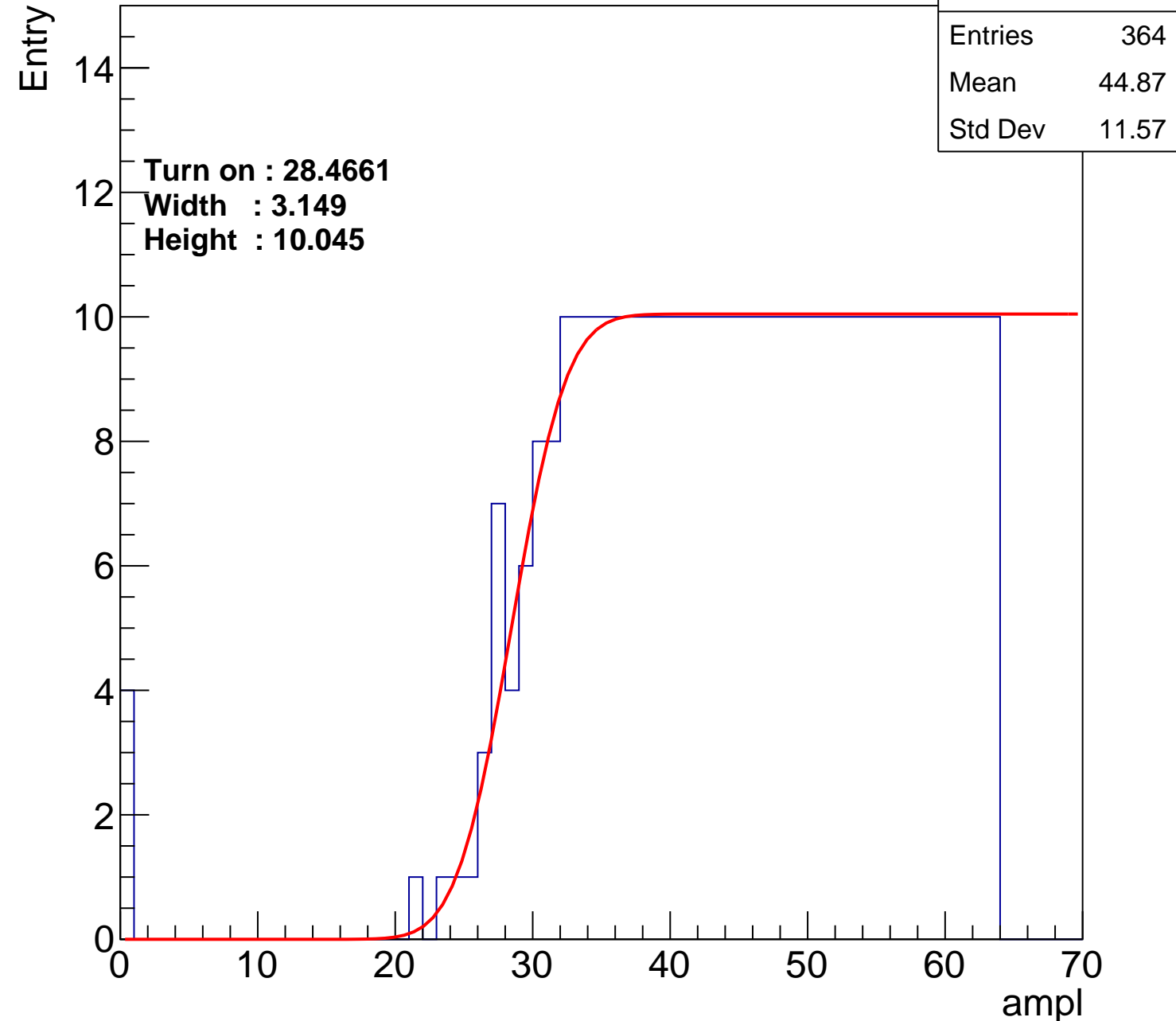
Width : 3.149

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch94

calib_packv5_042523_0143.root, FC#13, port D2

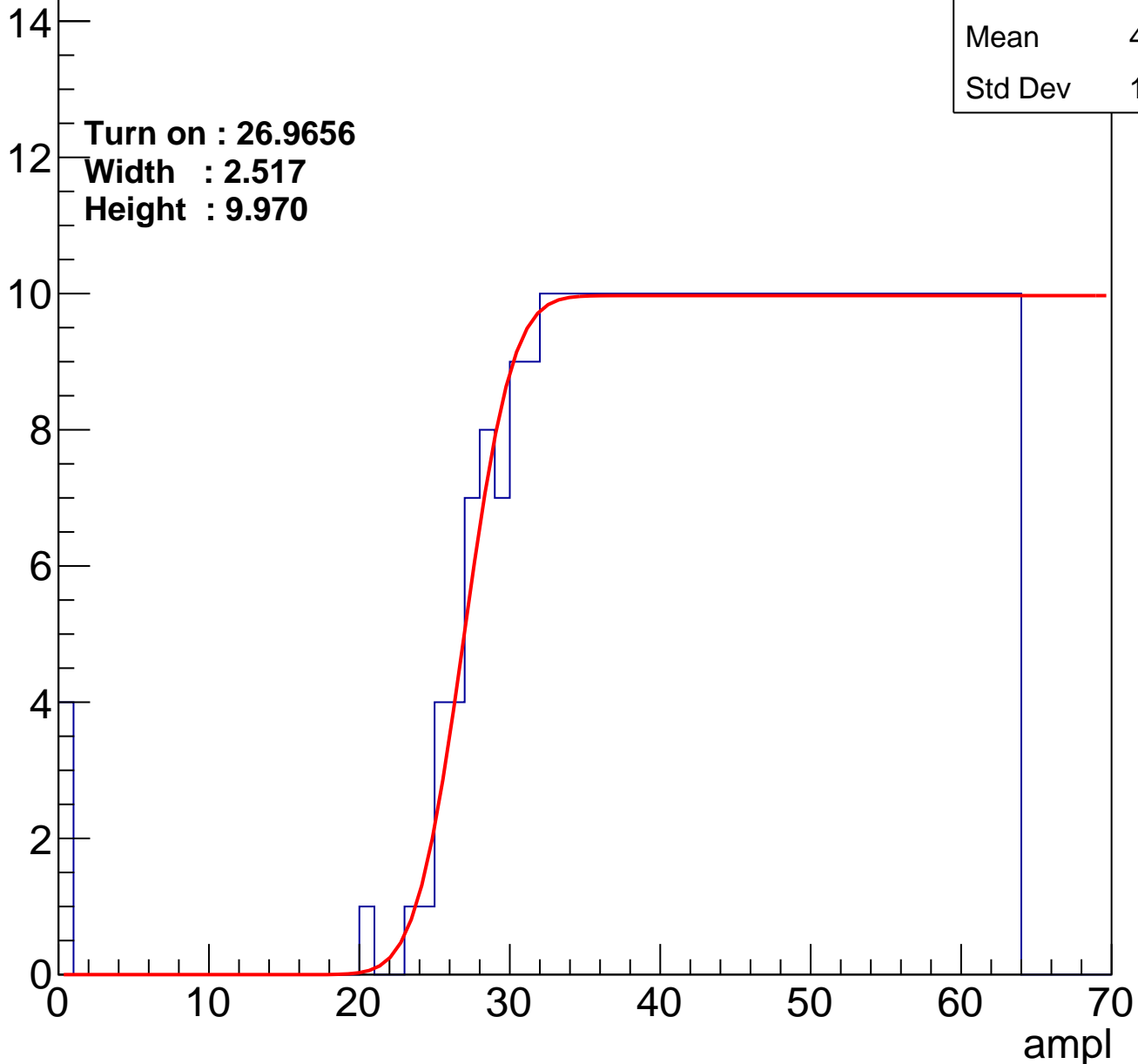
Entries	375
Mean	44.36
Std Dev	11.77

Turn on : 26.9656

Width : 2.517

Height : 9.970

Entry



B1L003S, U16-ch95

calib_packv5_042523_0143.root, FC#13, port D2

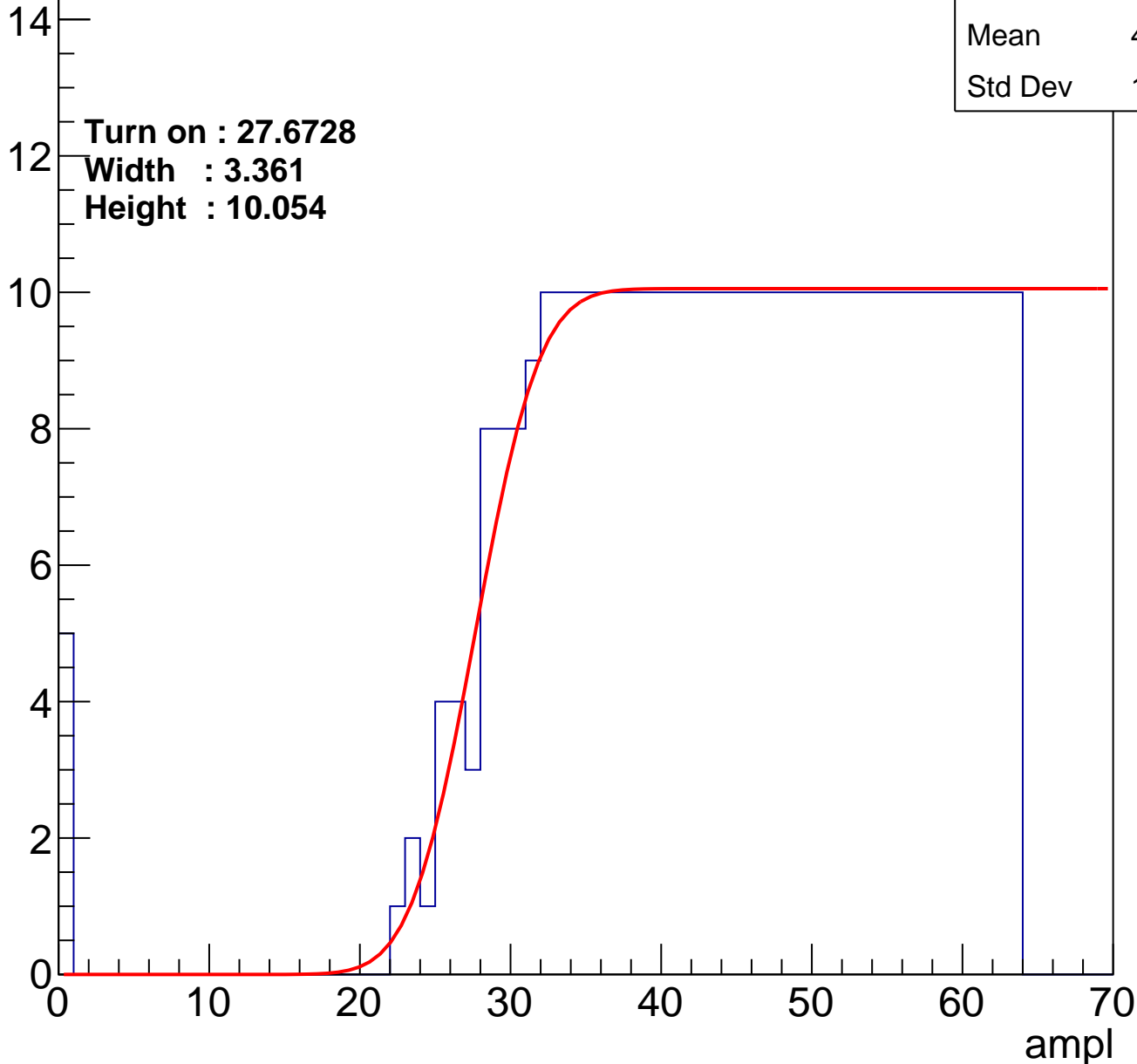
Entries	373
Mean	44.38
Std Dev	11.94

Turn on : 27.6728

Width : 3.361

Height : 10.054

Entry



B1L003S, U16-ch96

calib_packv5_042523_0143.root, FC#13, port D2

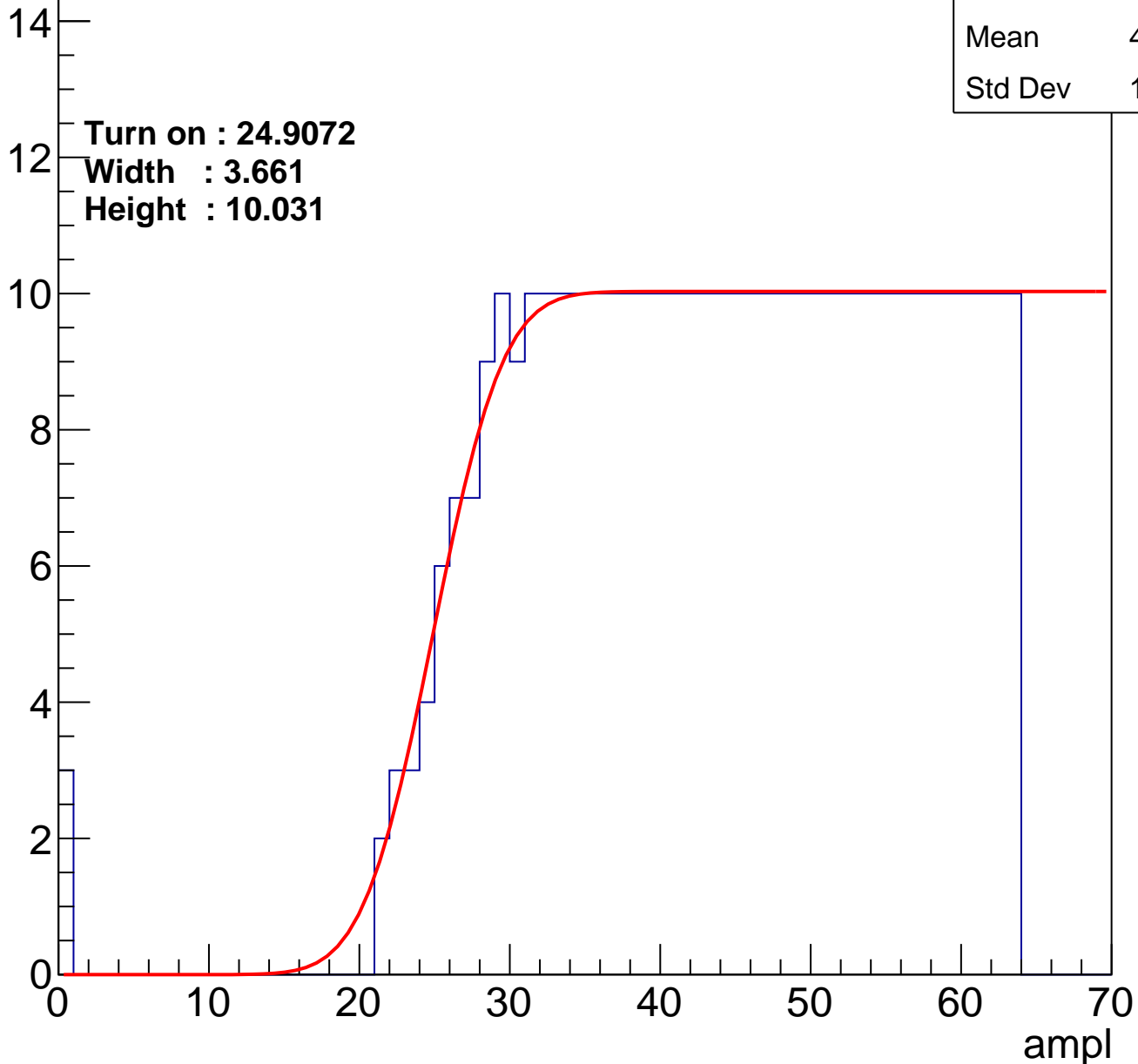
Entries	393
Mean	43.55
Std Dev	12.02

Turn on : 24.9072

Width : 3.661

Height : 10.031

Entry



B1L003S, U16-ch97

calib_packv5_042523_0143.root, FC#13, port D2

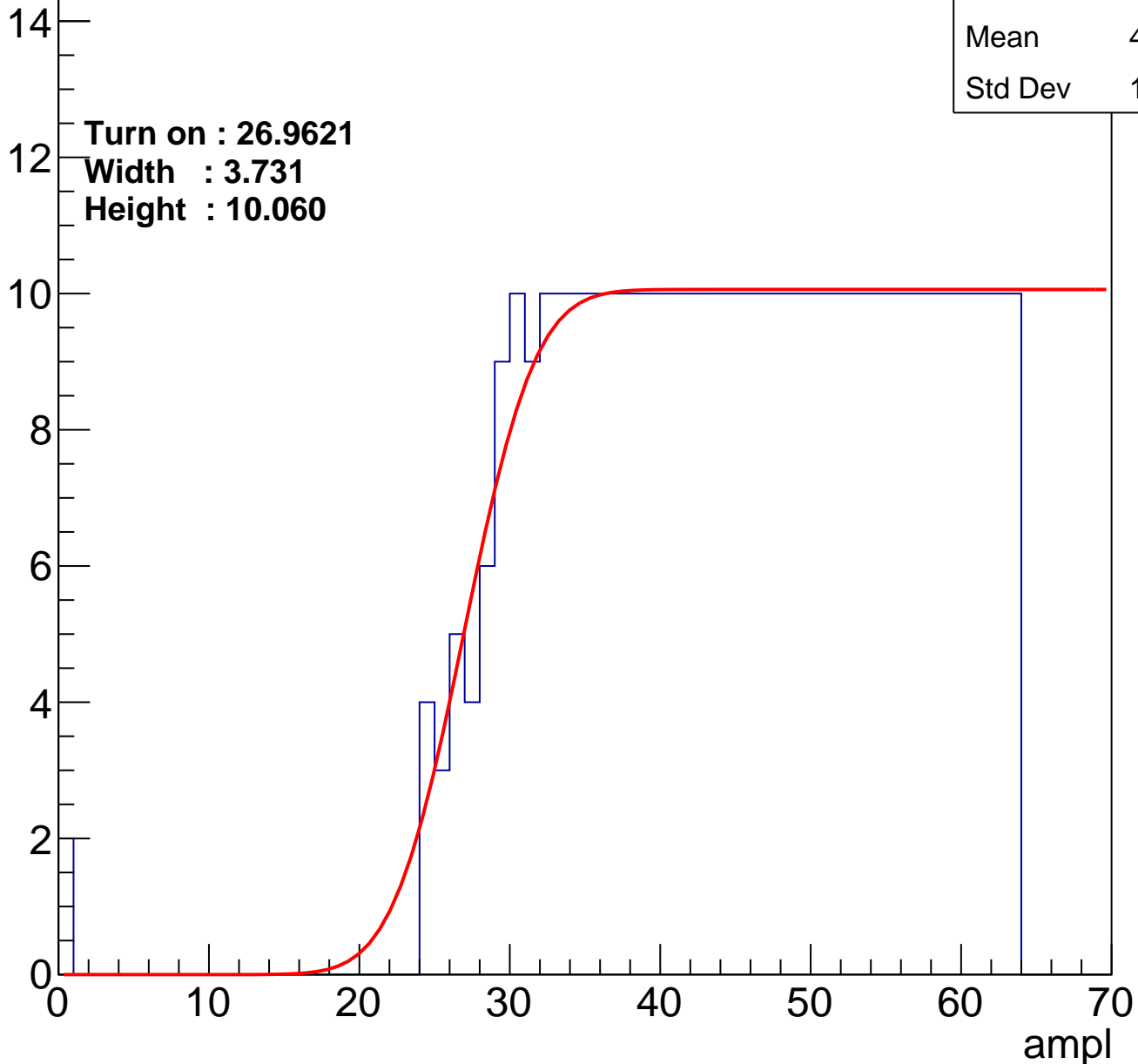
Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 26.9621

Width : 3.731

Height : 10.060

Entry



B1L003S, U16-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.3160

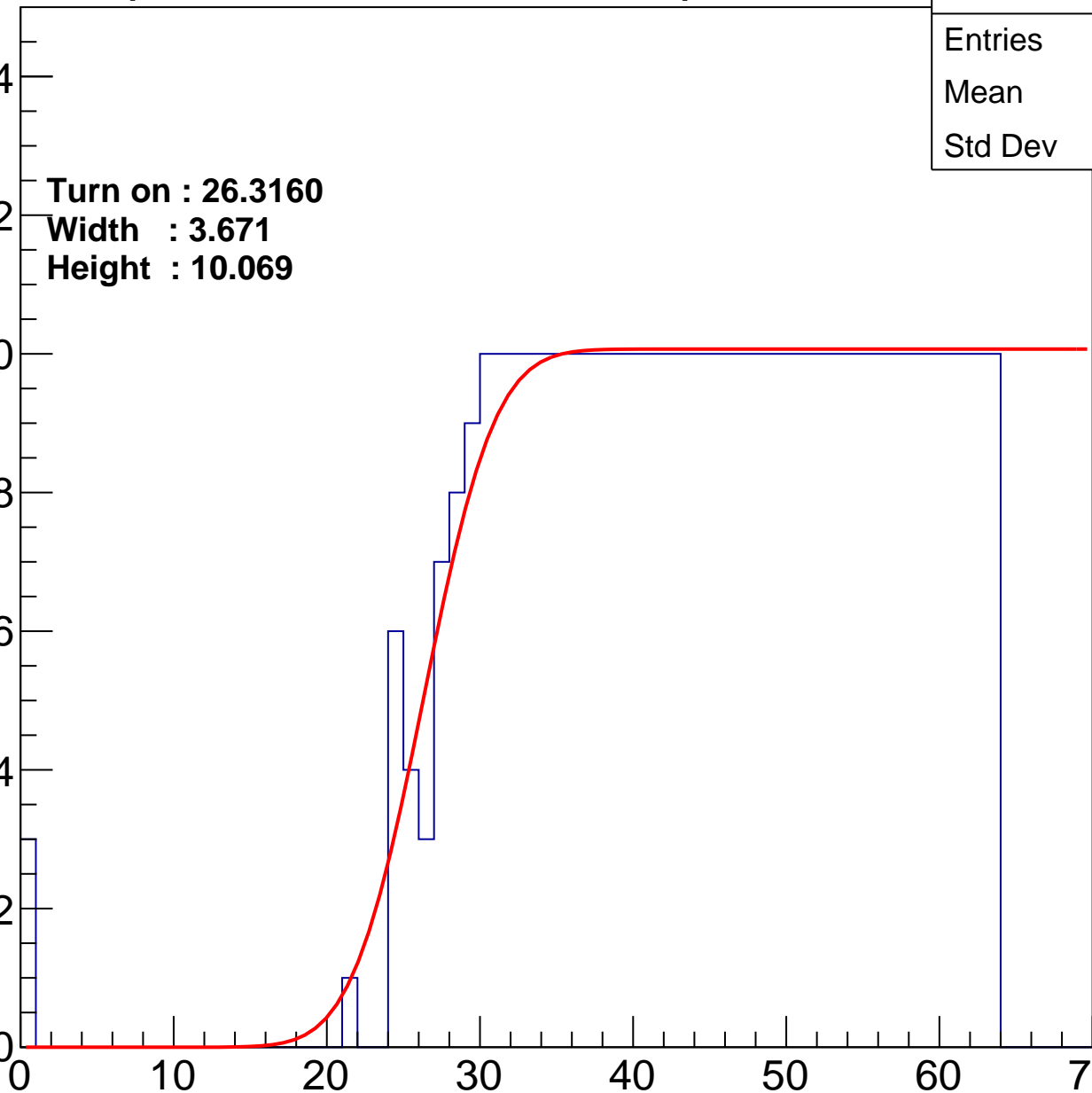
Width : 3.671

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.77
Std Dev	11.49

Turn on : 28.5692

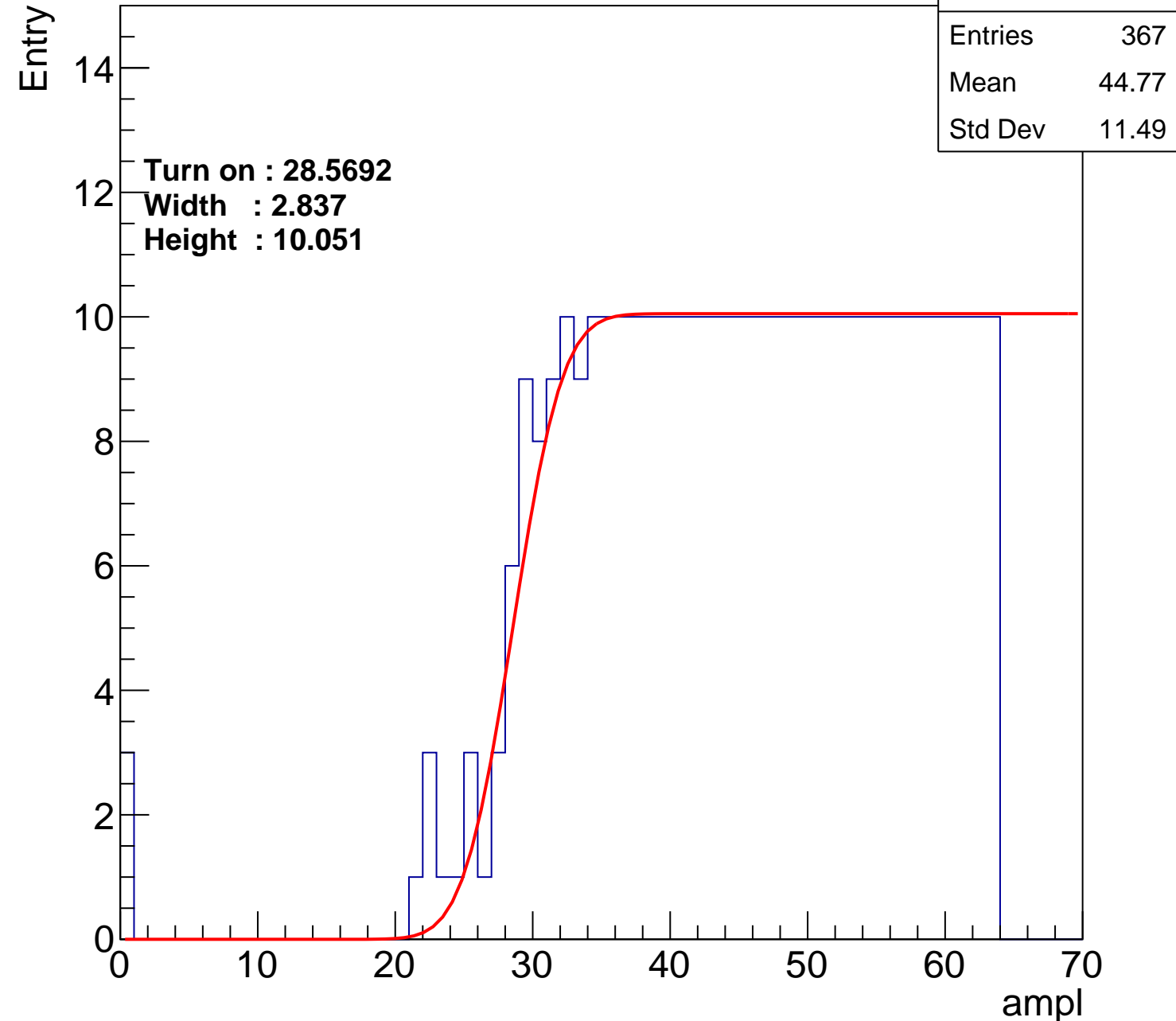
Width : 2.837

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch100

calib_packv5_042523_0143.root, FC#13, port D2

Entries	393
Mean	43.51
Std Dev	12.15

Turn on : 25.1960

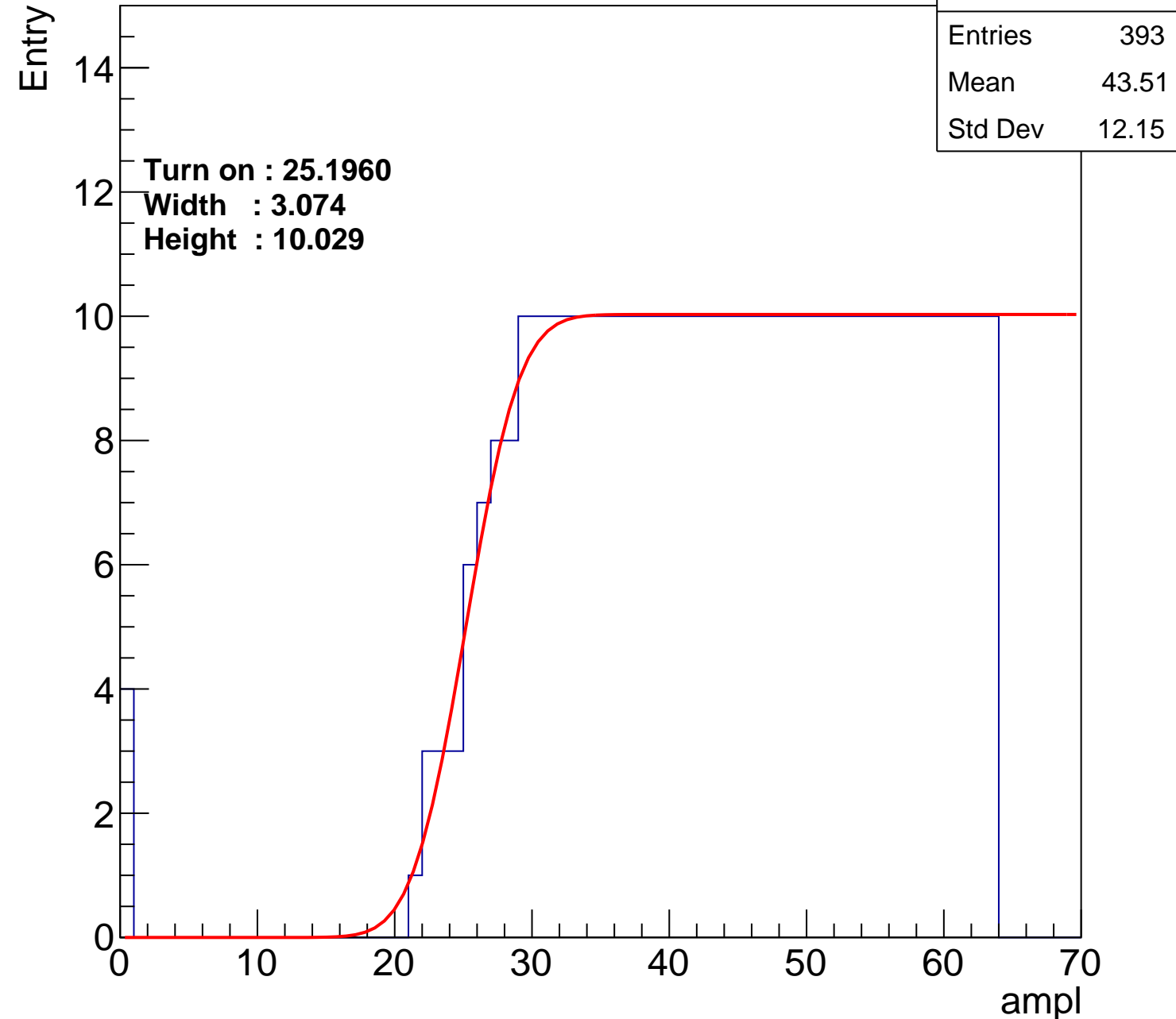
Width : 3.074

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch101

calib_packv5_042523_0143.root, FC#13, port D2

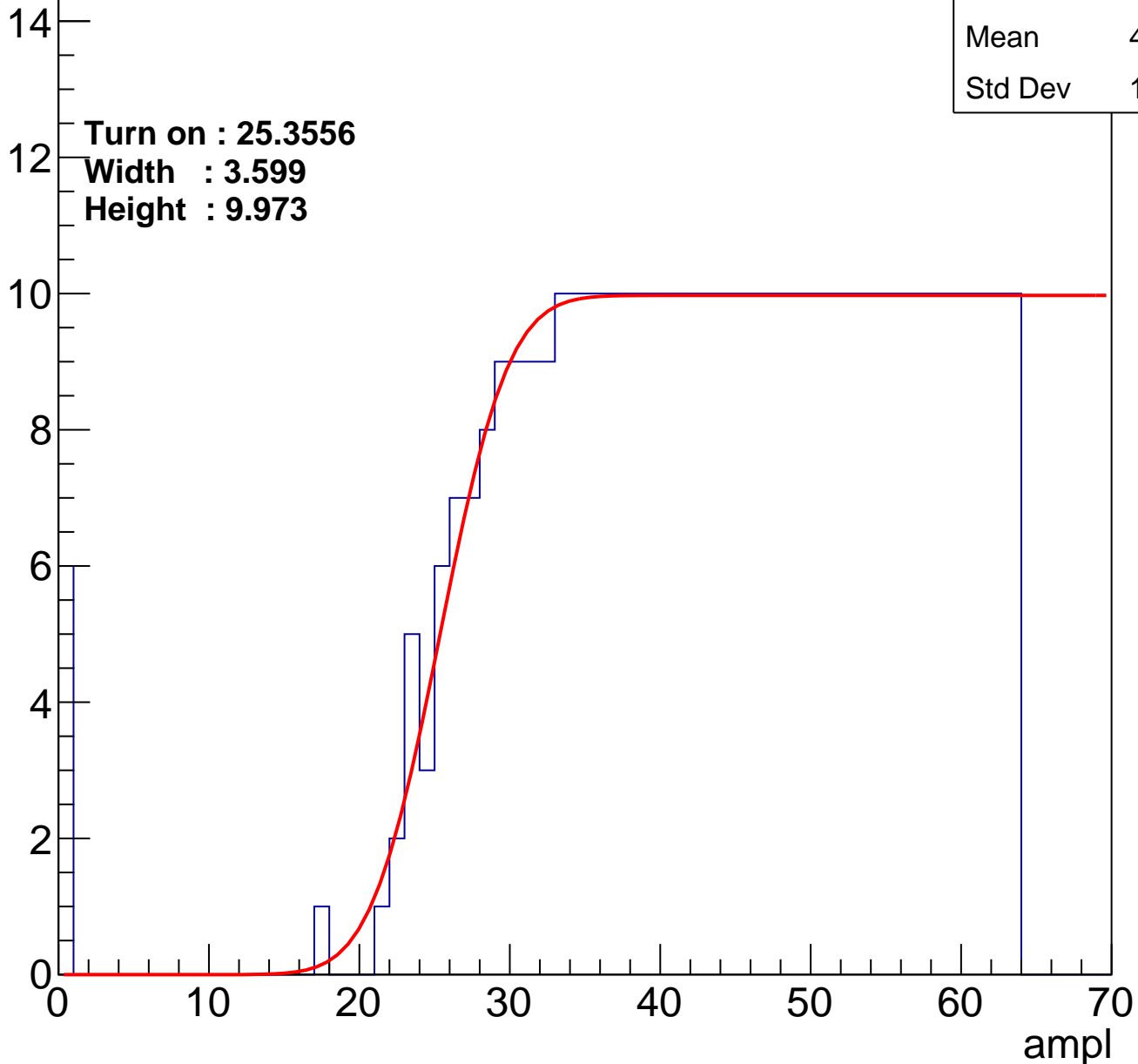
Entries	392
Mean	43.35
Std Dev	12.57

Turn on : 25.3556

Width : 3.599

Height : 9.973

Entry



B1L003S, U16-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.69
Std Dev	11.18

Turn on : 27.0807

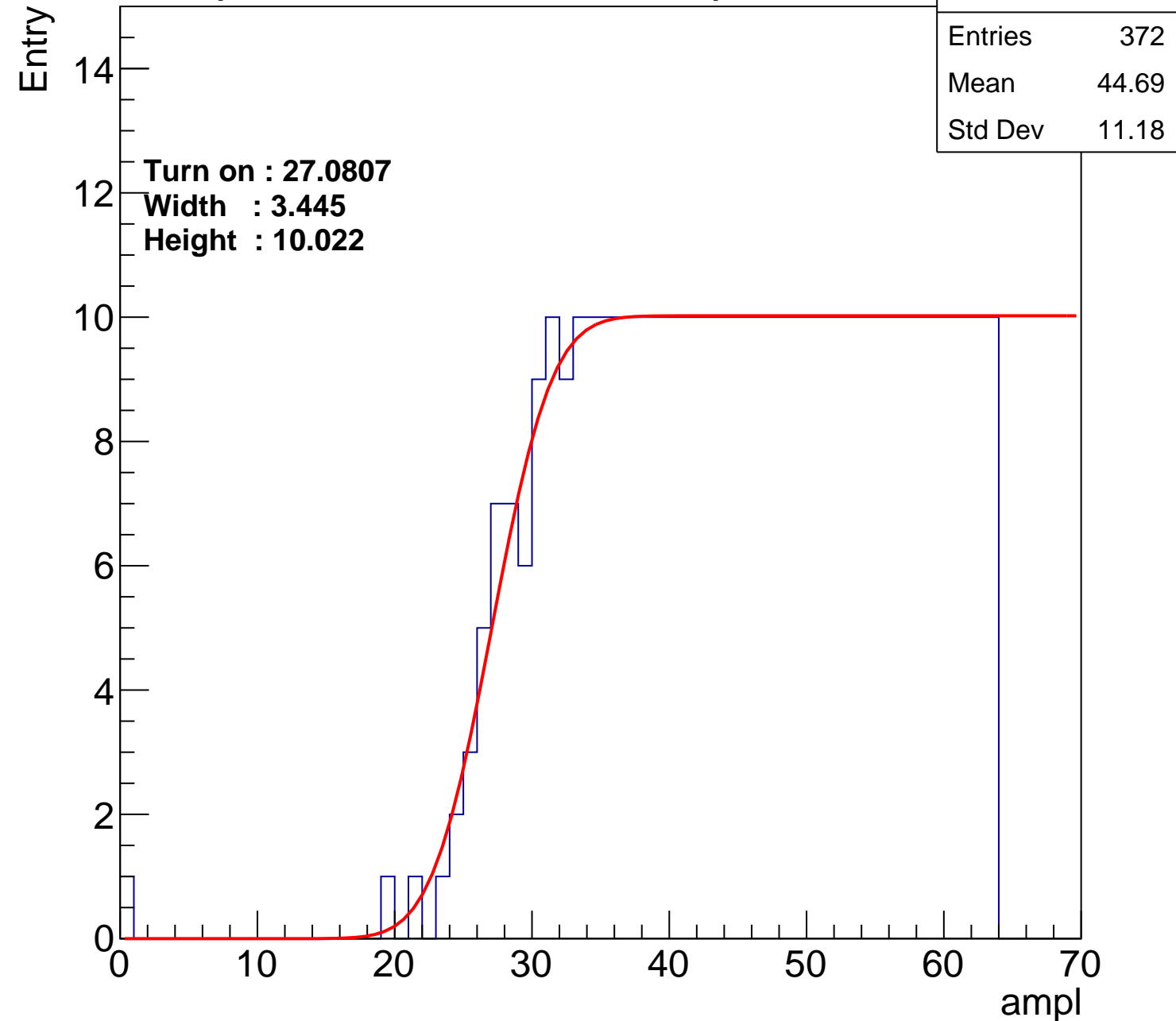
Width : 3.445

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch103

calib_packv5_042523_0143.root, FC#13, port D2

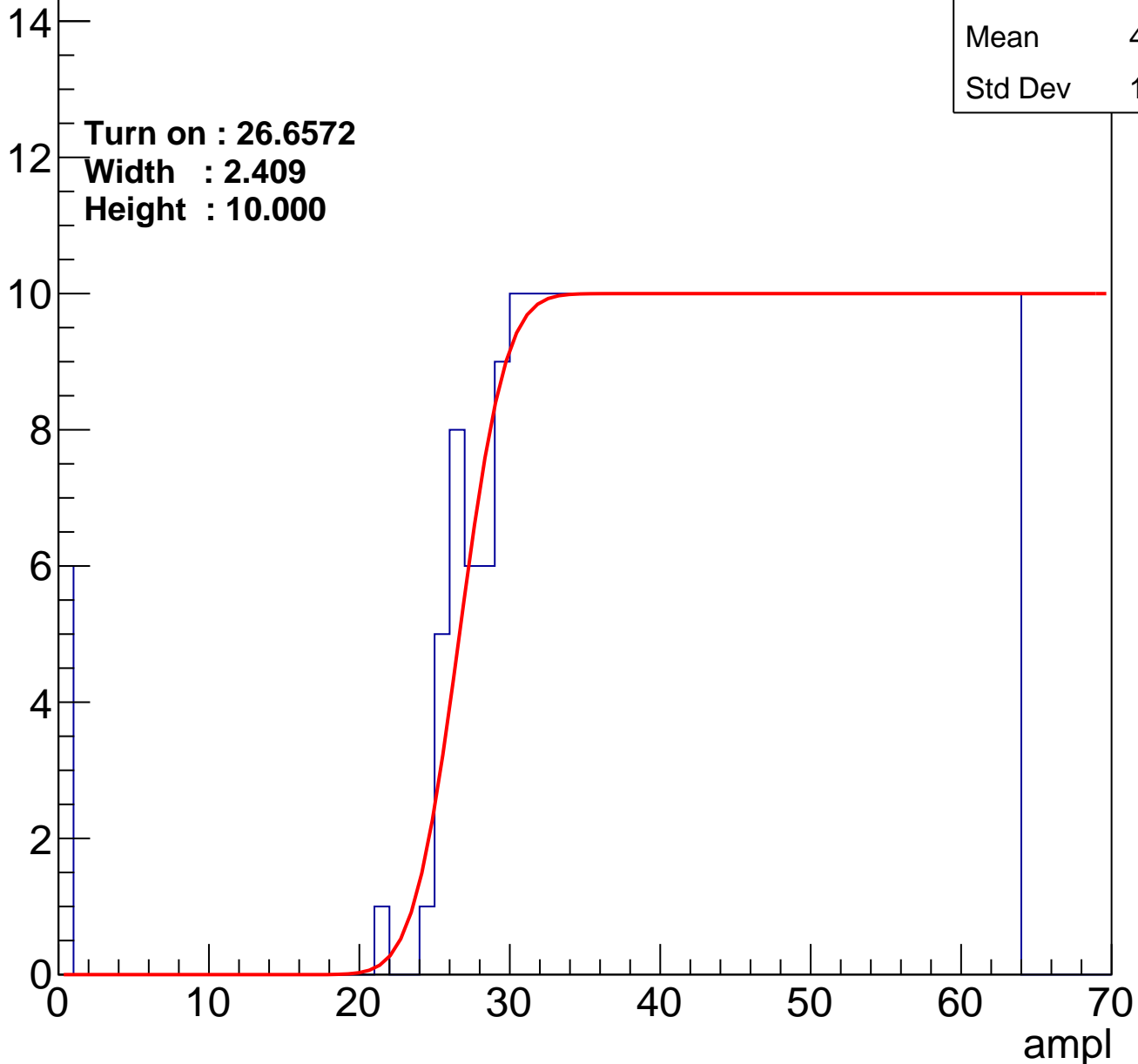
Entries	382
Mean	43.92
Std Dev	12.22

Turn on : 26.6572

Width : 2.409

Height : 10.000

Entry



B1L003S, U16-ch104

calib_packv5_042523_0143.root, FC#13, port D2

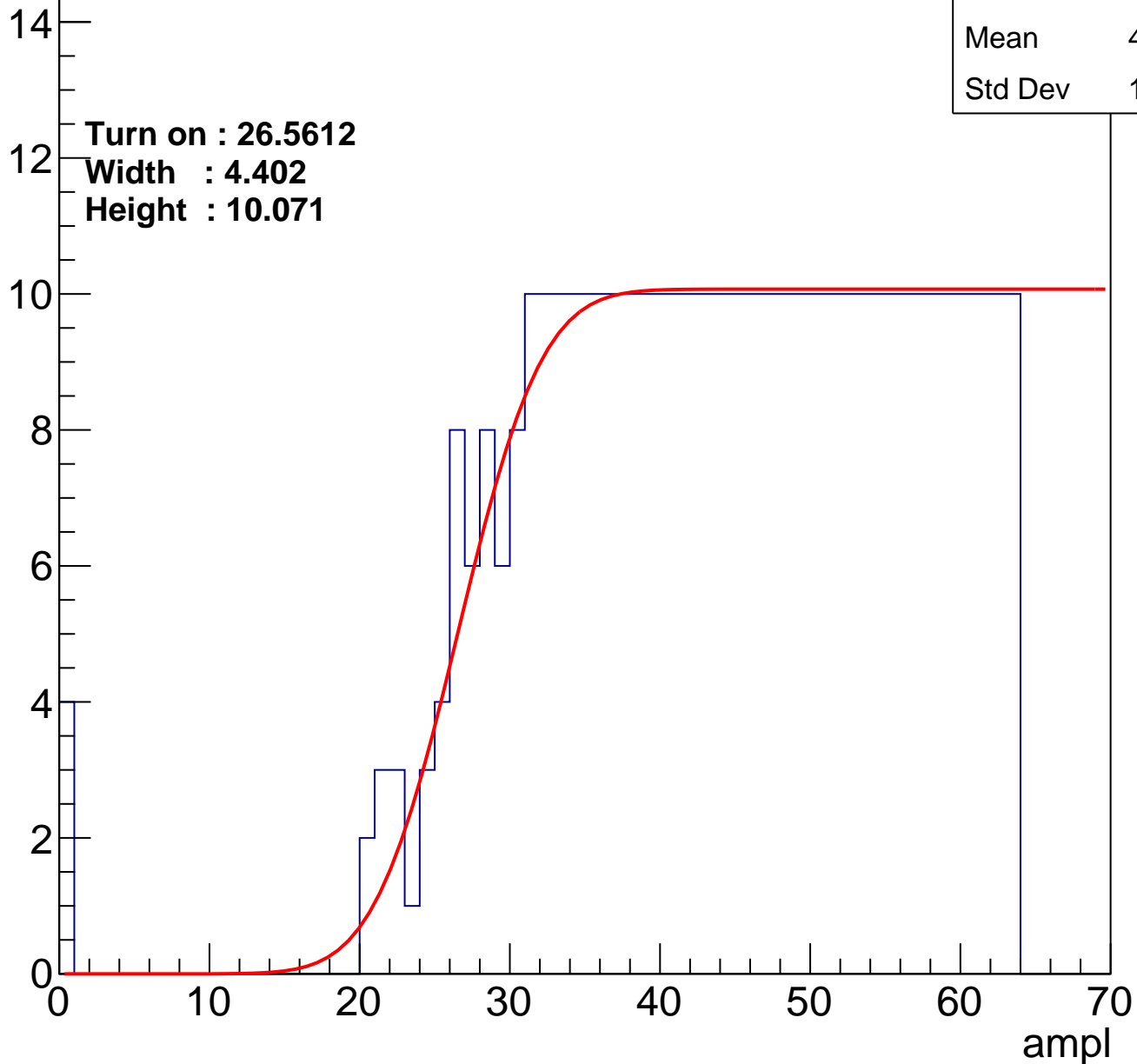
Entries	386
Mean	43.74
Std Dev	12.17

Turn on : 26.5612

Width : 4.402

Height : 10.071

Entry



B1L003S, U16-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.5650

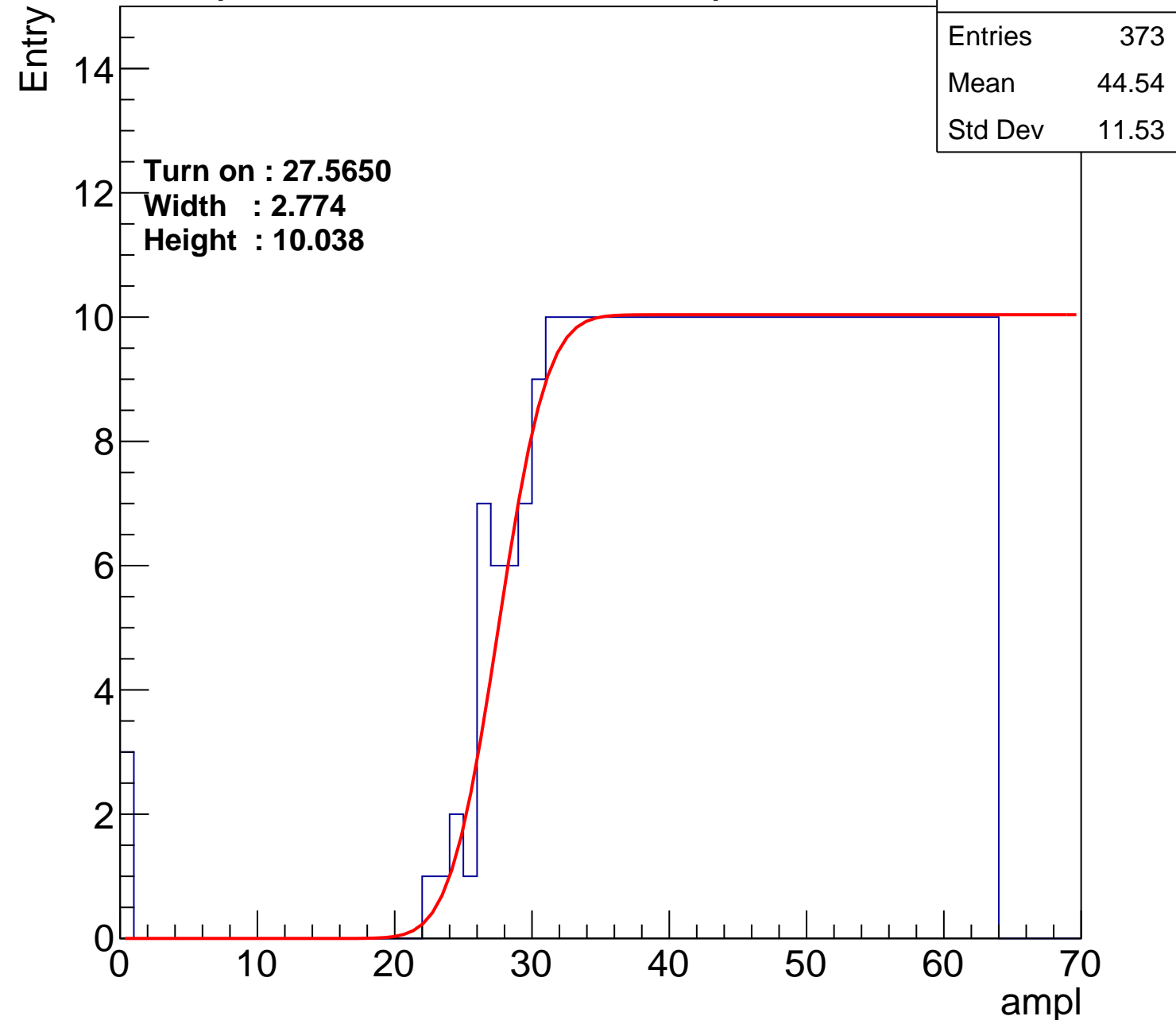
Width : 2.774

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 28.0368

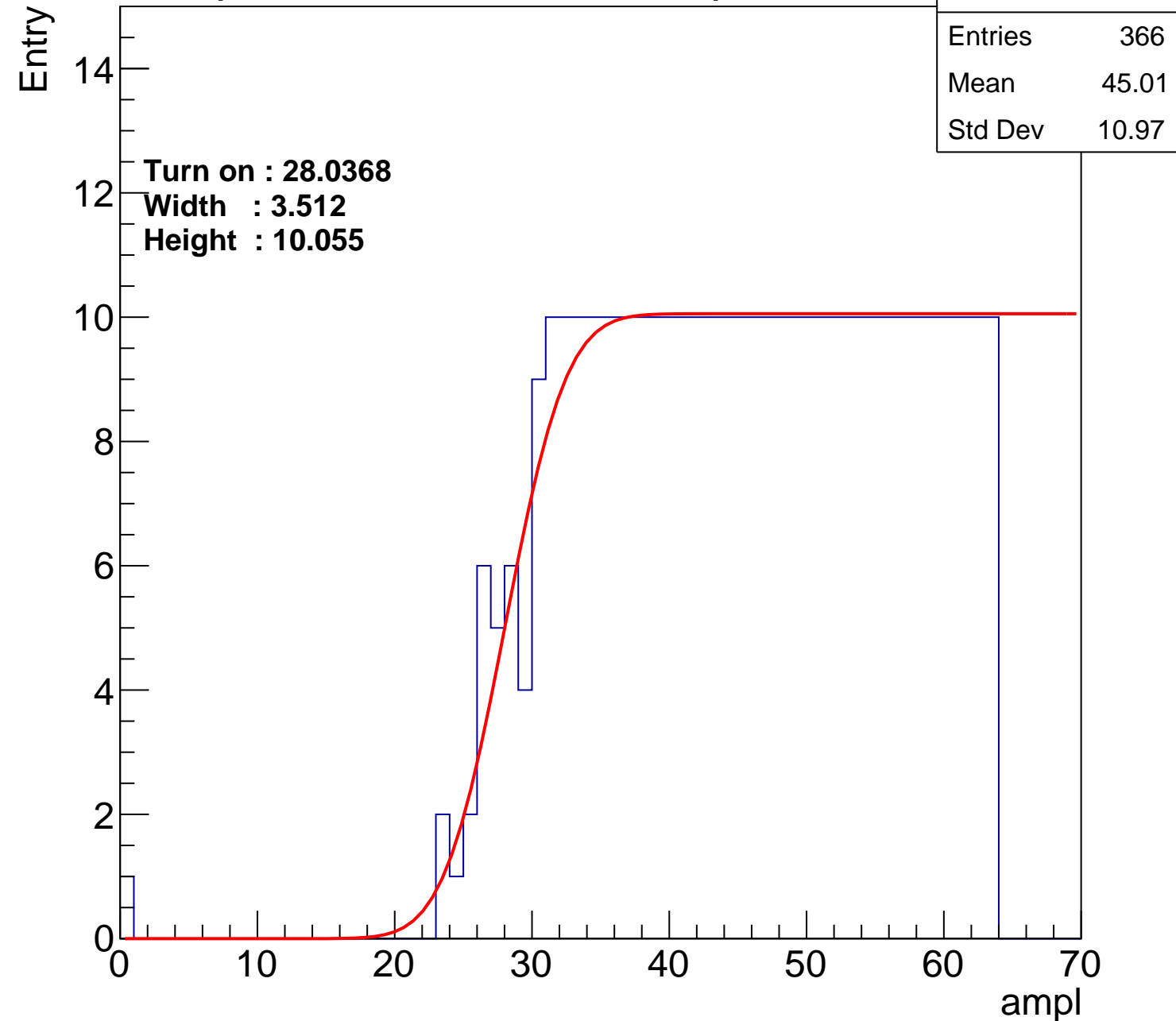
Width : 3.512

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 27.8470

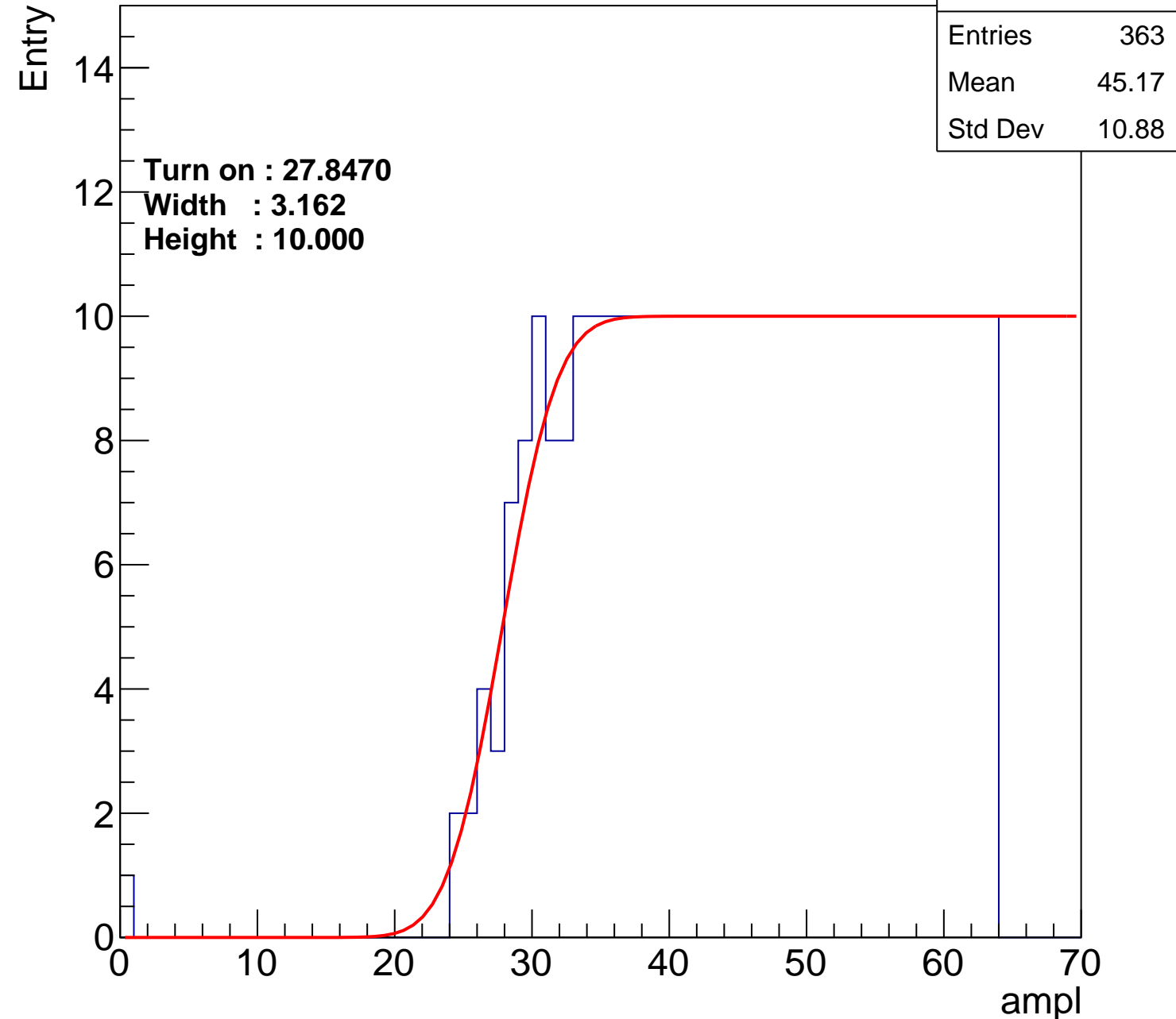
Width : 3.162

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	399
Mean	43.09
Std Dev	12.61

Turn on : 25.0188

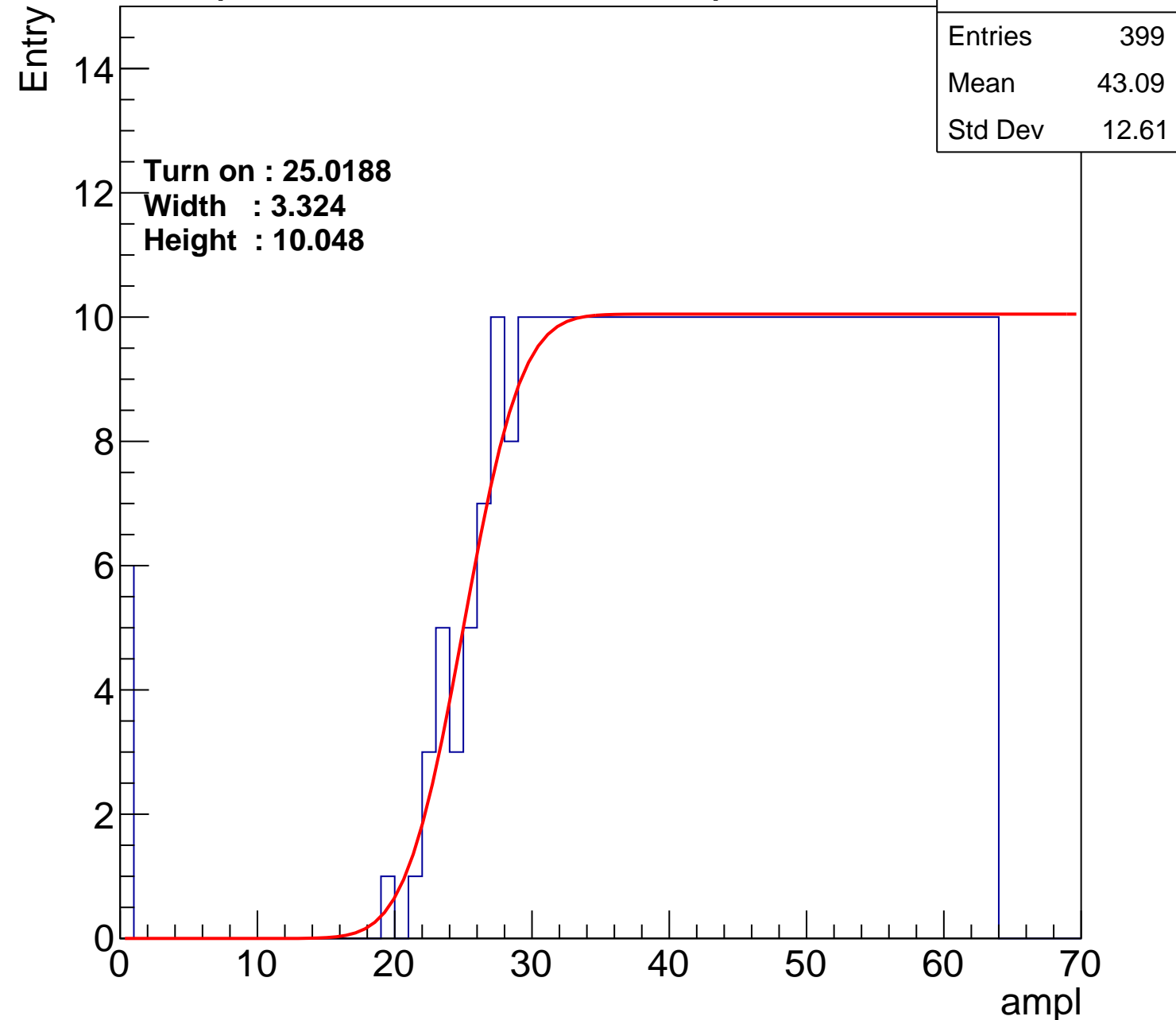
Width : 3.324

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.28
Std Dev	11.16

Turn on : 28.9941

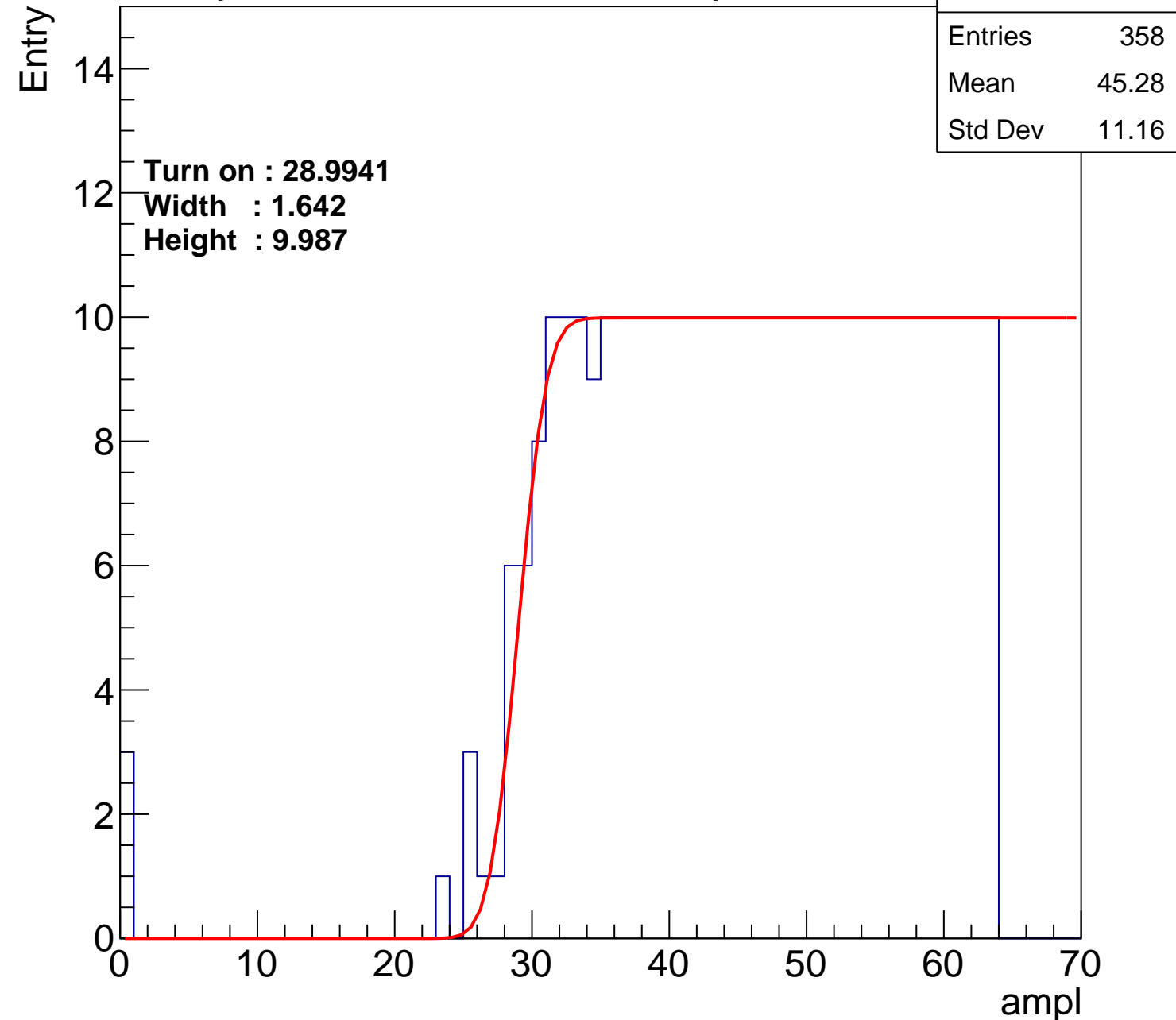
Width : 1.642

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.12
Std Dev	11.87

Turn on : 26.3366

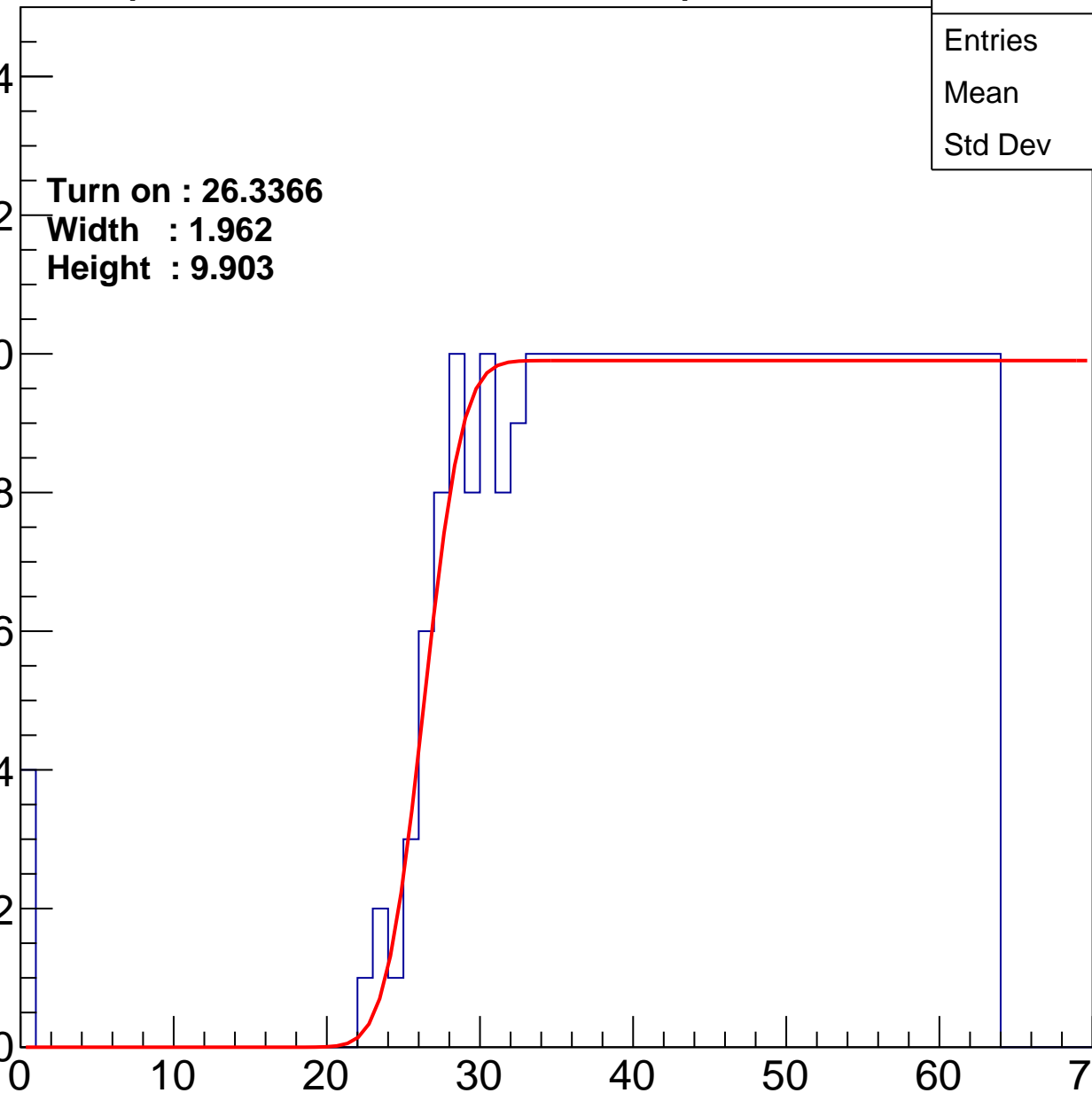
Width : 1.962

Height : 9.903

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.9064

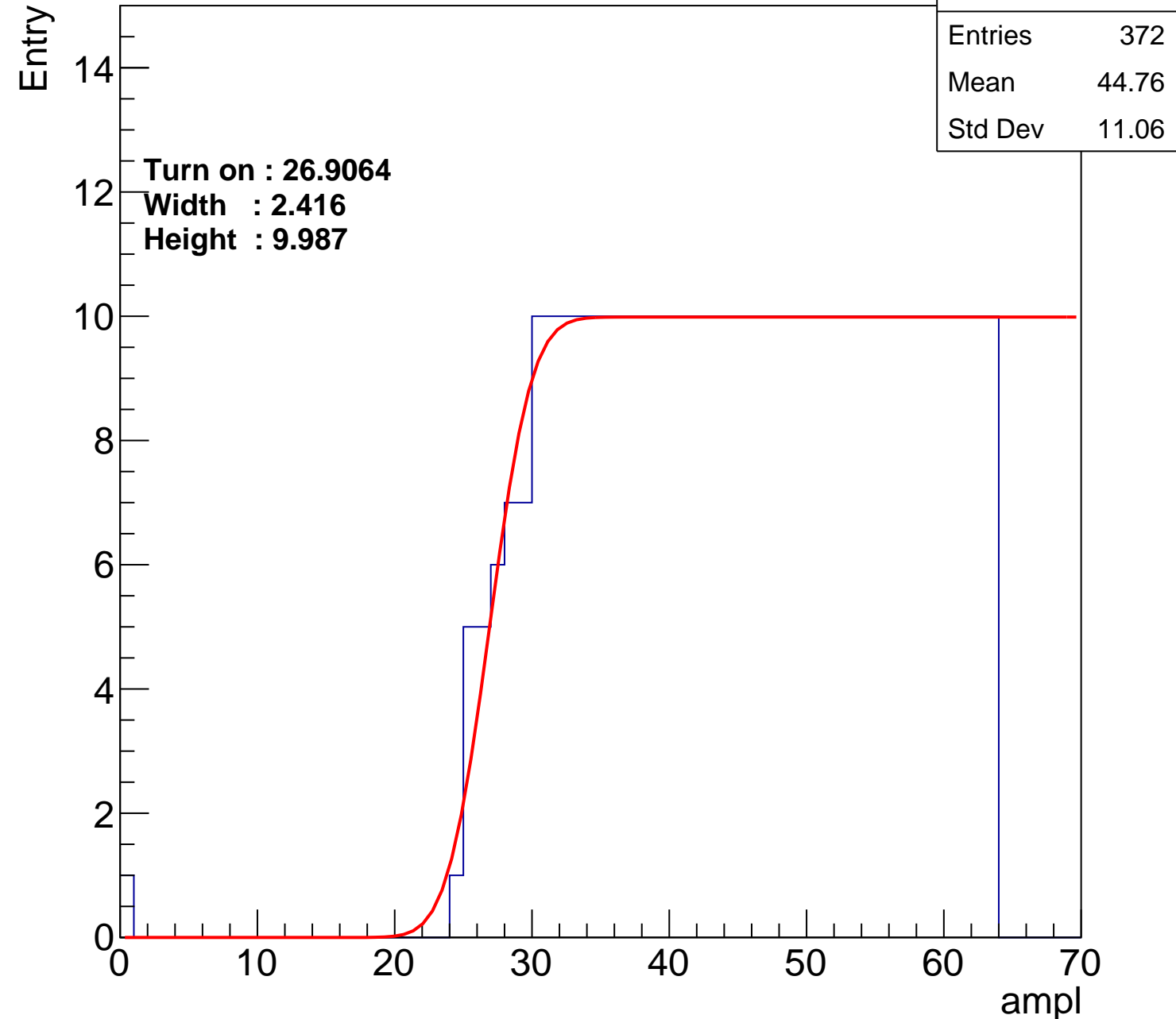
Width : 2.416

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.72
Std Dev	12.2

Turn on : 25.8585

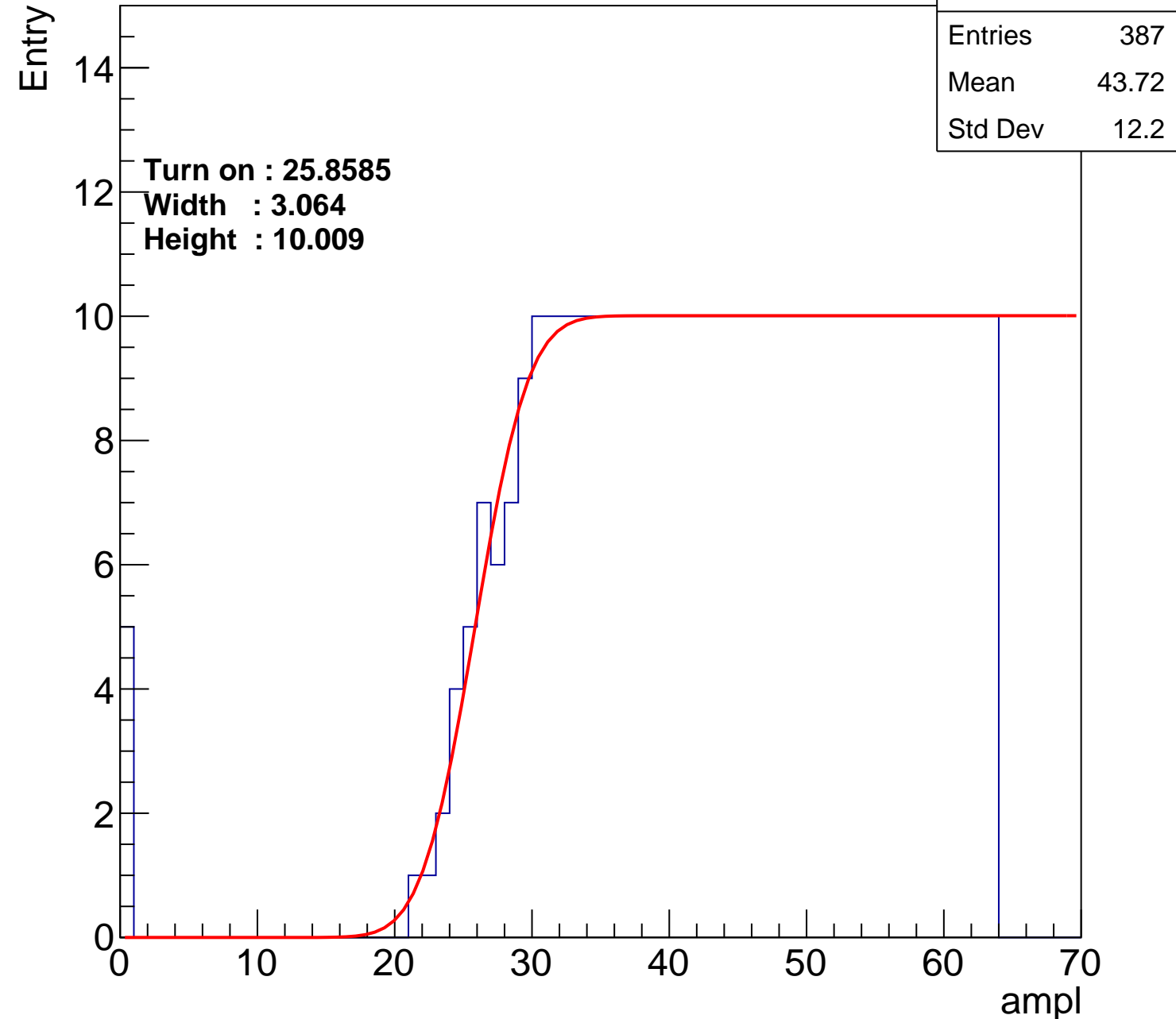
Width : 3.064

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	44.99
Std Dev	11.53

Turn on : 28.8152

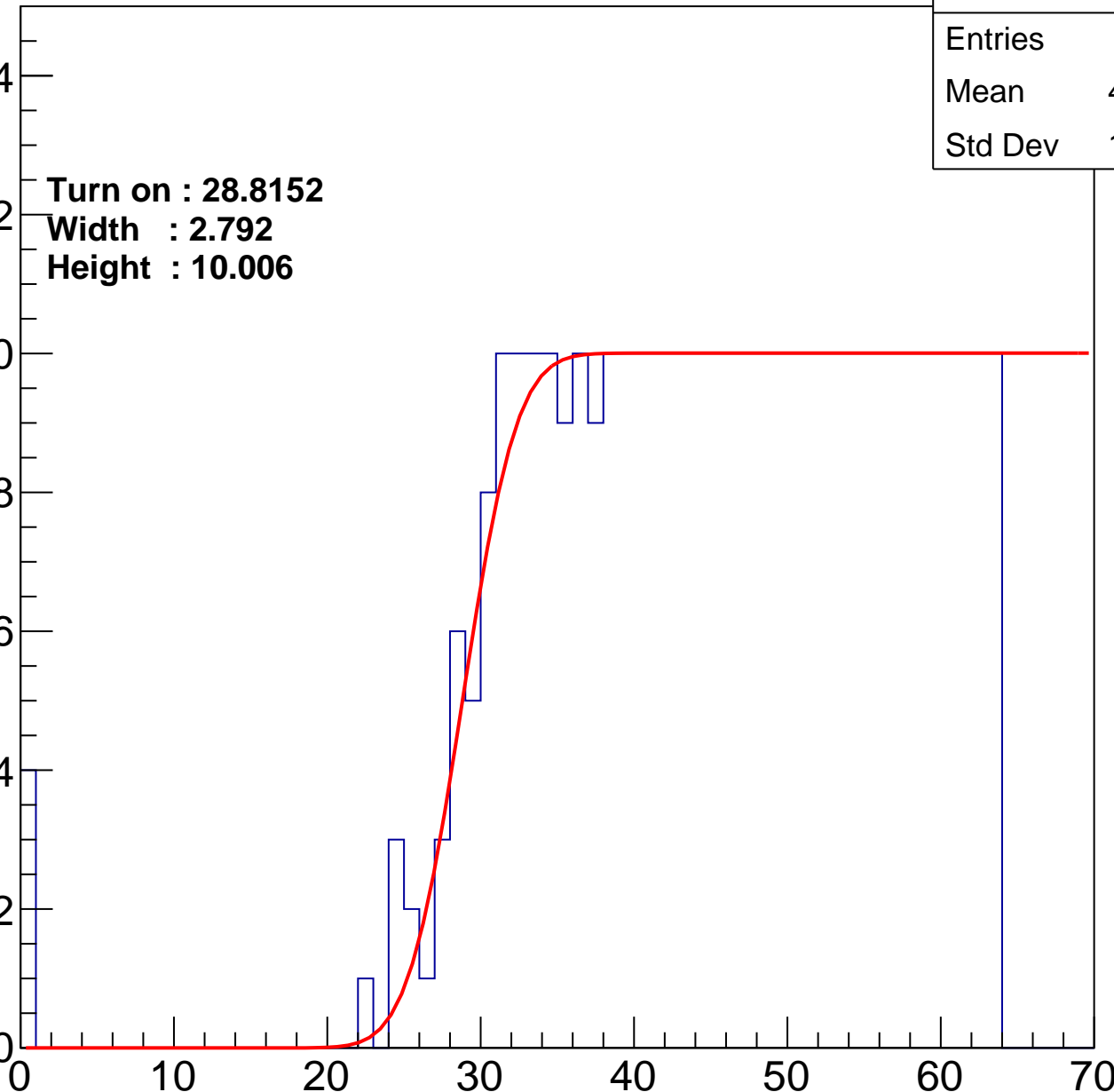
Width : 2.792

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	408
Mean	42.76
Std Dev	12.54

Turn on : 23.8450

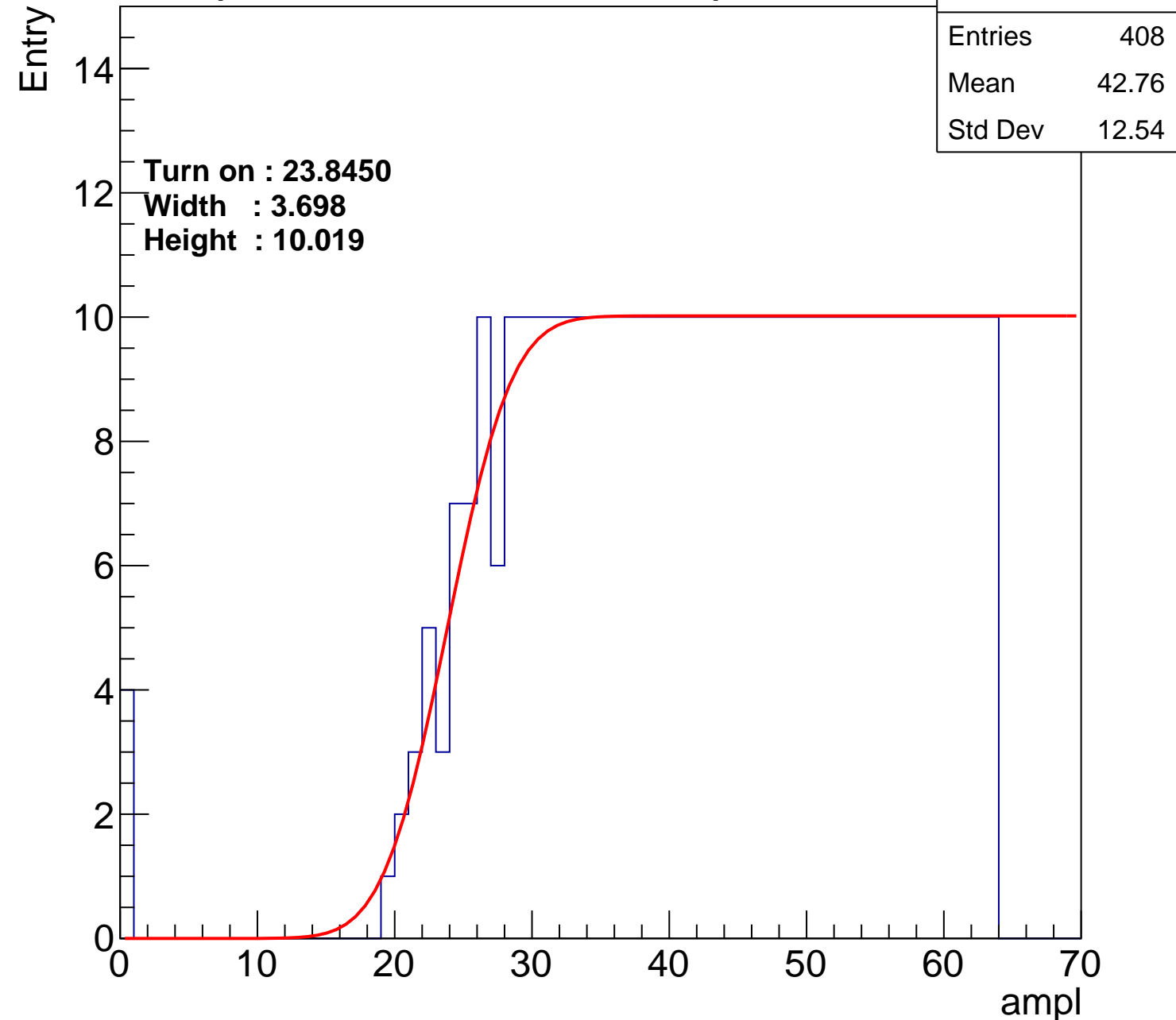
Width : 3.698

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch115

calib_packv5_042523_0143.root, FC#13, port D2

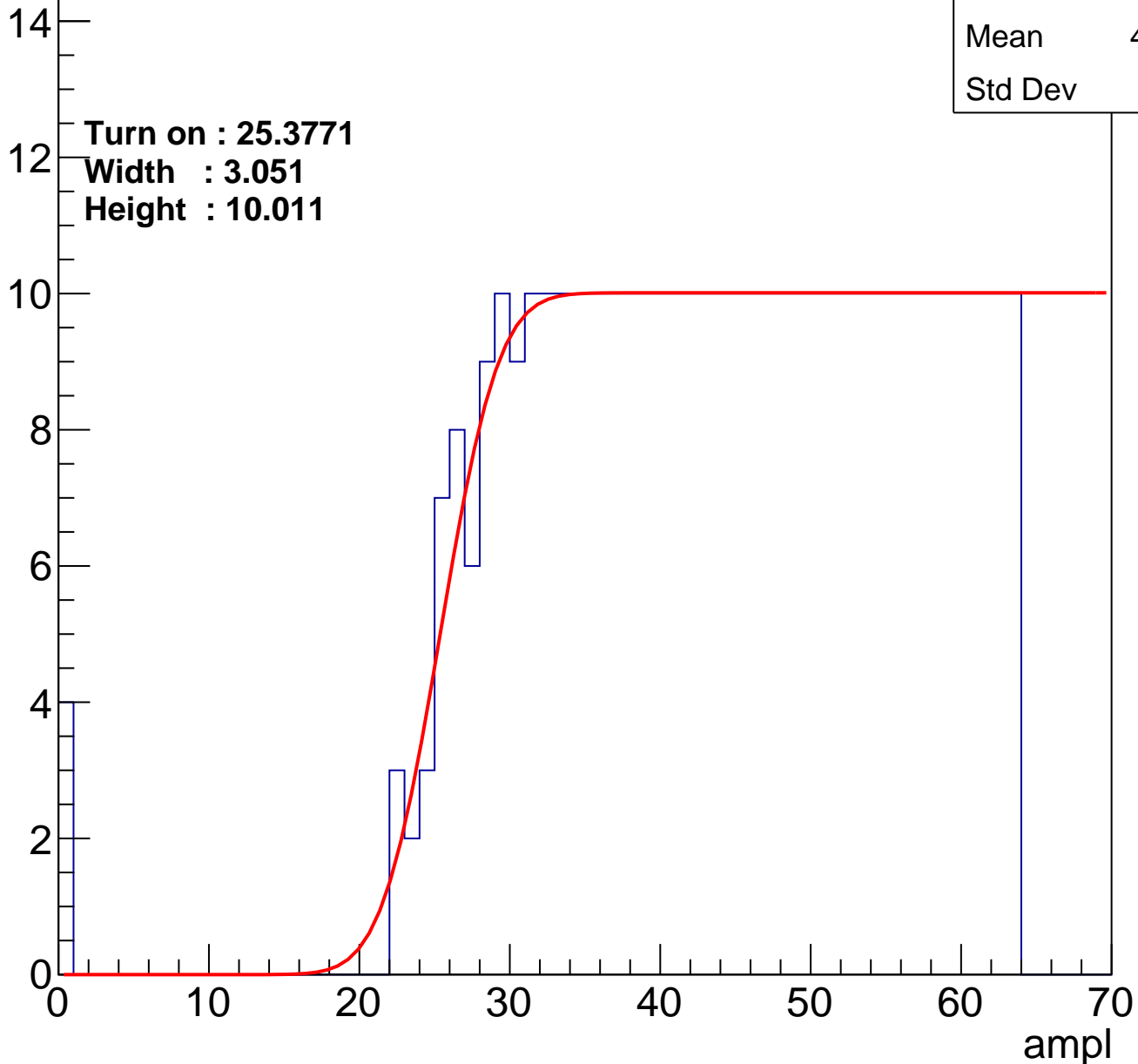
Entries	391
Mean	43.61
Std Dev	12.1

Turn on : 25.3771

Width : 3.051

Height : 10.011

Entry



B1L003S, U16-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.72
Std Dev	11.08

Turn on : 27.7751

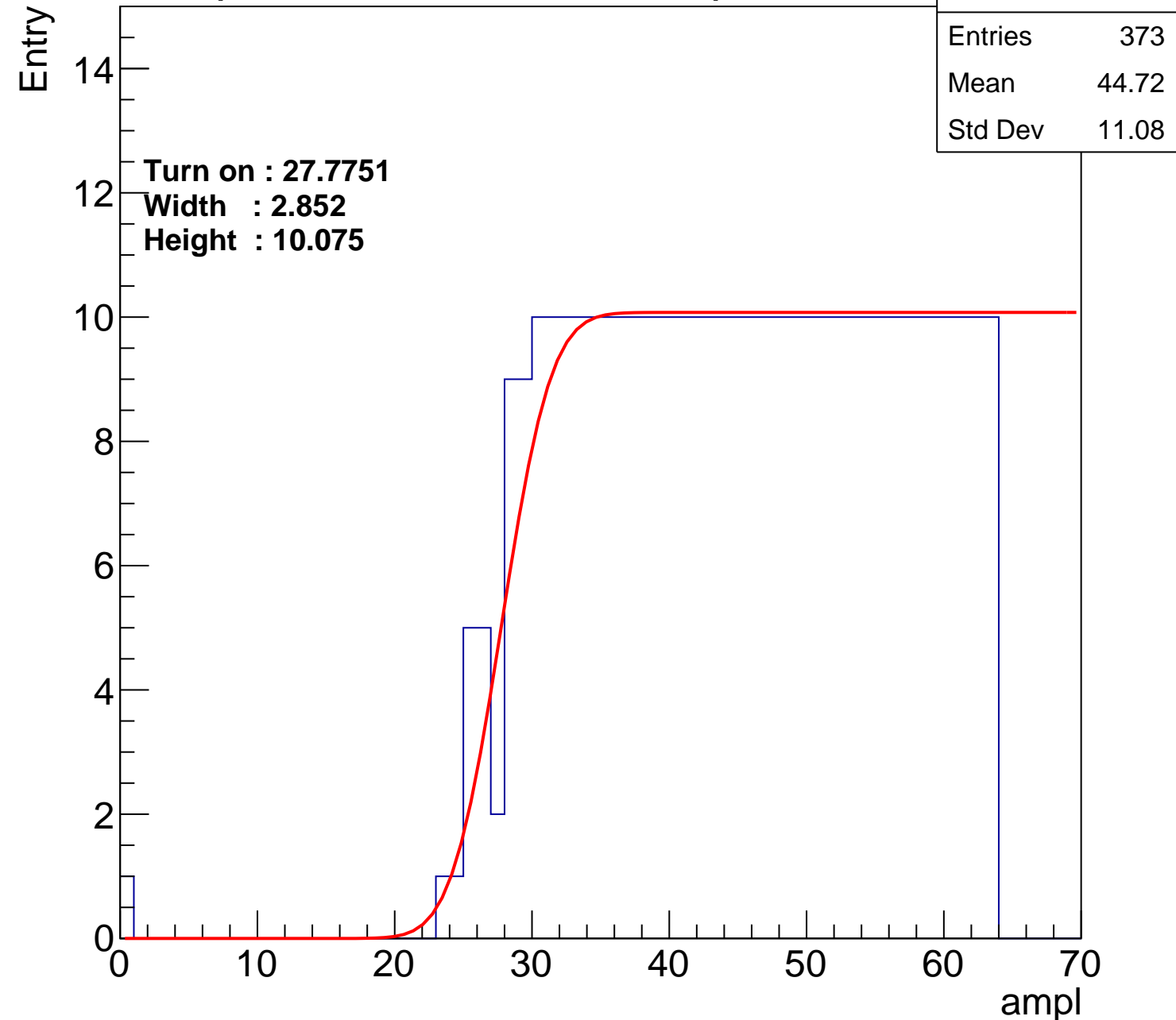
Width : 2.852

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch117

calib_packv5_042523_0143.root, FC#13, port D2

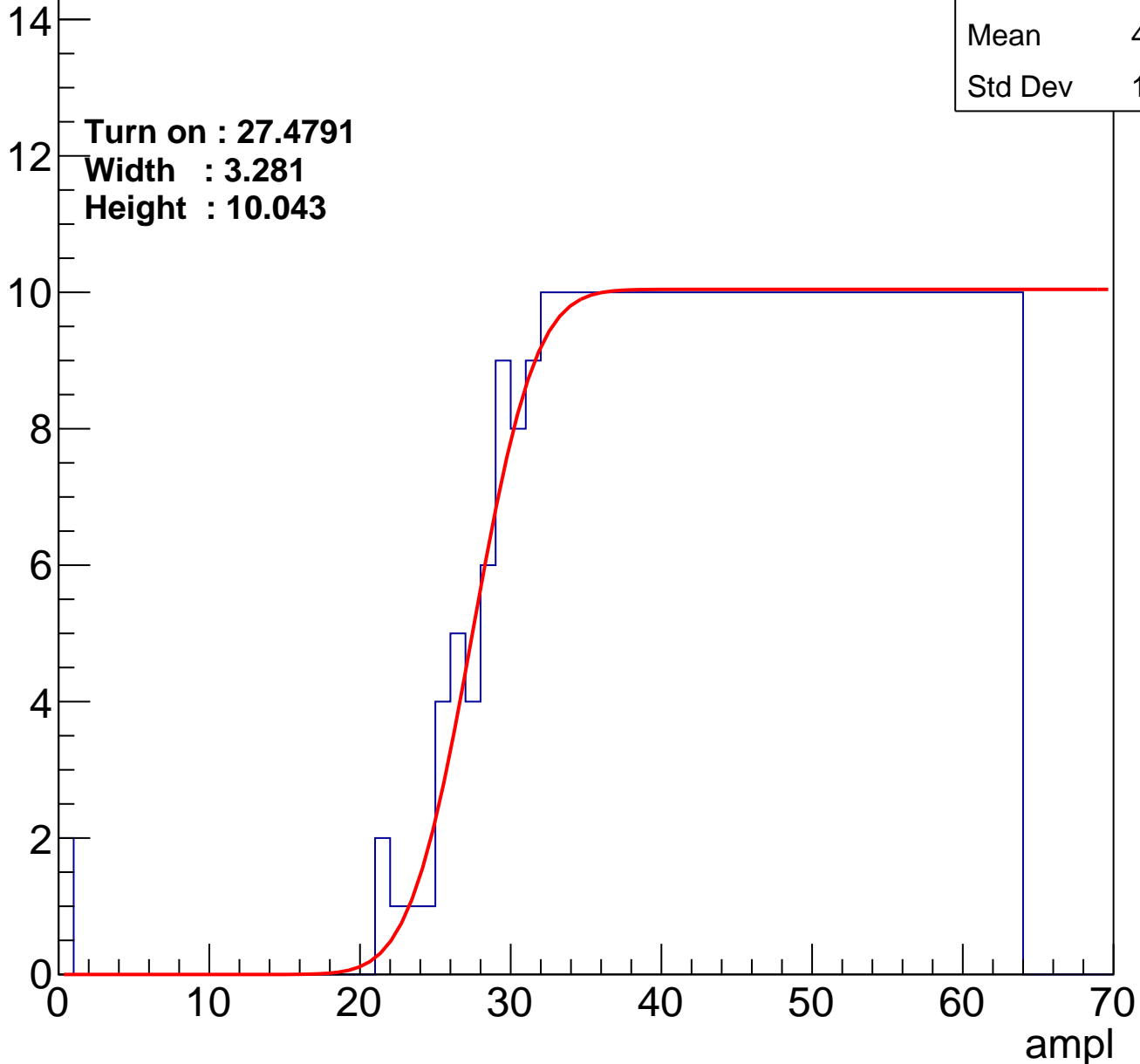
Entries	372
Mean	44.62
Std Dev	11.38

Turn on : 27.4791

Width : 3.281

Height : 10.043

Entry



B1L003S, U16-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.99
Std Dev	11.57

Turn on : 25.5599

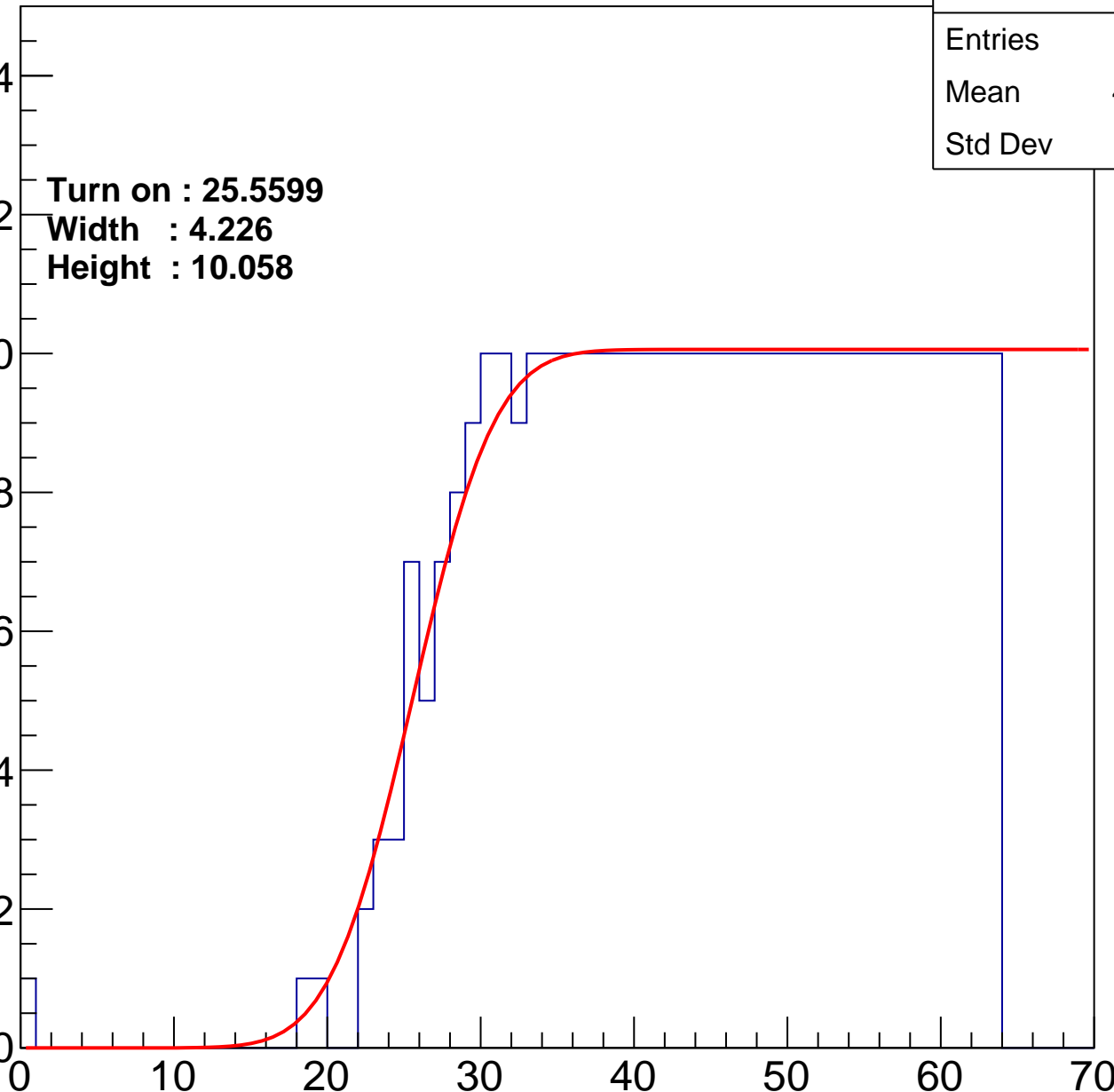
Width : 4.226

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch119

calib_packv5_042523_0143.root, FC#13, port D2

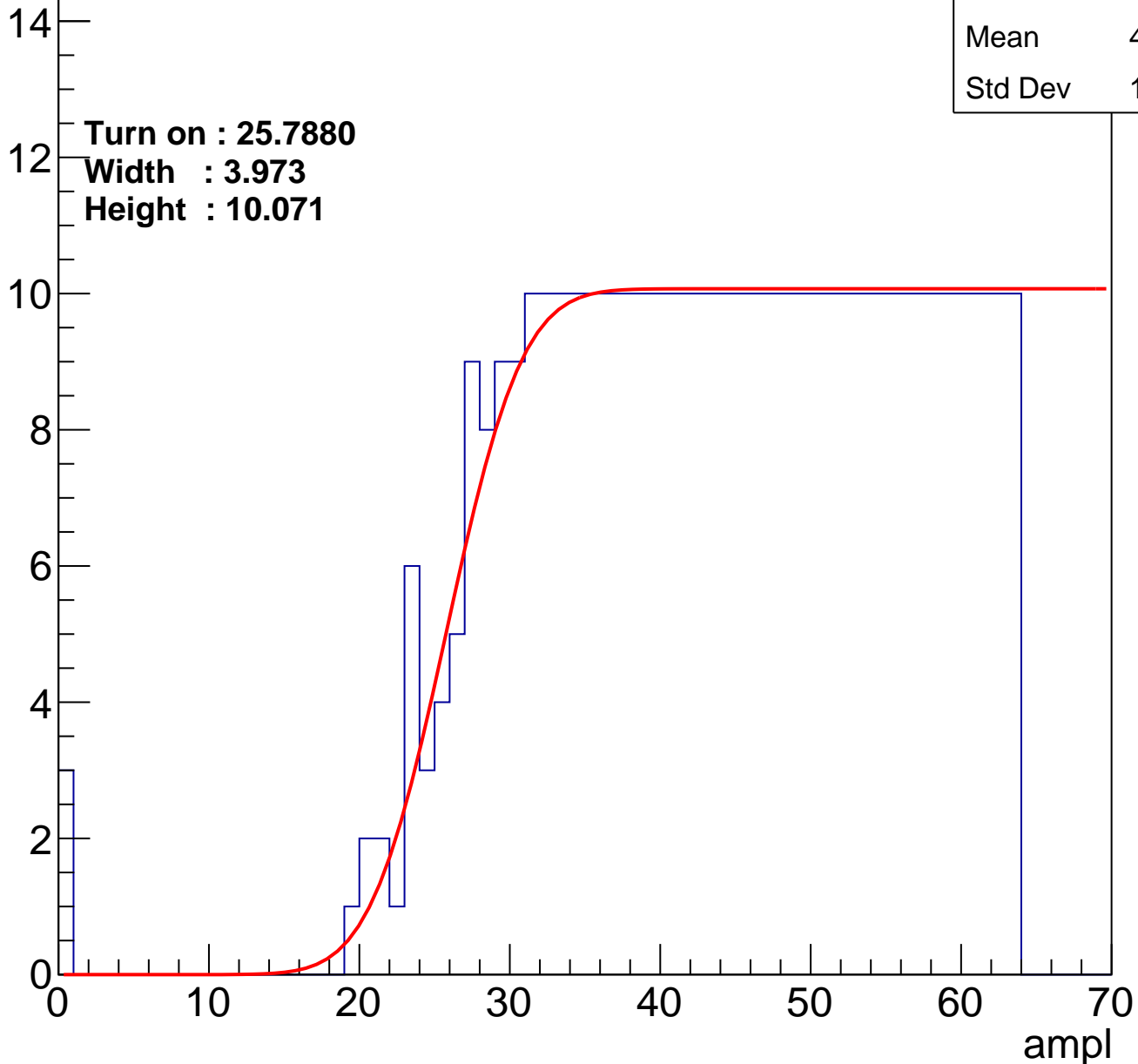
Entries	392
Mean	43.55
Std Dev	12.09

Turn on : 25.7880

Width : 3.973

Height : 10.071

Entry



B1L003S, U16-ch120

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.96
Std Dev	11.74

Turn on : 26.0103

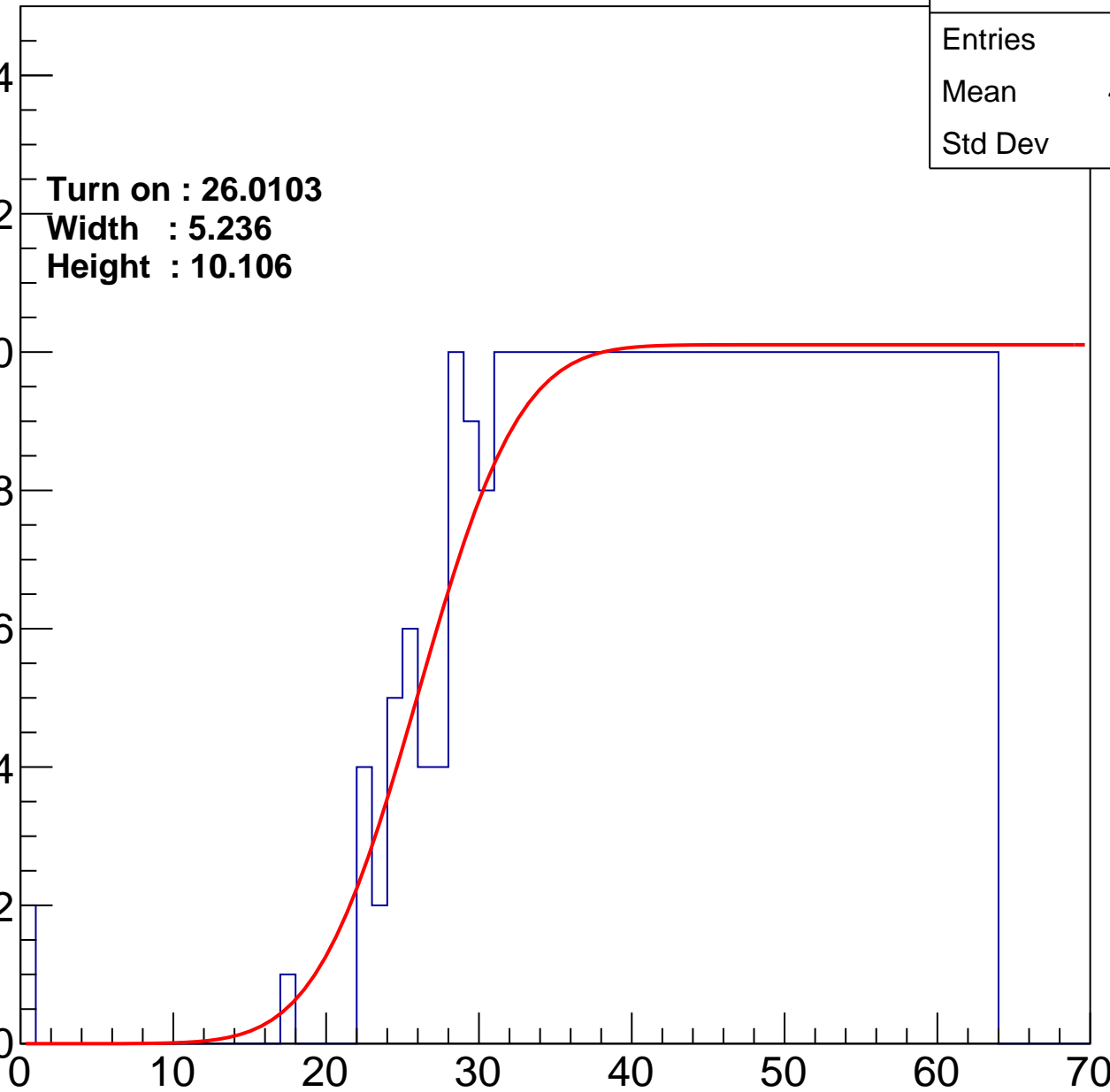
Width : 5.236

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch121

calib_packv5_042523_0143.root, FC#13, port D2

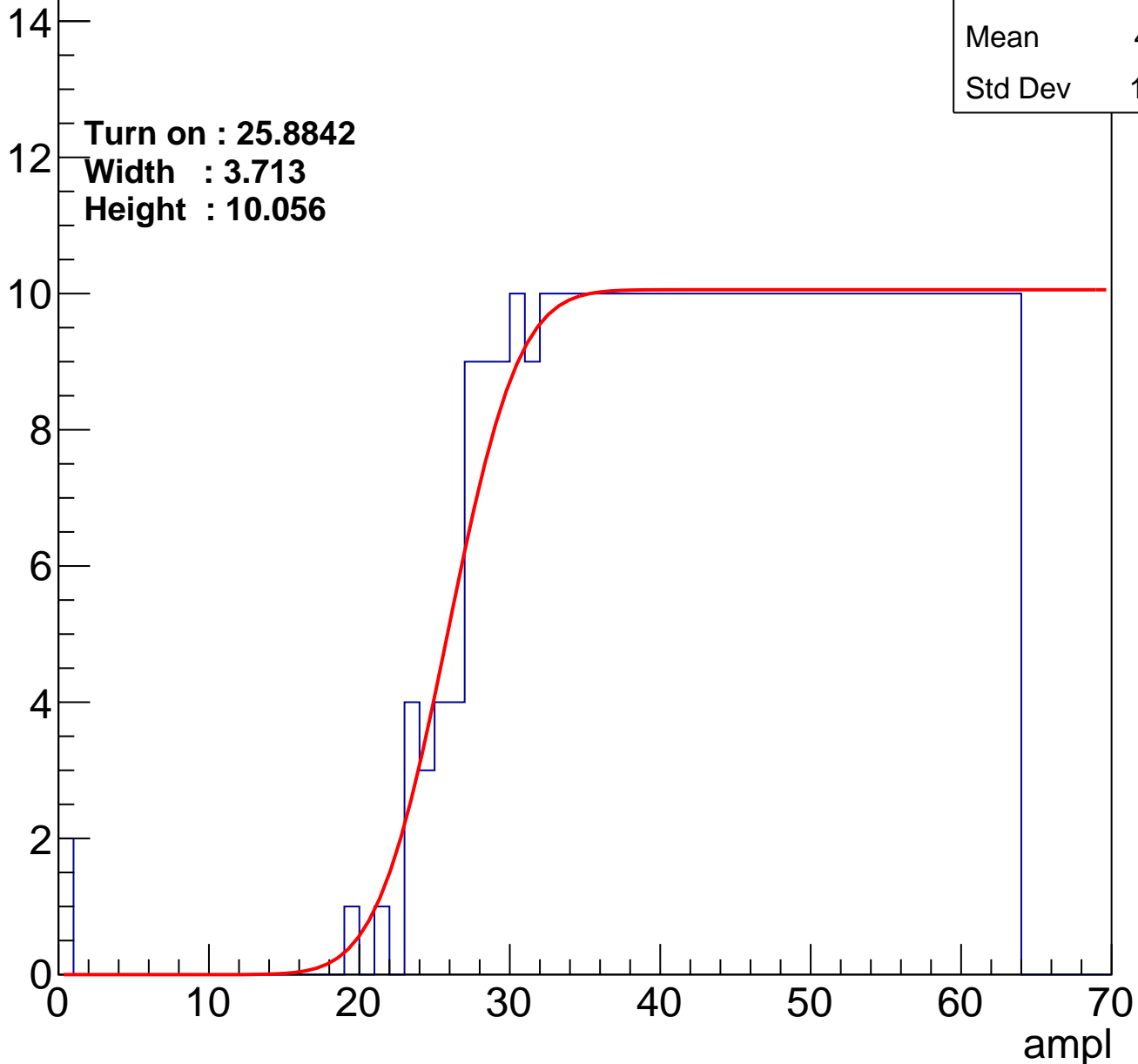
Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 25.8842

Width : 3.713

Height : 10.056

Entry



B1L003S, U16-ch122

calib_packv5_042523_0143.root, FC#13, port D2

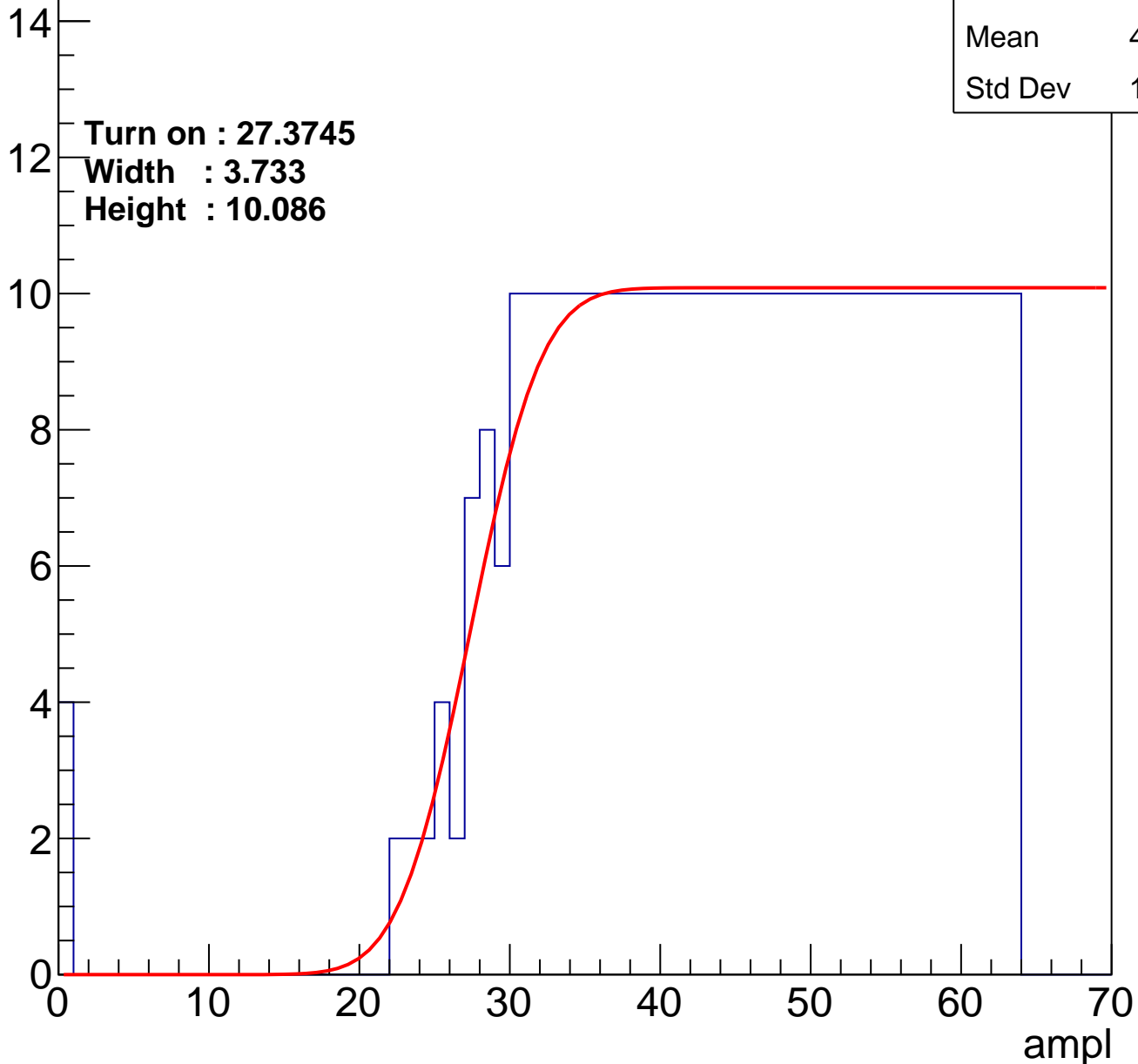
Entries	377
Mean	44.26
Std Dev	11.83

Turn on : 27.3745

Width : 3.733

Height : 10.086

Entry



B1L003S, U16-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.5495

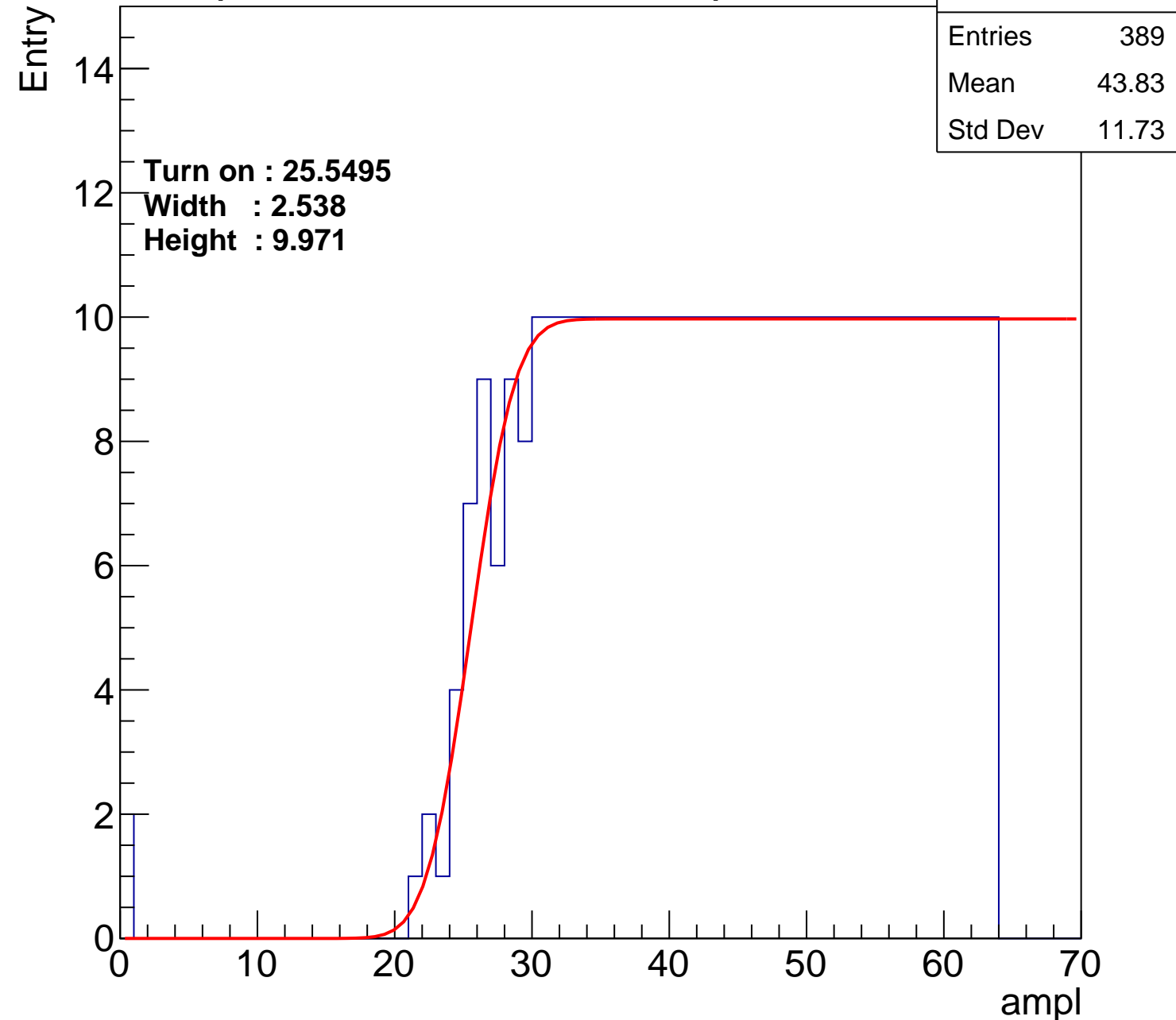
Width : 2.538

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.84
Std Dev	12.17

Turn on : 26.1829

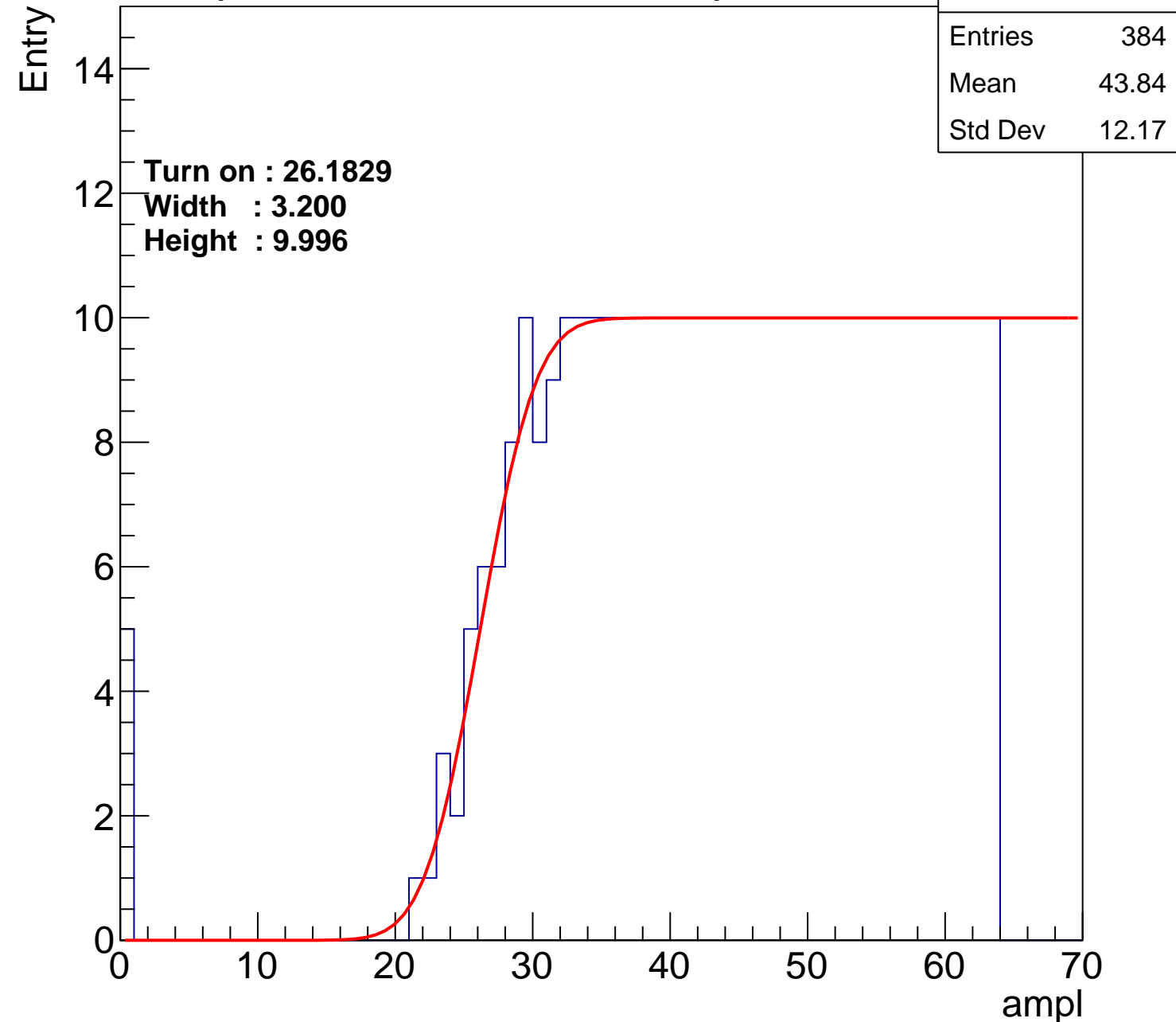
Width : 3.200

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch125

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.43
Std Dev	12.63

Turn on : 26.4616

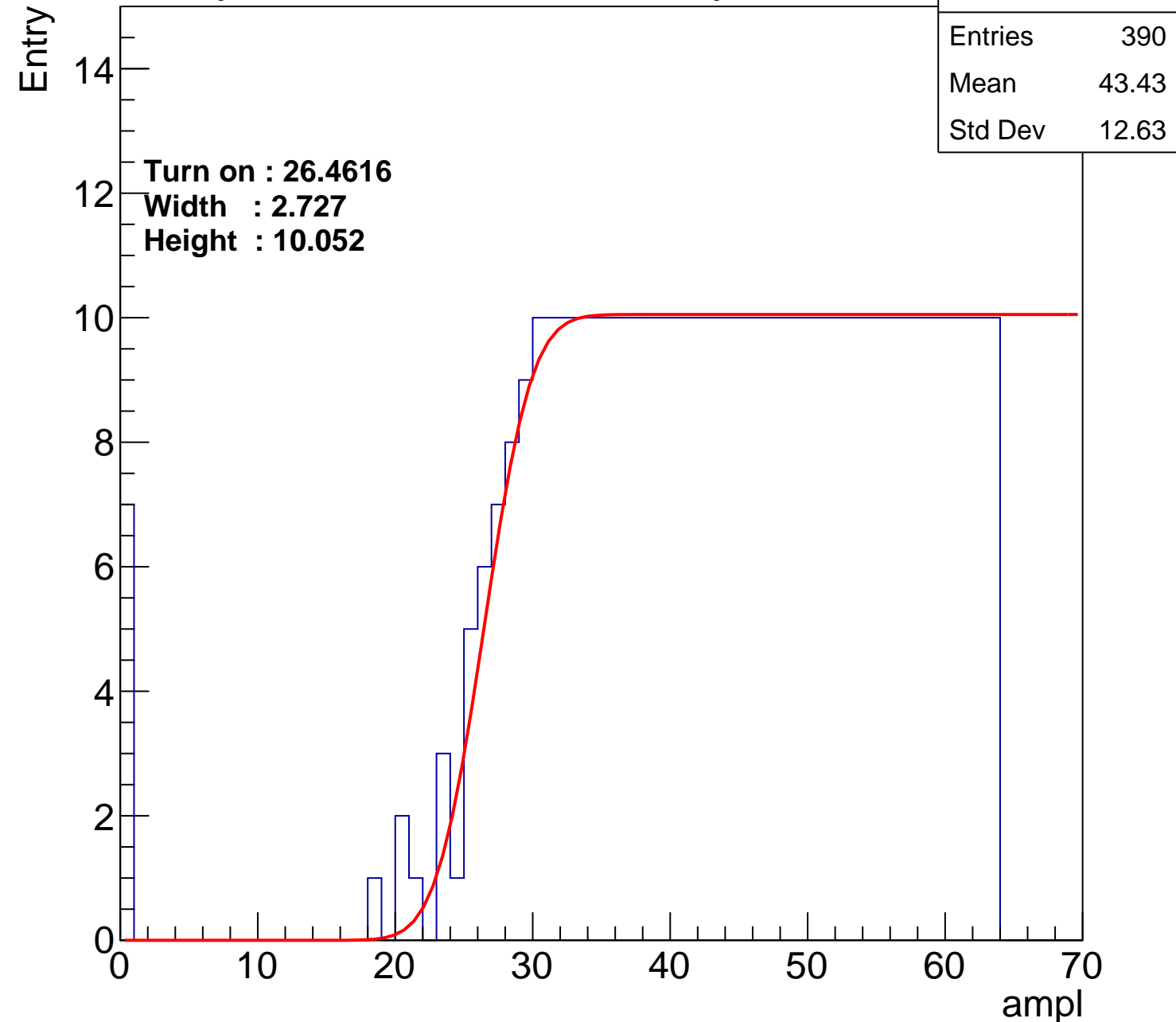
Width : 2.727

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.03
Std Dev	11.68

Turn on : 26.3817

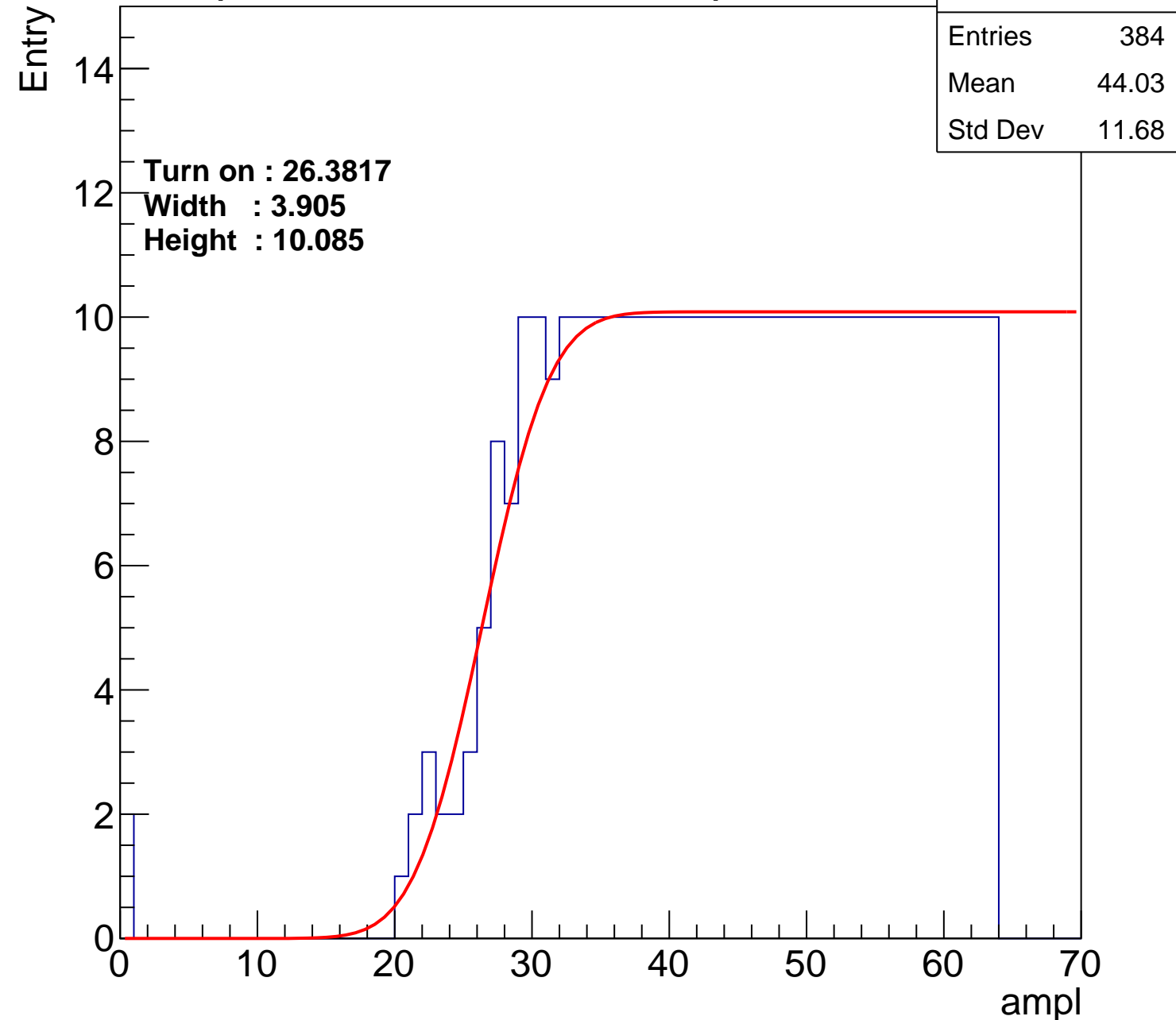
Width : 3.905

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.34
Std Dev	12.64

Turn on : 25.7131

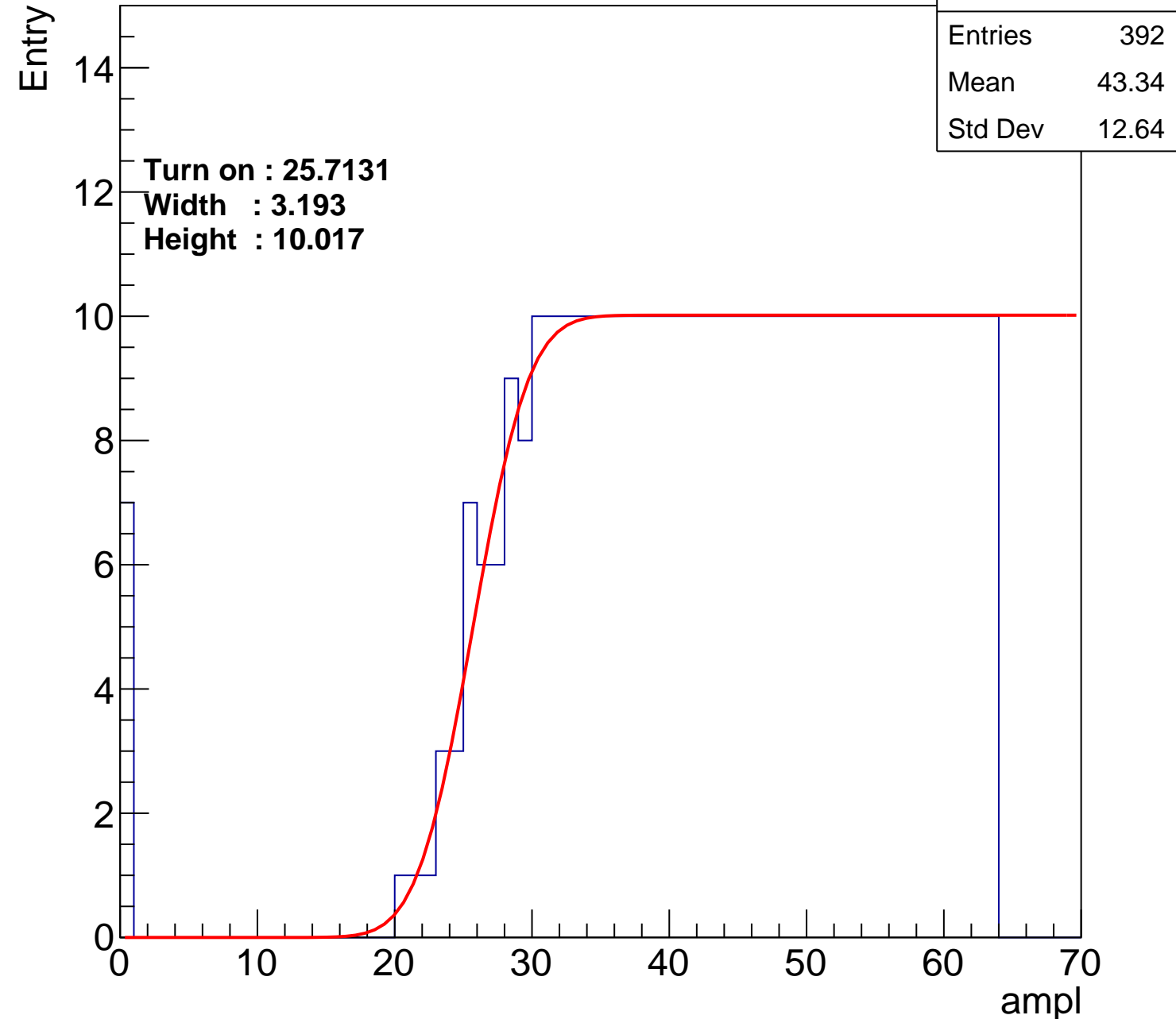
Width : 3.193

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U16-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.34
Std Dev	12.64

Turn on : 25.7131

Width : 3.193

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl

