



# B0L000S, U13-ch0, adc0

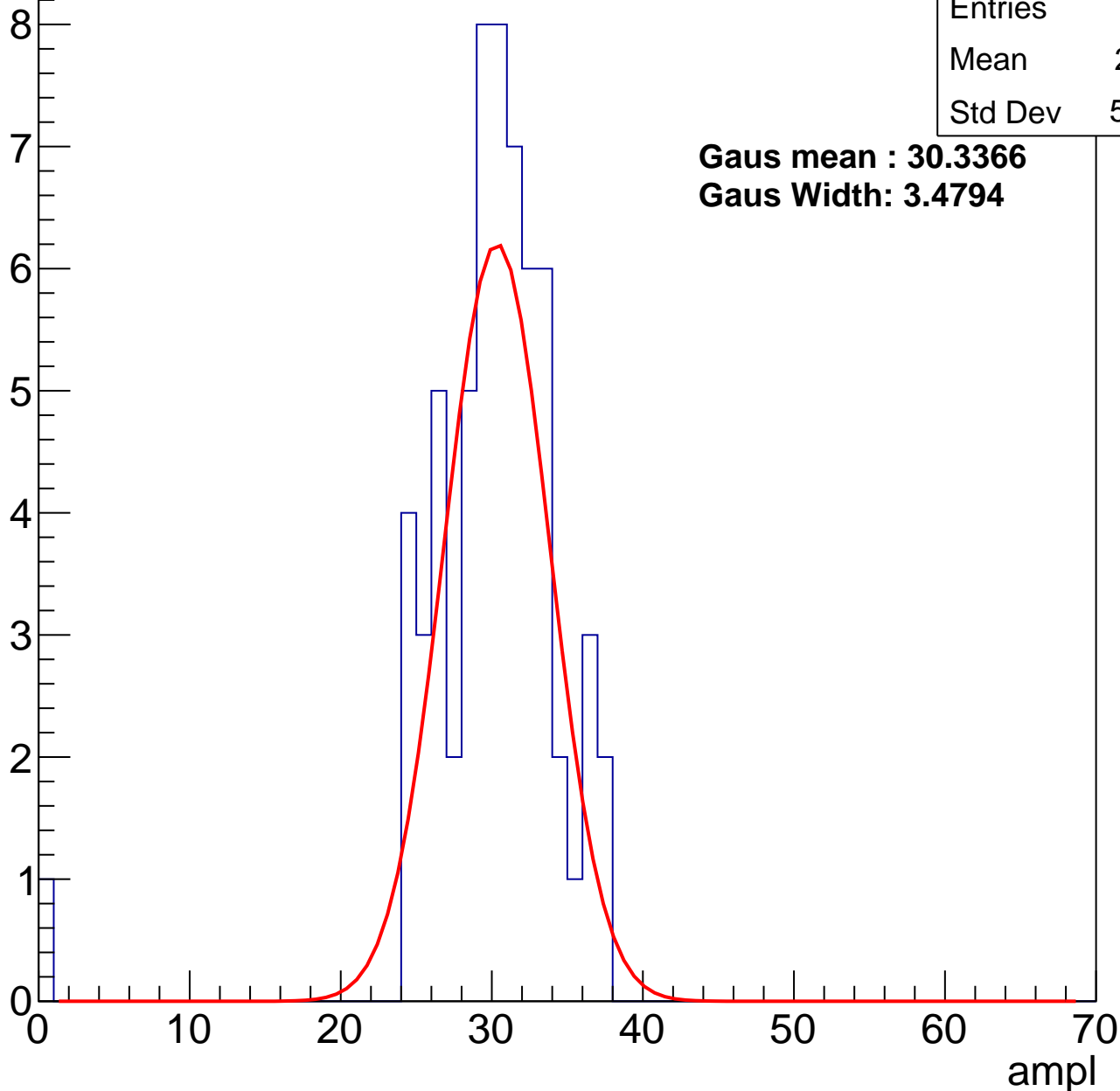
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.51
Std Dev	5.004

**Gaus mean : 30.3366**

**Gaus Width: 3.4794**



# B0L000S, U13-ch0, adc1

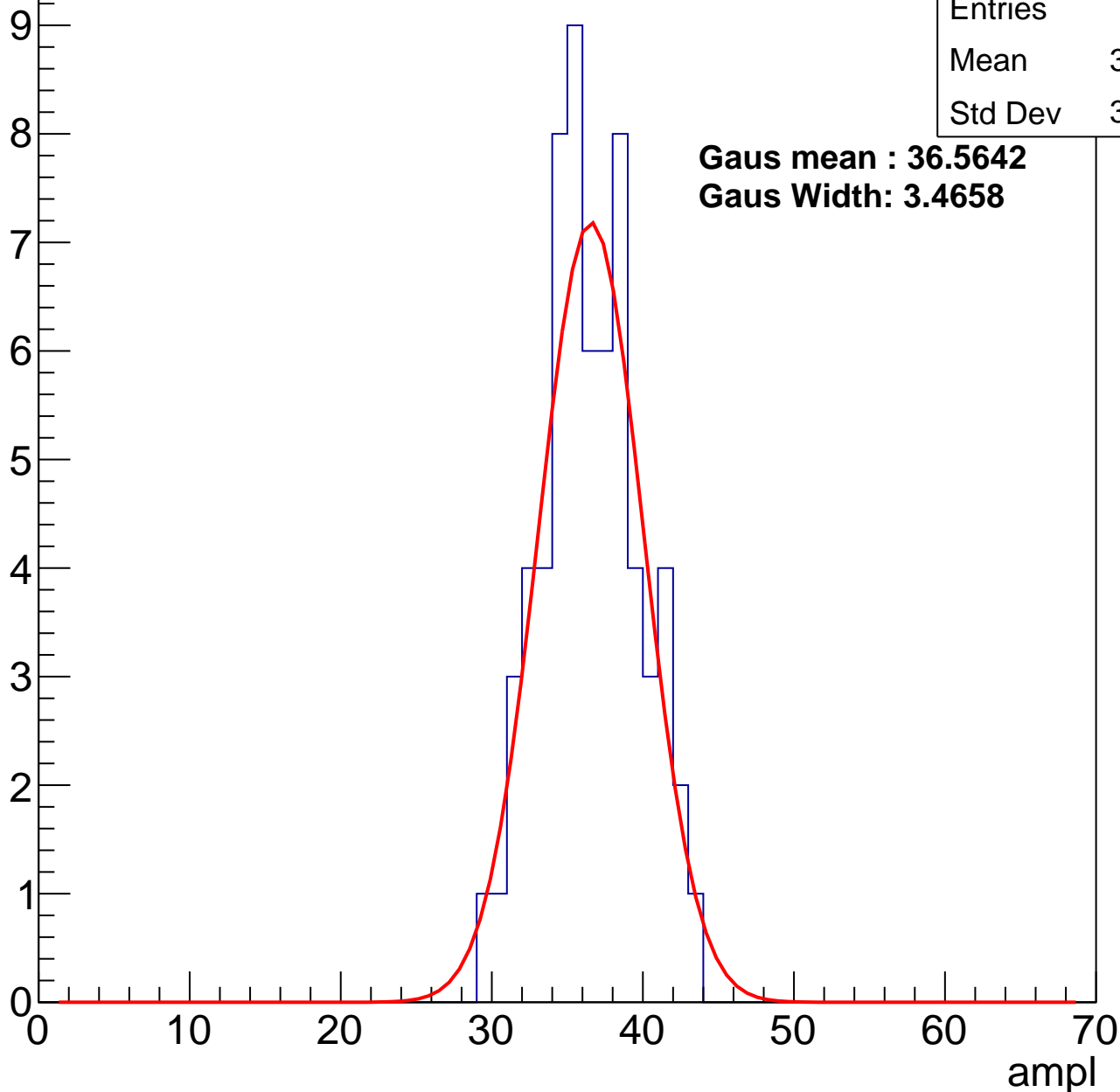
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.06
Std Dev	3.172

**Gaus mean : 36.5642**

**Gaus Width: 3.4658**



# B0L000S, U13-ch0, adc2

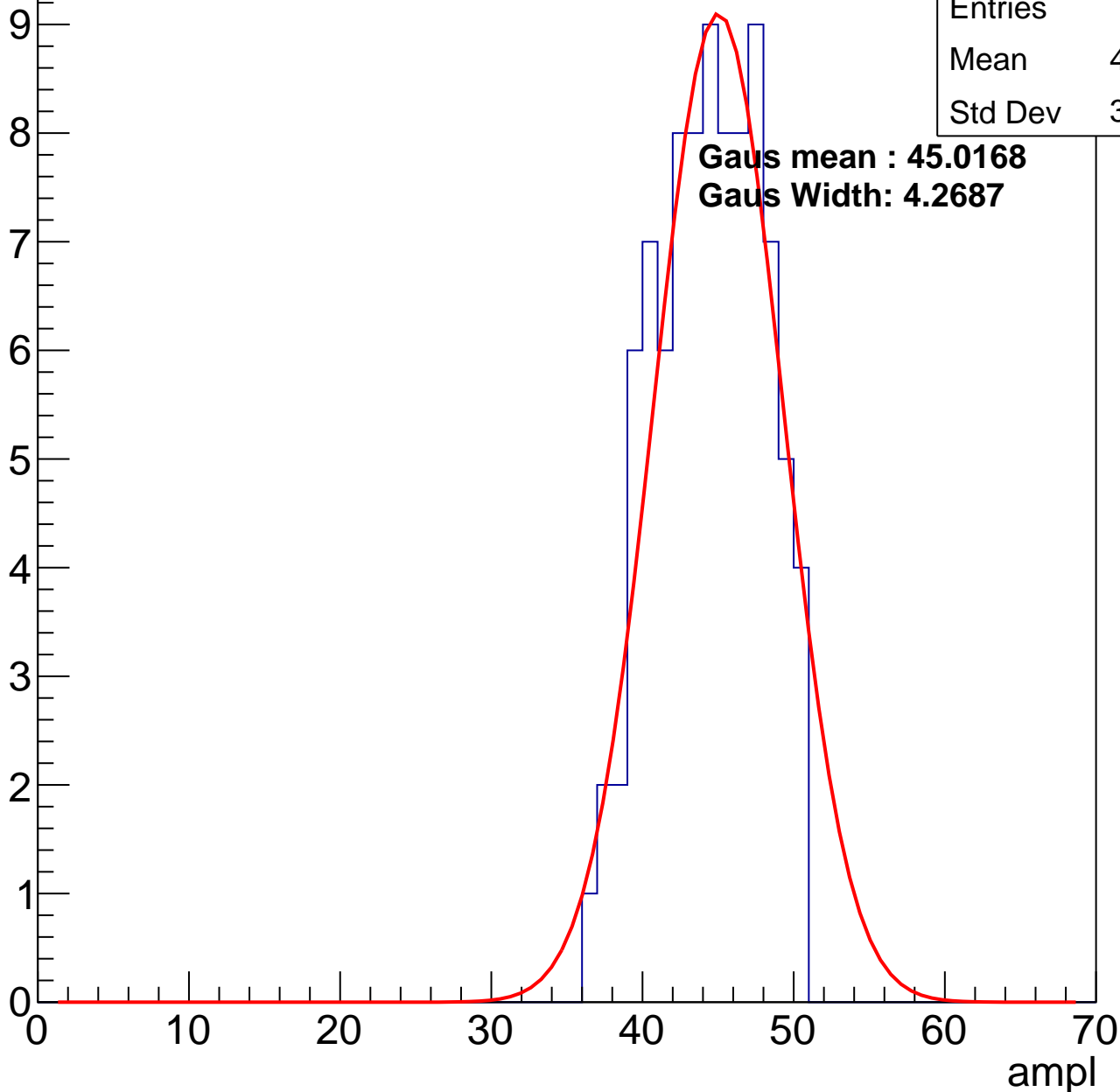
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	90
Mean	43.93
Std Dev	3.483

**Gaus mean : 45.0168**

**Gaus Width: 4.2687**

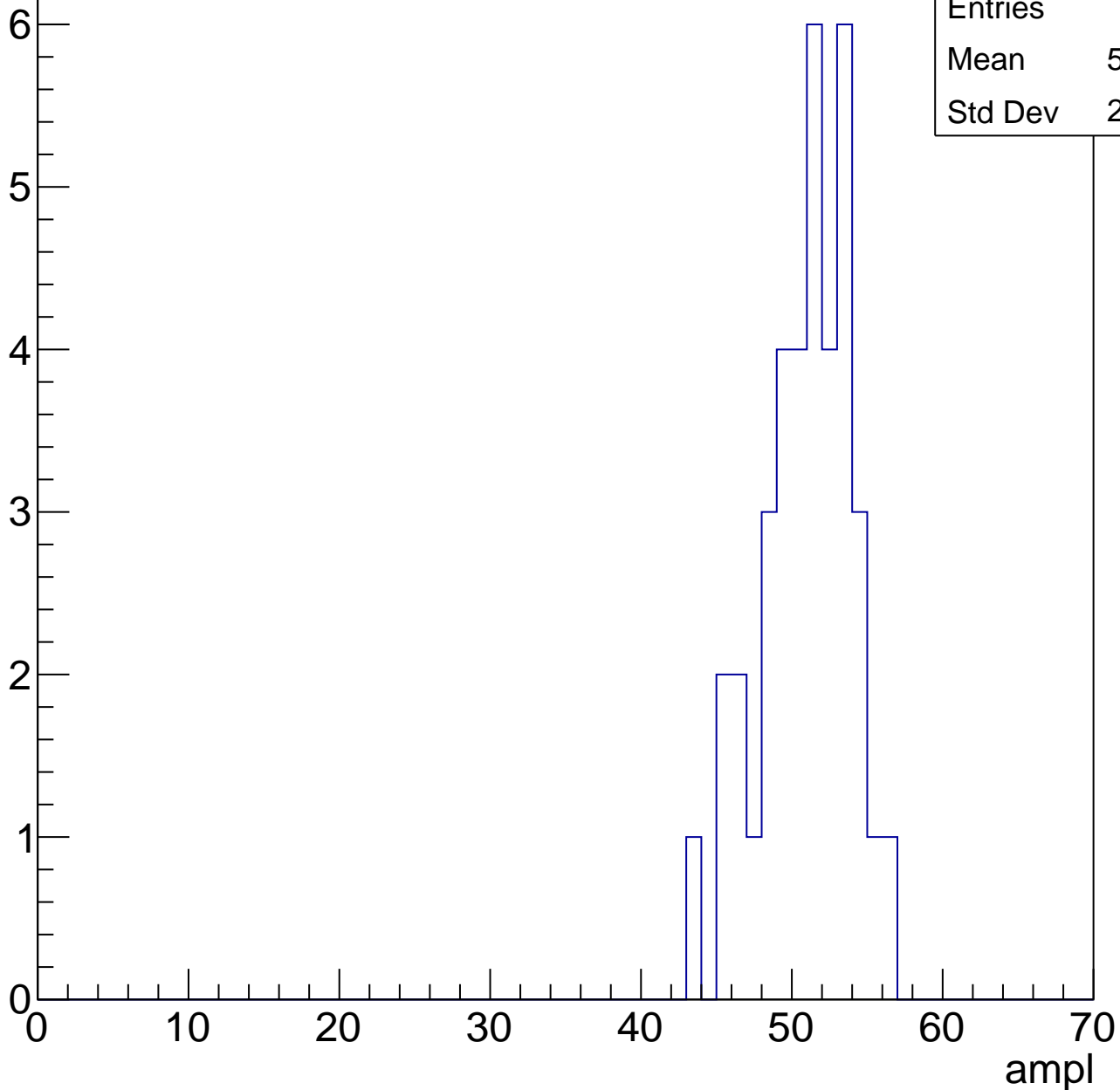


# B0L000S, U13-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	50.45
Std Dev	2.962

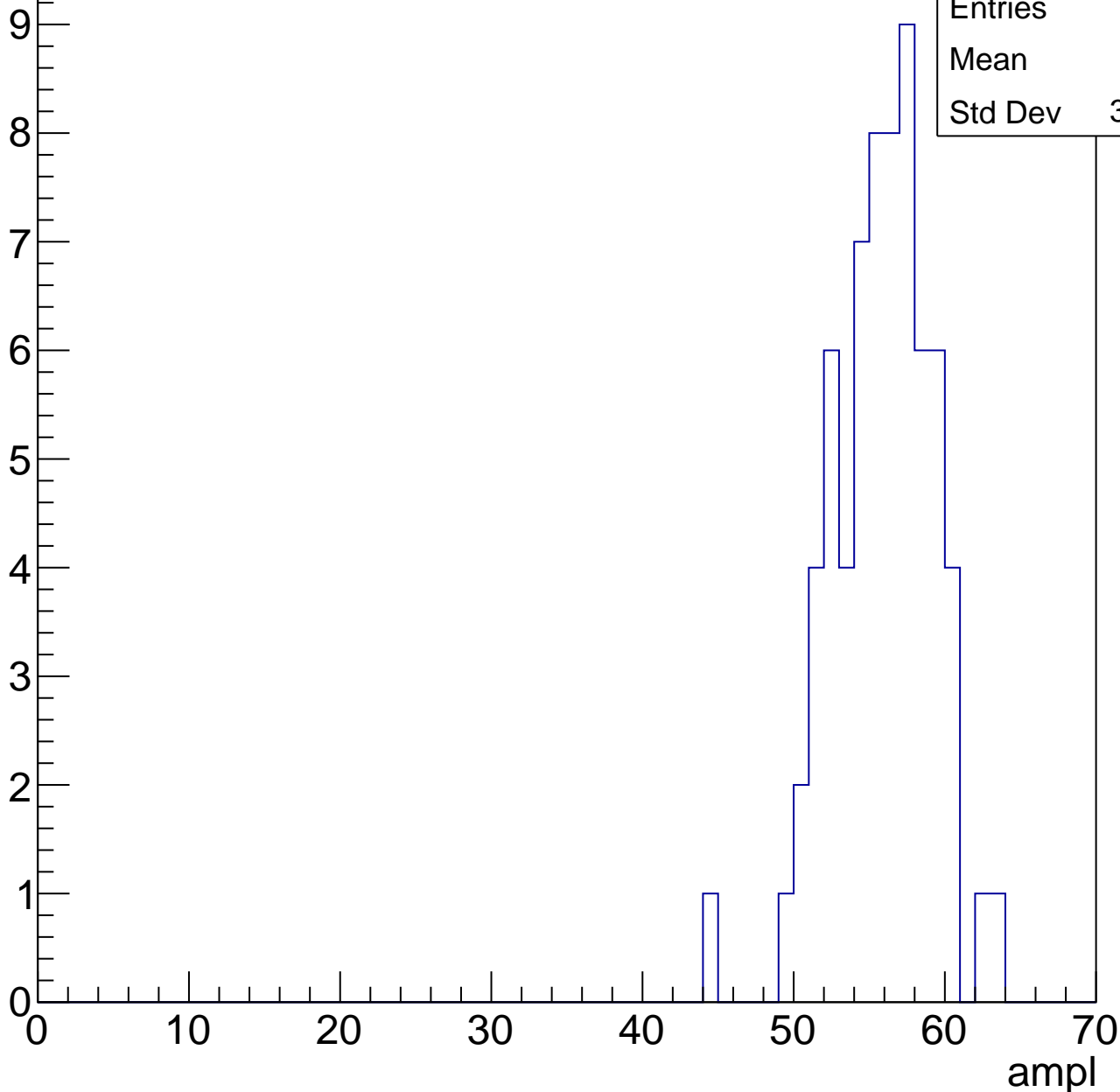


# B0L000S, U13-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	55.4
Std Dev	3.308

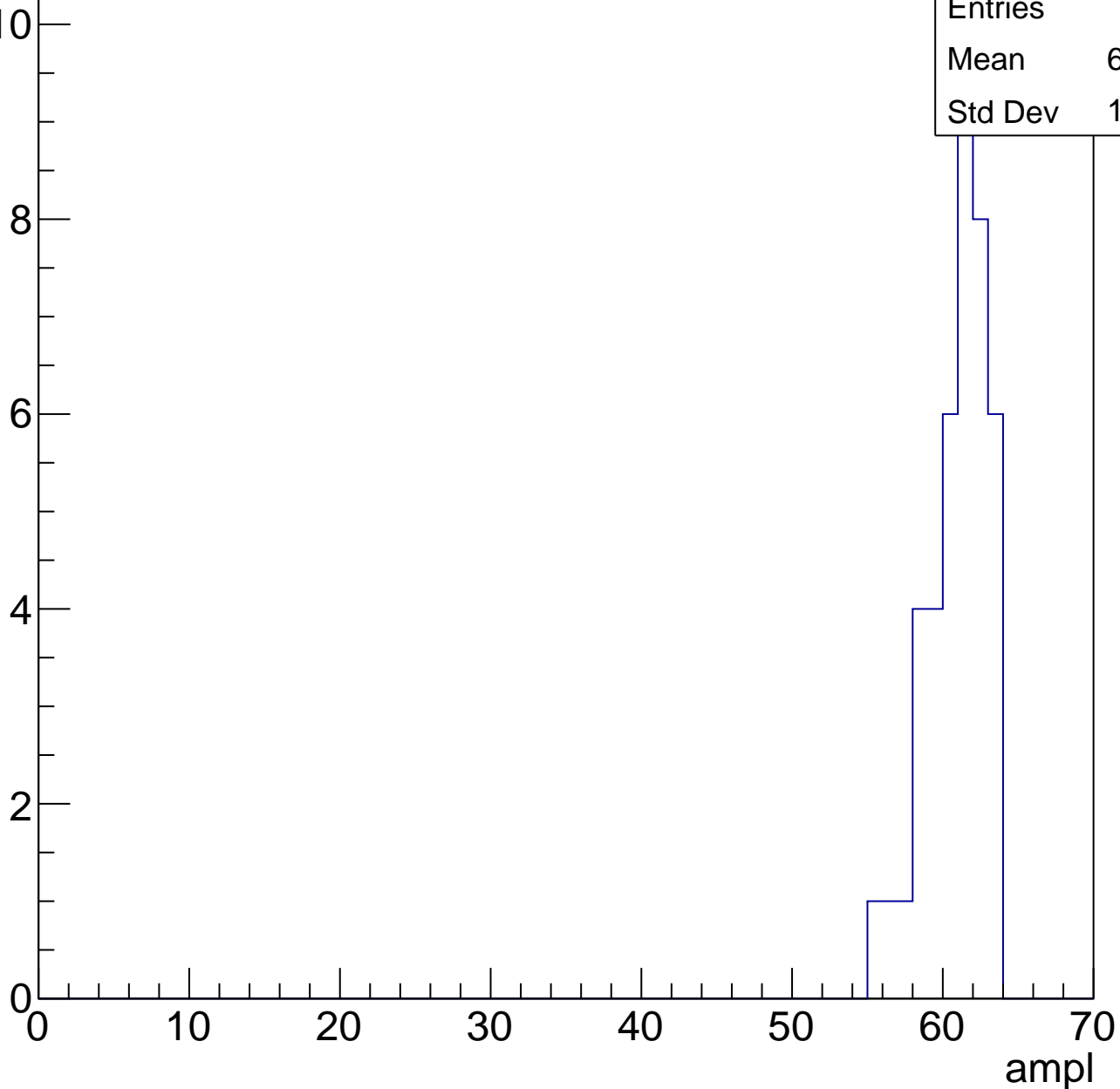


# B0L000S, U13-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

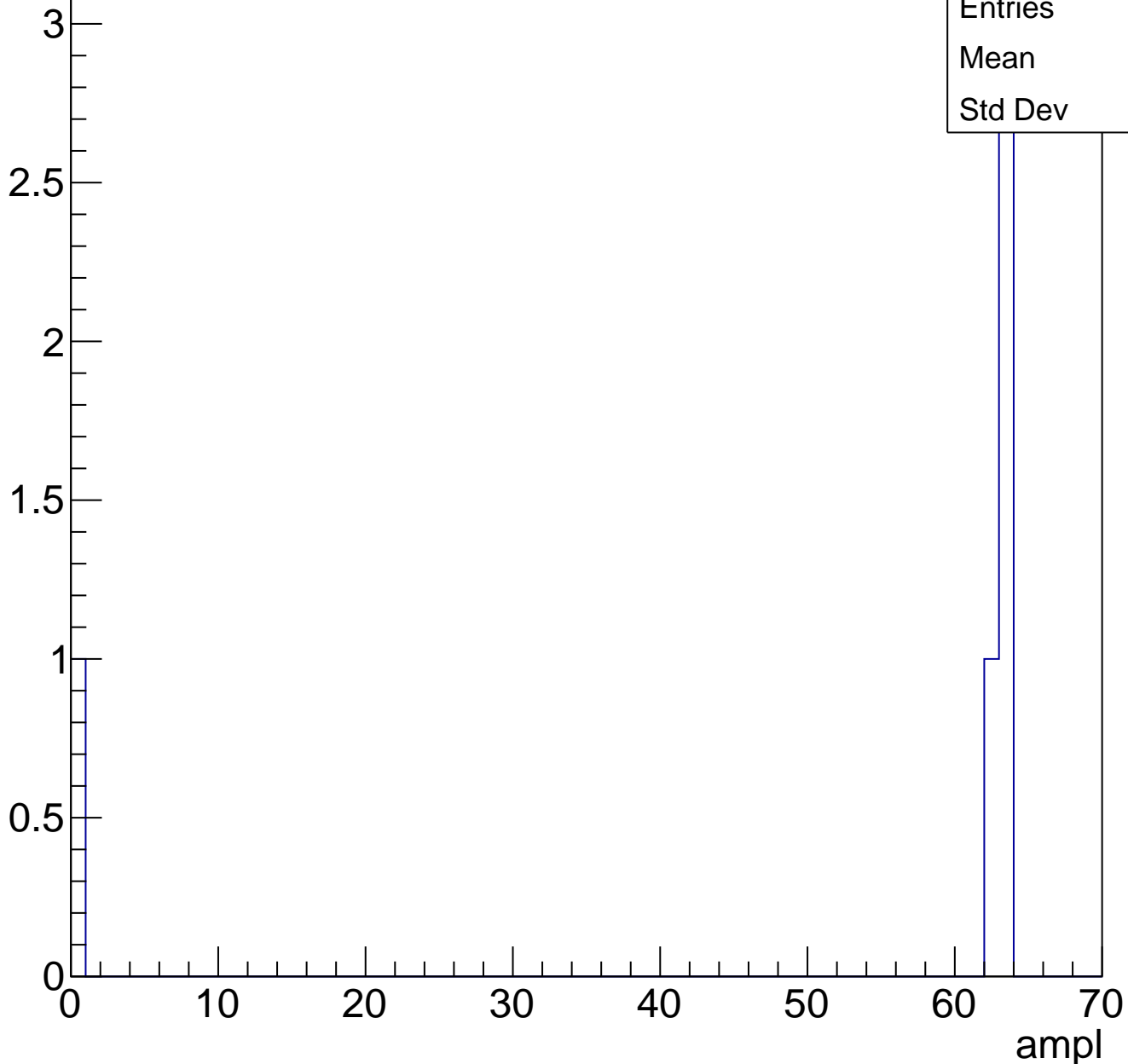
Entries	41
Mean	60.49
Std Dev	1.952



# B0L000S, U13-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



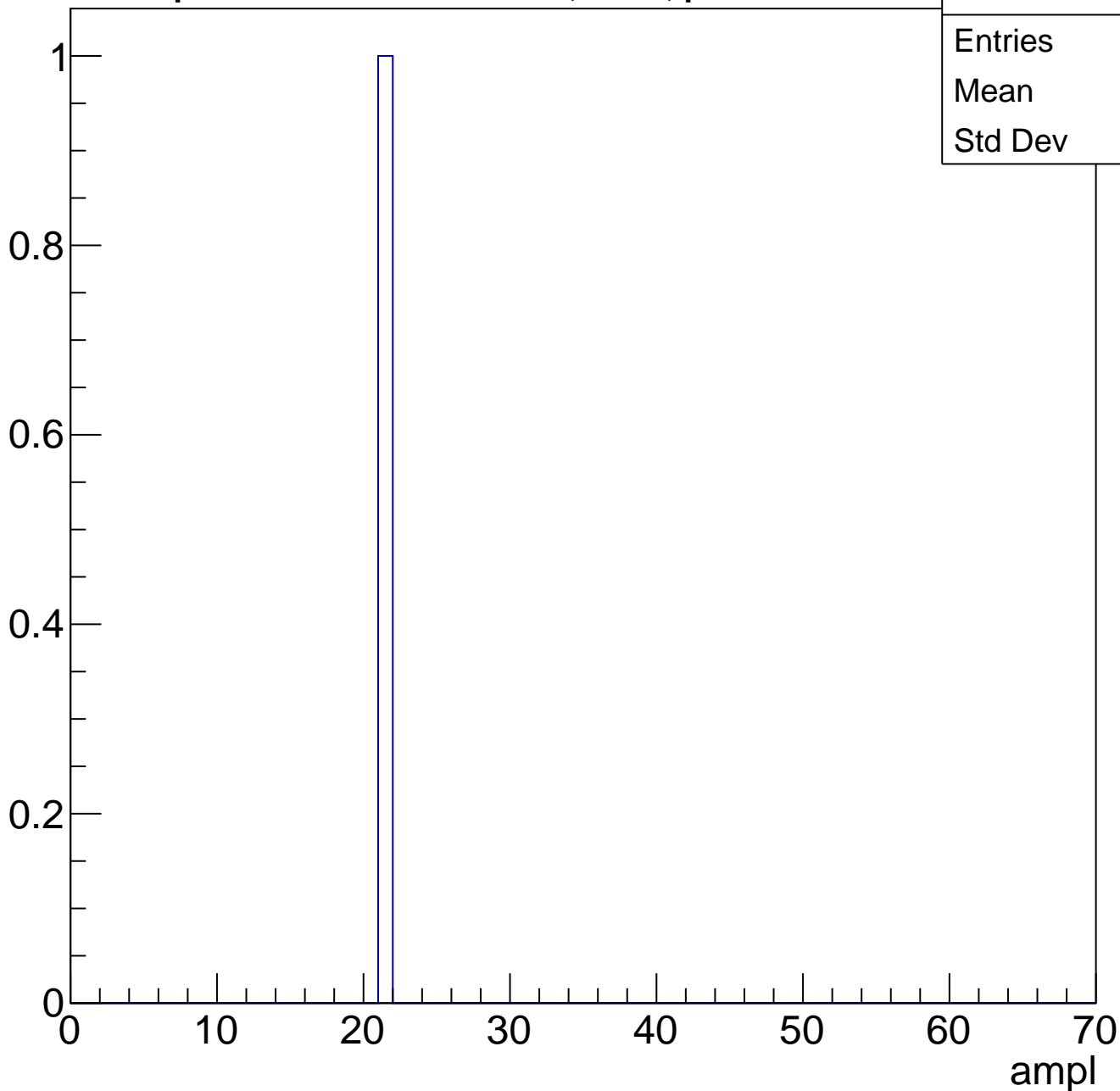
Entries	5
Mean	50.2
Std Dev	25.1



# B0L000S, U13-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch1, adc0

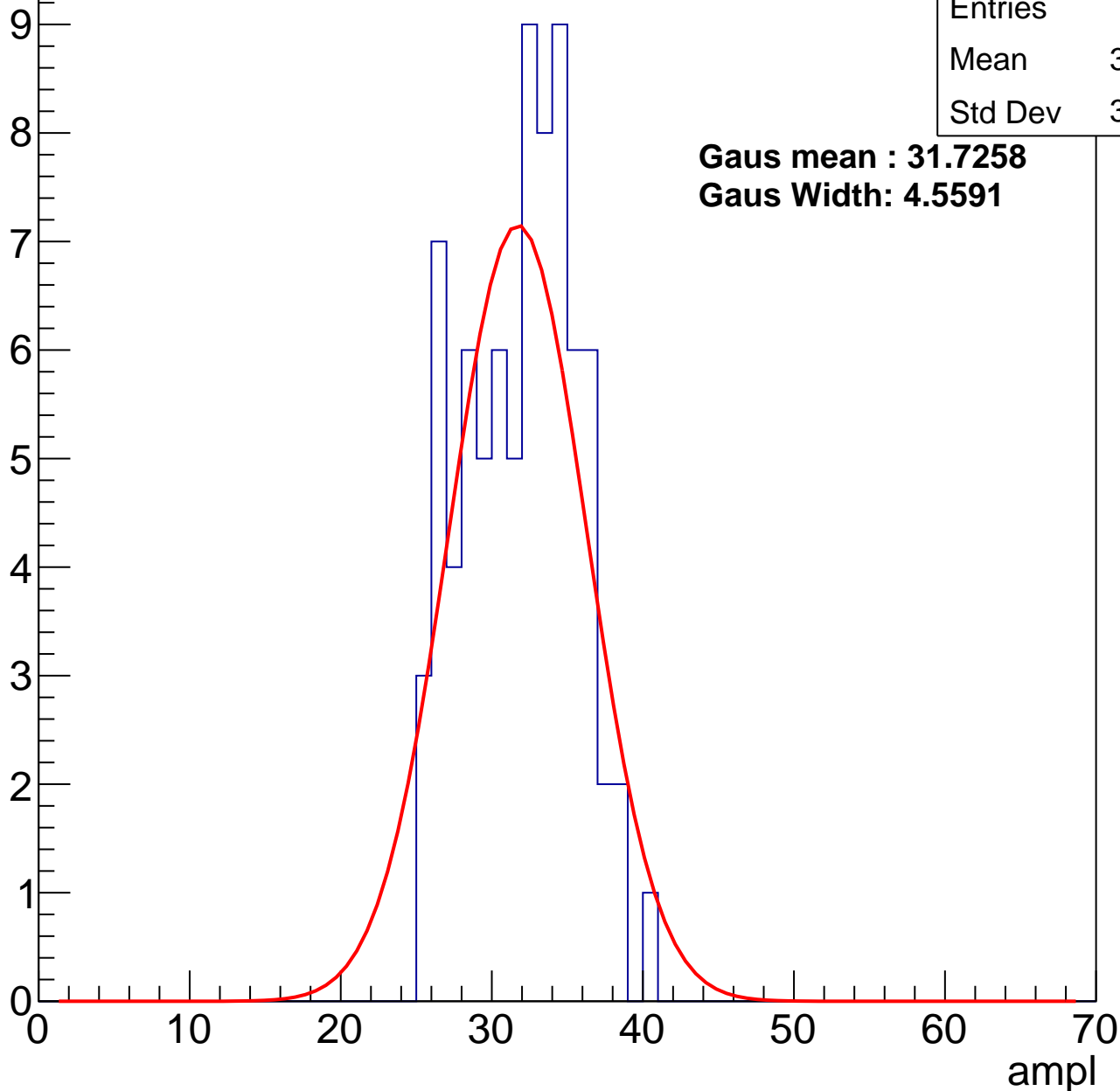
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	31.48
Std Dev	3.607

**Gaus mean : 31.7258**

**Gaus Width: 4.5591**



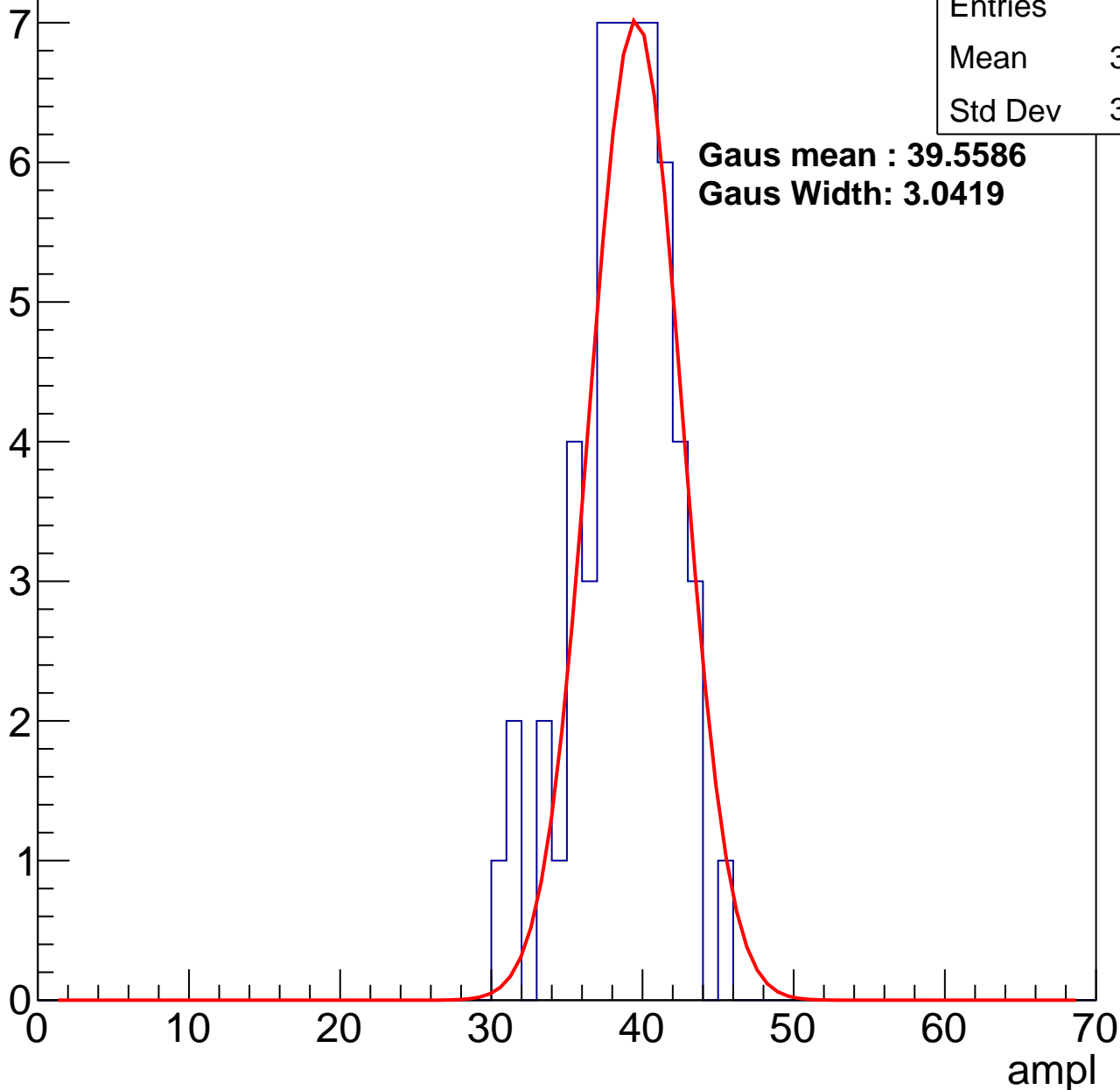
# B0L000S, U13-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	38.29
Std Dev	3.178

**Gaus mean : 39.5586**  
**Gaus Width: 3.0419**



# B0L000S, U13-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	45.14
Std Dev	3.238

**Gaus mean : 46.0428**

**Gaus Width: 3.6536**

Entry

10

8

6

4

2

0

0

10

20

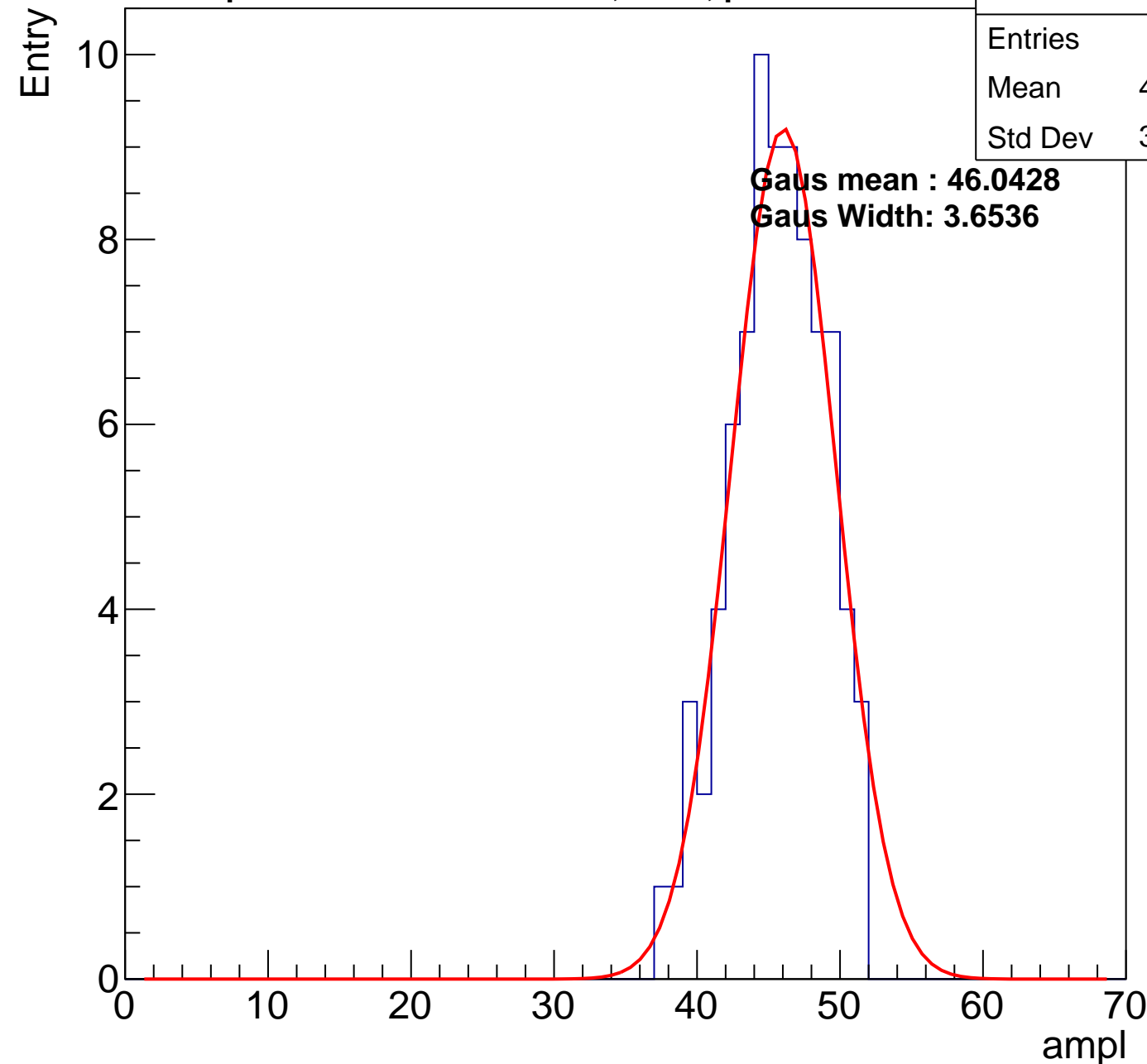
30

40

50

60

ampl



# B0L000S, U13-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

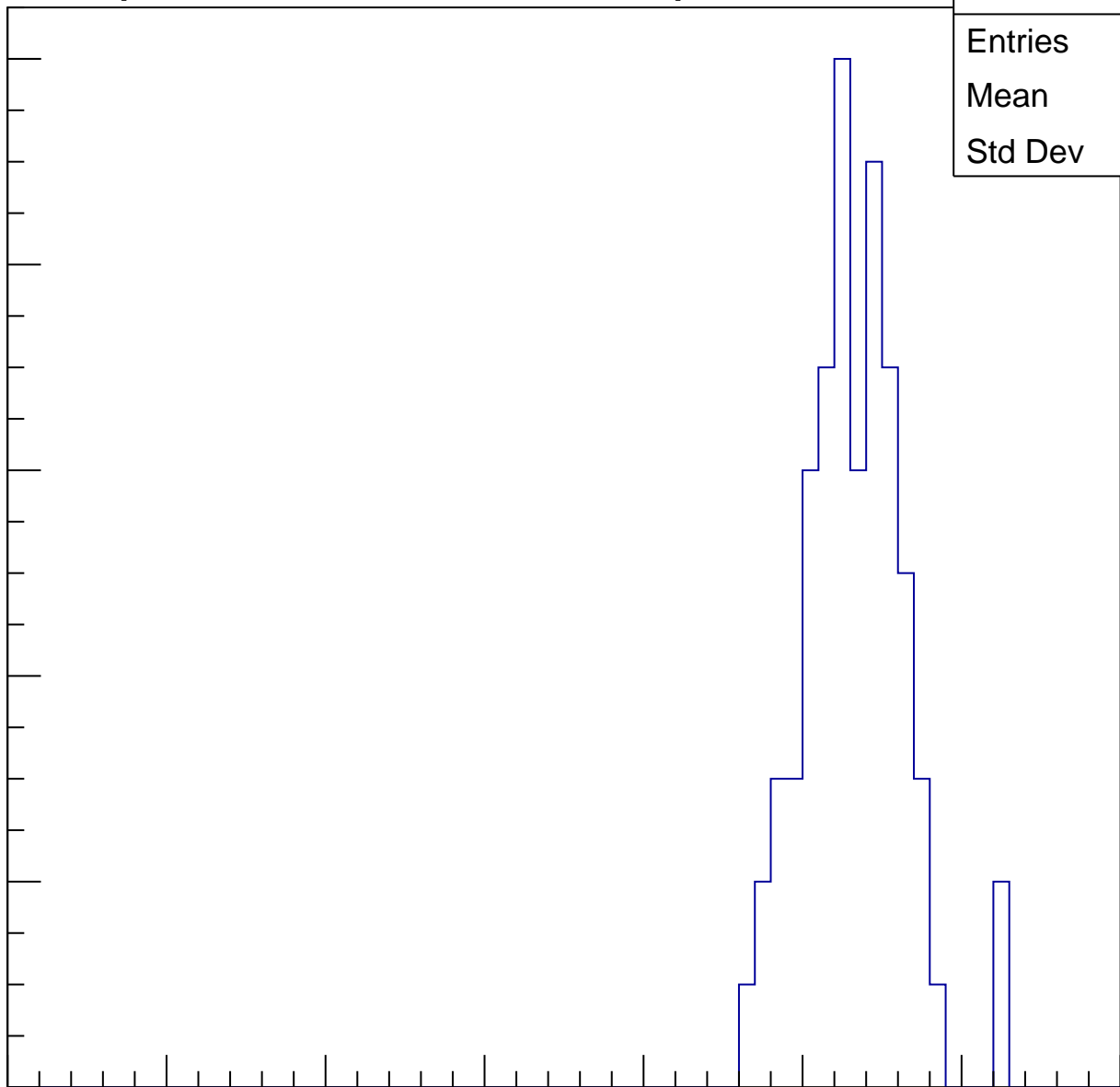
Entries	65
Mean	52.77
Std Dev	3.161

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

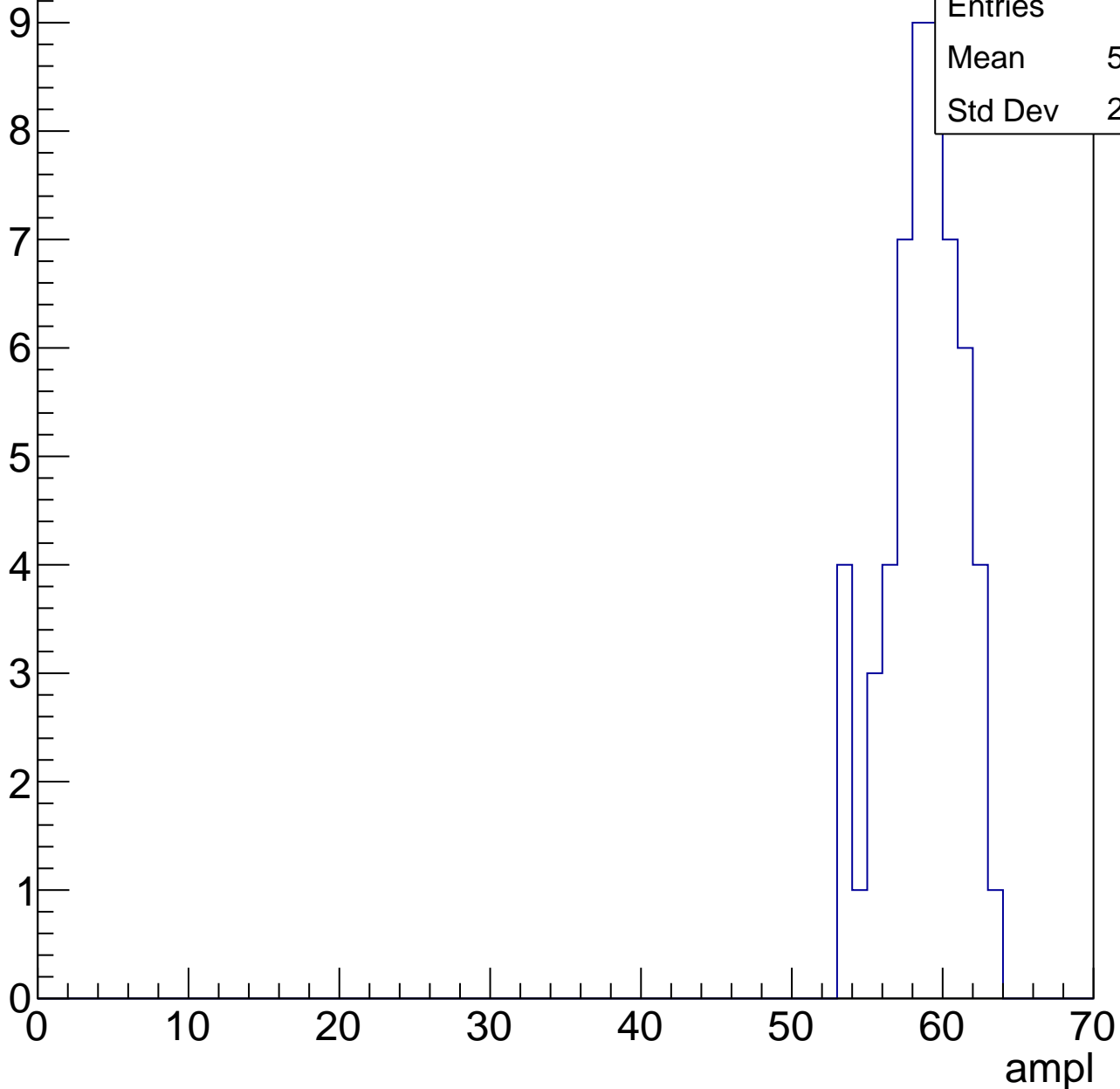
ampl



# B0L000S, U13-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

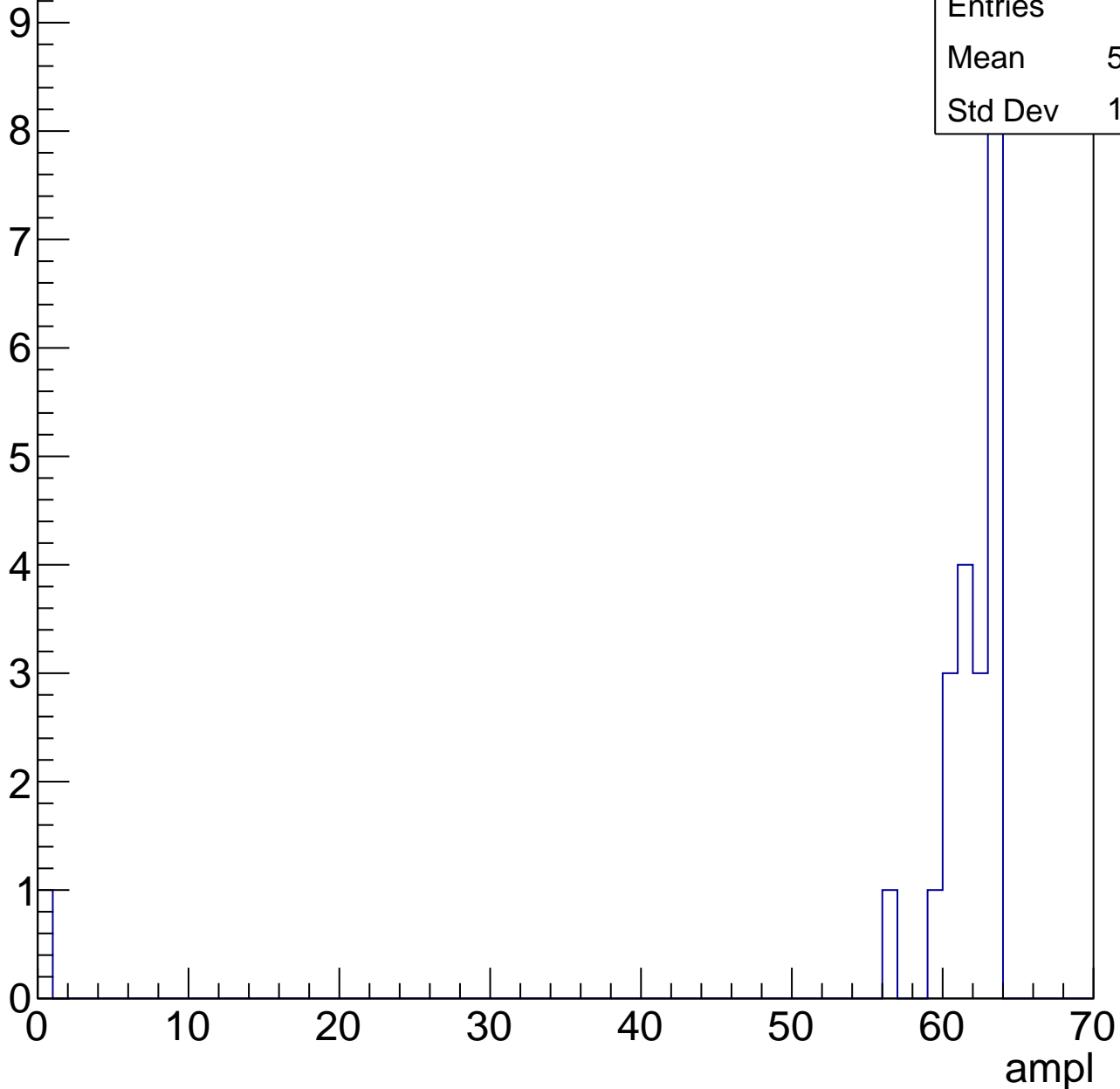


# B0L000S, U13-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	58.73
Std Dev	12.93



# B0L000S, U13-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch2, adc0

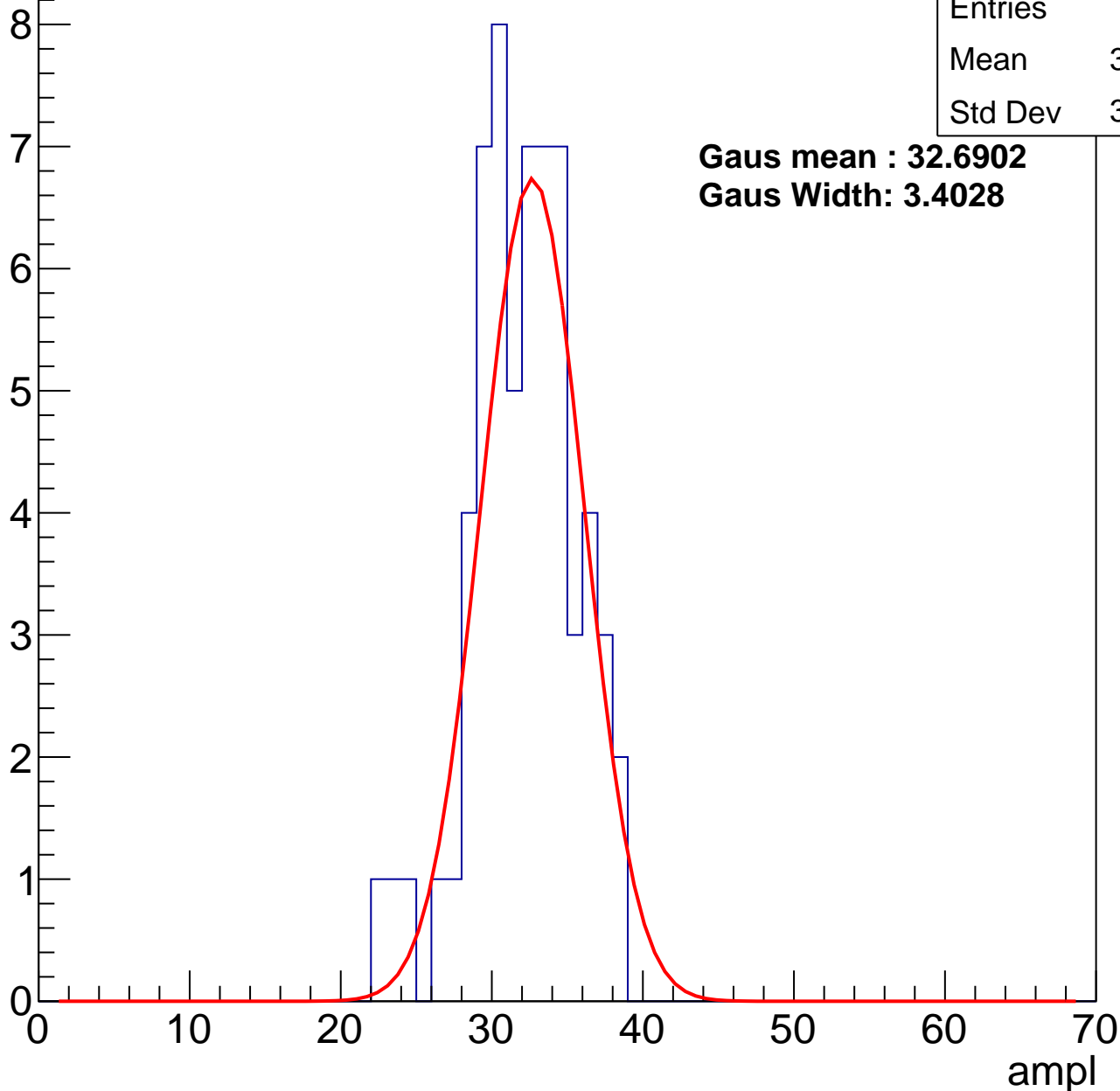
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	31.63
Std Dev	3.442

**Gaus mean : 32.6902**

**Gaus Width: 3.4028**



# B0L000S, U13-ch2, adc1

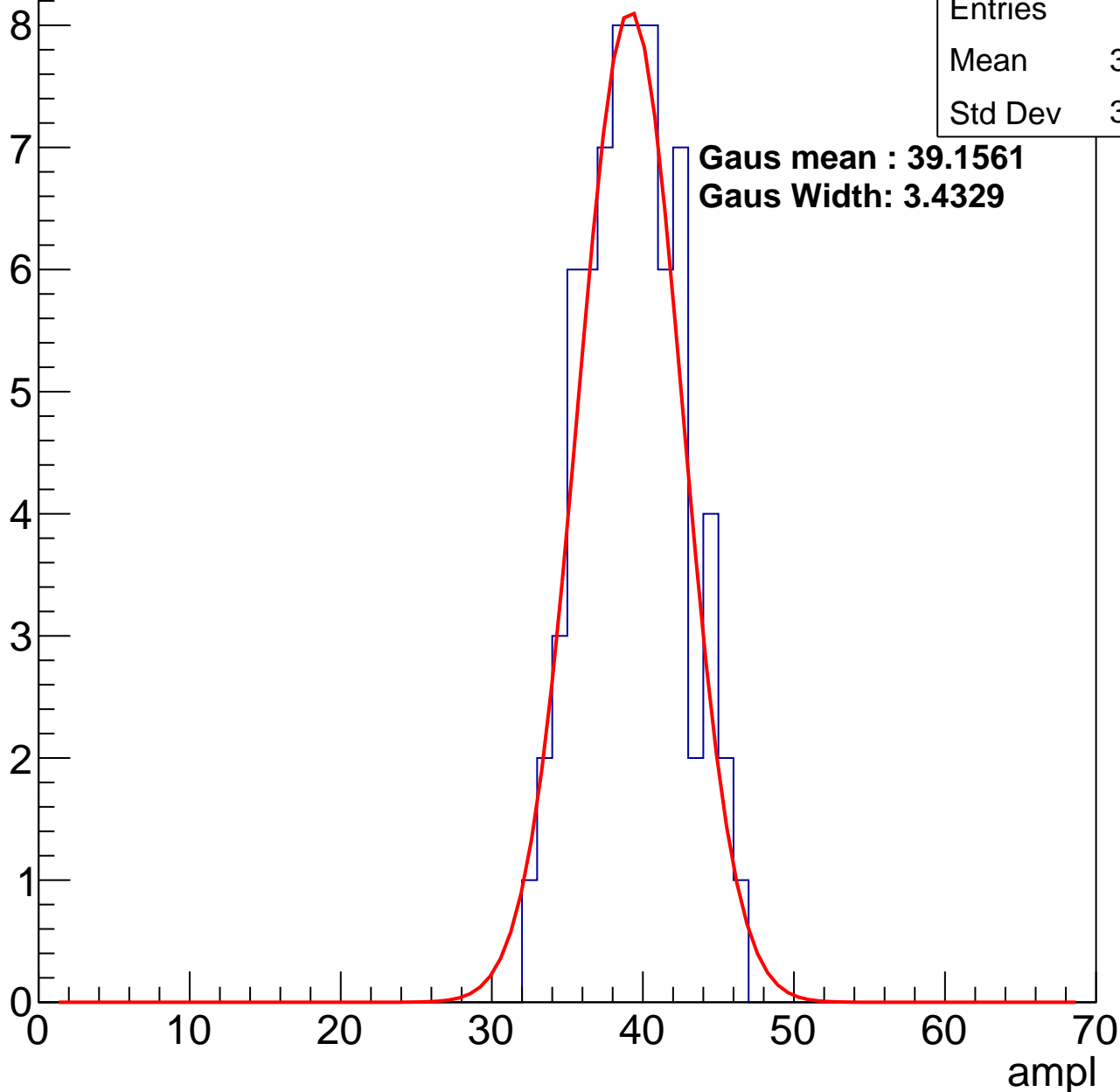
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	38.86
Std Dev	3.203

**Gaus mean : 39.1561**

**Gaus Width: 3.4329**



# B0L000S, U13-ch2, adc2

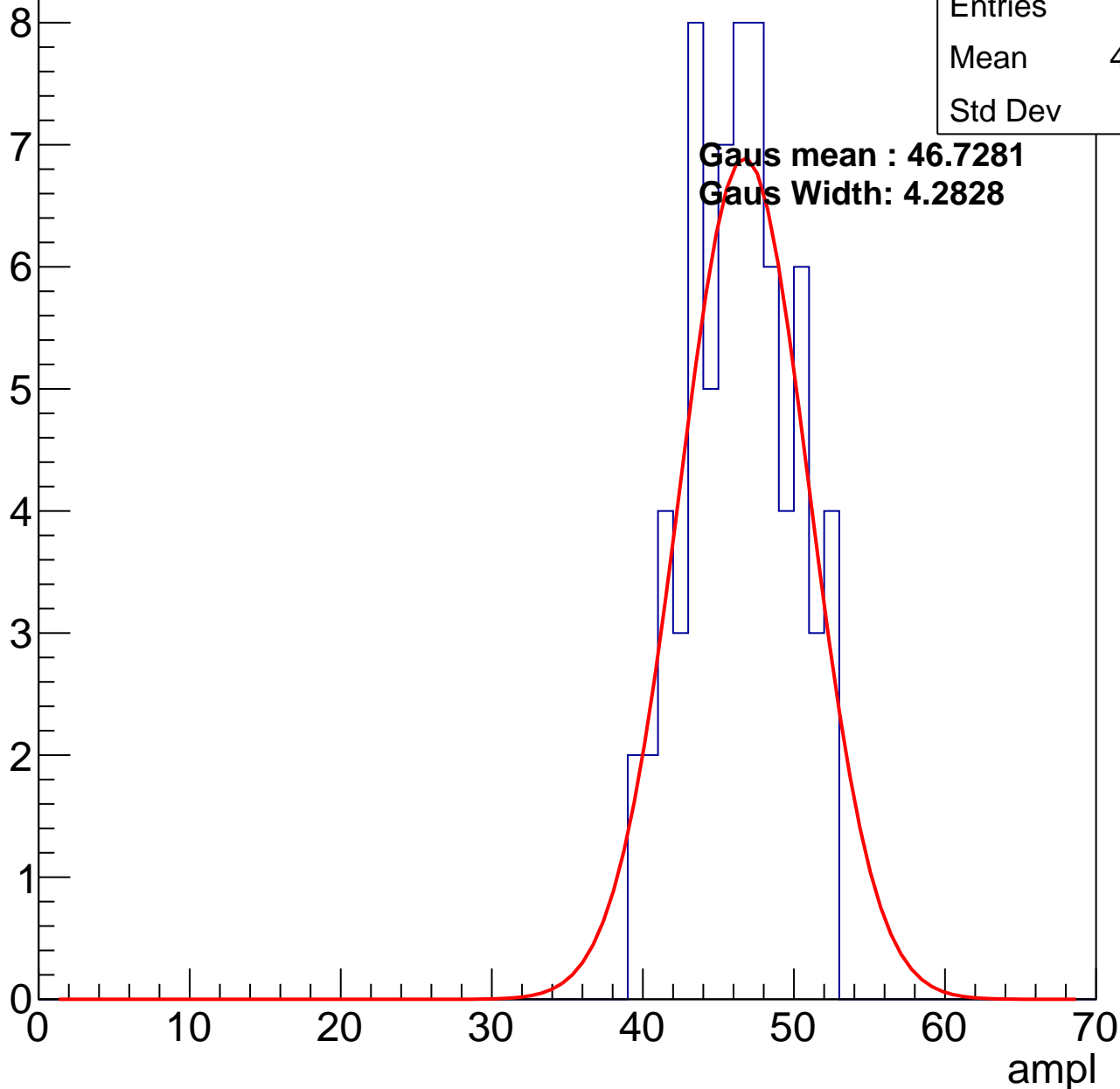
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	45.94
Std Dev	3.38

**Gaus mean : 46.7281**

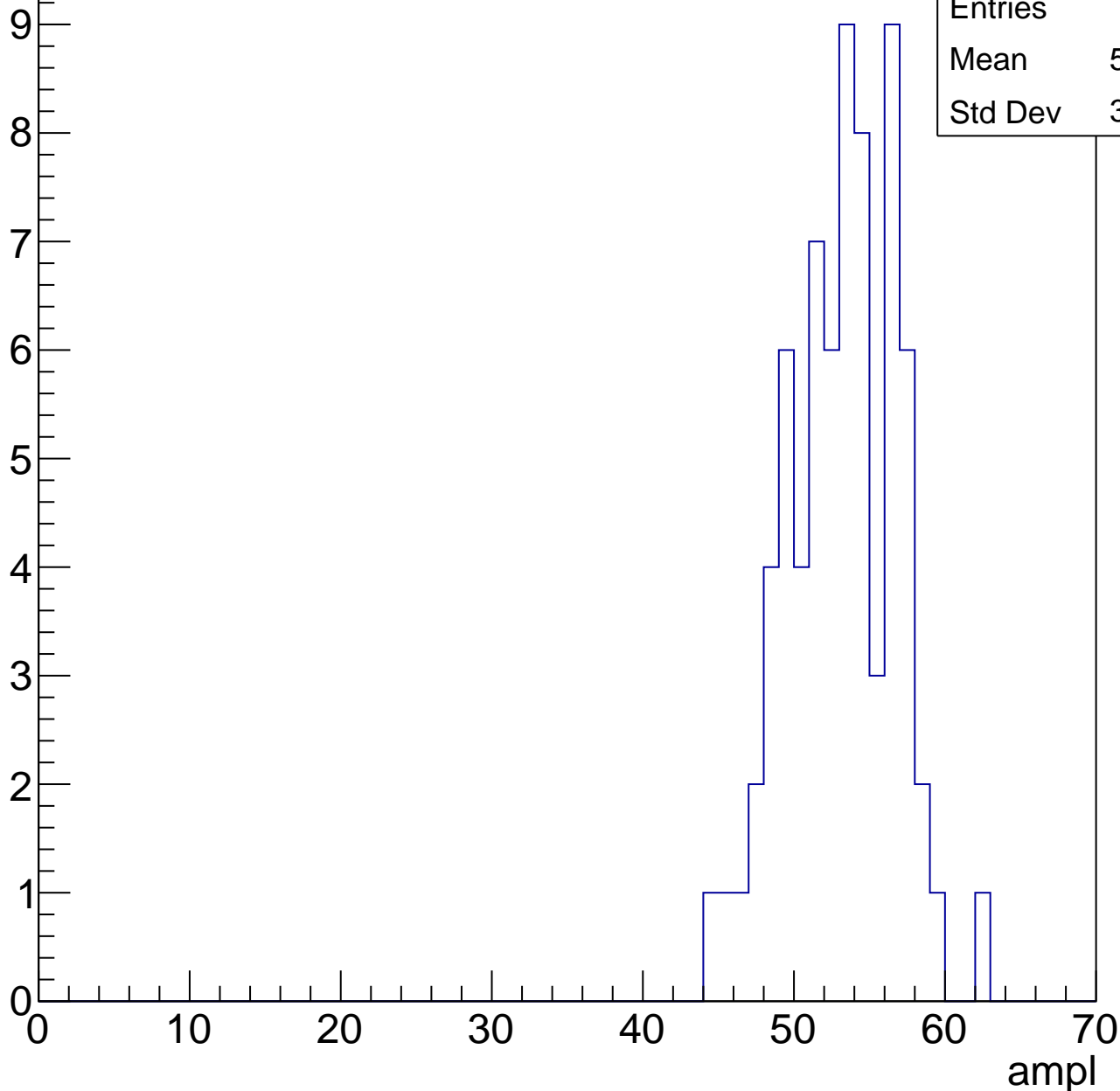
**Gaus Width: 4.2828**



# B0L000S, U13-ch2, adc3

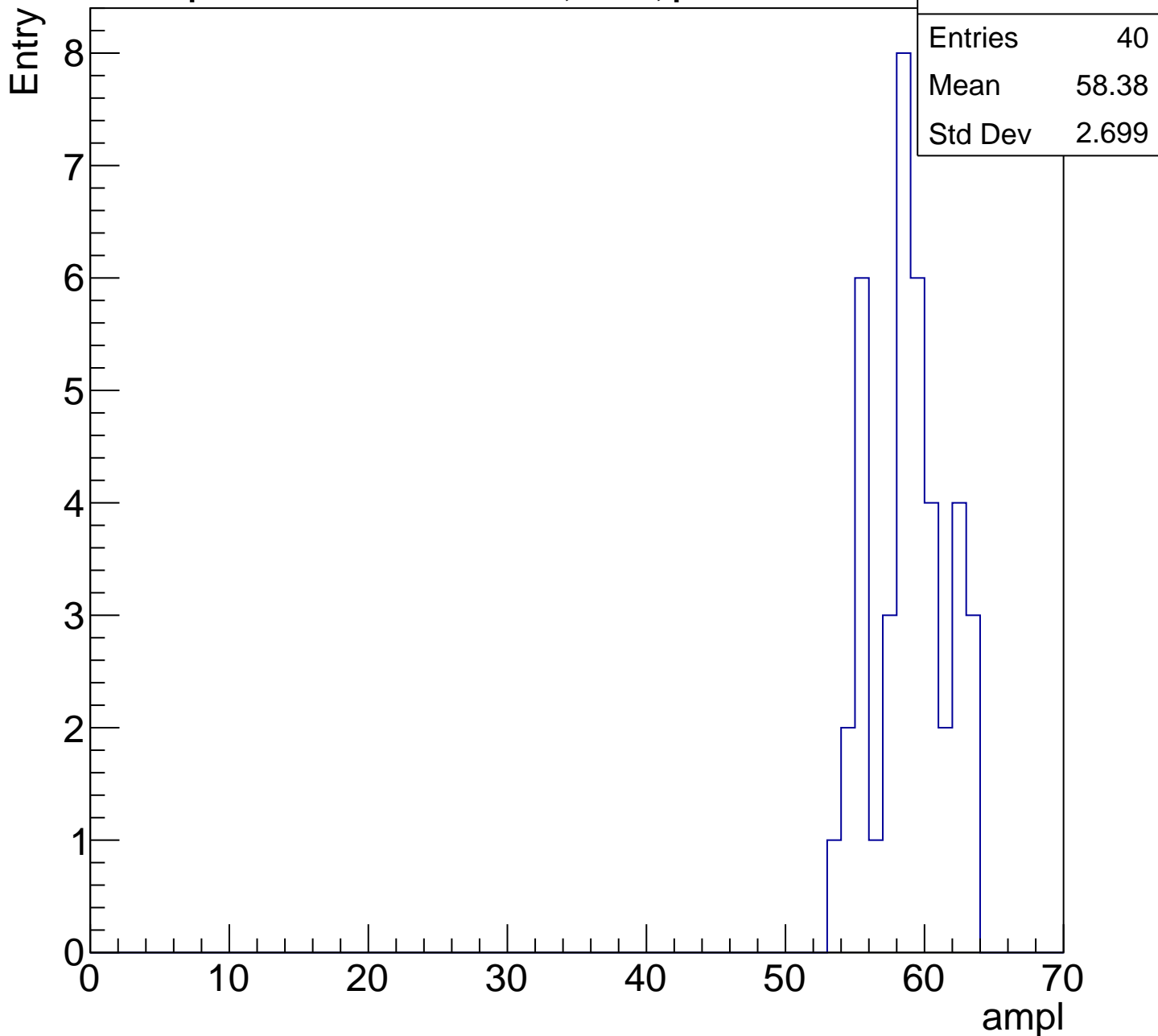
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

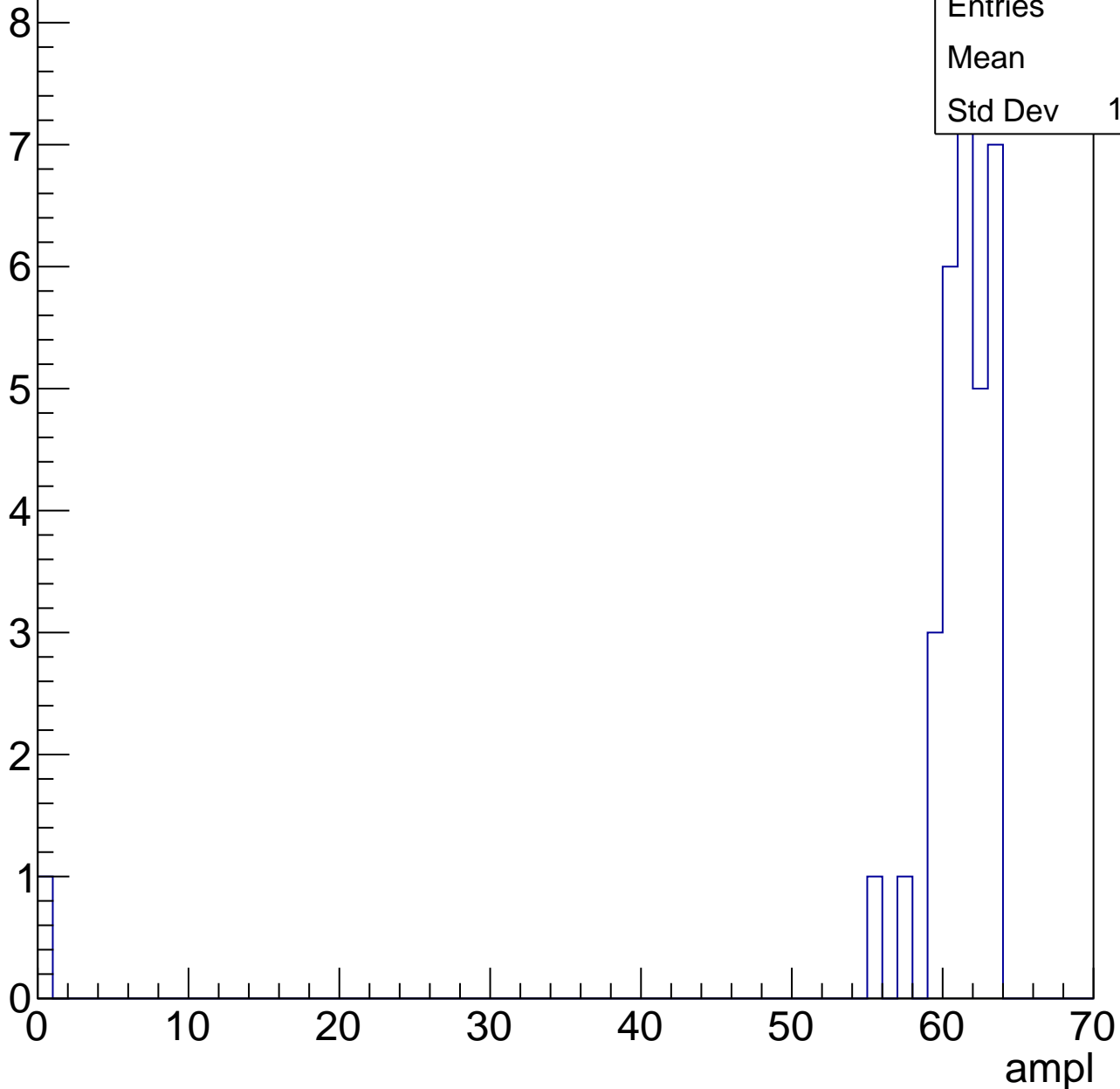


# B0L000S, U13-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	59
Std Dev	10.75



# B0L000S, U13-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch3, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	31.96
Std Dev	3.156

**Gaus mean : 32.0983**

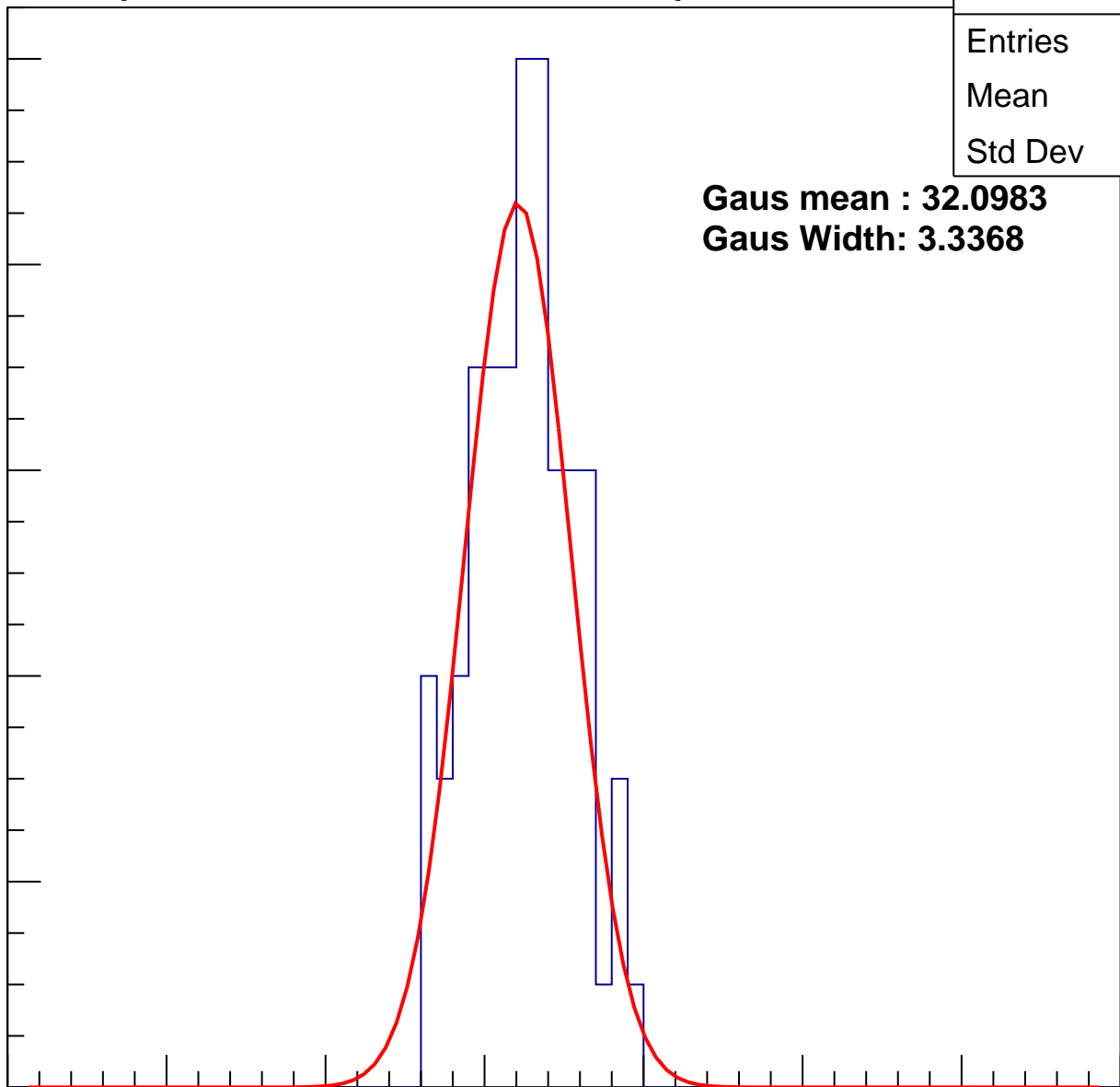
**Gaus Width: 3.3368**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch3, adc1

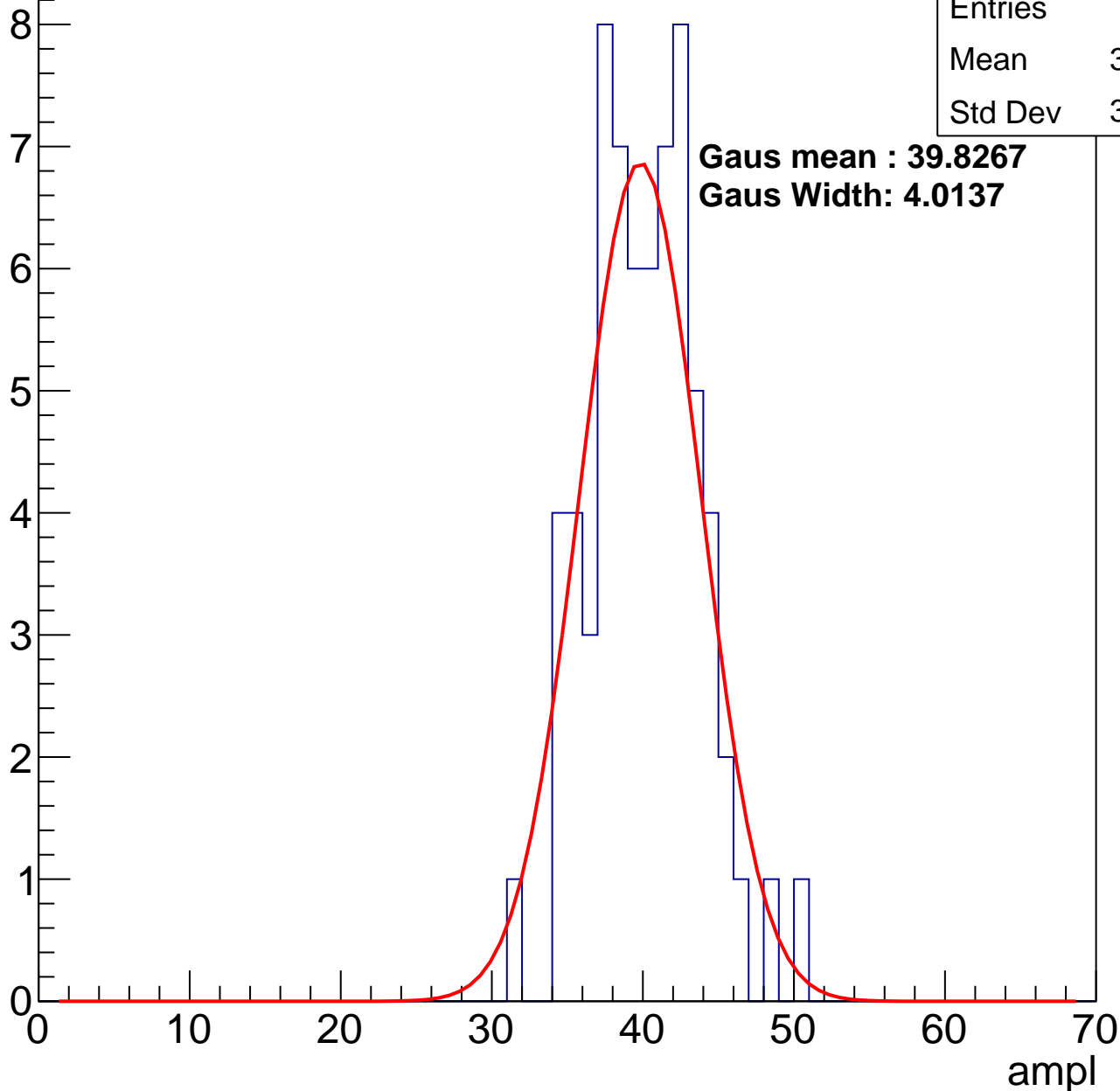
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	39.69
Std Dev	3.578

**Gaus mean : 39.8267**

**Gaus Width: 4.0137**



# B0L000S, U13-ch3, adc2

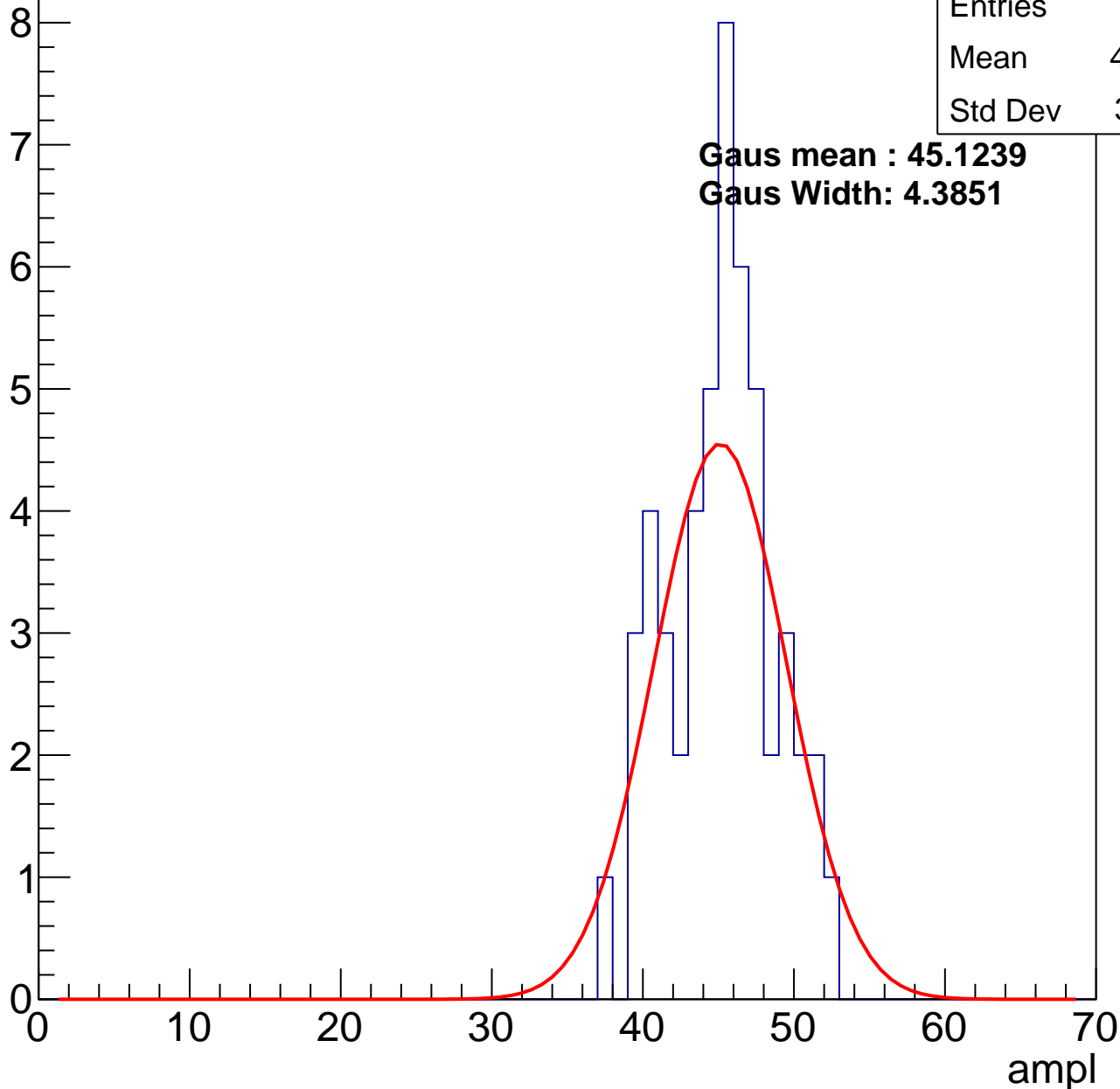
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44.73
Std Dev	3.481

**Gaus mean : 45.1239**

**Gaus Width: 4.3851**

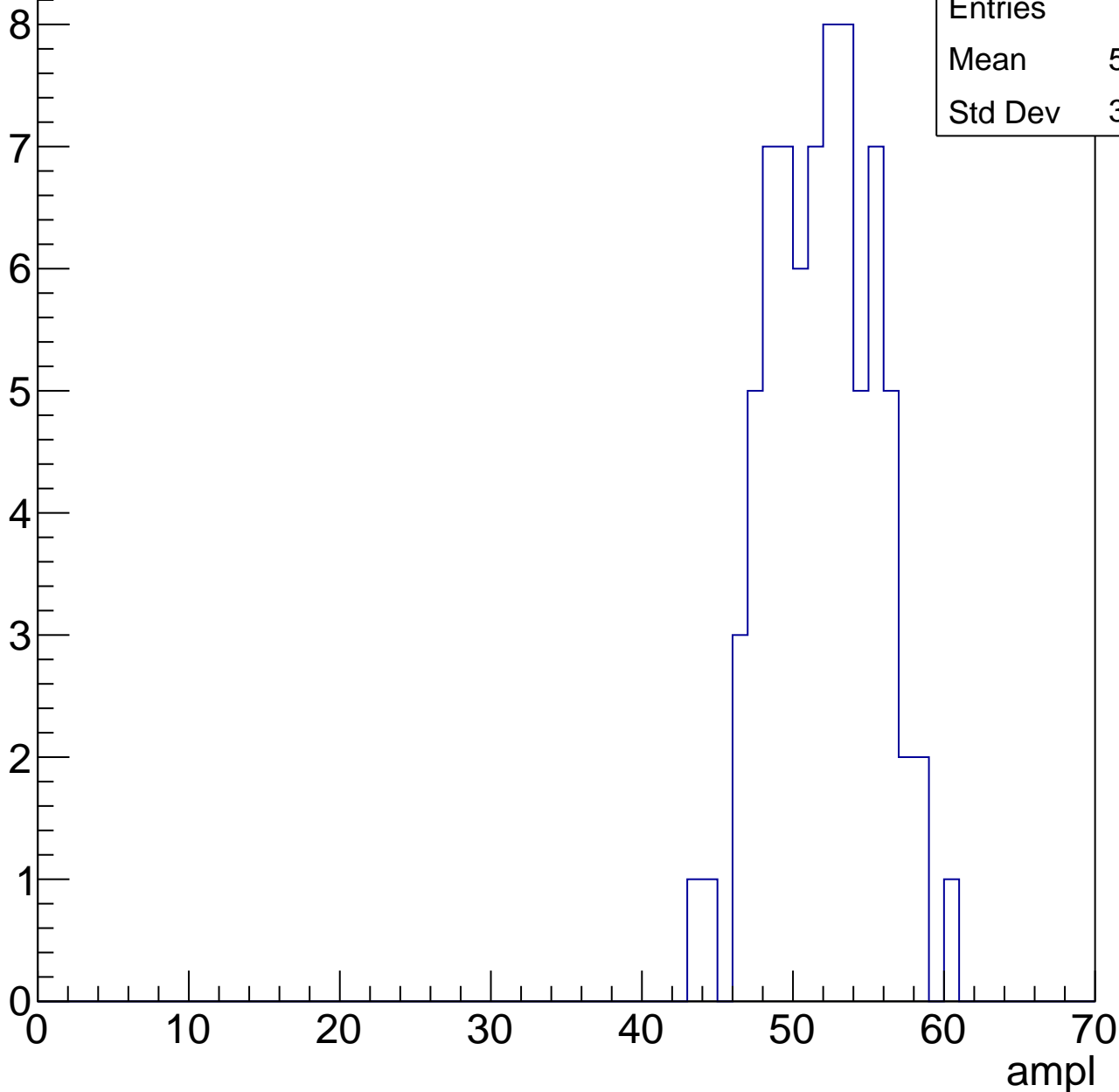


# B0L000S, U13-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	51.48
Std Dev	3.504

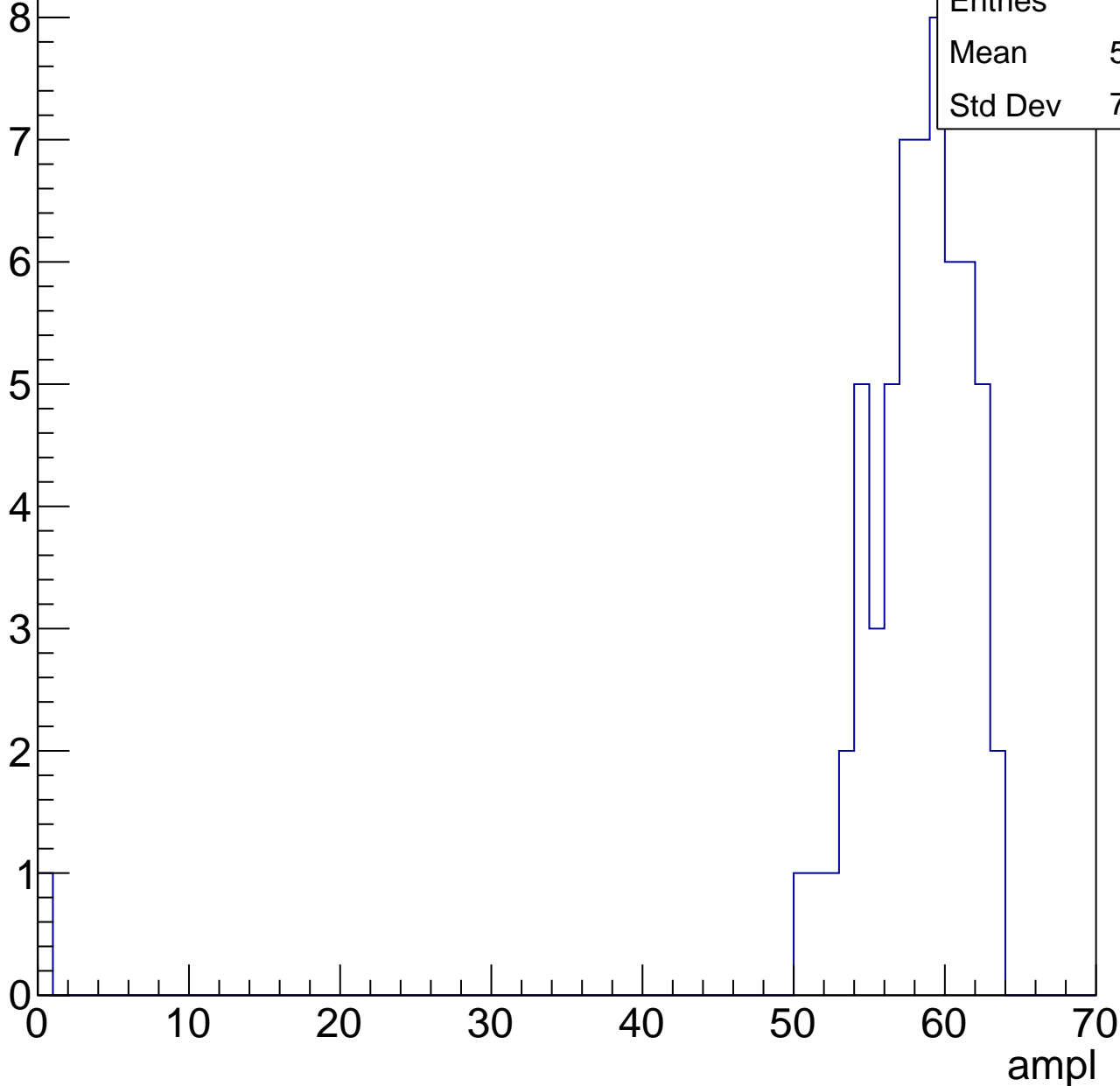


# B0L000S, U13-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	56.88
Std Dev	7.998

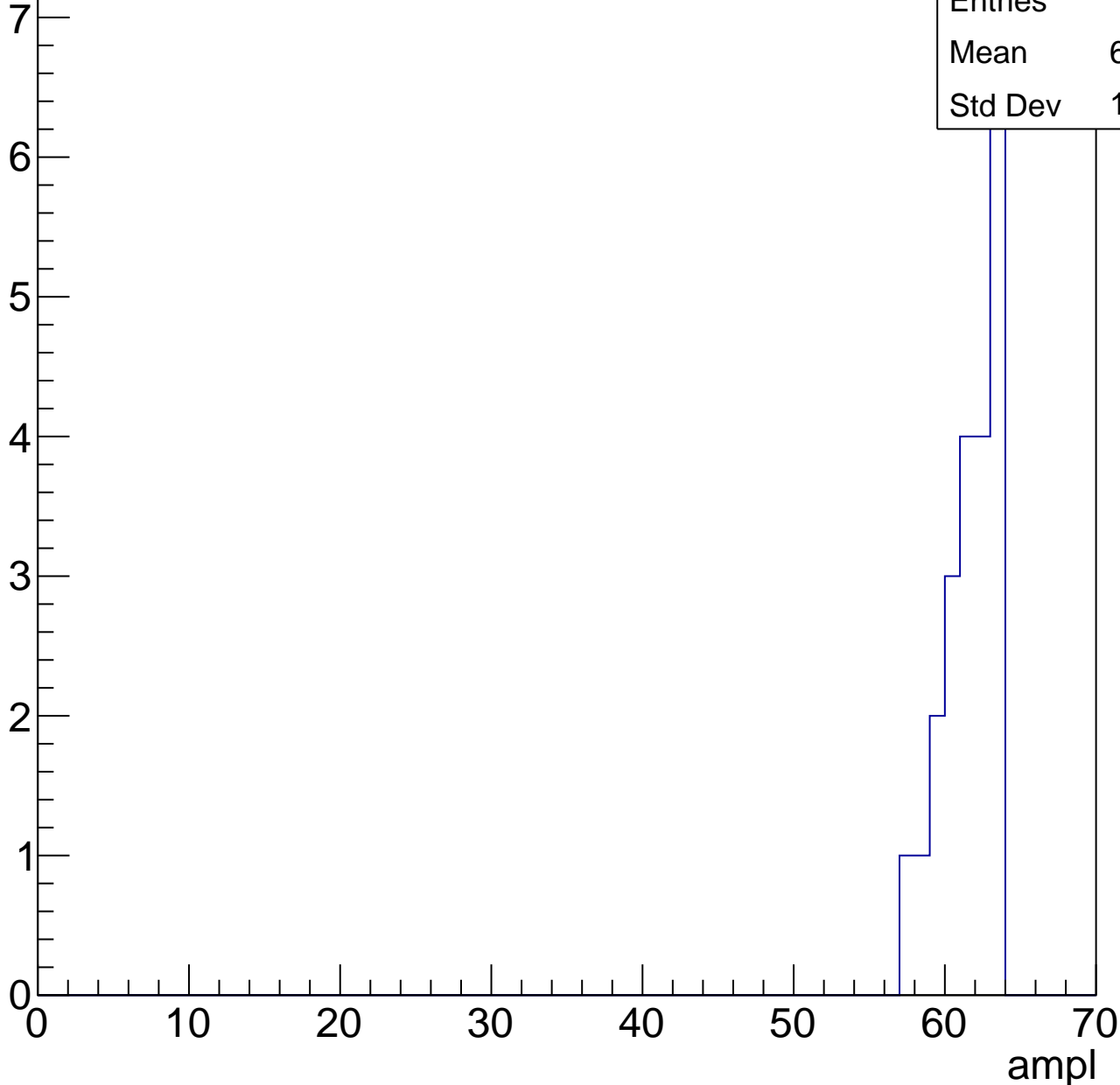


# B0L000S, U13-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	61.18
Std Dev	1.749



# B0L000S, U13-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L000S, U13-ch4, adc0

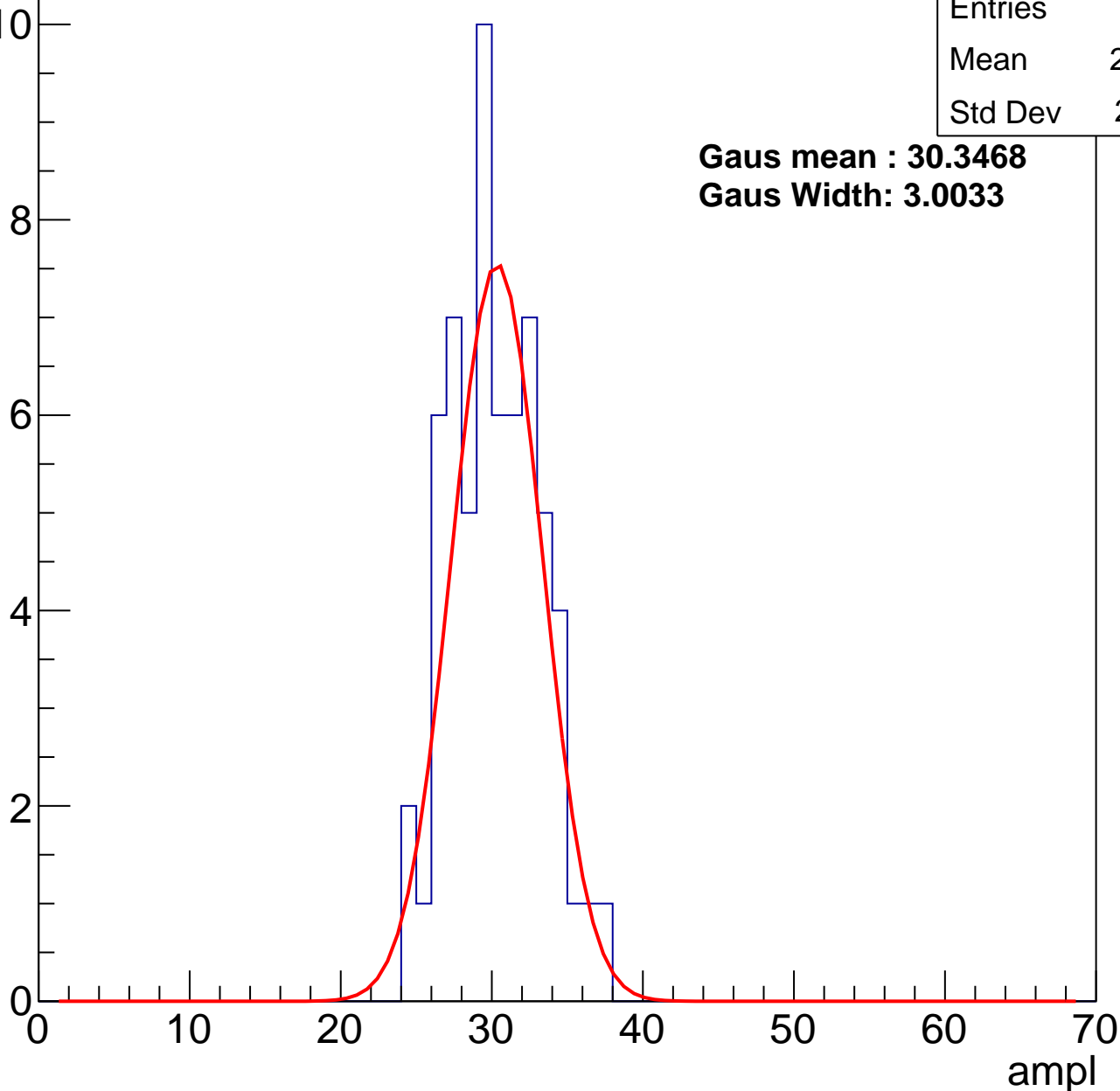
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	29.79
Std Dev	2.941

**Gaus mean : 30.3468**

**Gaus Width: 3.0033**



# B0L000S, U13-ch4, adc1

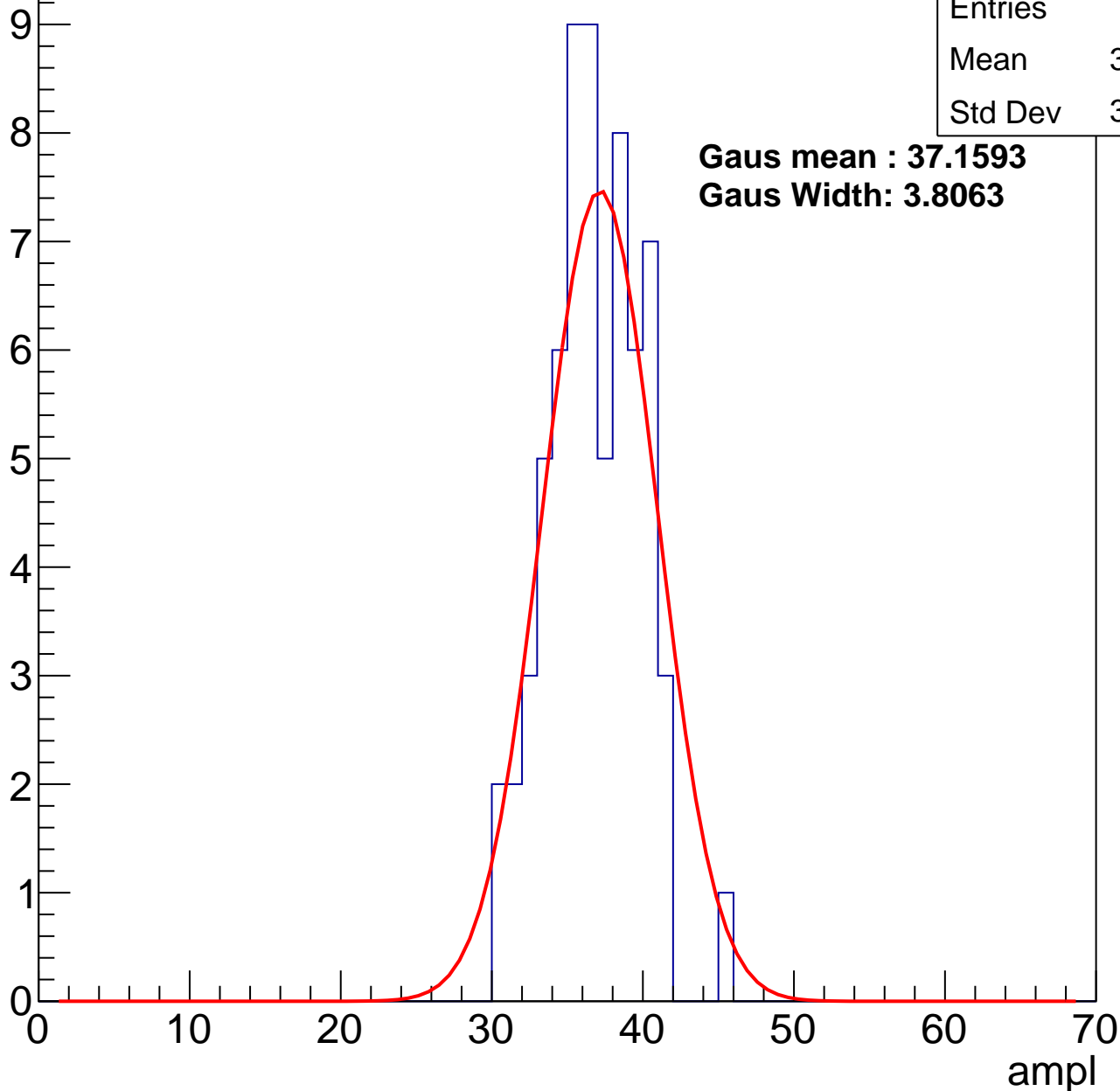
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.32
Std Dev	3.016

**Gaus mean : 37.1593**

**Gaus Width: 3.8063**



# B0L000S, U13-ch4, adc2

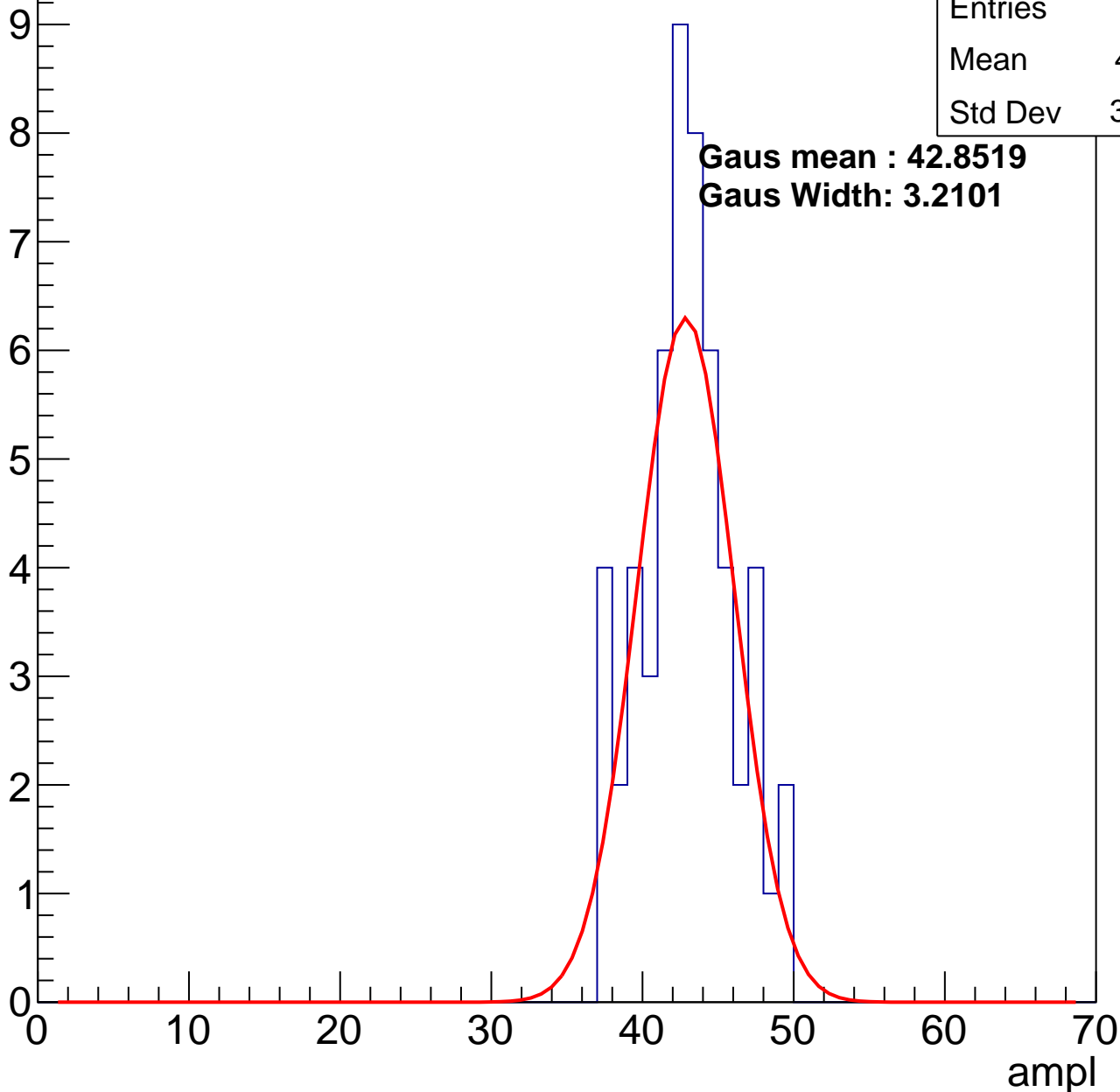
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.51
Std Dev	3.032

**Gaus mean : 42.8519**

**Gaus Width: 3.2101**

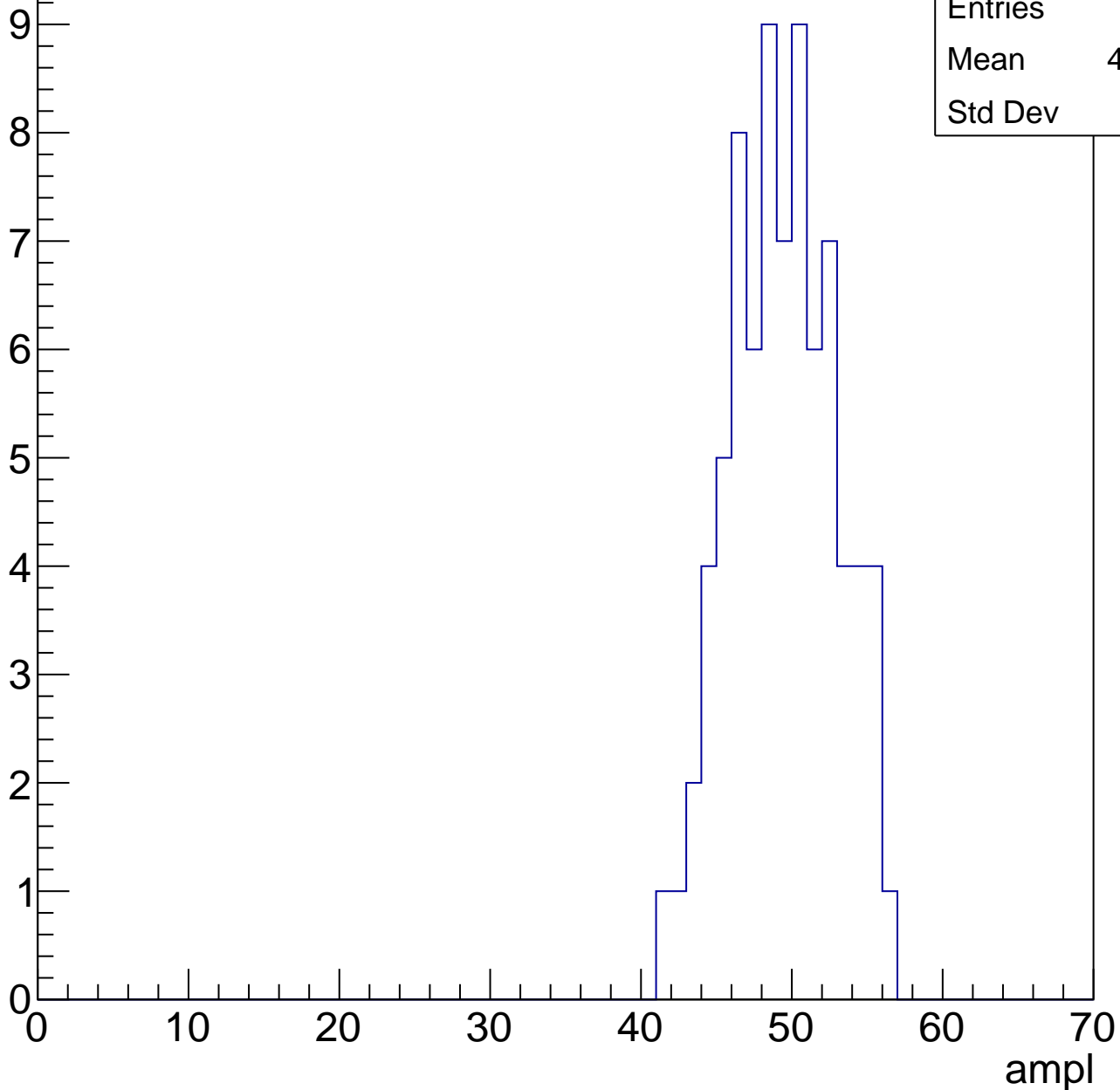


# B0L000S, U13-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	48.96
Std Dev	3.44

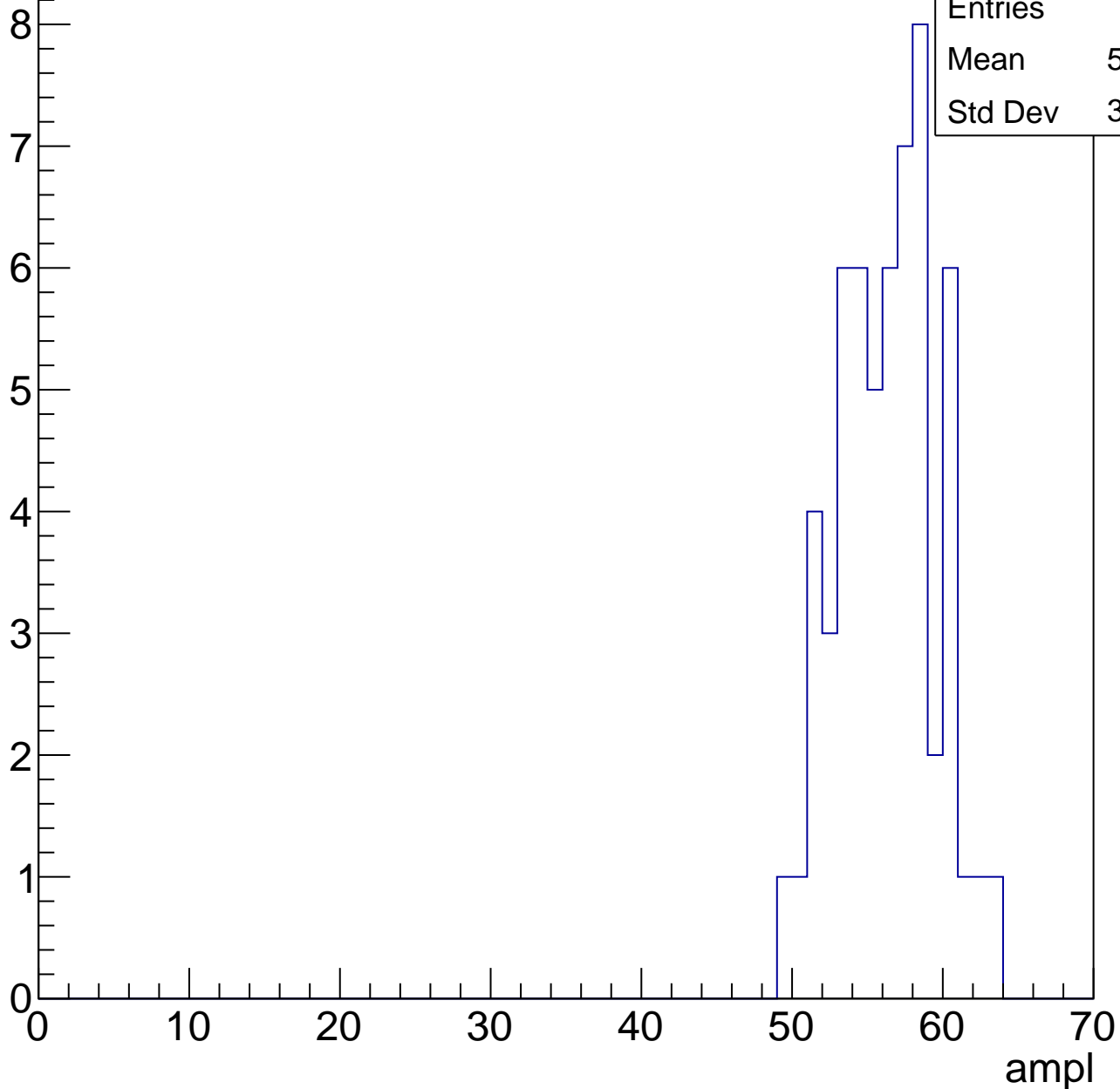


# B0L000S, U13-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.84
Std Dev	3.156



# B0L000S, U13-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

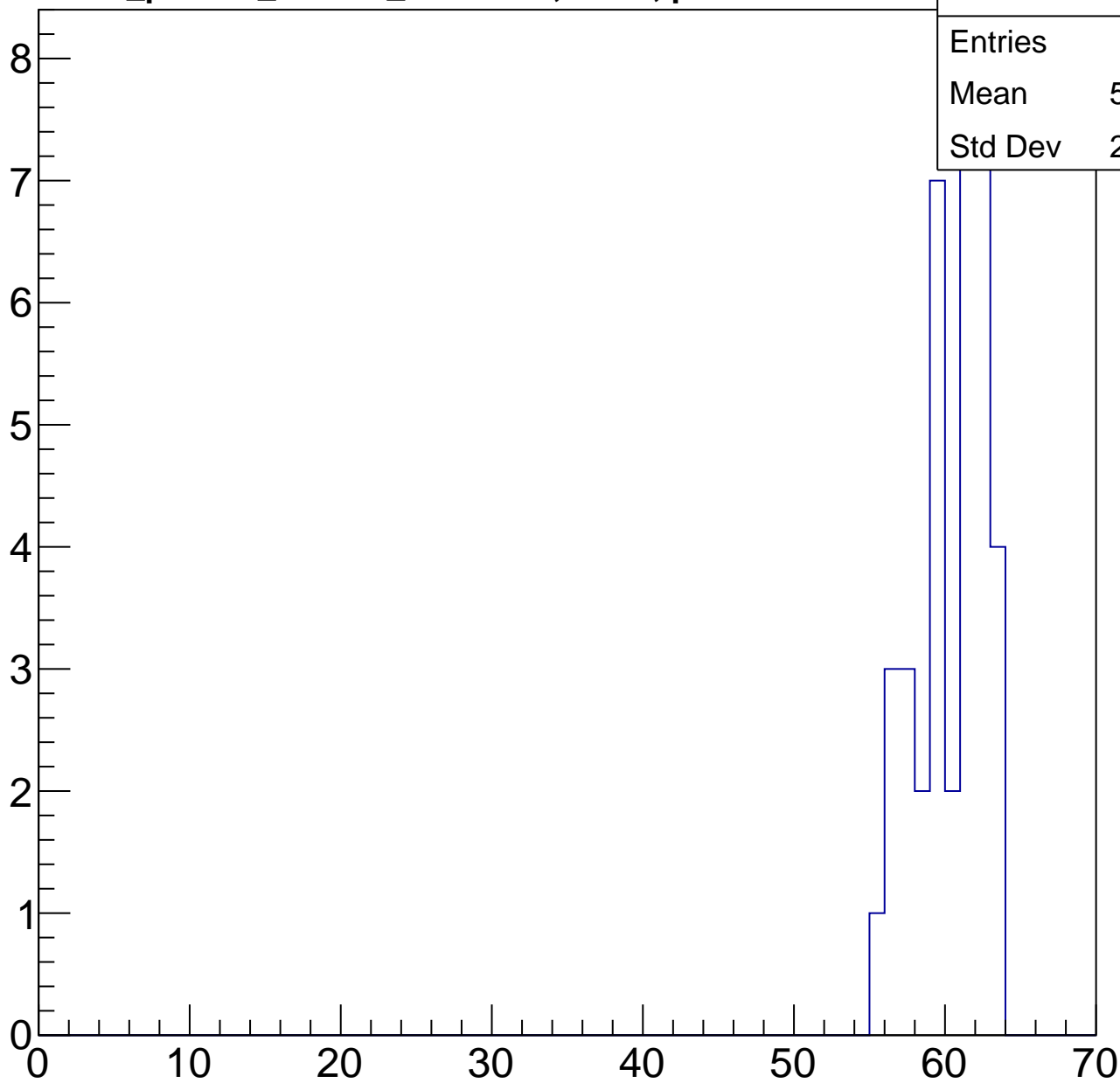
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	38
Mean	59.97
Std Dev	2.242

ampl

0 10 20 30 40 50 60 70

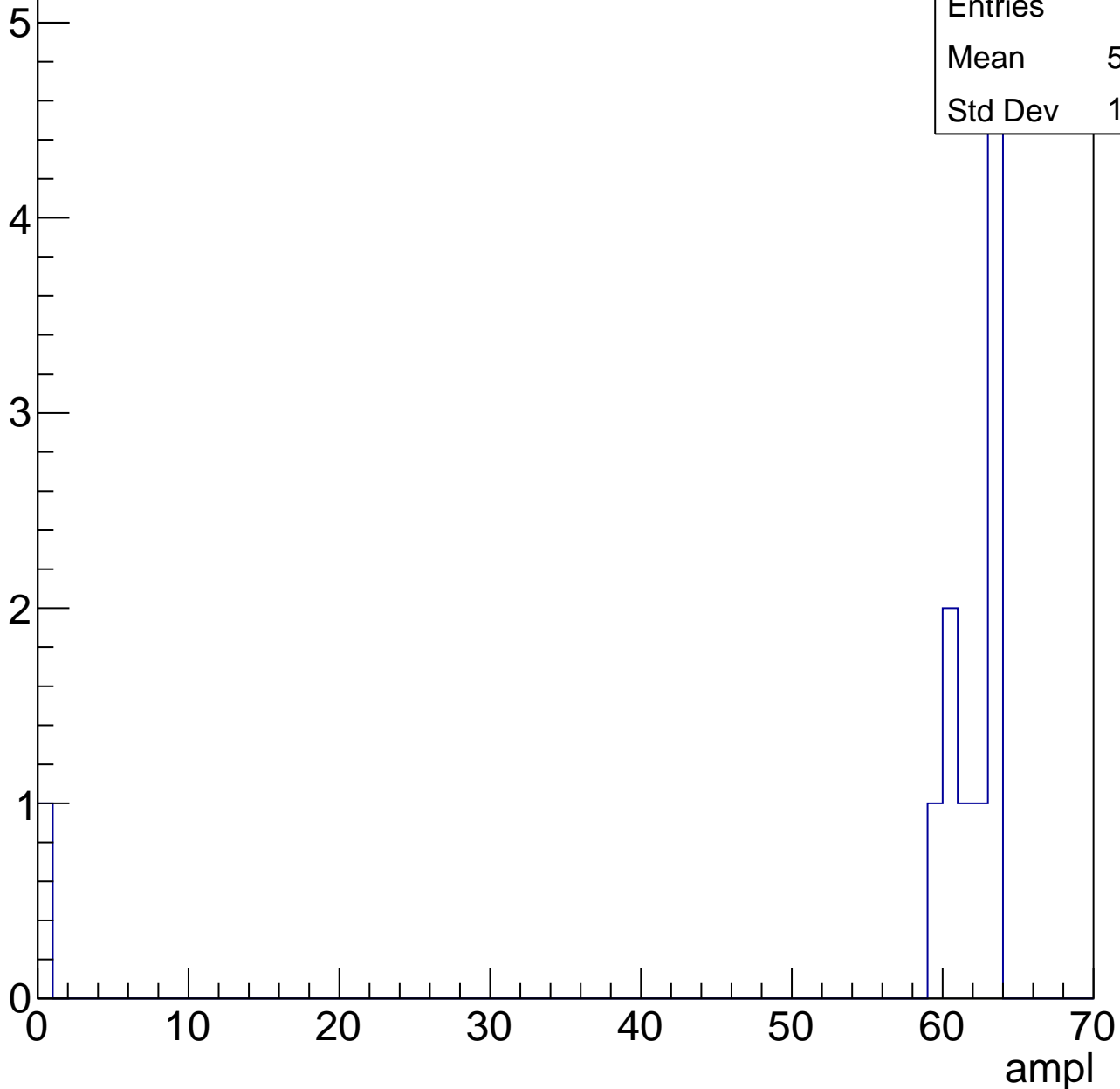


# B0L000S, U13-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	56.09
Std Dev	17.79





# B0L000S, U13-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U13-ch5, adc0

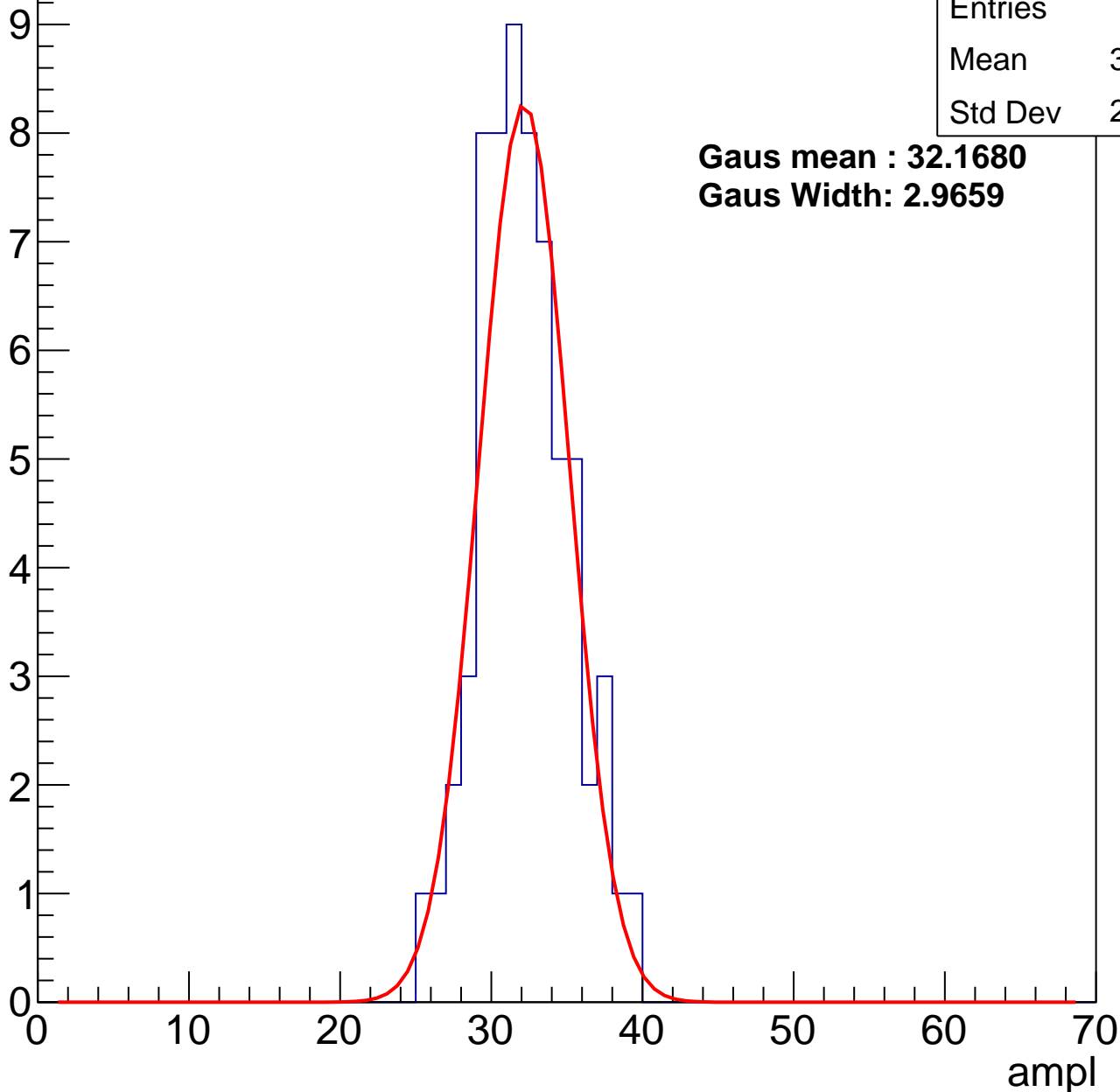
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.75
Std Dev	2.947

**Gaus mean : 32.1680**

**Gaus Width: 2.9659**



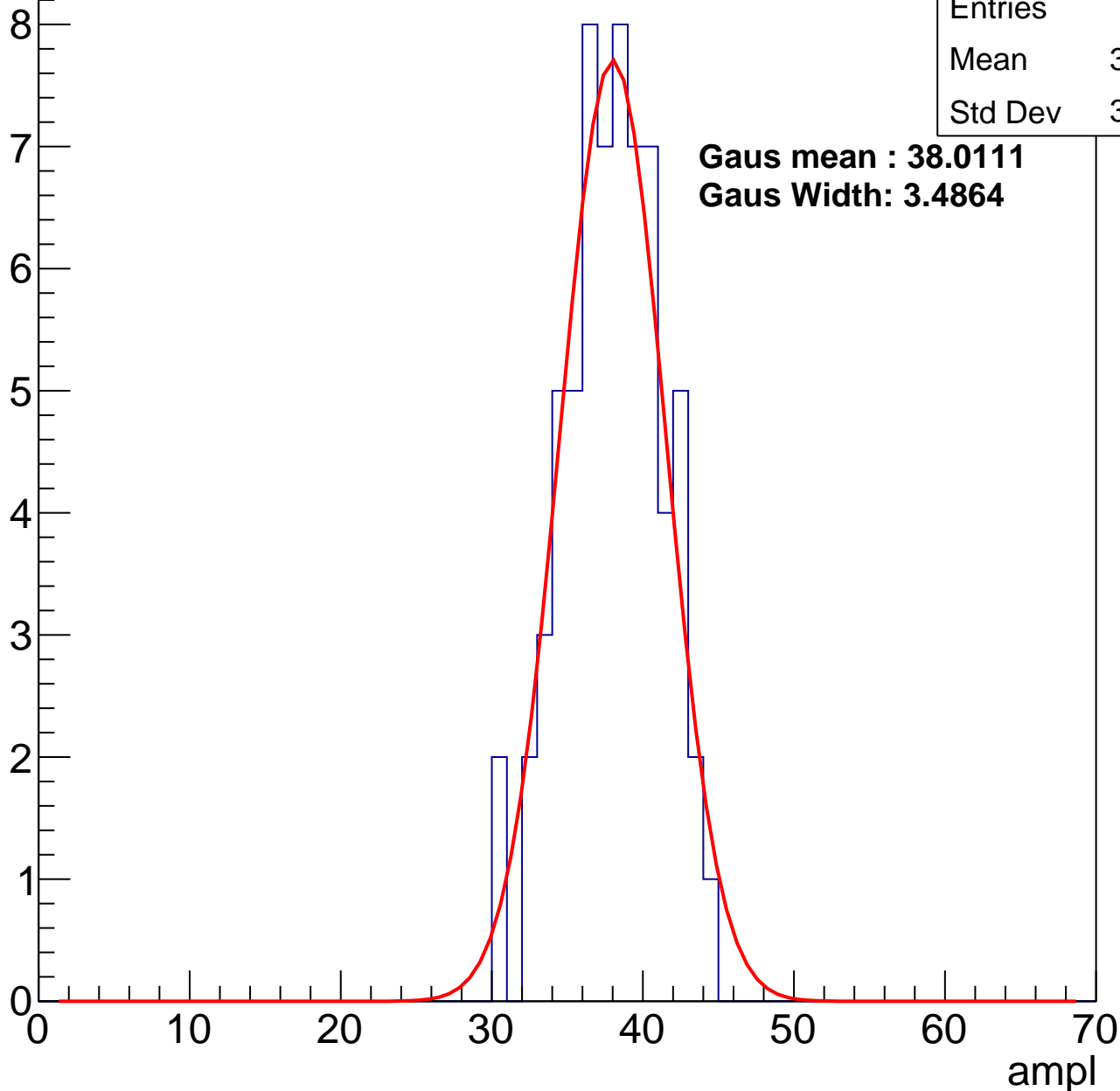
# B0L000S, U13-ch5, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.52
Std Dev	3.163

**Gaus mean : 38.0111**  
**Gaus Width: 3.4864**



# B0L000S, U13-ch5, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	45.19
Std Dev	3.321

**Gaus mean : 45.5063**

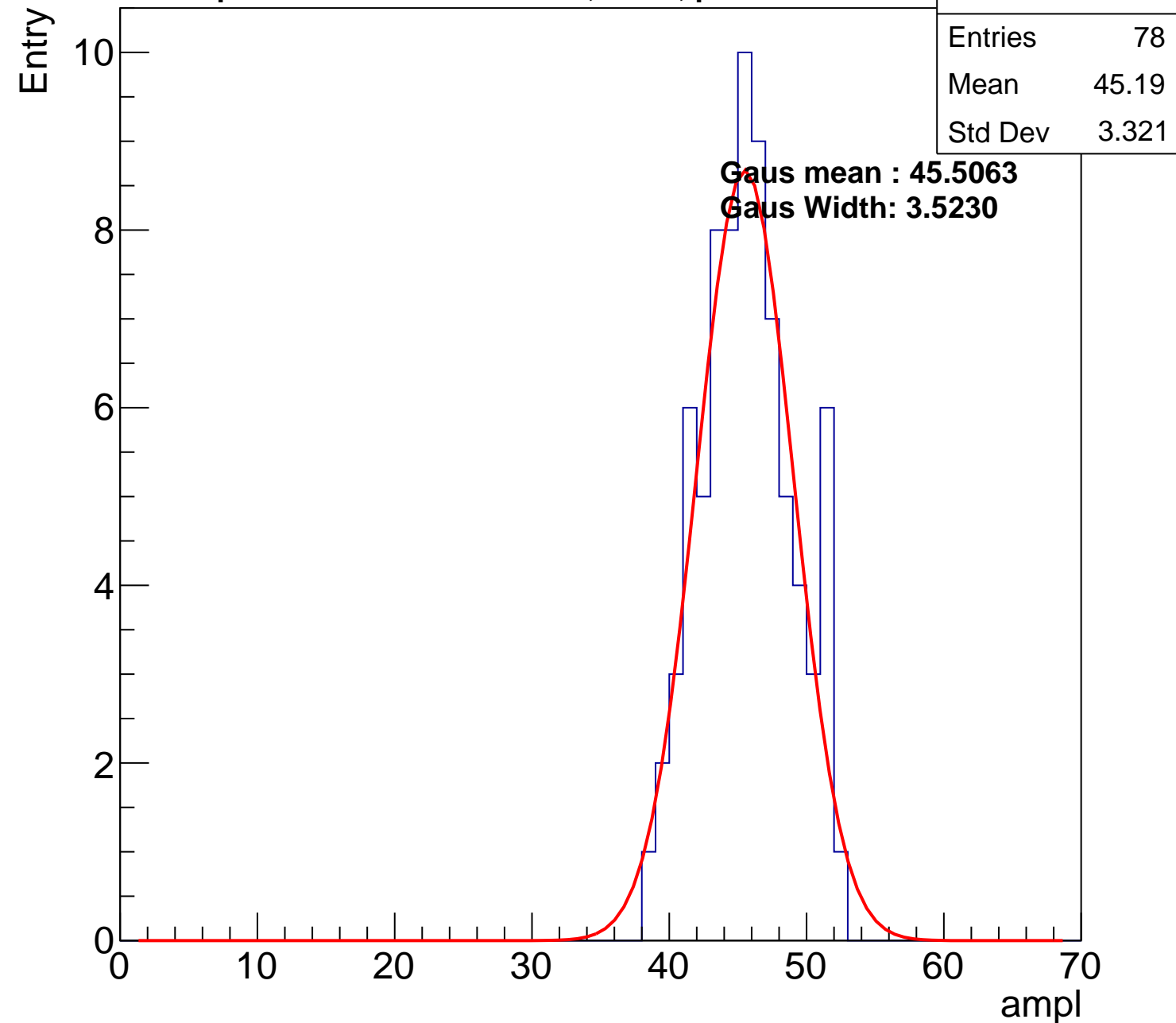
**Gaus Width: 3.5230**

Entry

10  
8  
6  
4  
2  
0

ampl

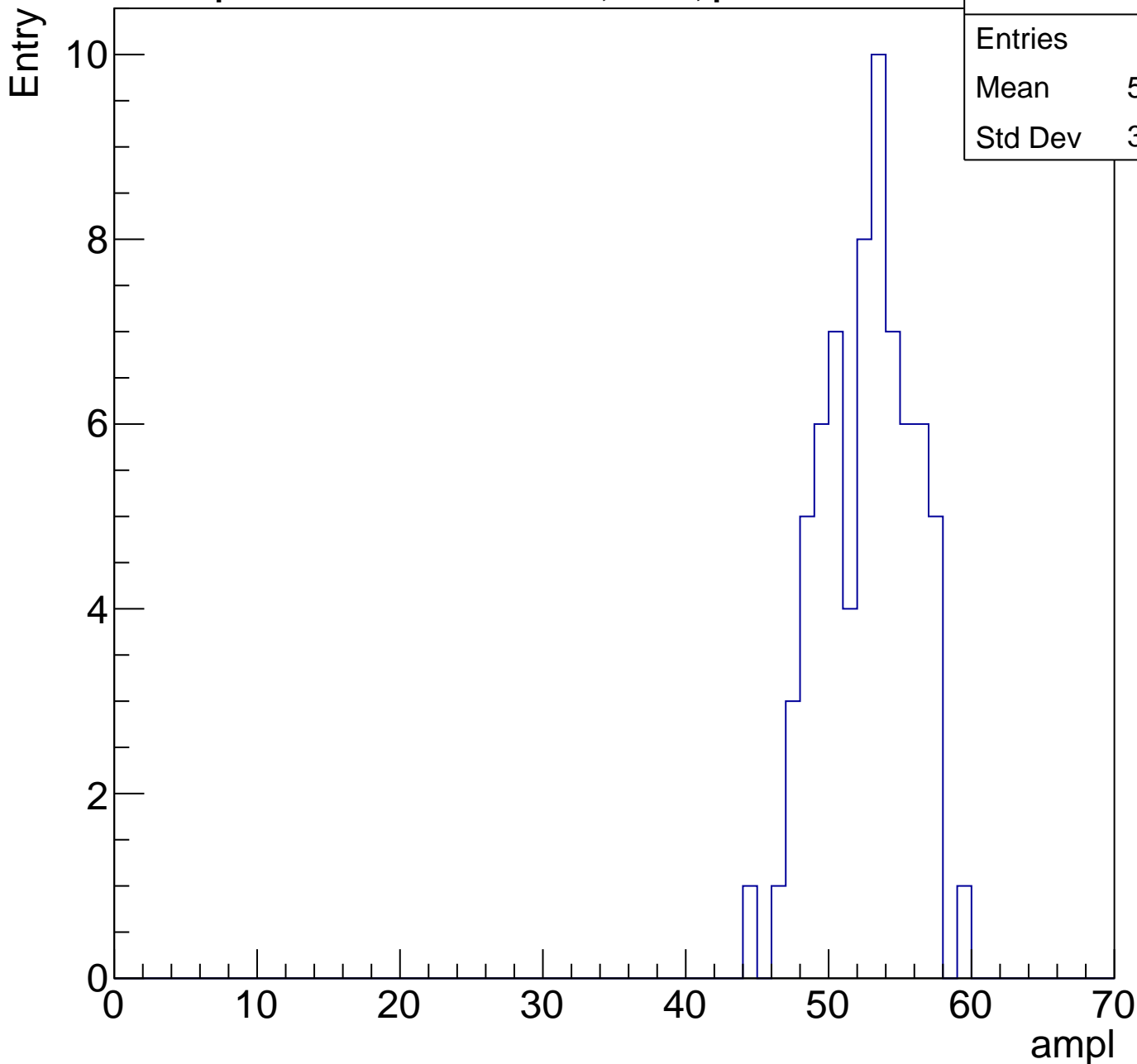
0 10 20 30 40 50 60 70



# B0L000S, U13-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	52.19
Std Dev	3.164

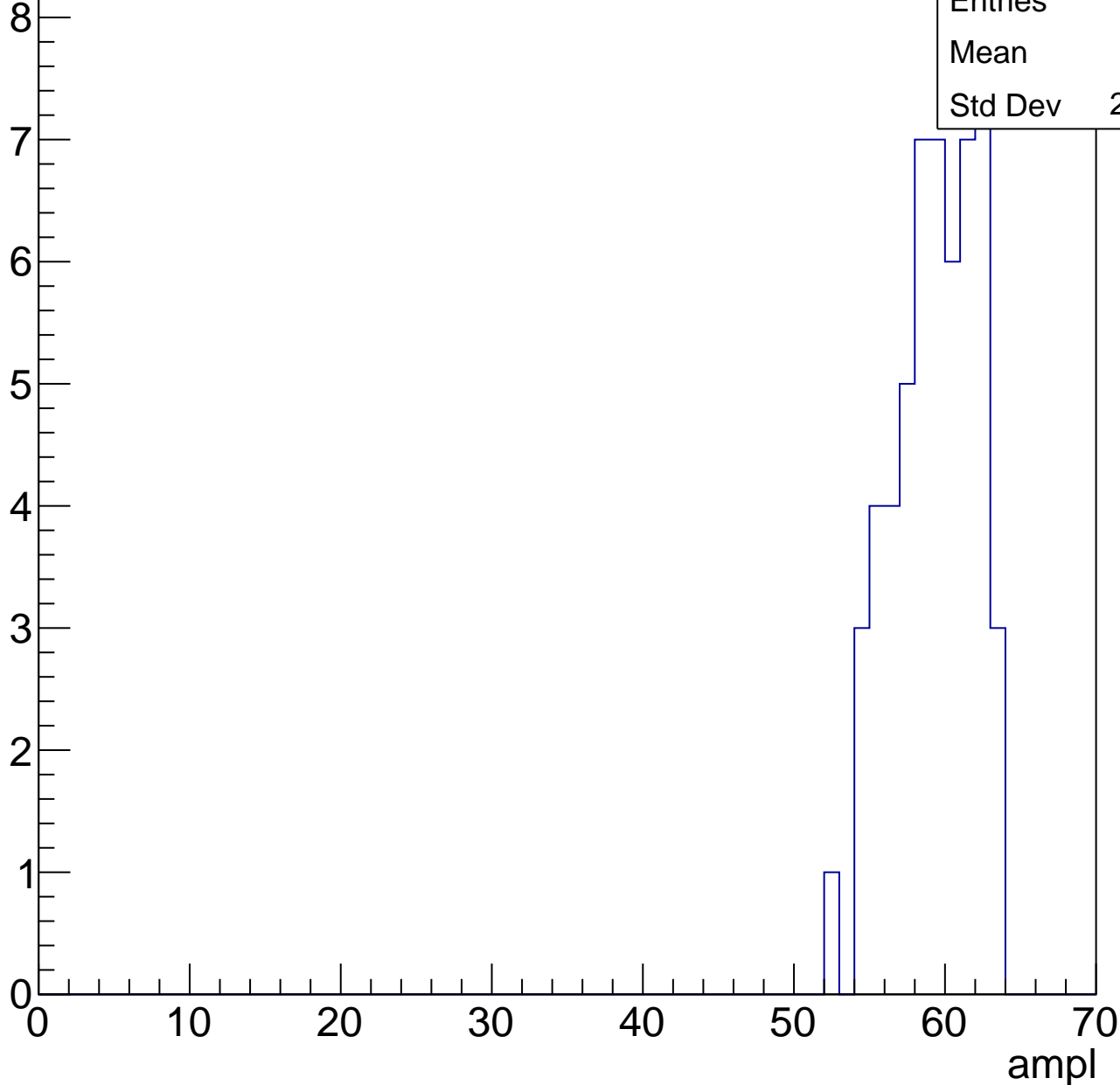


# B0L000S, U13-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.8
Std Dev	2.706

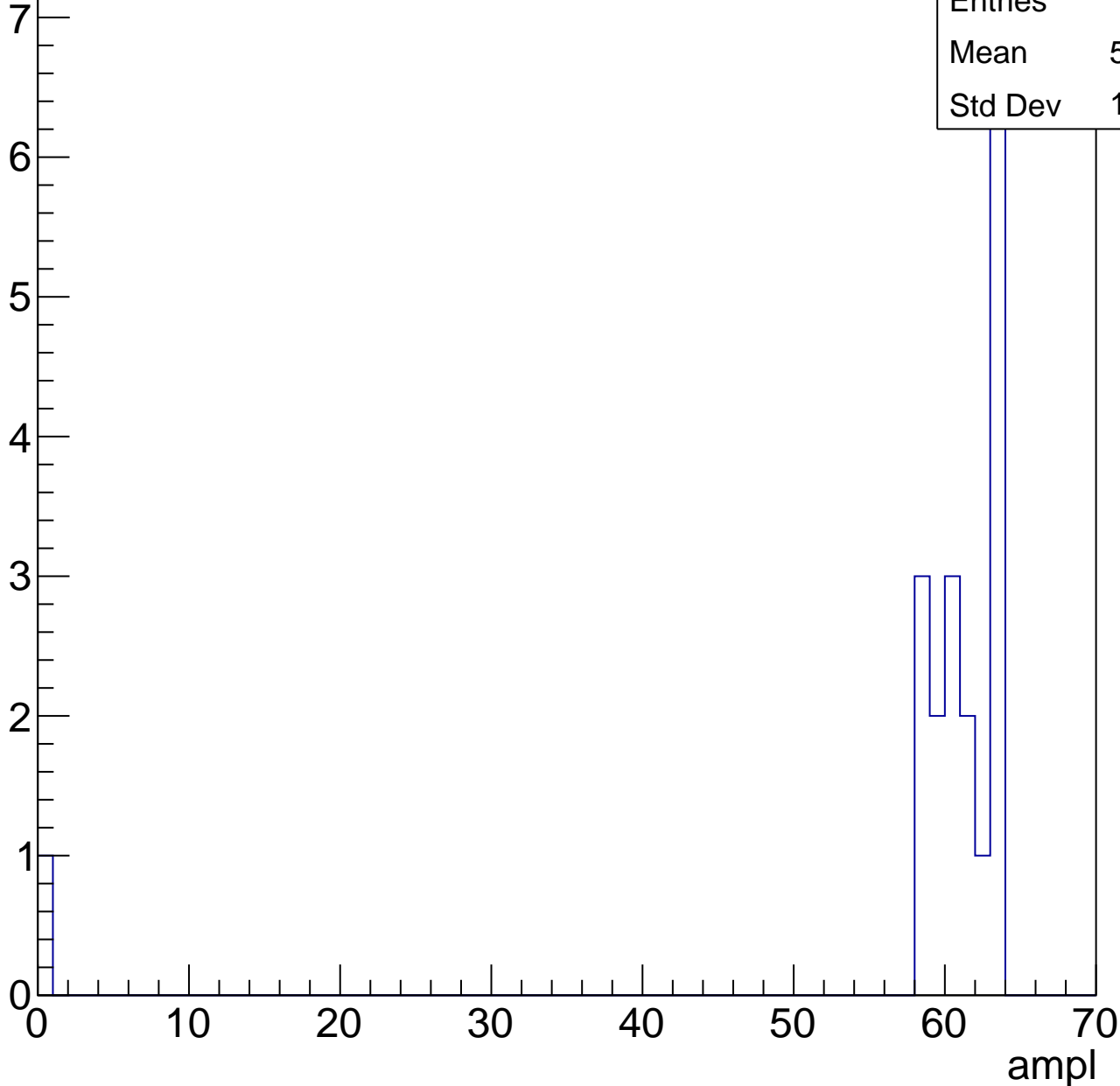


# B0L000S, U13-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	57.74
Std Dev	13.74



# B0L000S, U13-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch6, adc0

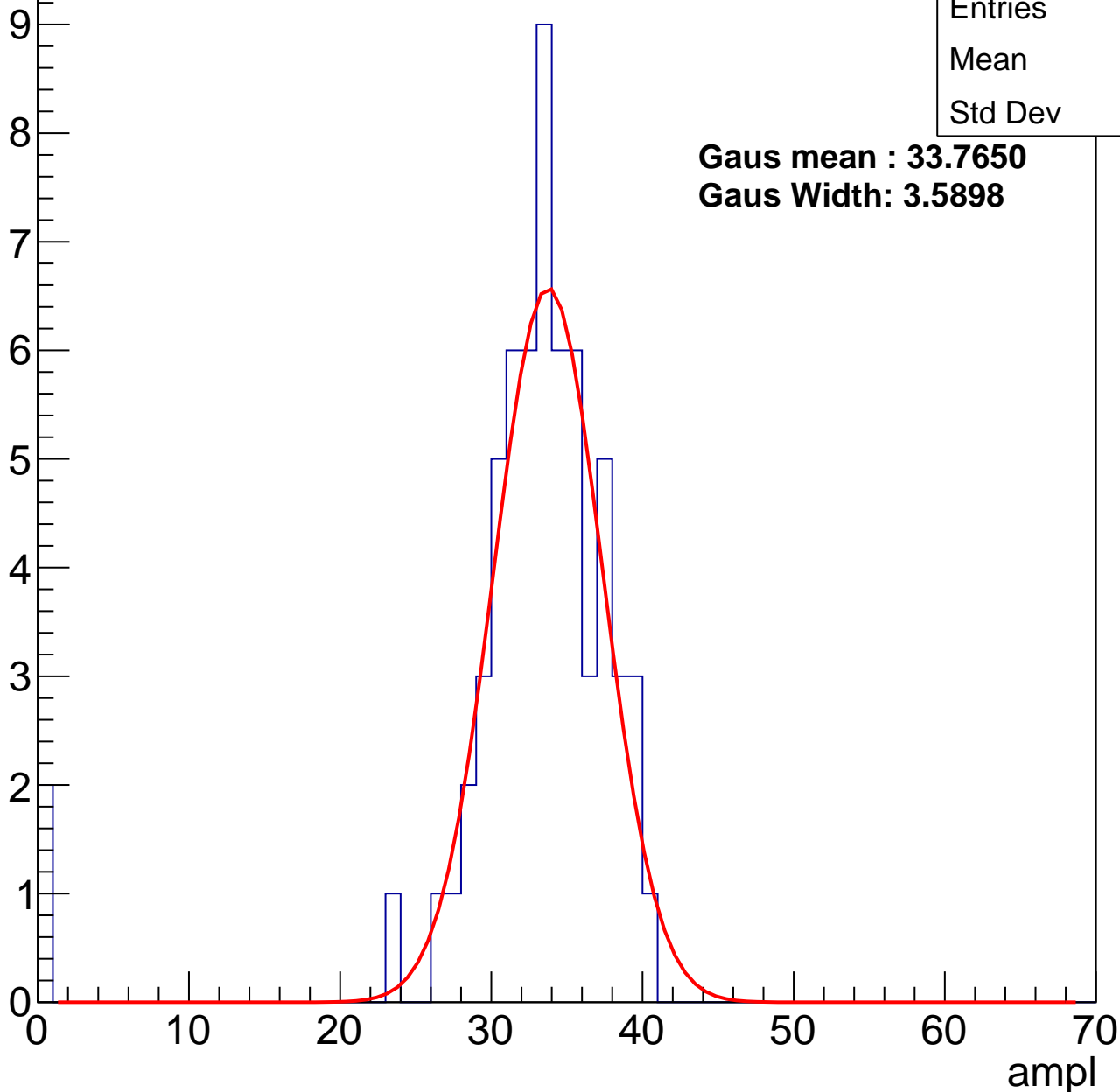
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	32.1
Std Dev	6.73

**Gaus mean : 33.7650**

**Gaus Width: 3.5898**



# B0L000S, U13-ch6, adc1

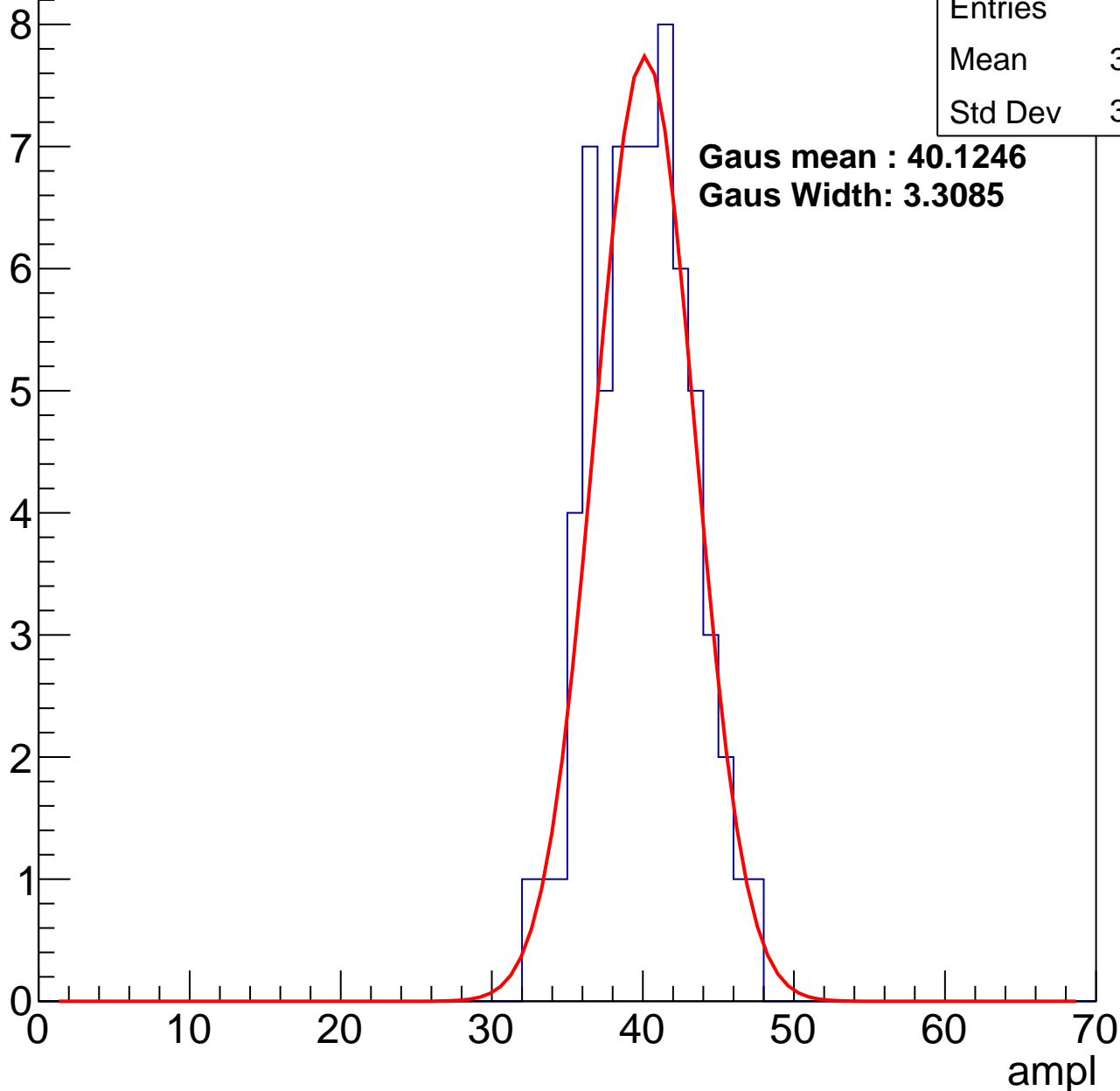
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	39.47
Std Dev	3.216

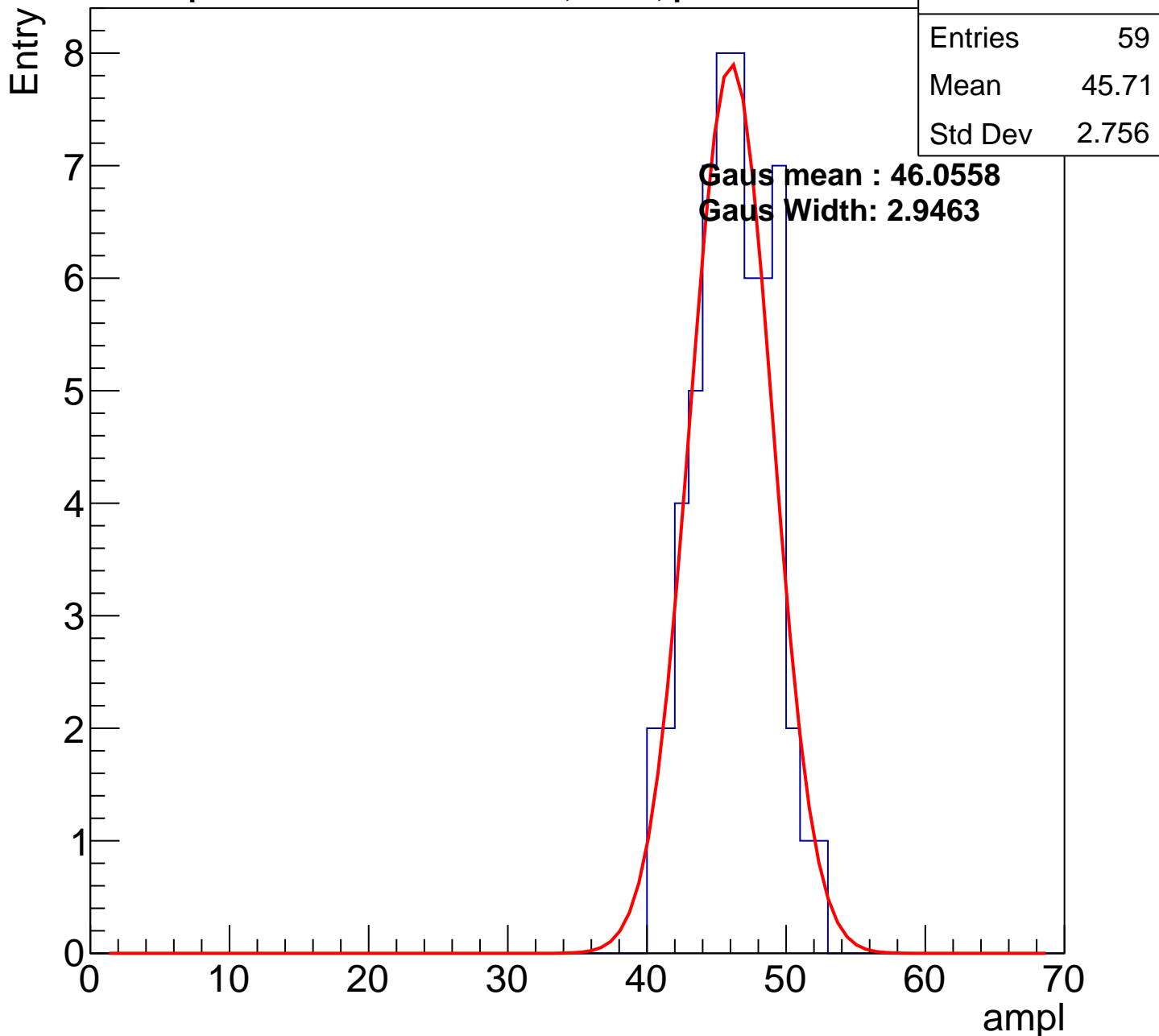
**Gaus mean : 40.1246**

**Gaus Width: 3.3085**



# B0L000S, U13-ch6, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

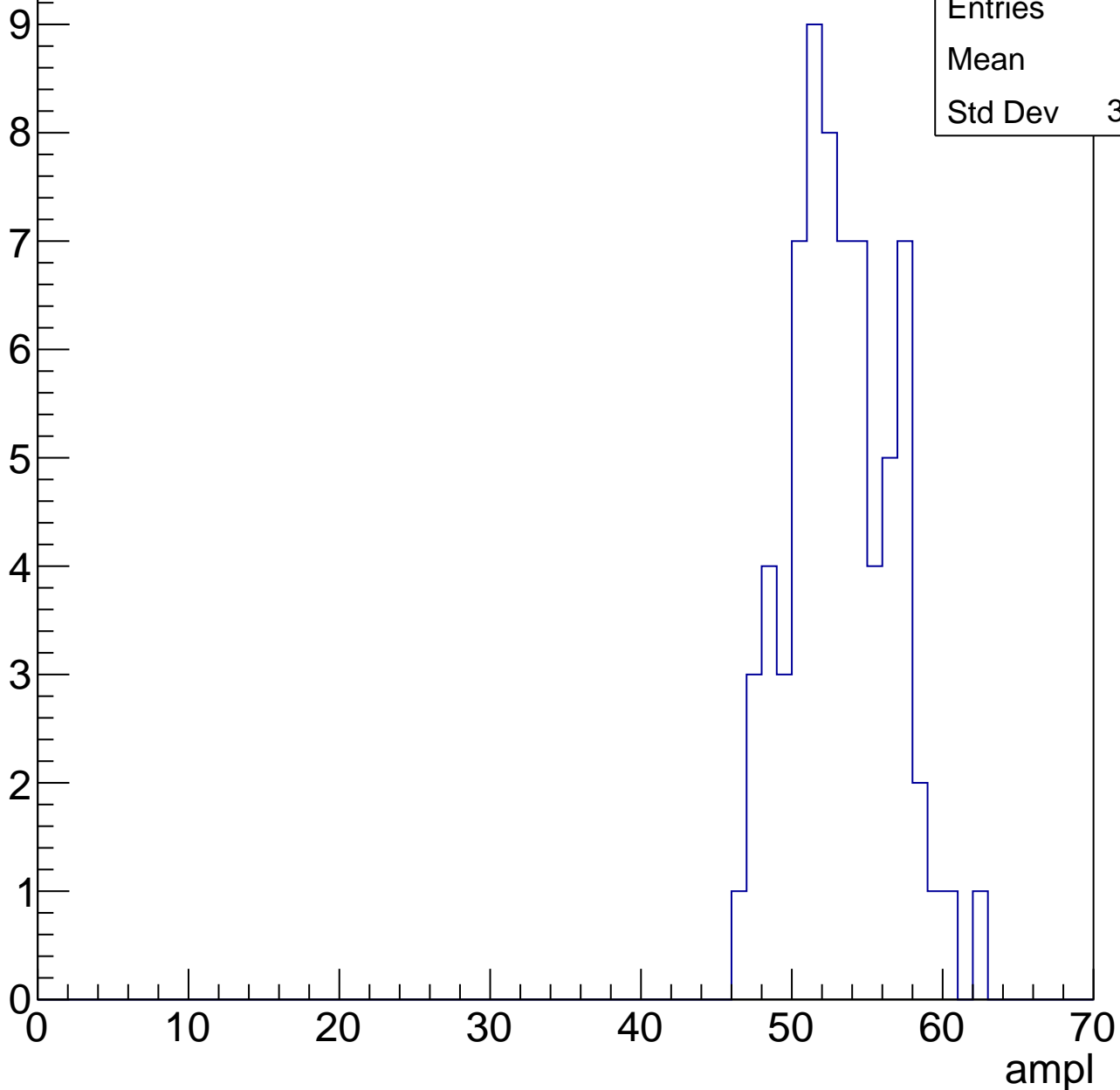


# B0L000S, U13-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

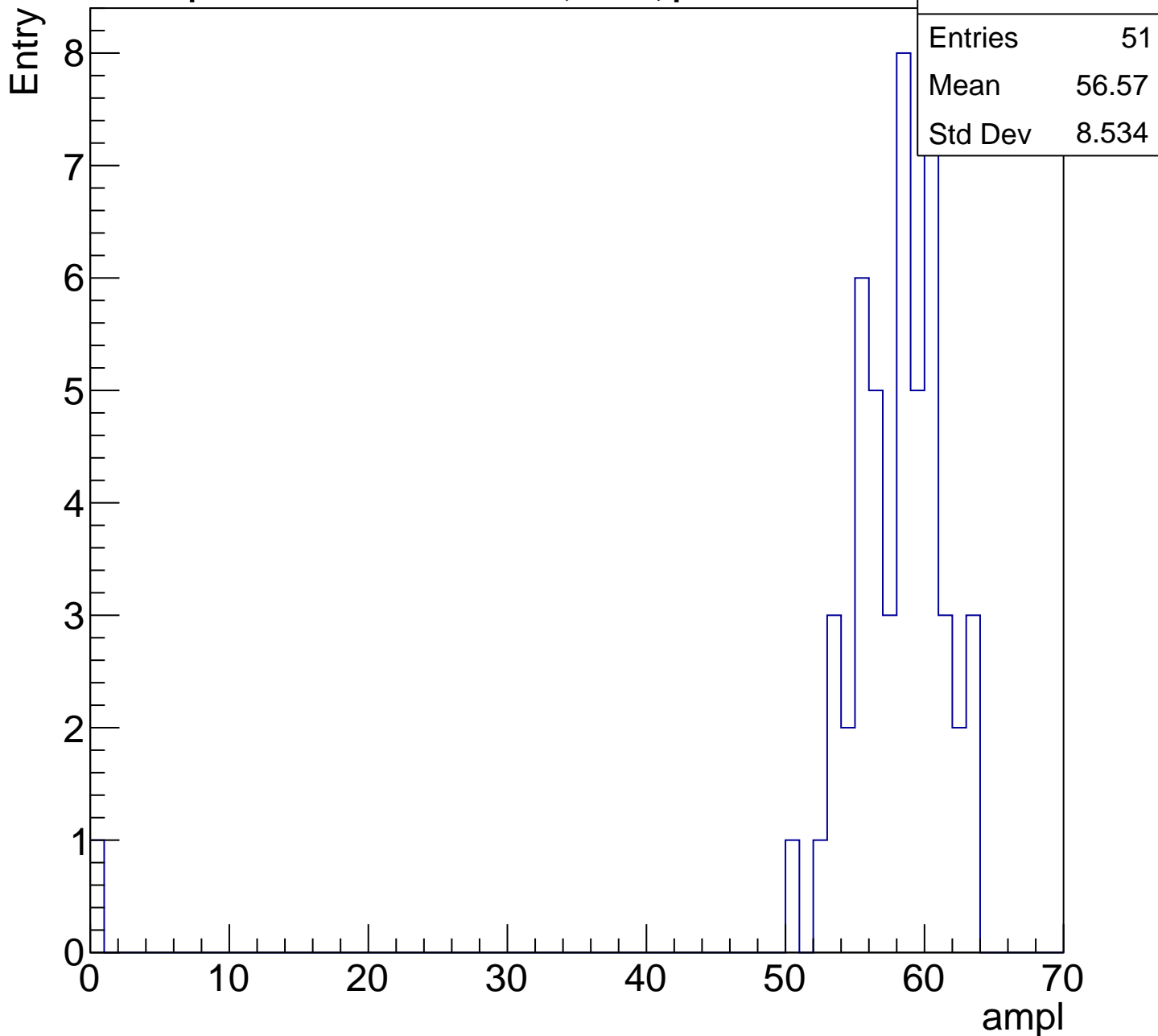
Entry

Entries	70
Mean	52.8
Std Dev	3.392



# B0L000S, U13-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

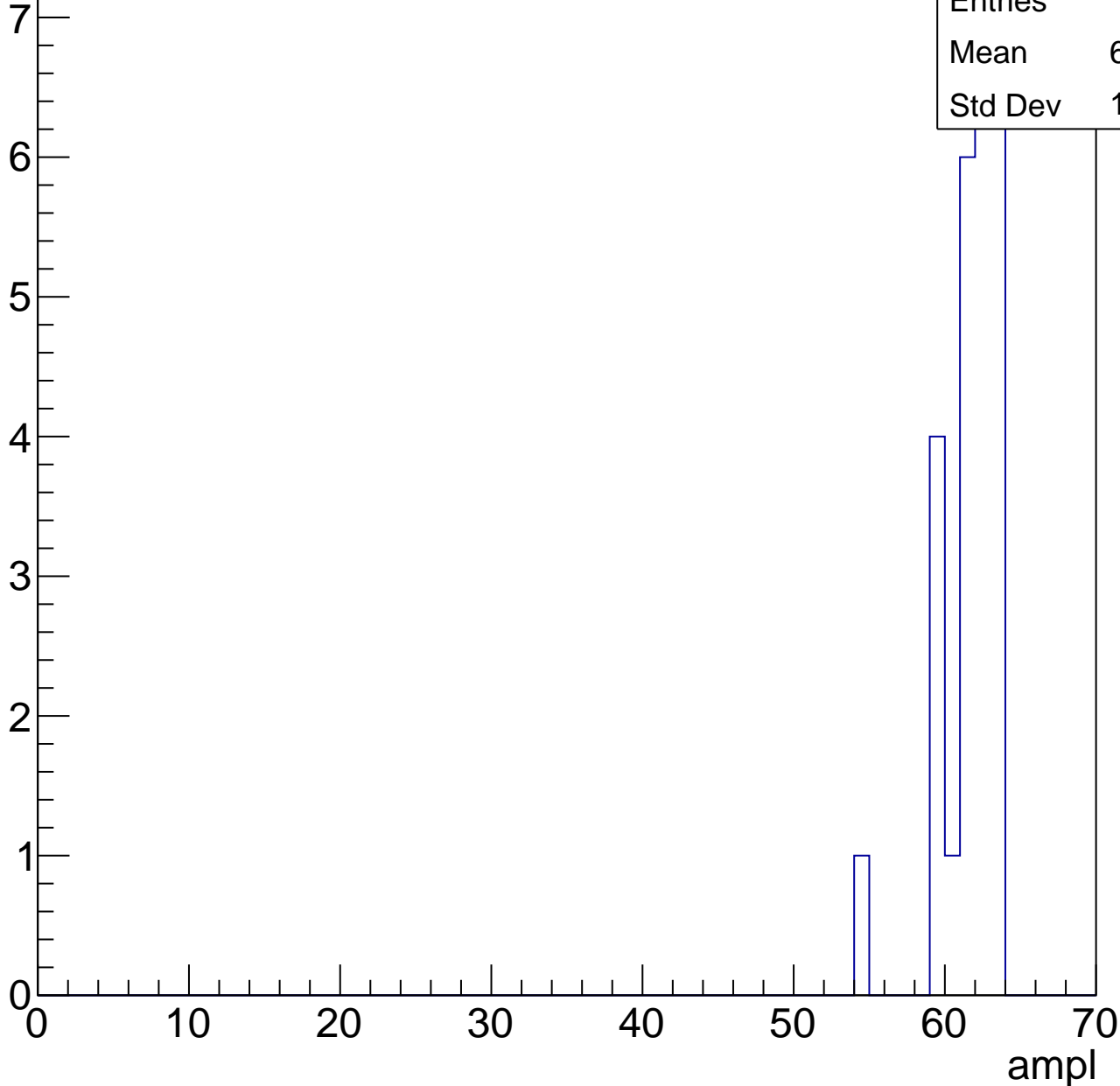


# B0L000S, U13-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	61.19
Std Dev	1.962



# B0L000S, U13-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch7, adc0

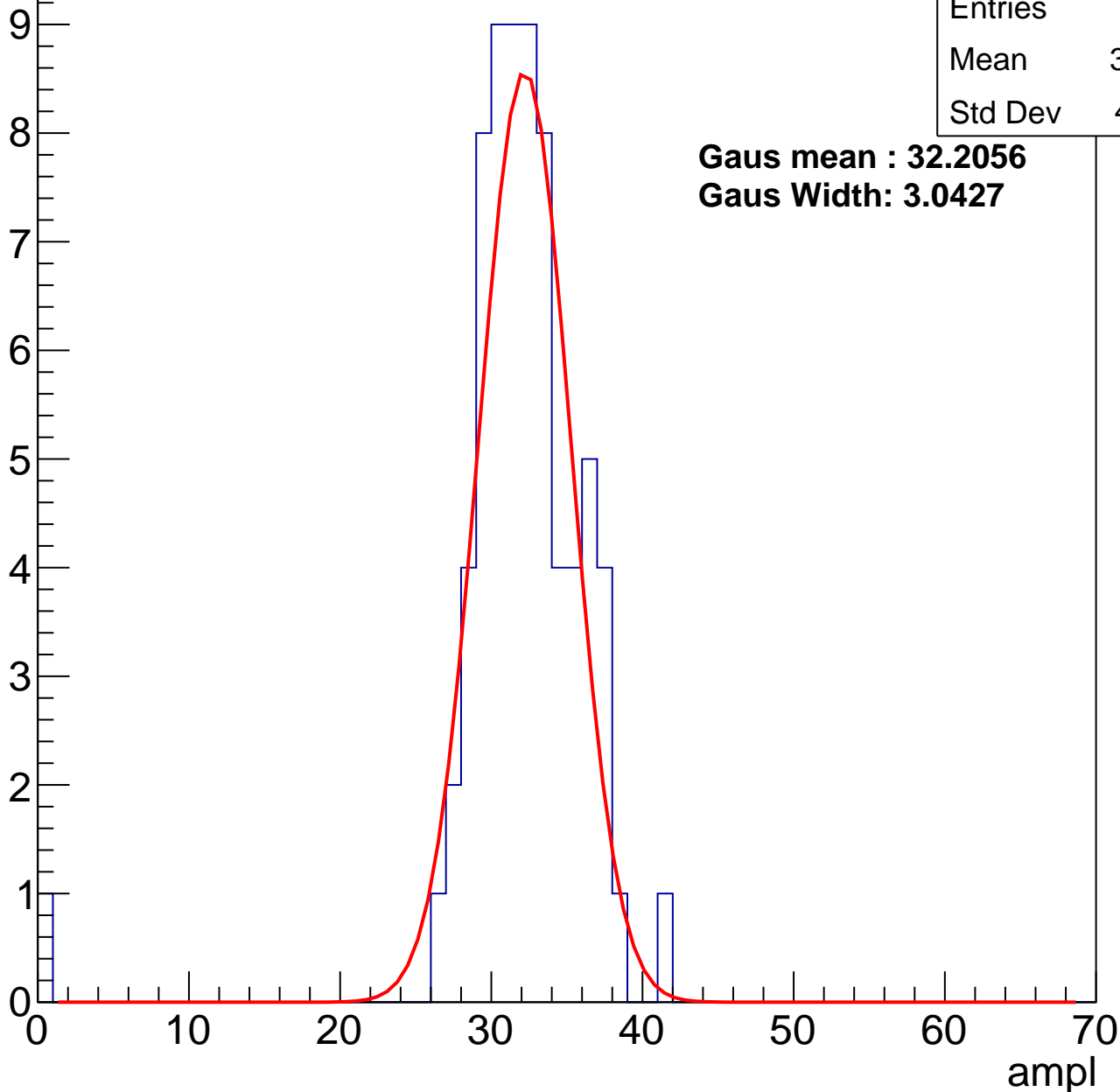
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	31.54
Std Dev	4.831

**Gaus mean : 32.2056**

**Gaus Width: 3.0427**



# B0L000S, U13-ch7, adc1

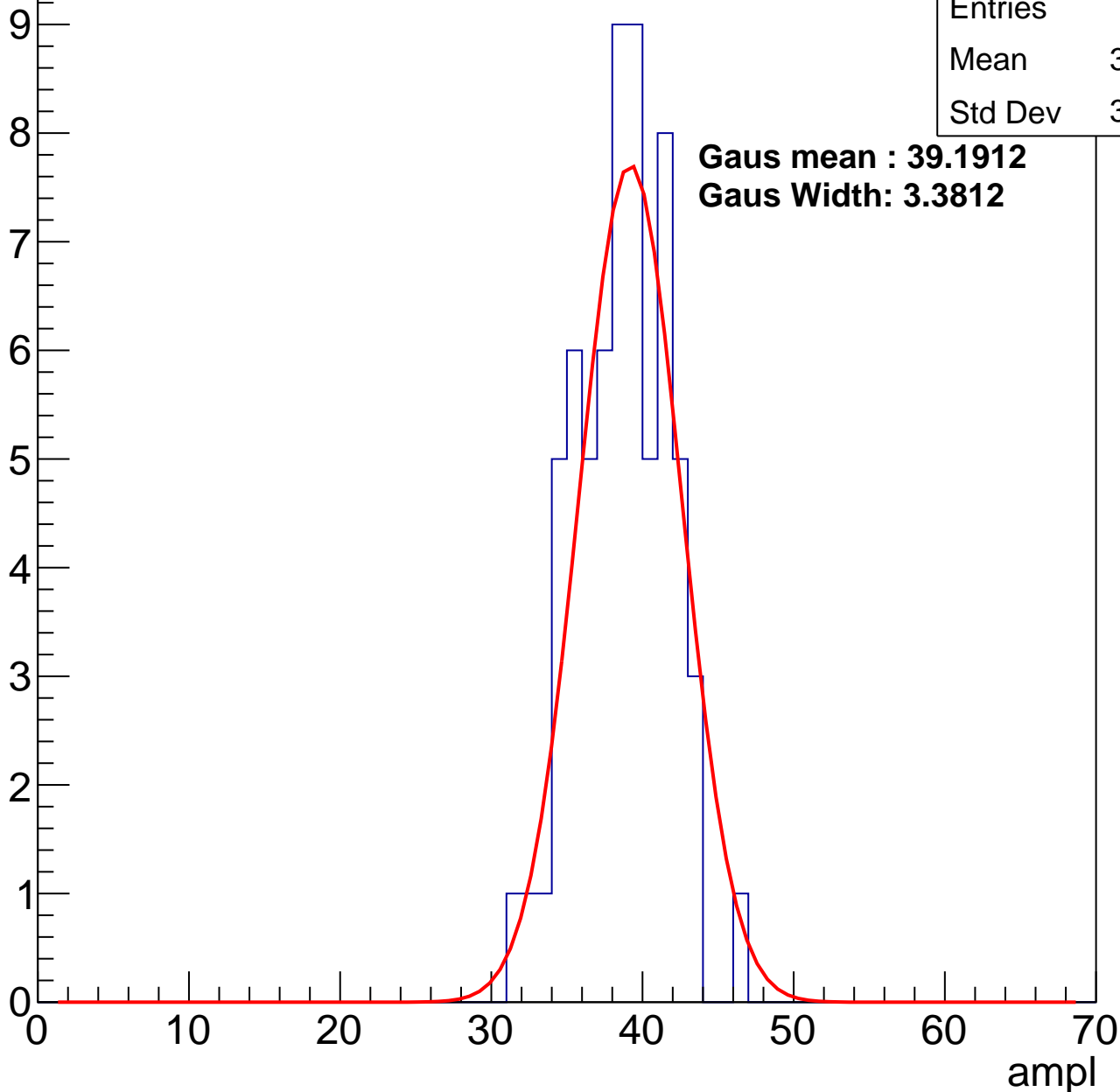
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	38.22
Std Dev	3.005

**Gaus mean : 39.1912**

**Gaus Width: 3.3812**



# B0L000S, U13-ch7, adc2

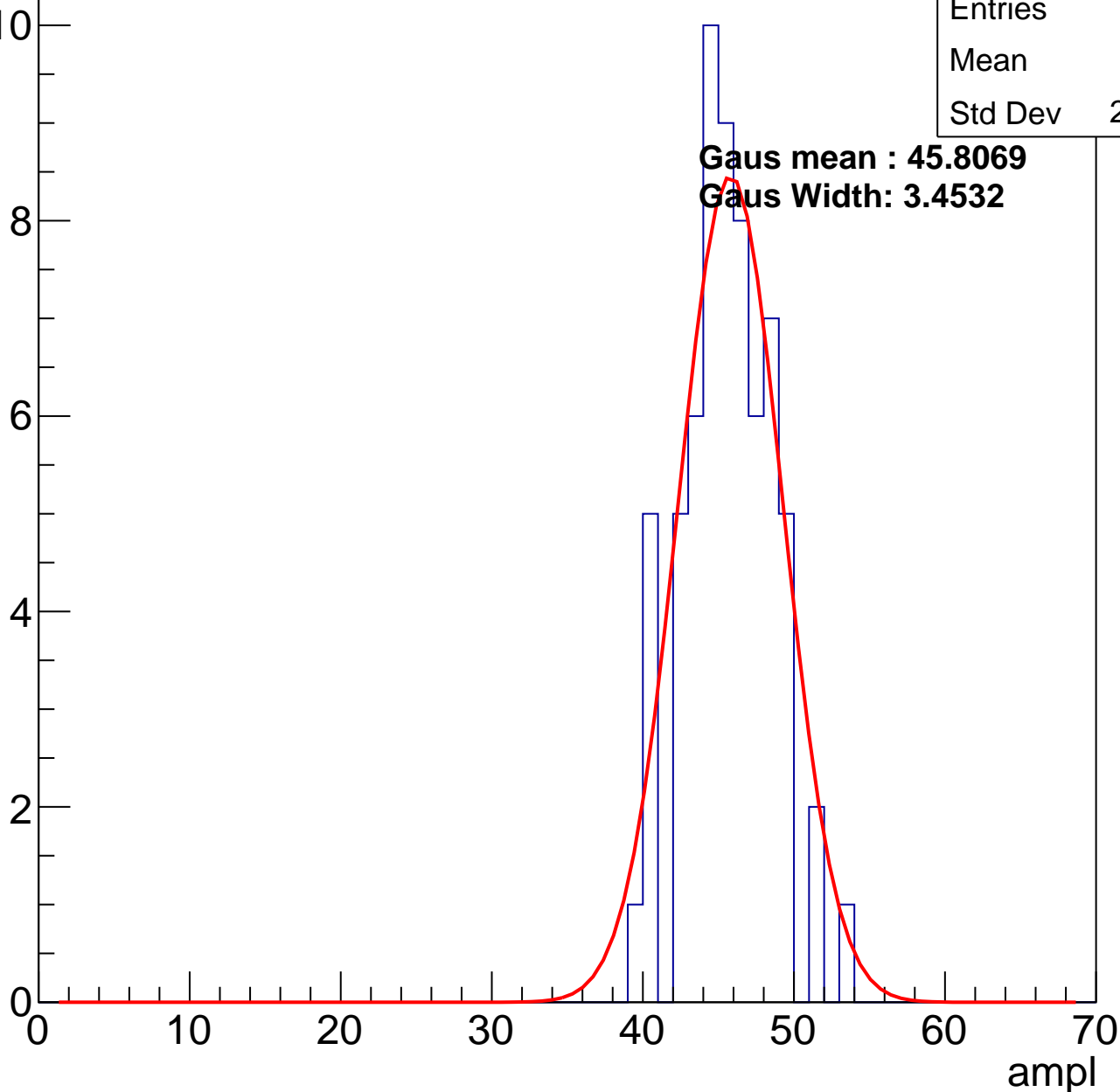
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	45.2
Std Dev	2.905

**Gaus mean : 45.8069**

**Gaus Width: 3.4532**

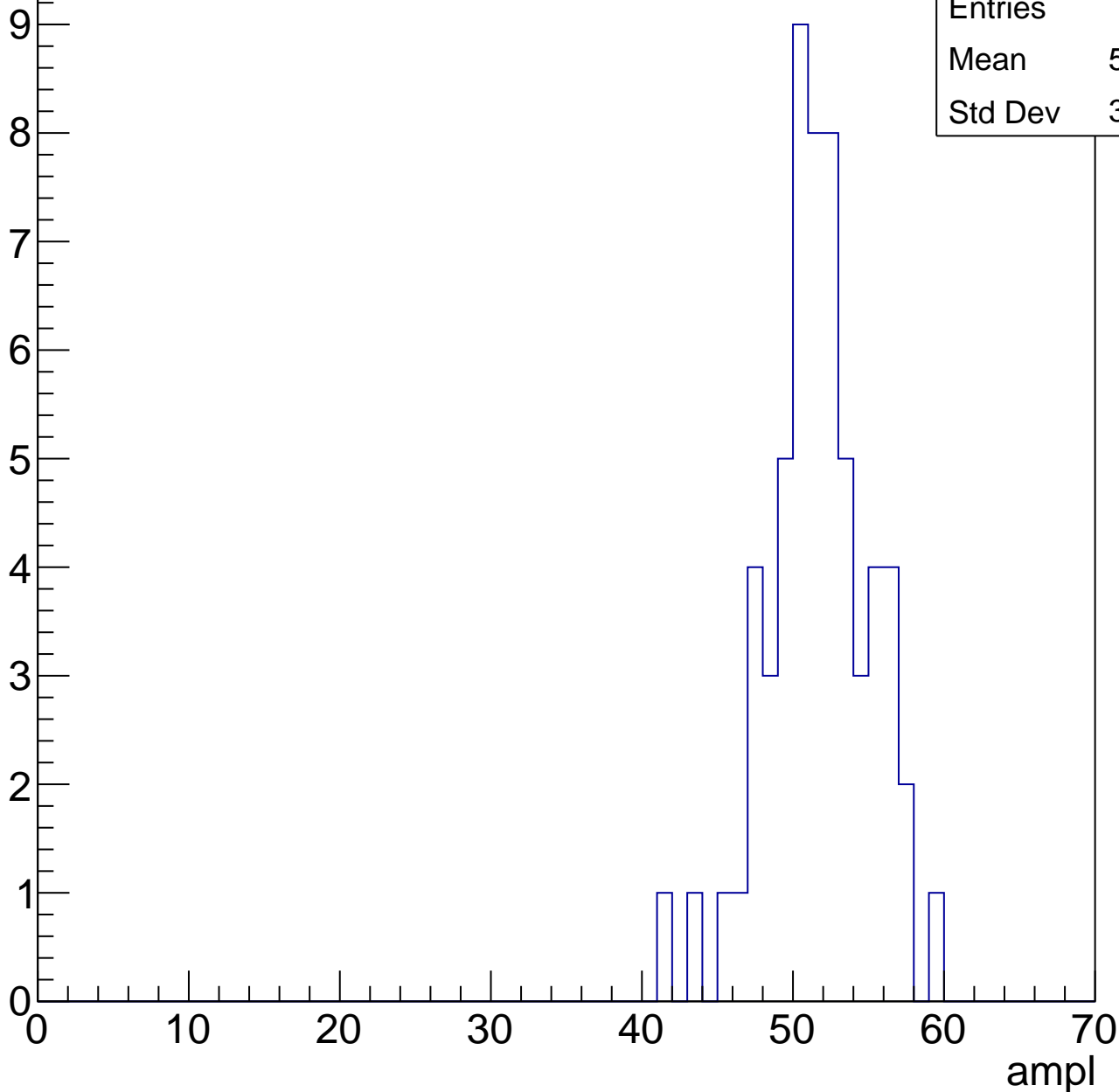


# B0L000S, U13-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.17
Std Dev	3.412

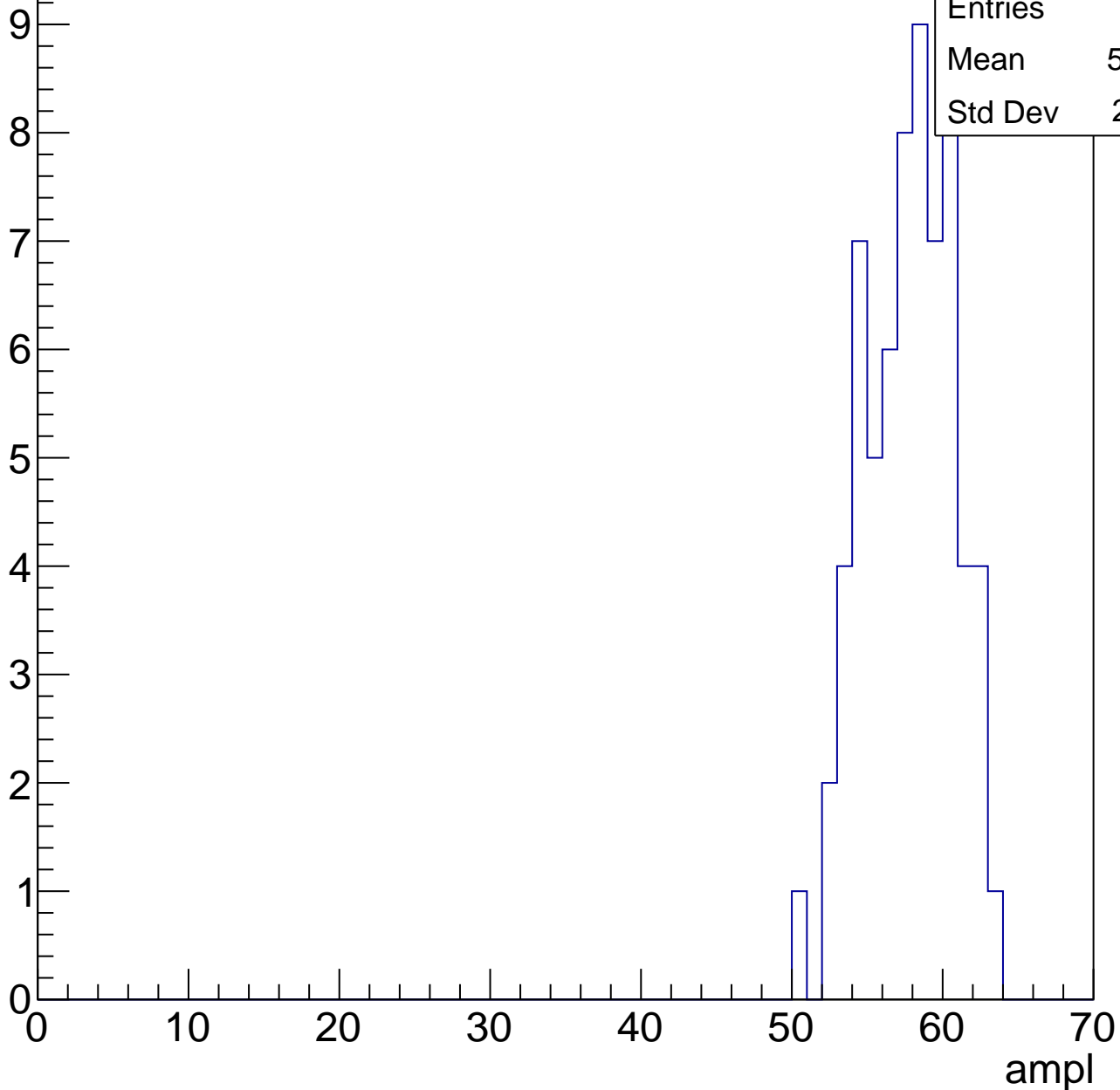


# B0L000S, U13-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	57.29
Std Dev	2.901

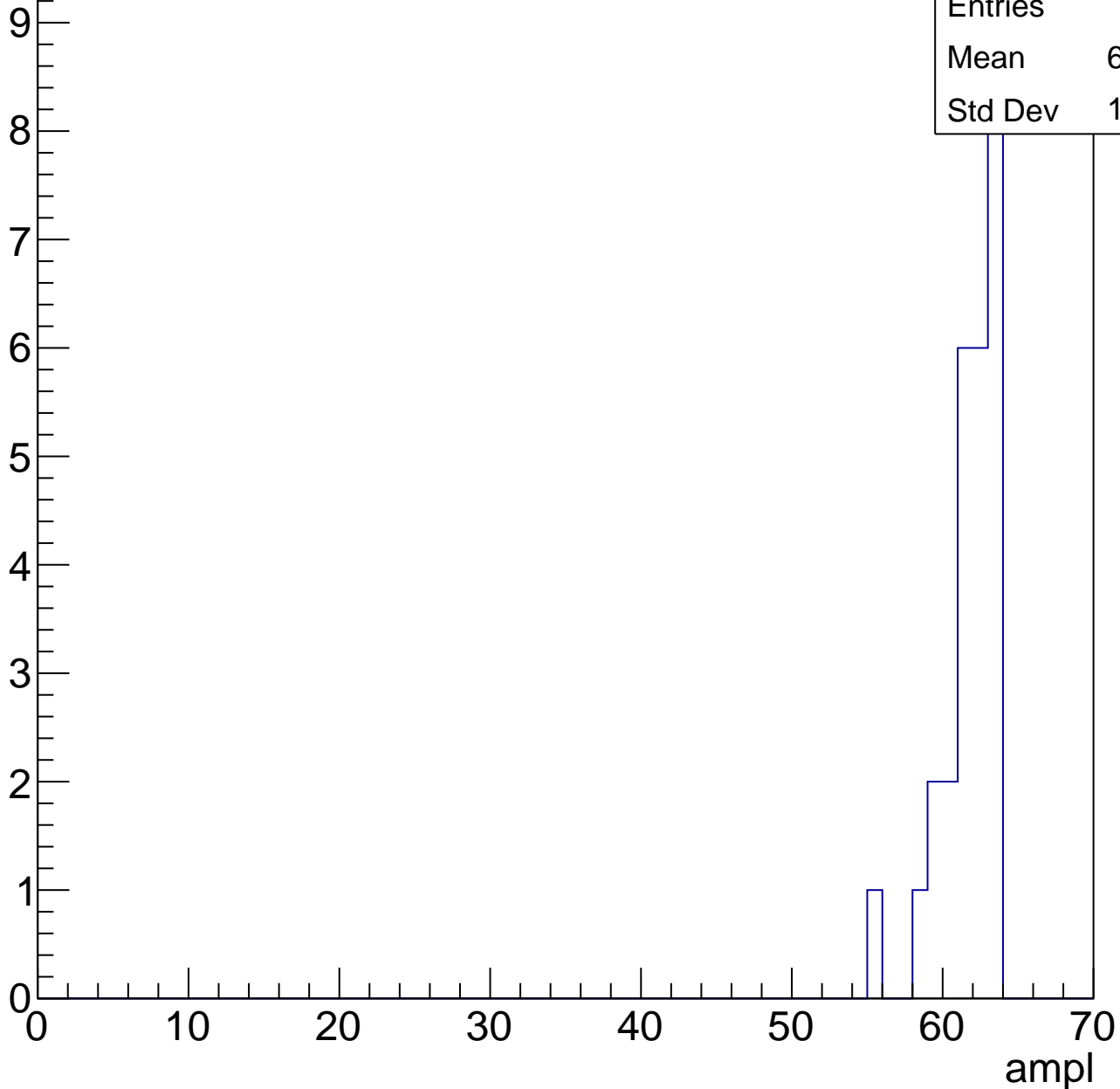


# B0L000S, U13-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	61.33
Std Dev	1.866



# B0L000S, U13-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



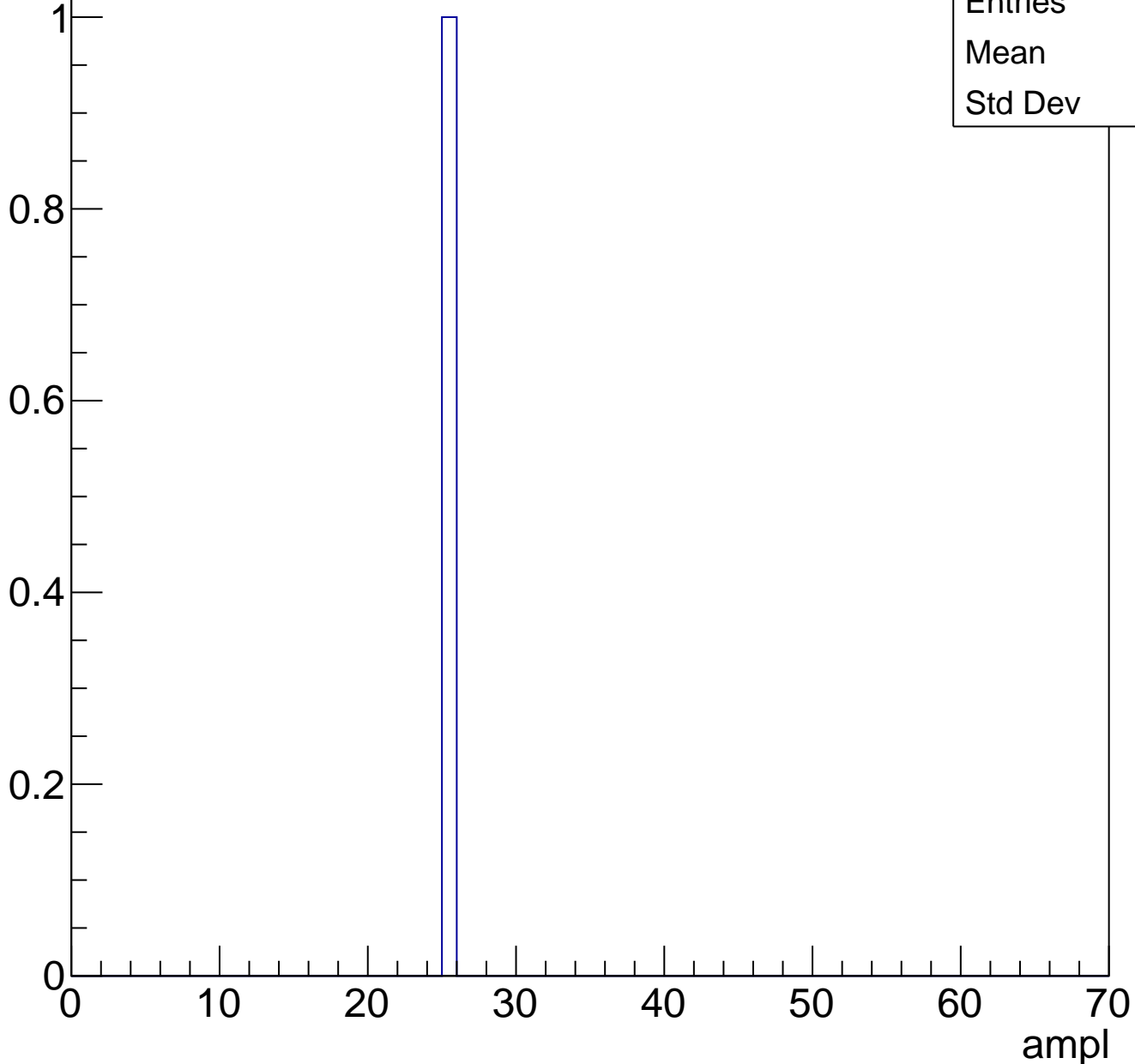
Entries	1
Mean	0
Std Dev	0



# B0L000S, U13-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	25
Std Dev	0

# B0L000S, U13-ch8, adc0

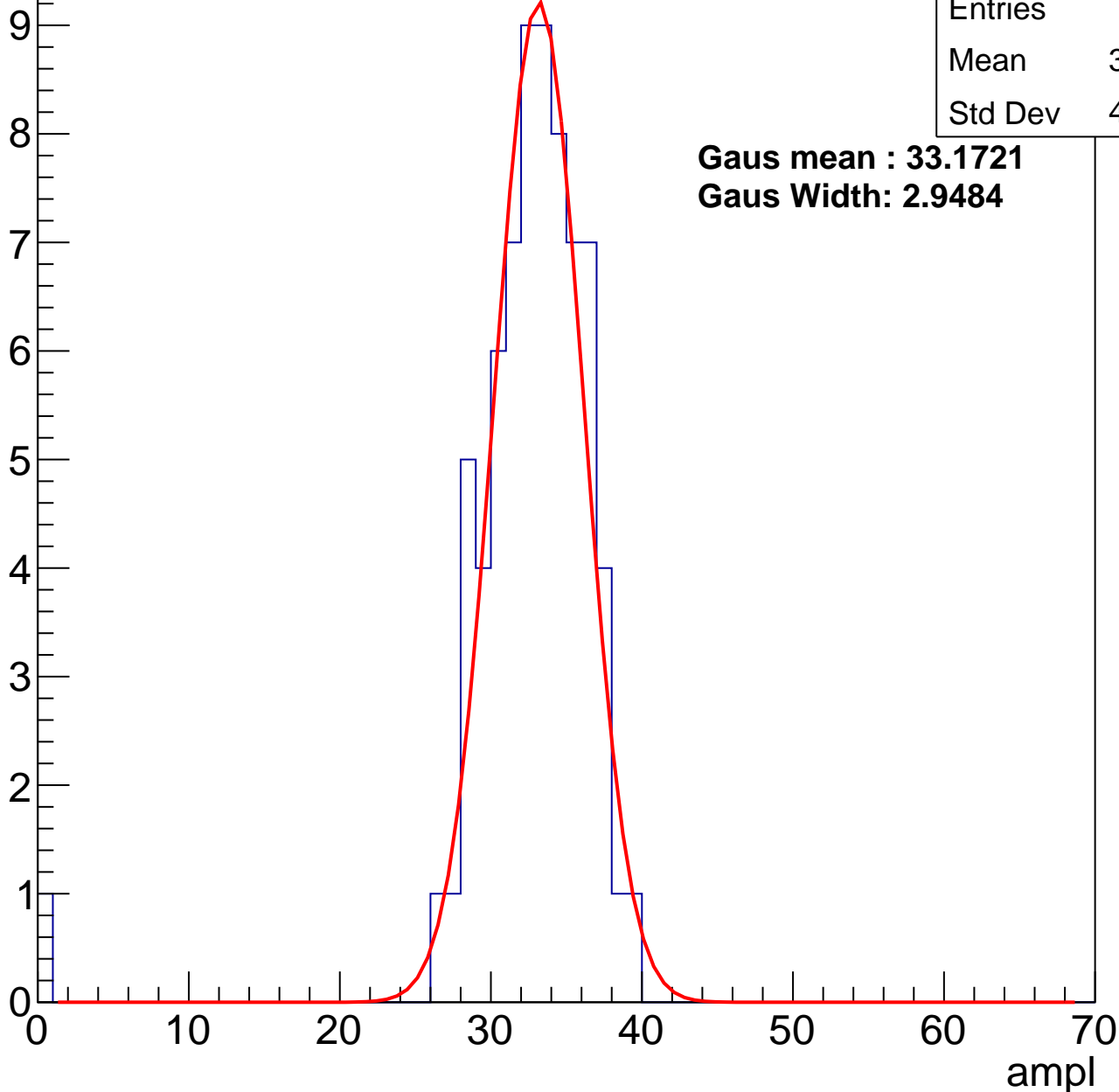
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	32.18
Std Dev	4.792

**Gaus mean : 33.1721**

**Gaus Width: 2.9484**



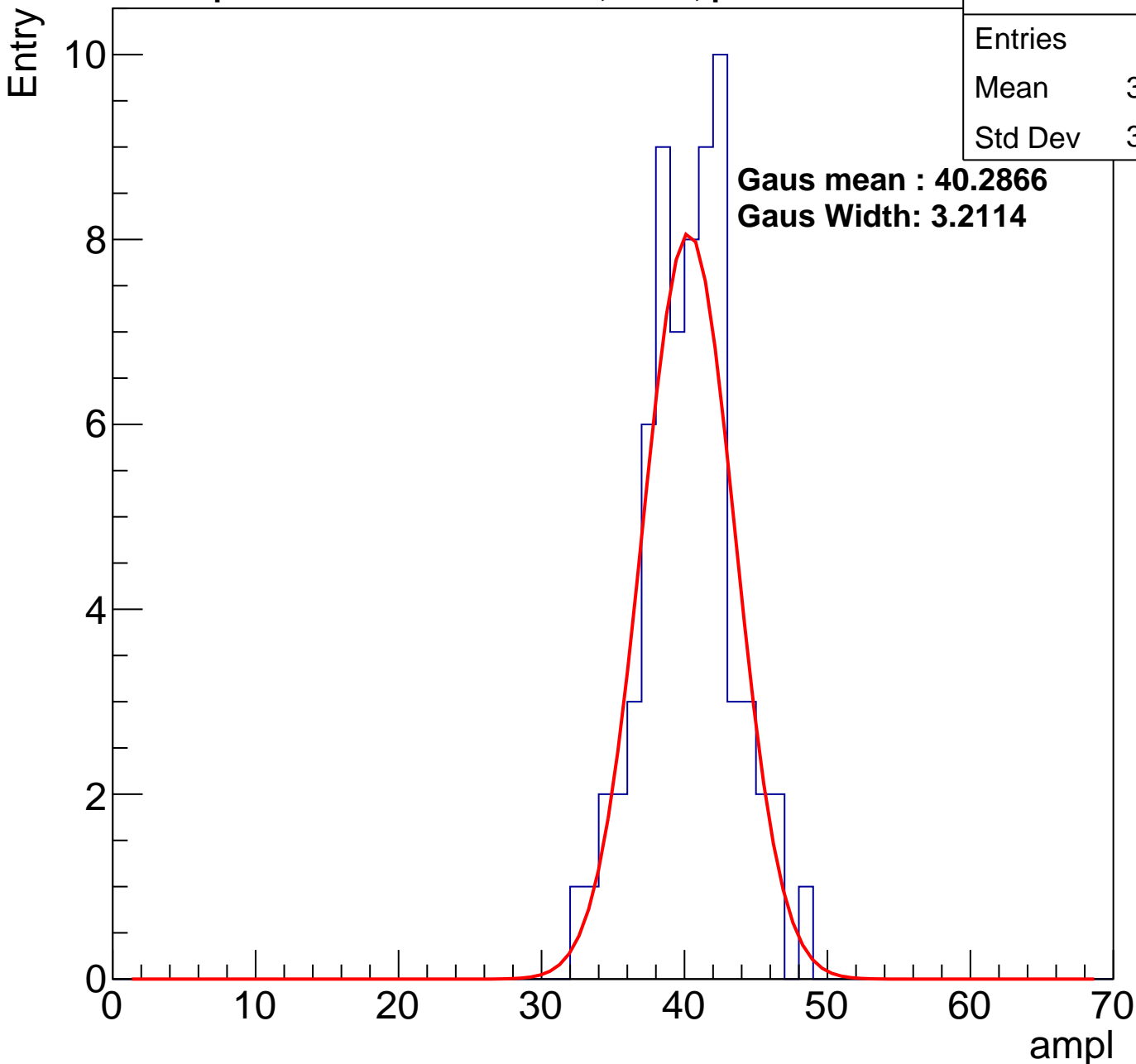
# B0L000S, U13-ch8, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	39.83
Std Dev	3.157

**Gaus mean : 40.2866**

**Gaus Width: 3.2114**

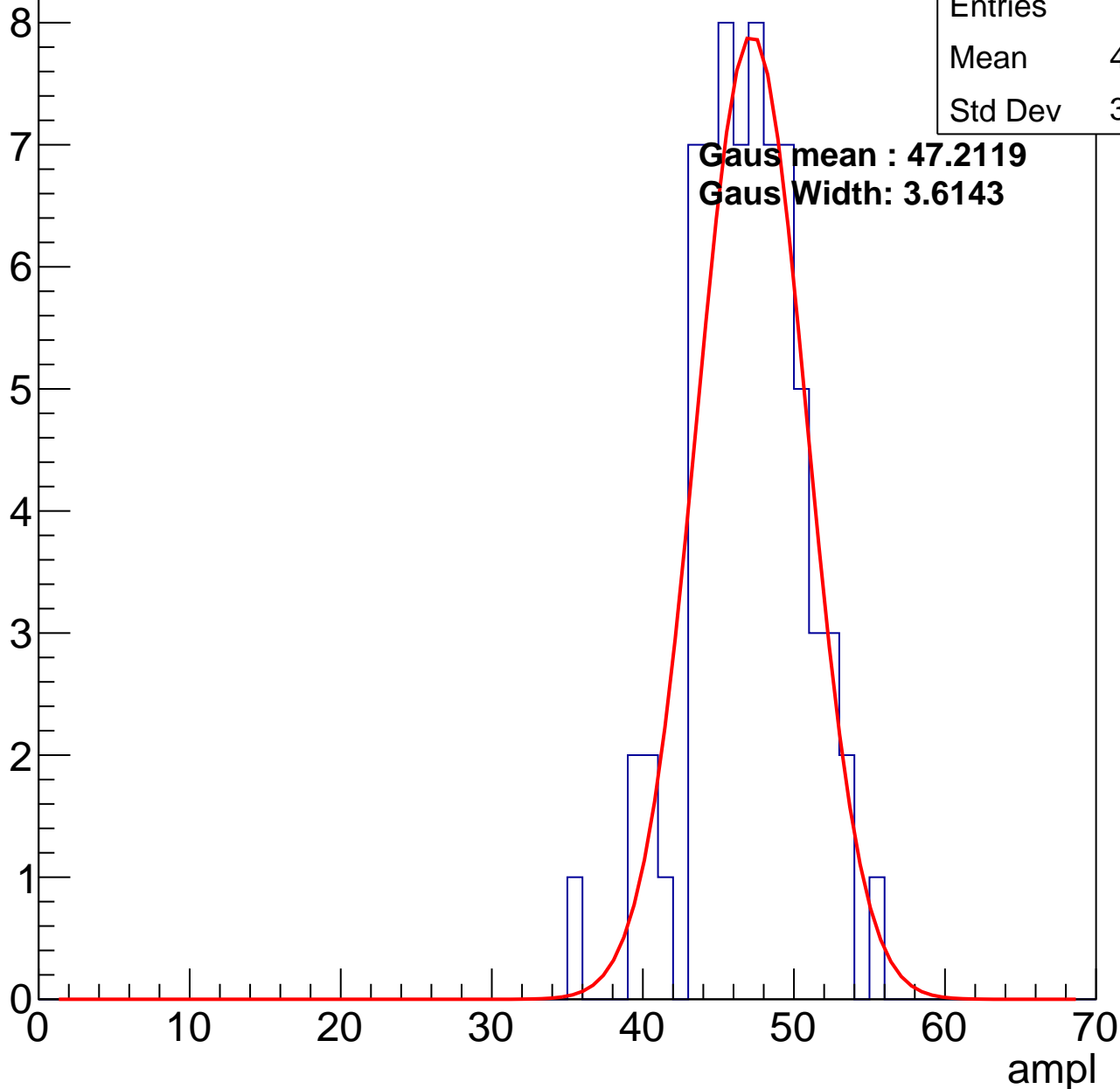


# B0L000S, U13-ch8, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	46.48
Std Dev	3.638

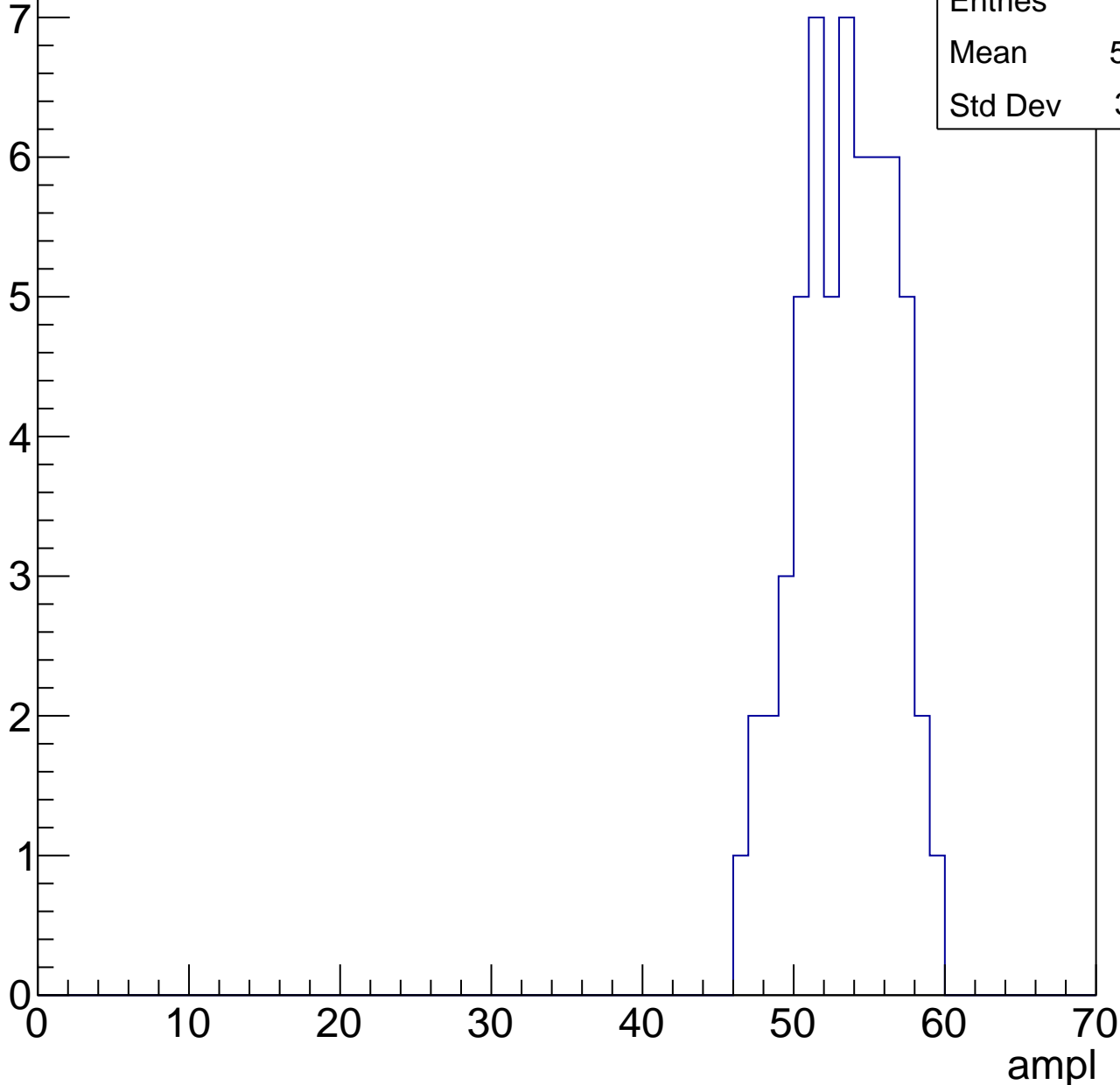


# B0L000S, U13-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	52.95
Std Dev	3.071

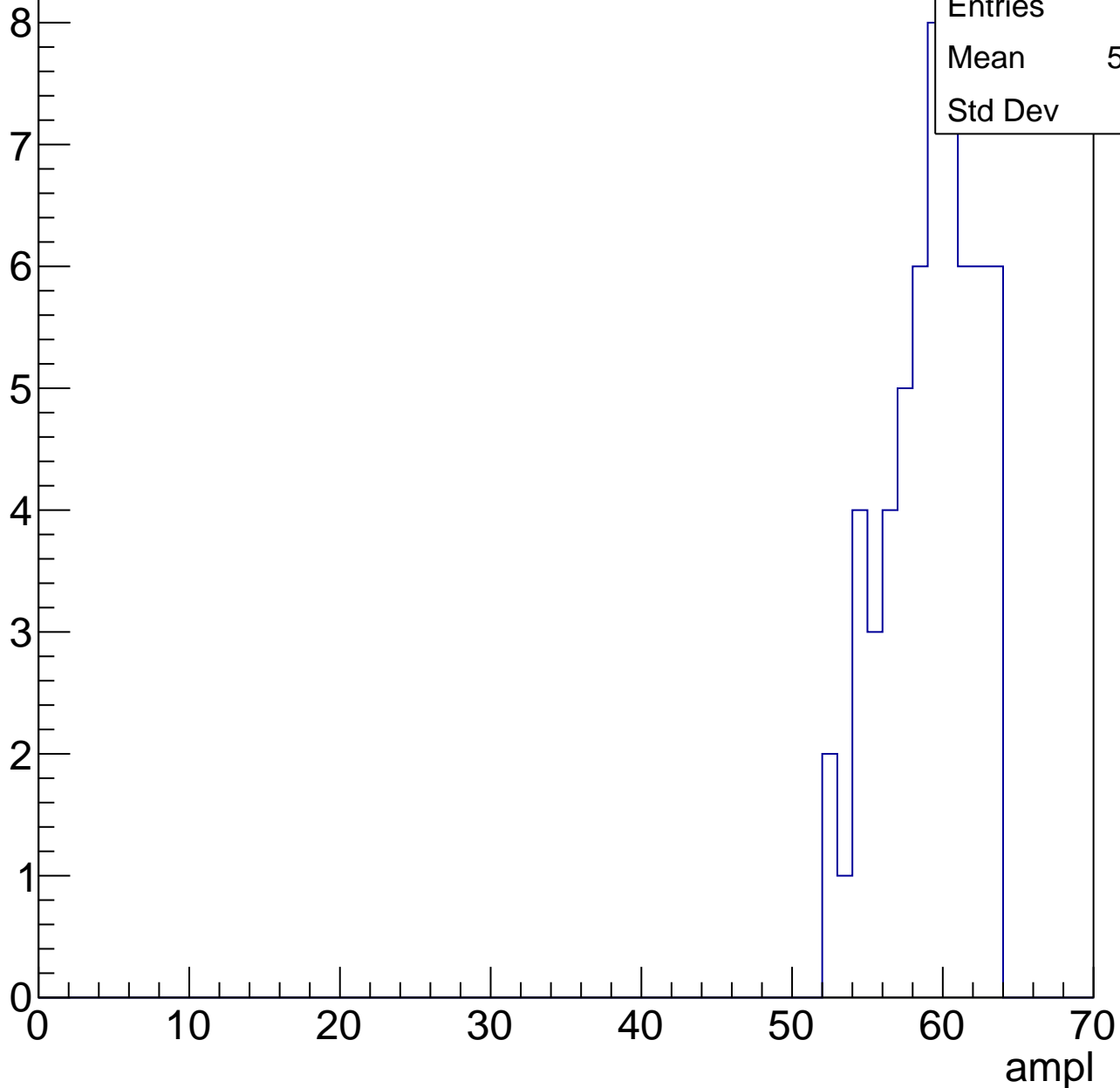


# B0L000S, U13-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

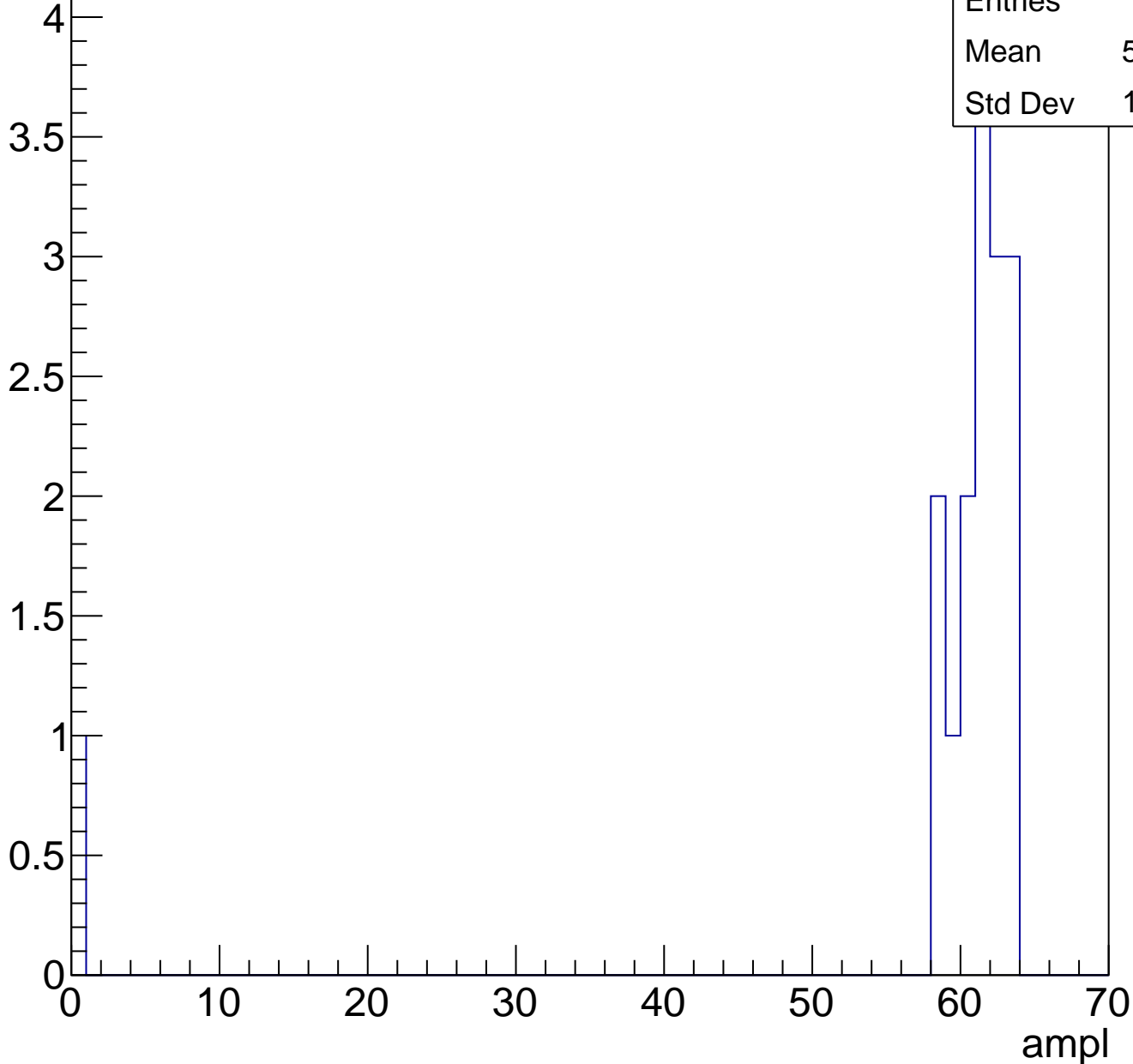
Entries	59
Mean	58.69
Std Dev	2.97



# B0L000S, U13-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch9, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	30
Std Dev	3.46

**Gaus mean : 31.0779**

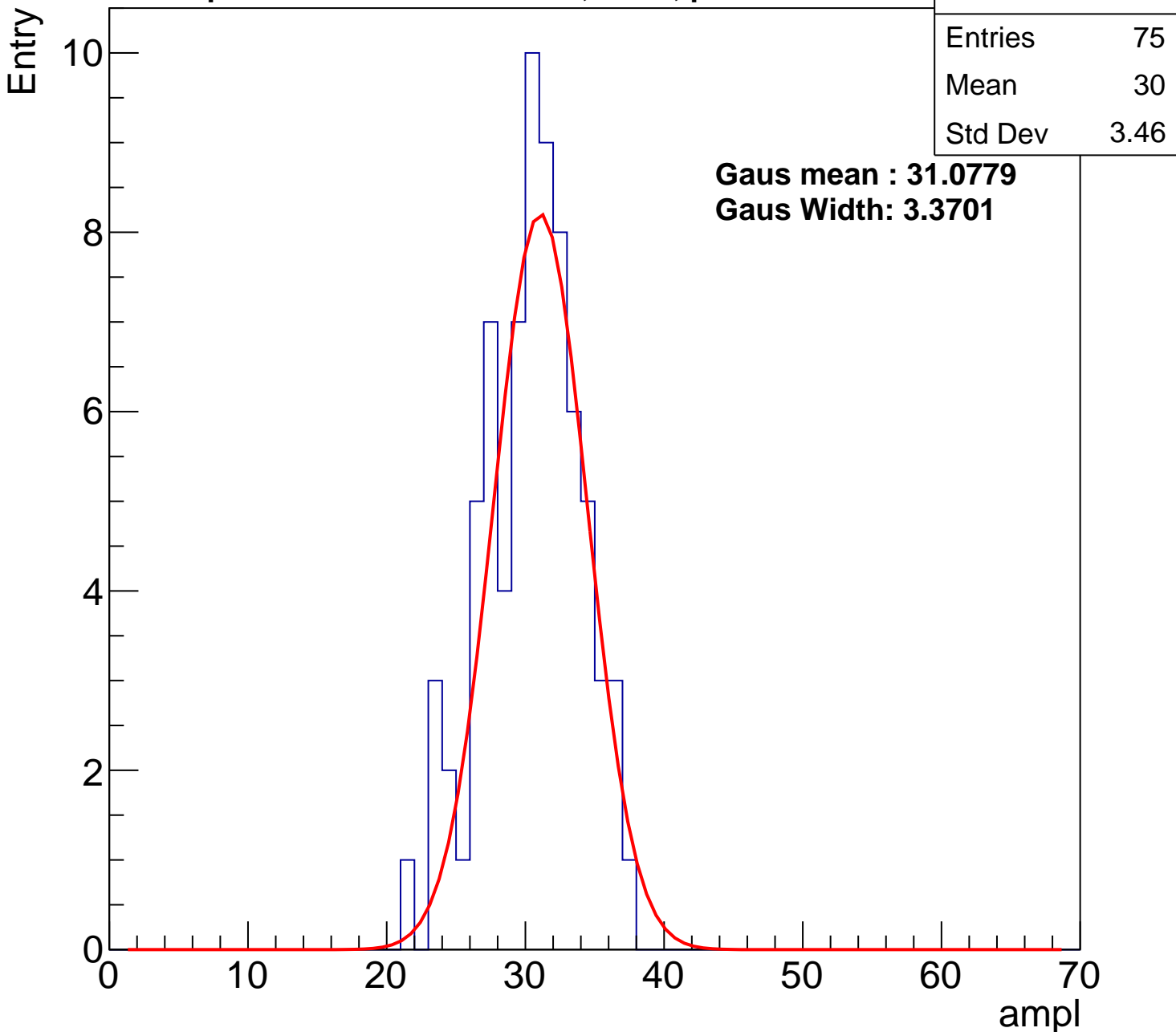
**Gaus Width: 3.3701**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch9, adc1

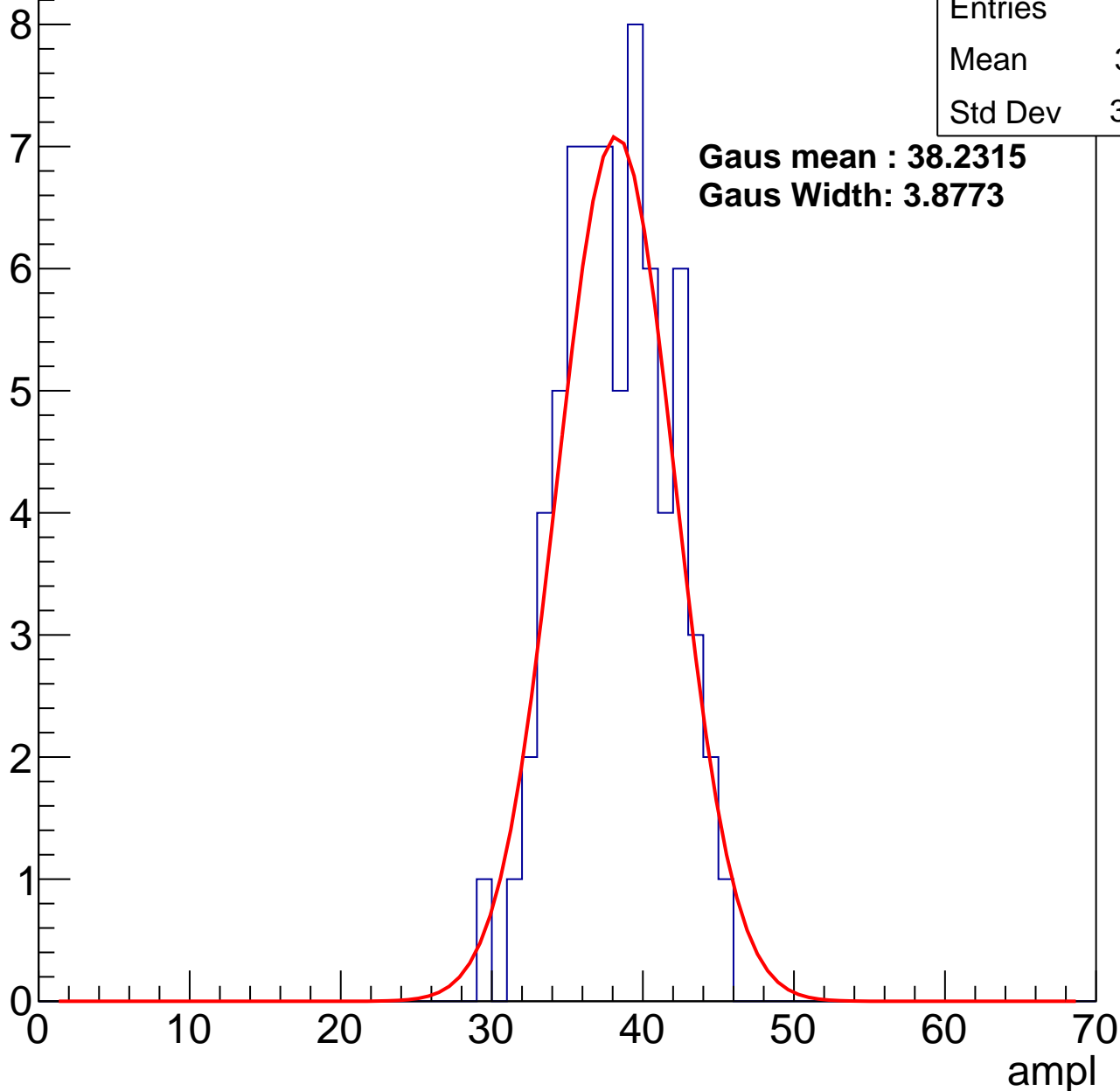
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	37.71
Std Dev	3.477

**Gaus mean : 38.2315**

**Gaus Width: 3.8773**



# B0L000S, U13-ch9, adc2

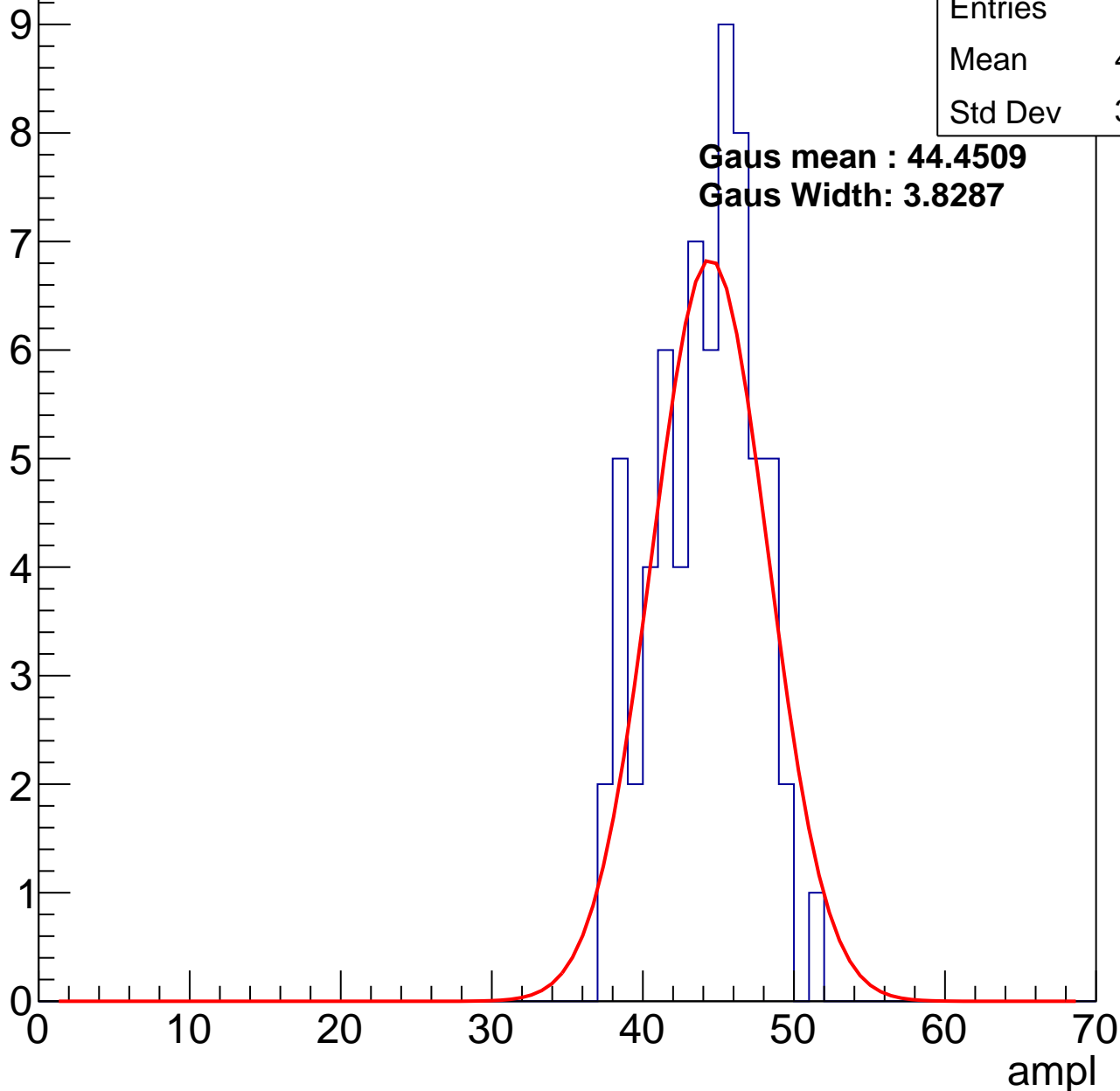
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.61
Std Dev	3.311

**Gaus mean : 44.4509**

**Gaus Width: 3.8287**

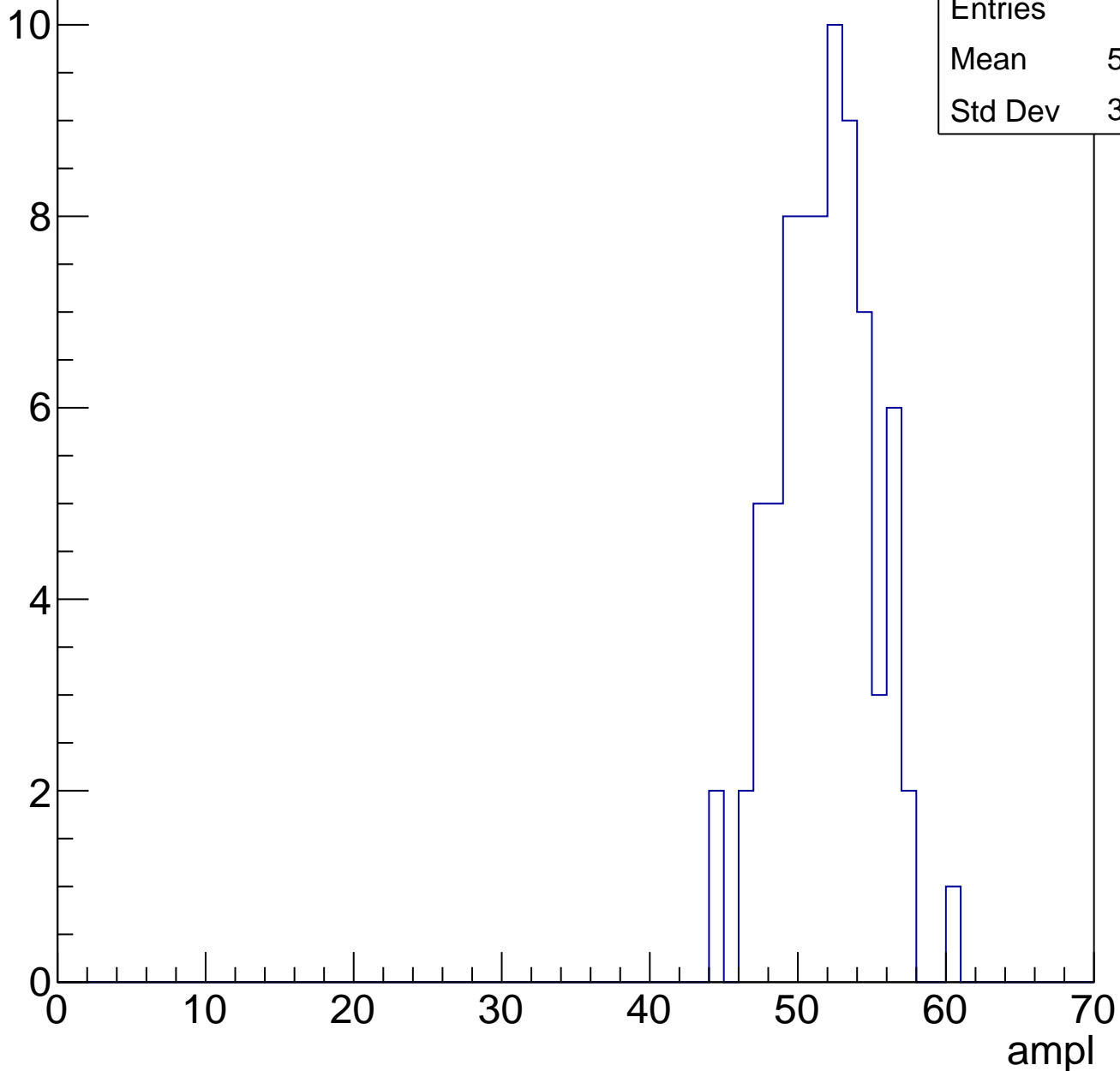


# B0L000S, U13-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	51.38
Std Dev	3.166

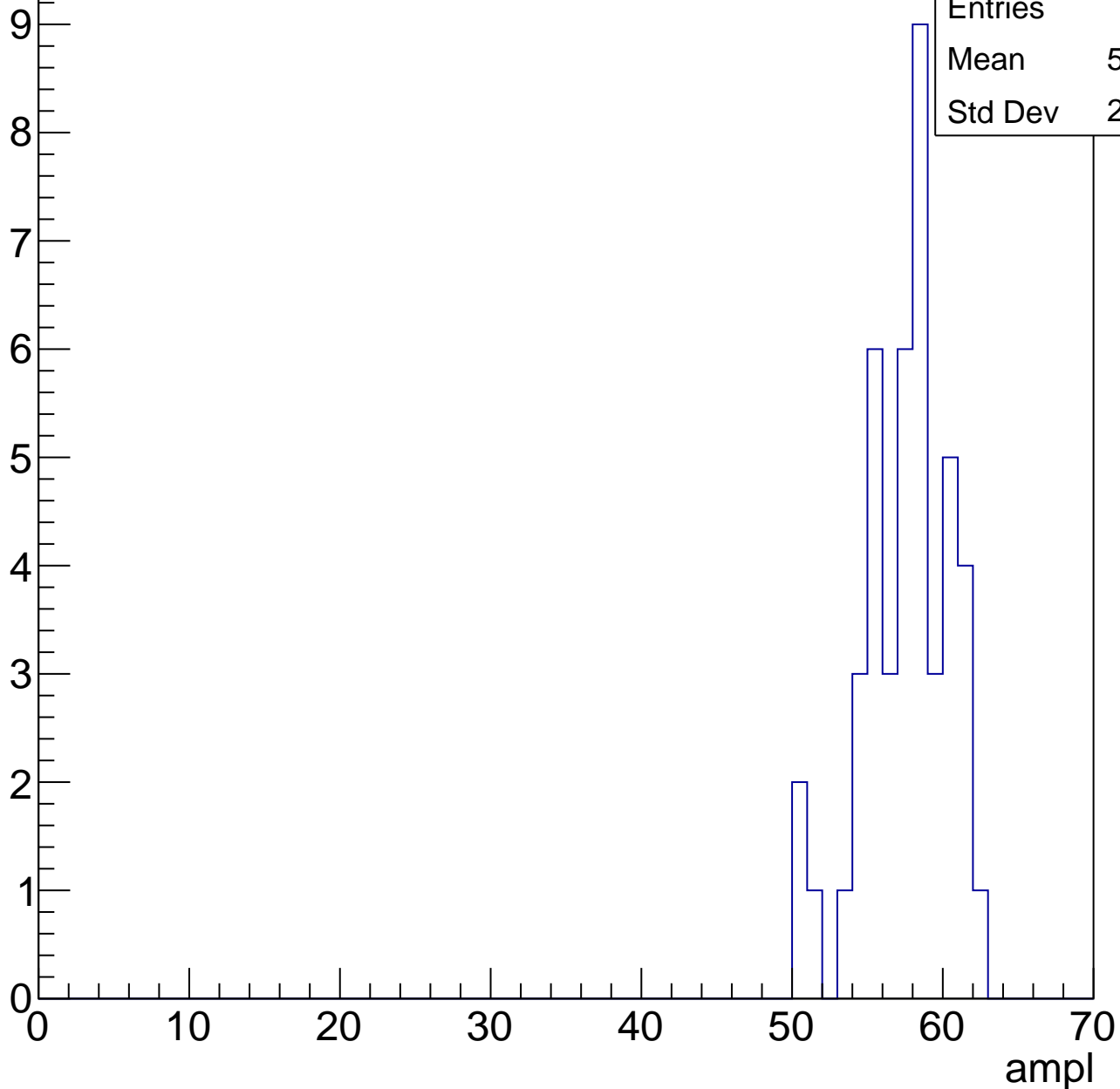
Entry



# B0L000S, U13-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

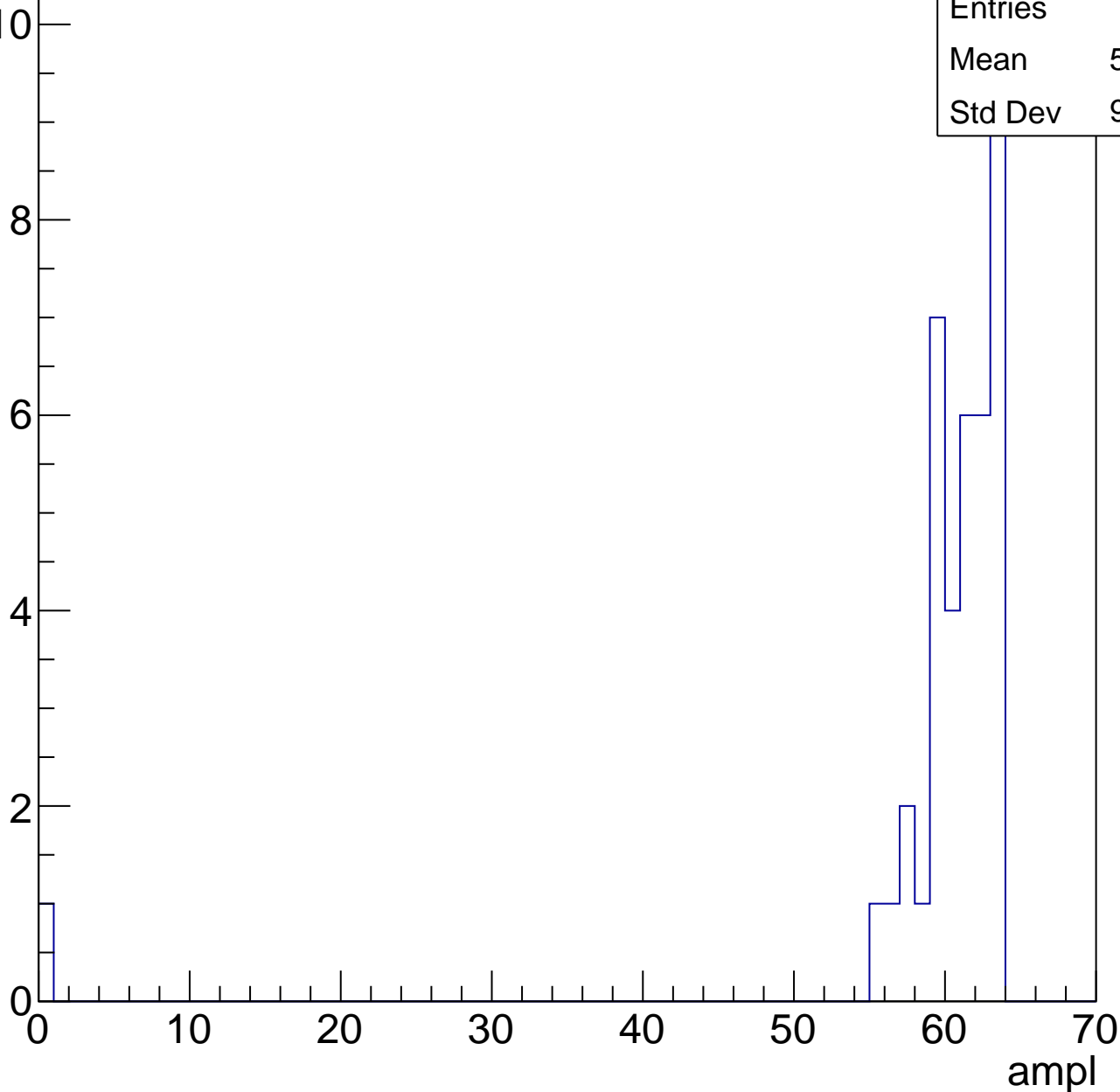


# B0L000S, U13-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	59.08
Std Dev	9.815



# B0L000S, U13-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

62

Std Dev

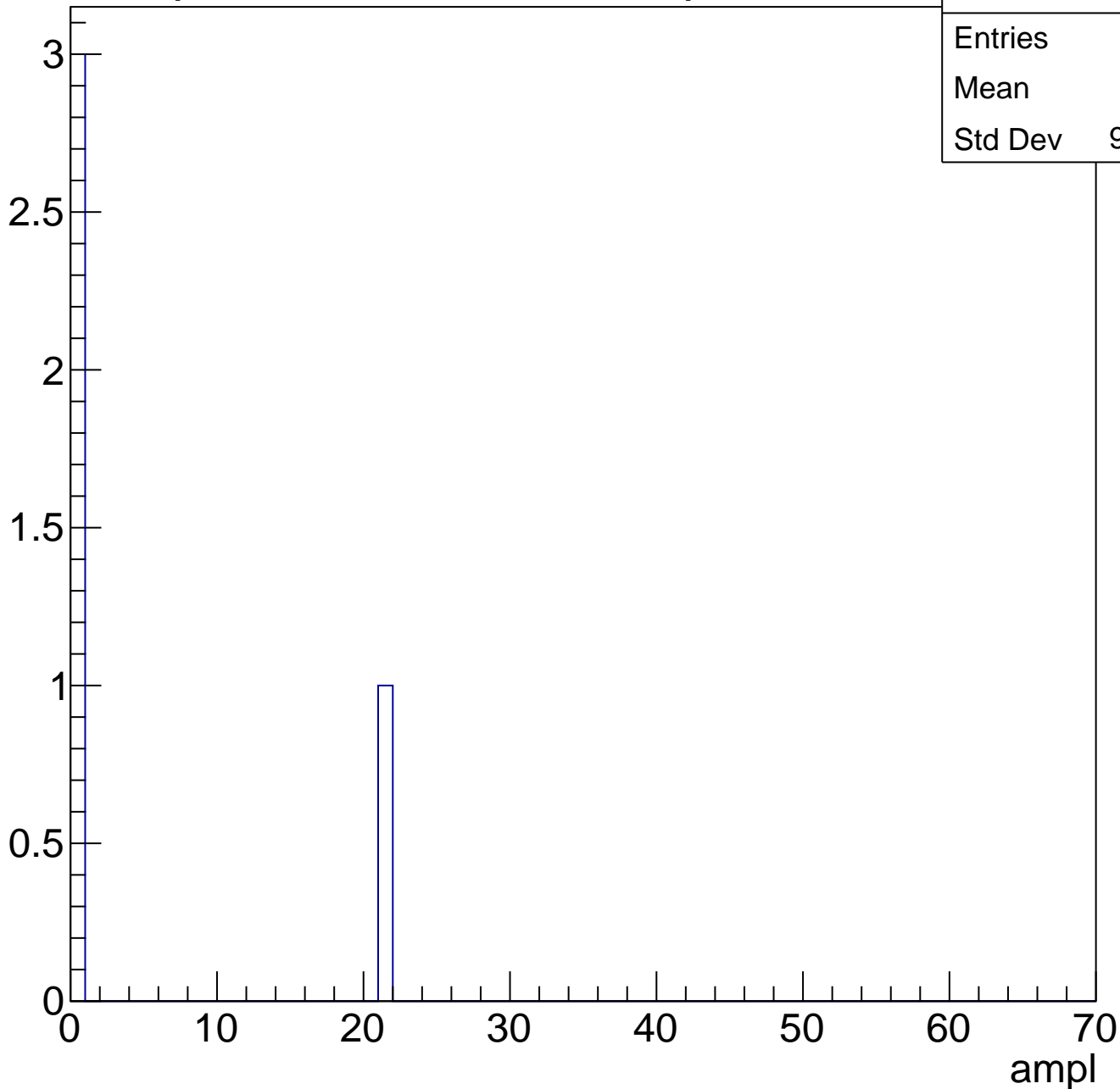
0



# B0L000S, U13-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch10, adc0

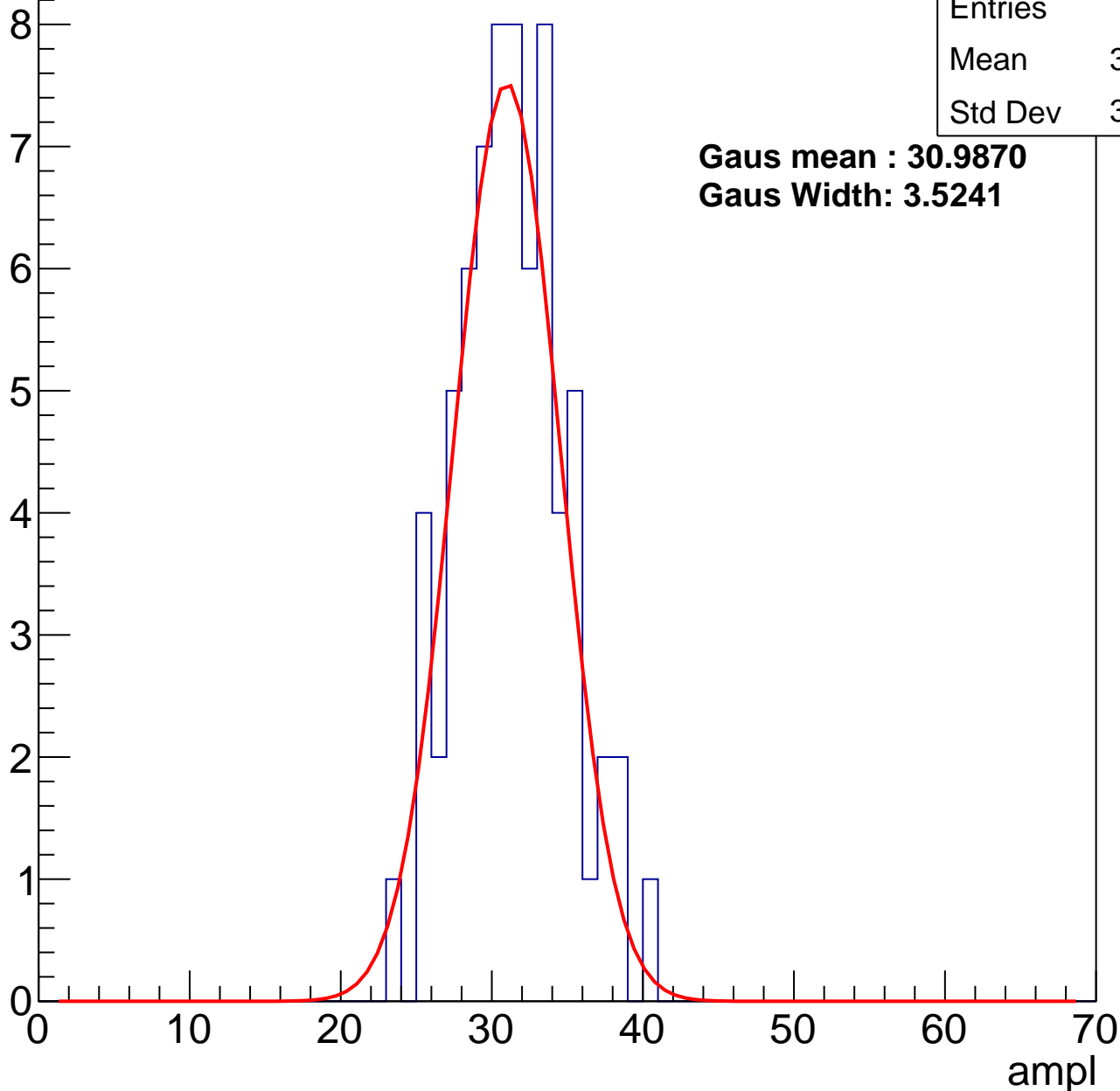
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.89
Std Dev	3.499

**Gaus mean : 30.9870**

**Gaus Width: 3.5241**



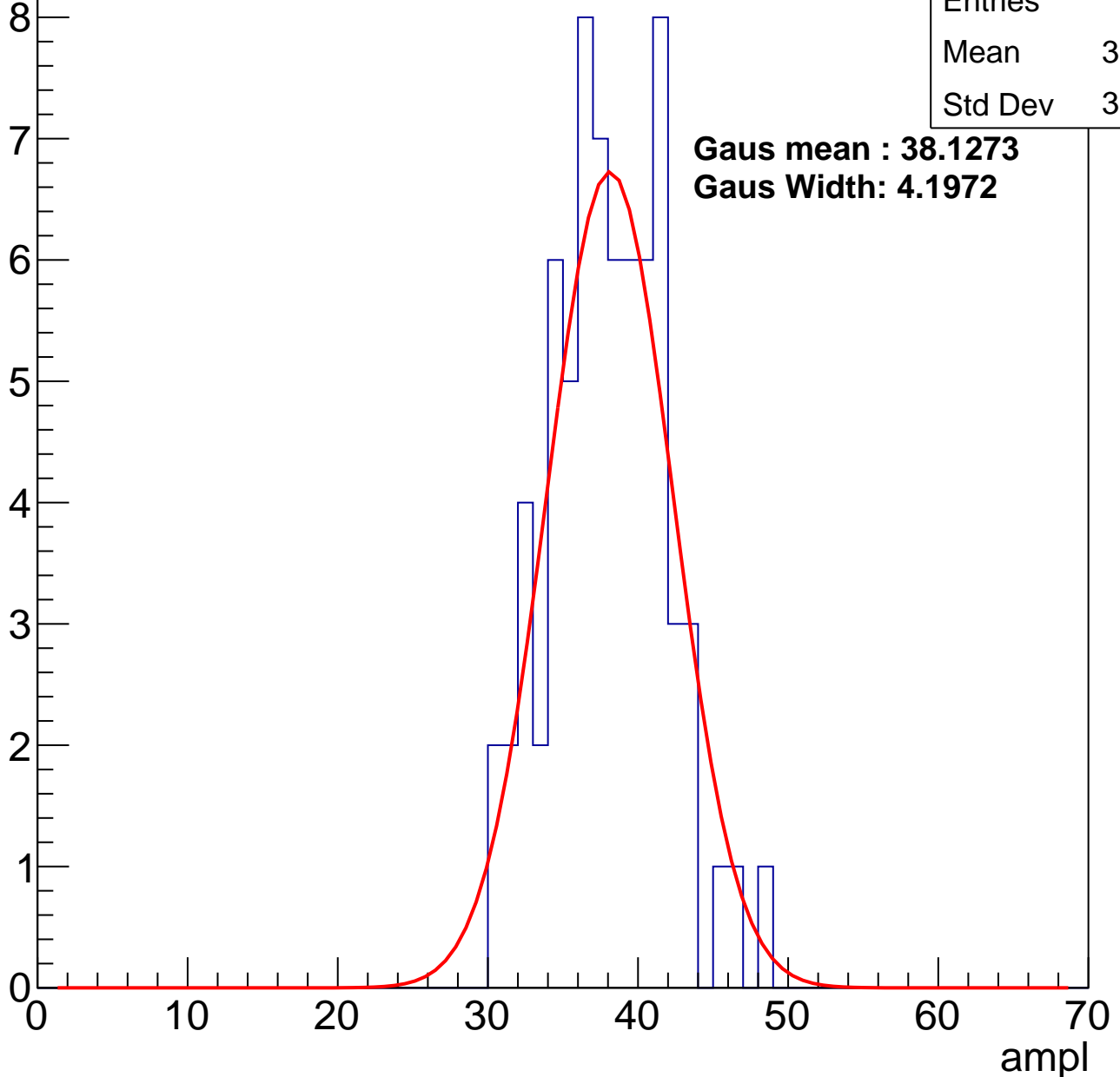
# B0L000S, U13-ch10, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.55
Std Dev	3.797

**Gaus mean : 38.1273**  
**Gaus Width: 4.1972**



# B0L000S, U13-ch10, adc2

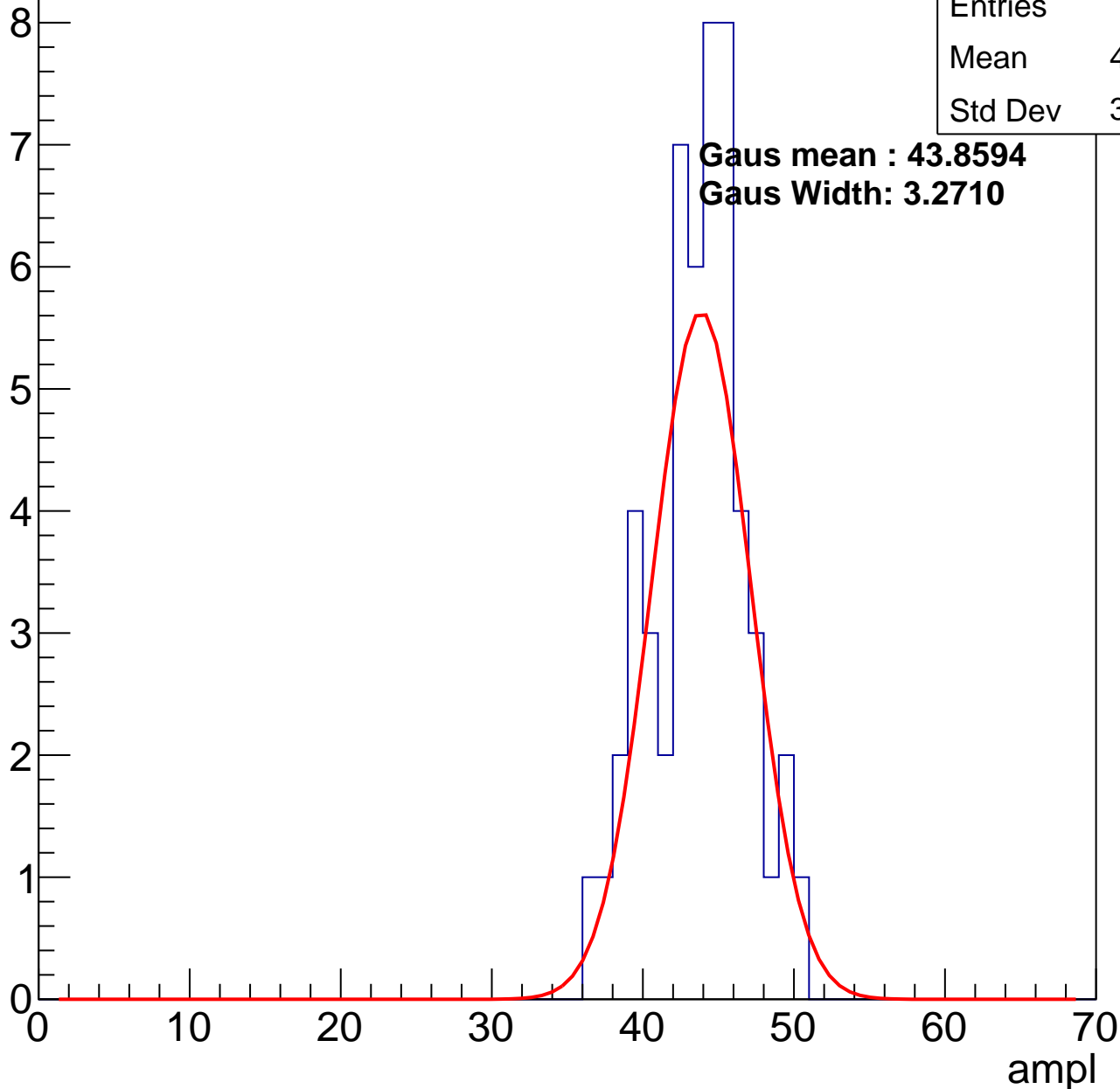
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.25
Std Dev	3.095

**Gaus mean : 43.8594**

**Gaus Width: 3.2710**

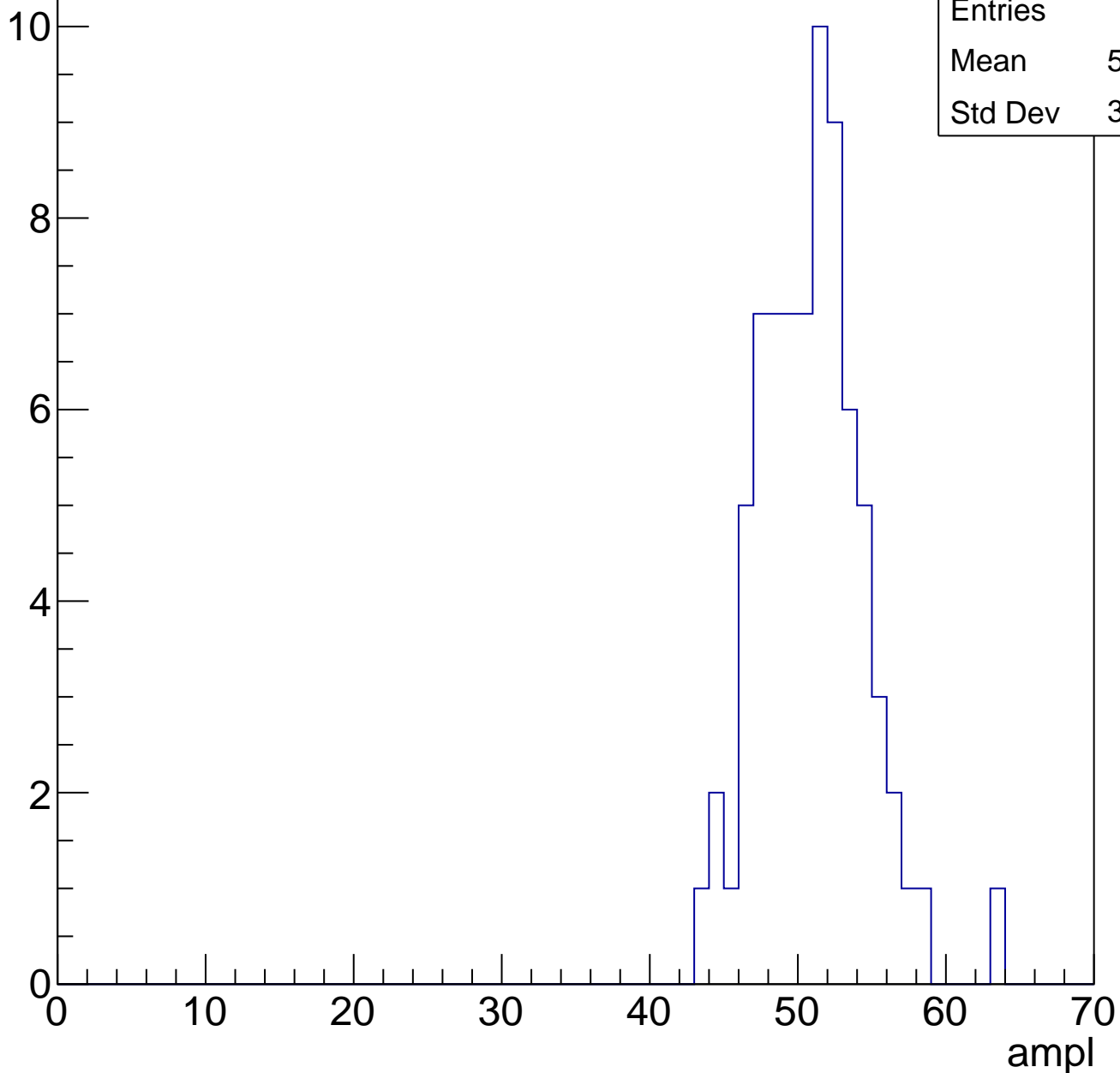


# B0L000S, U13-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

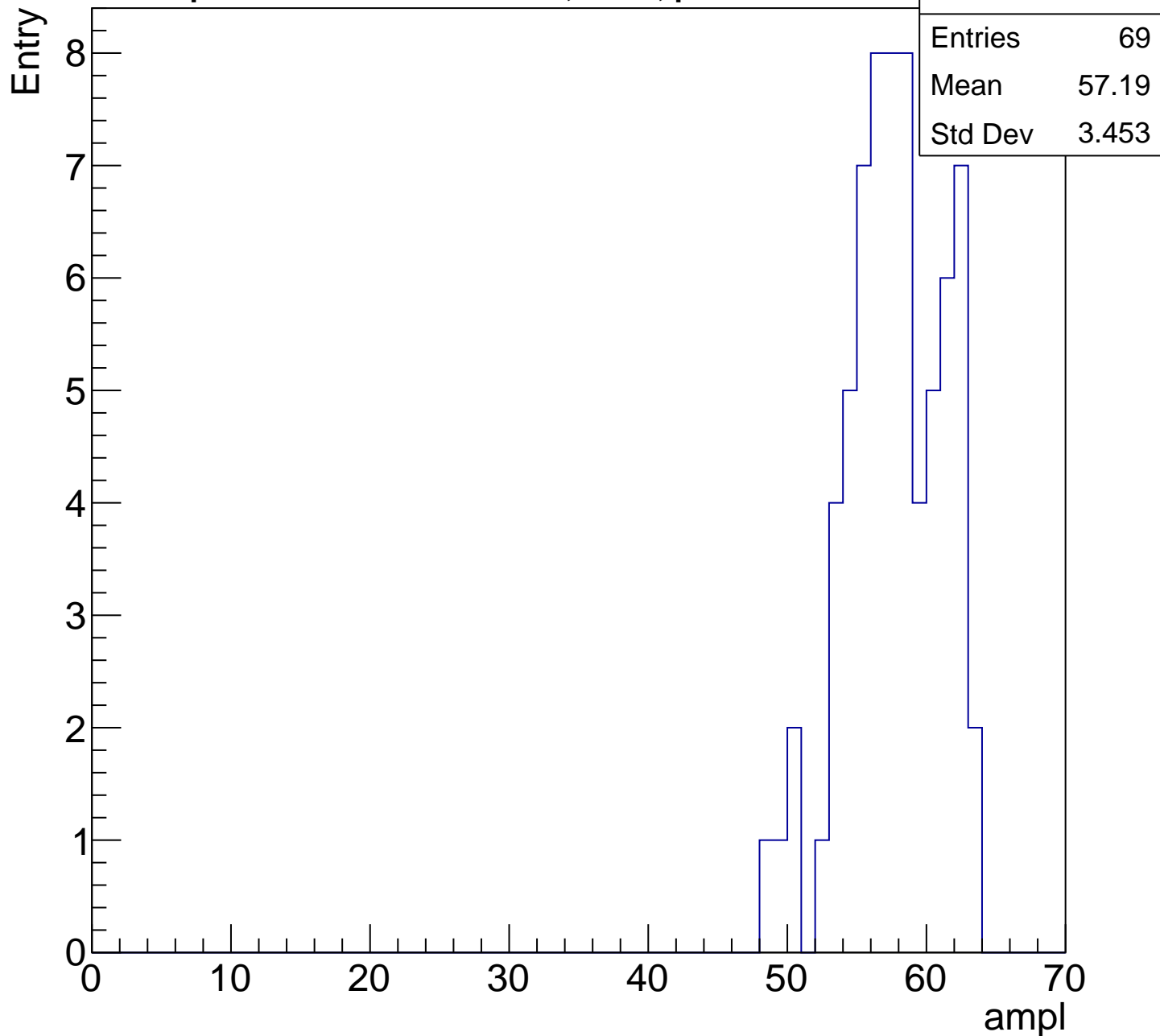
Entries	75
Mean	50.47
Std Dev	3.496

Entry



# B0L000S, U13-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

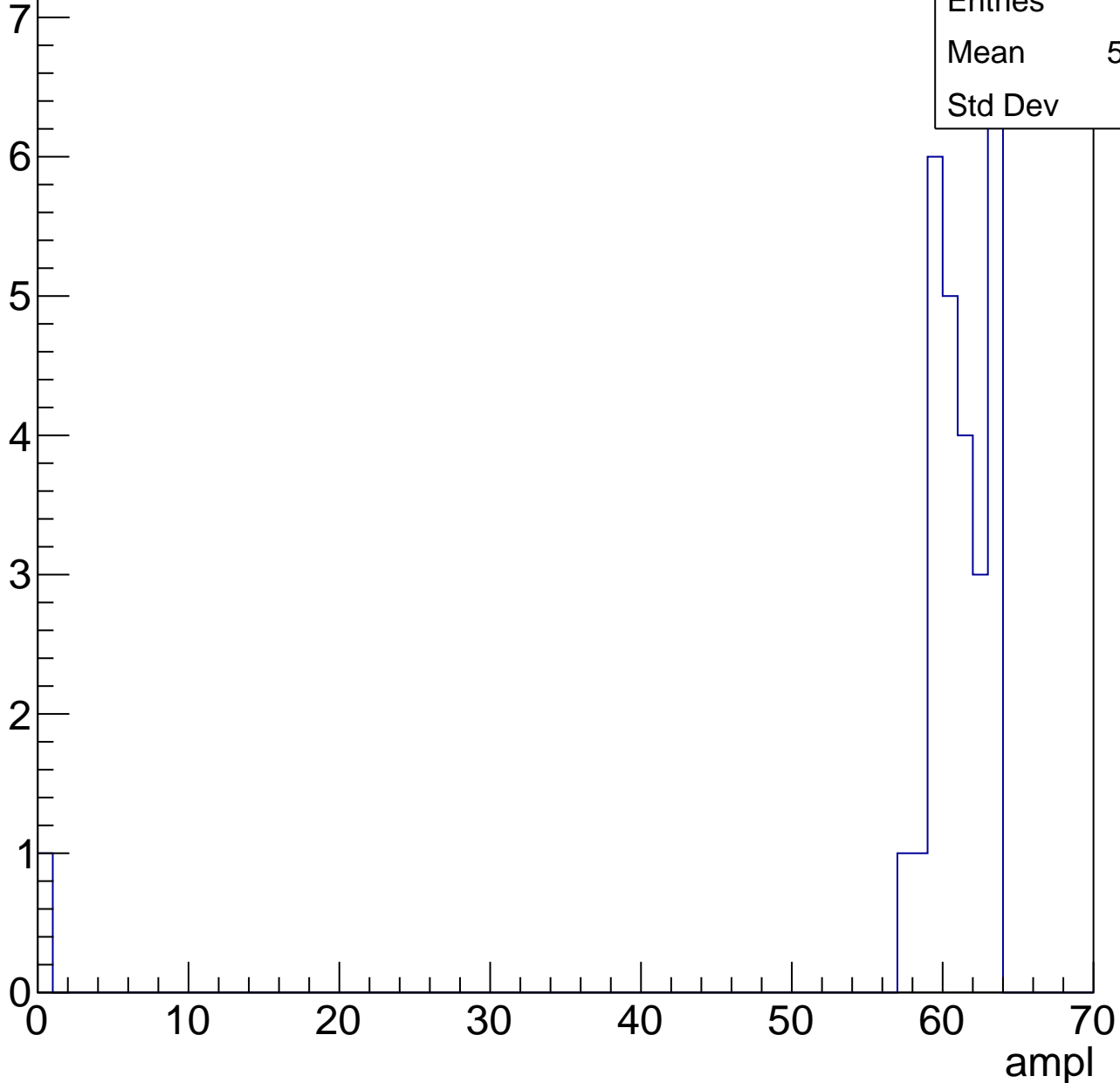


# B0L000S, U13-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.57
Std Dev	11.4



# B0L000S, U13-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U13-ch11, adc0

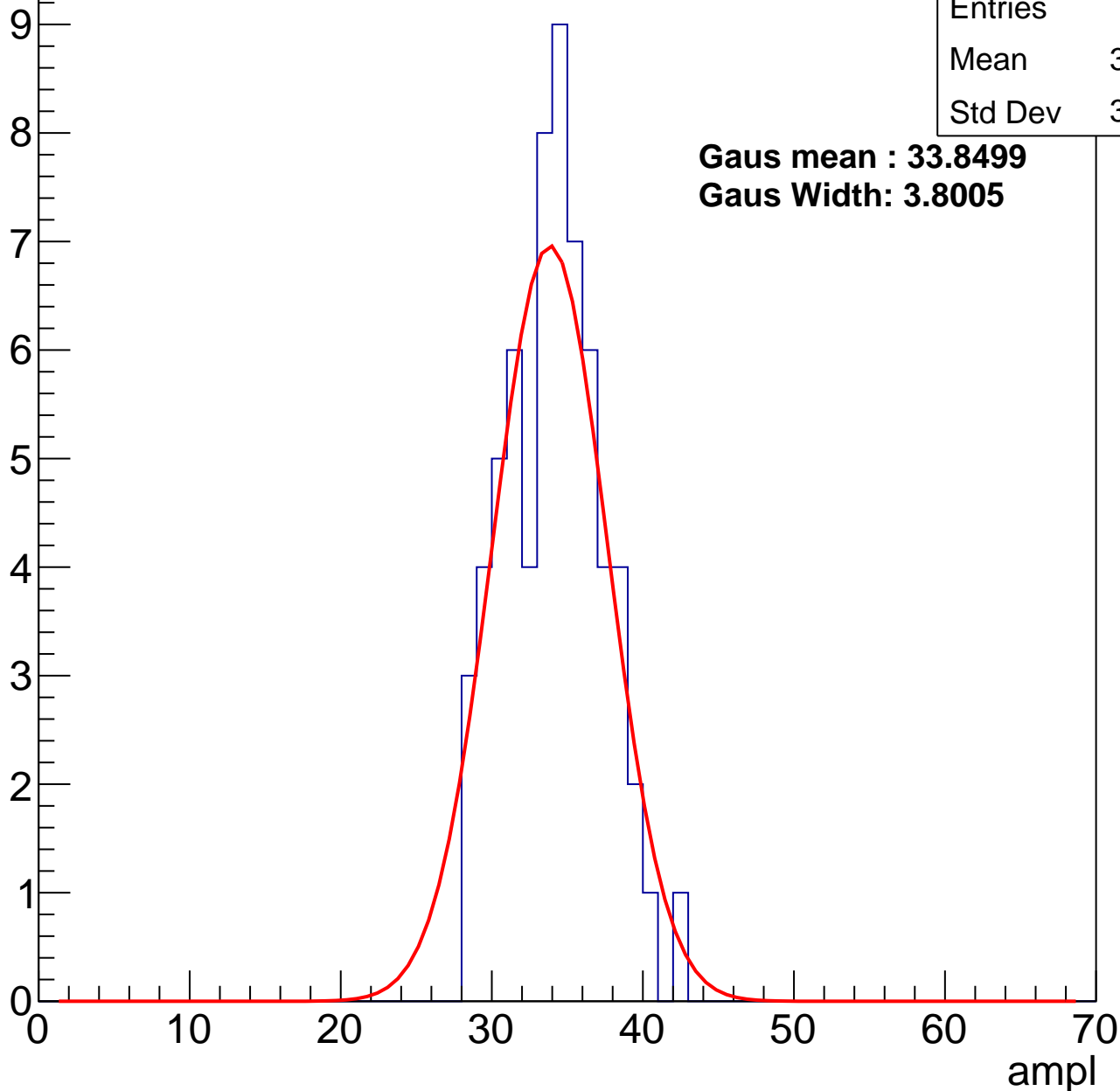
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	33.67
Std Dev	3.163

**Gaus mean : 33.8499**

**Gaus Width: 3.8005**



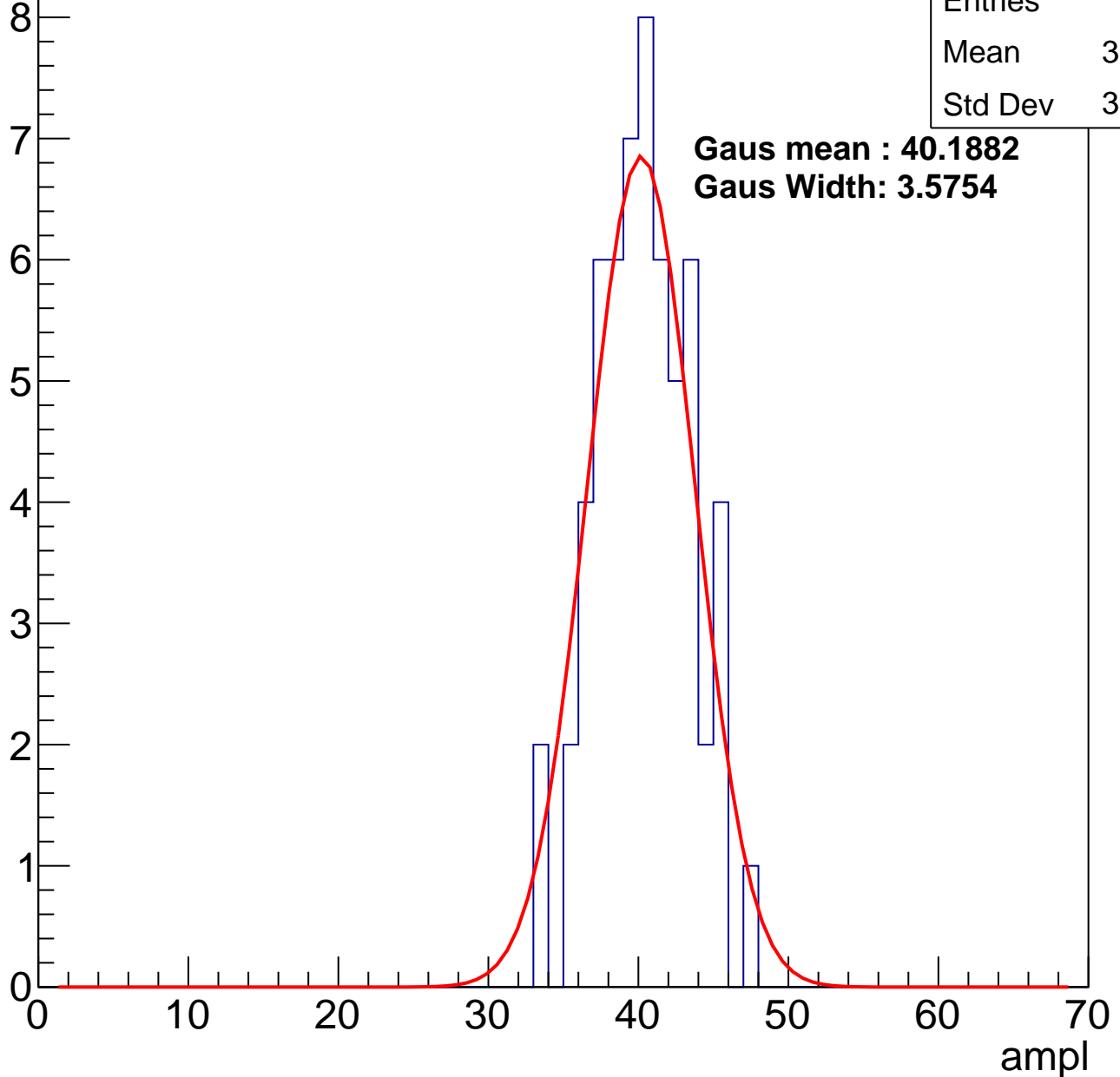
# B0L000S, U13-ch11, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	39.86
Std Dev	3.072

**Gaus mean : 40.1882**  
**Gaus Width: 3.5754**



# B0L000S, U13-ch11, adc2

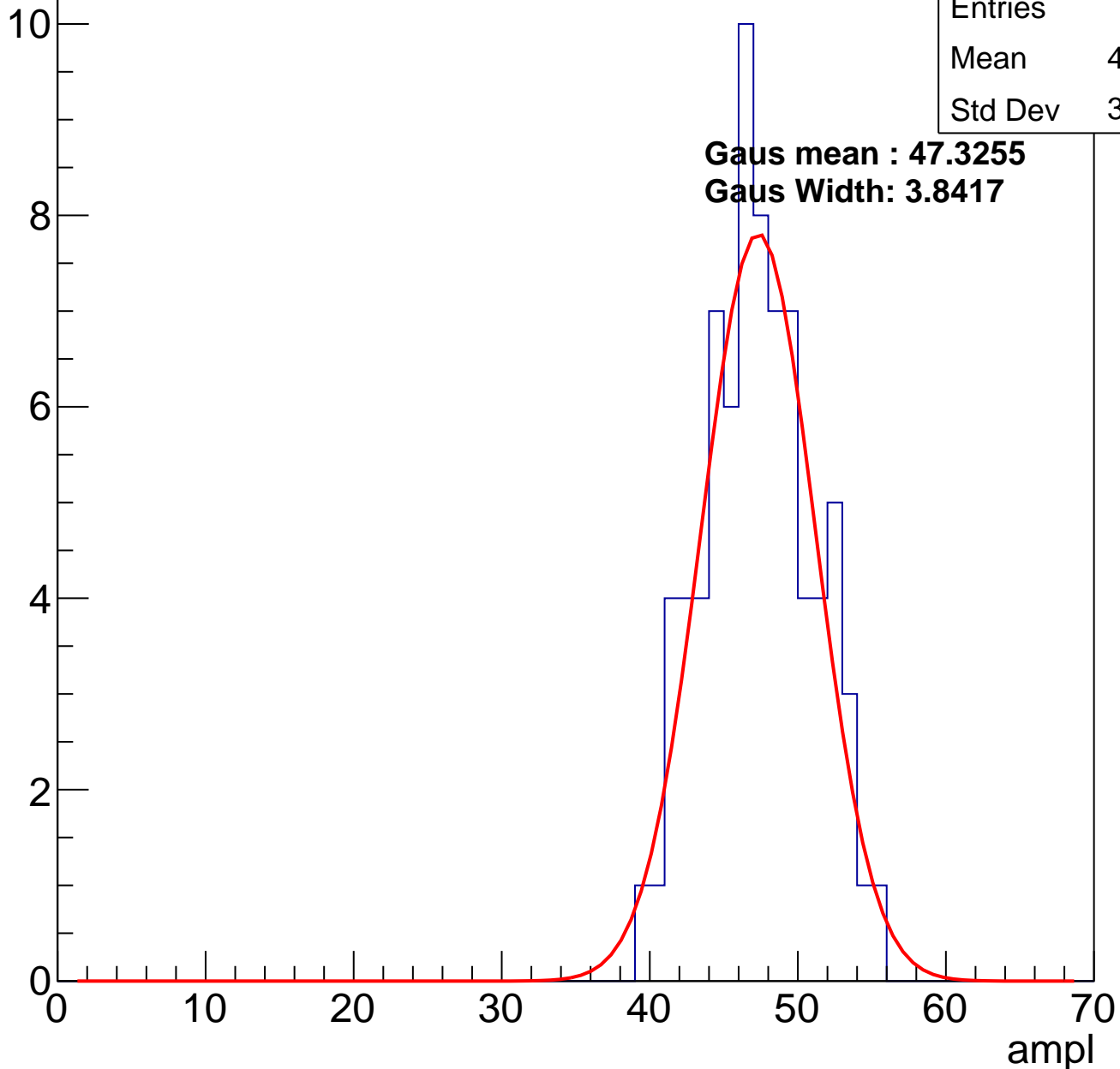
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	46.86
Std Dev	3.599

**Gaus mean : 47.3255**

**Gaus Width: 3.8417**

Entry

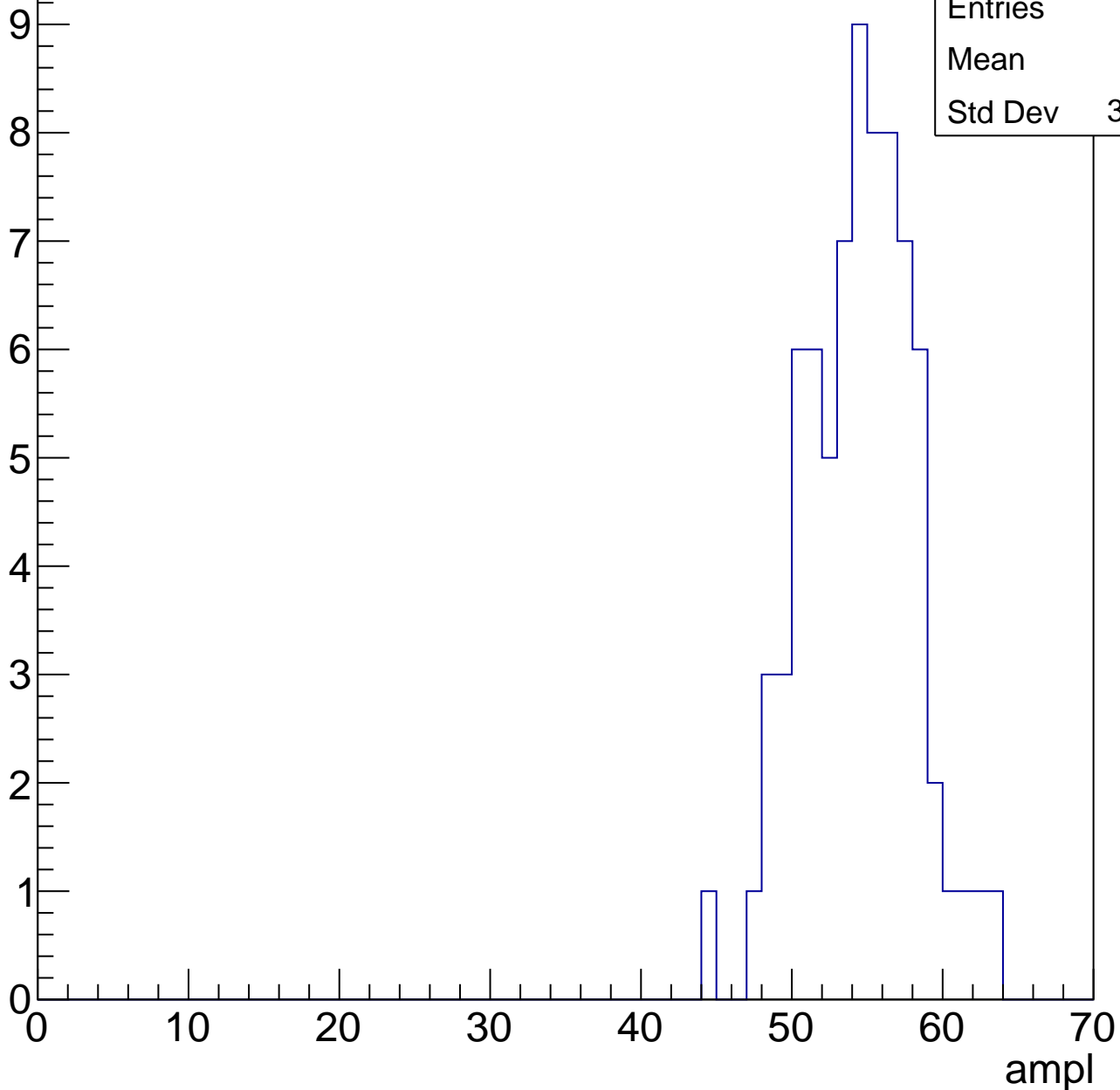


# B0L000S, U13-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	54
Std Dev	3.606

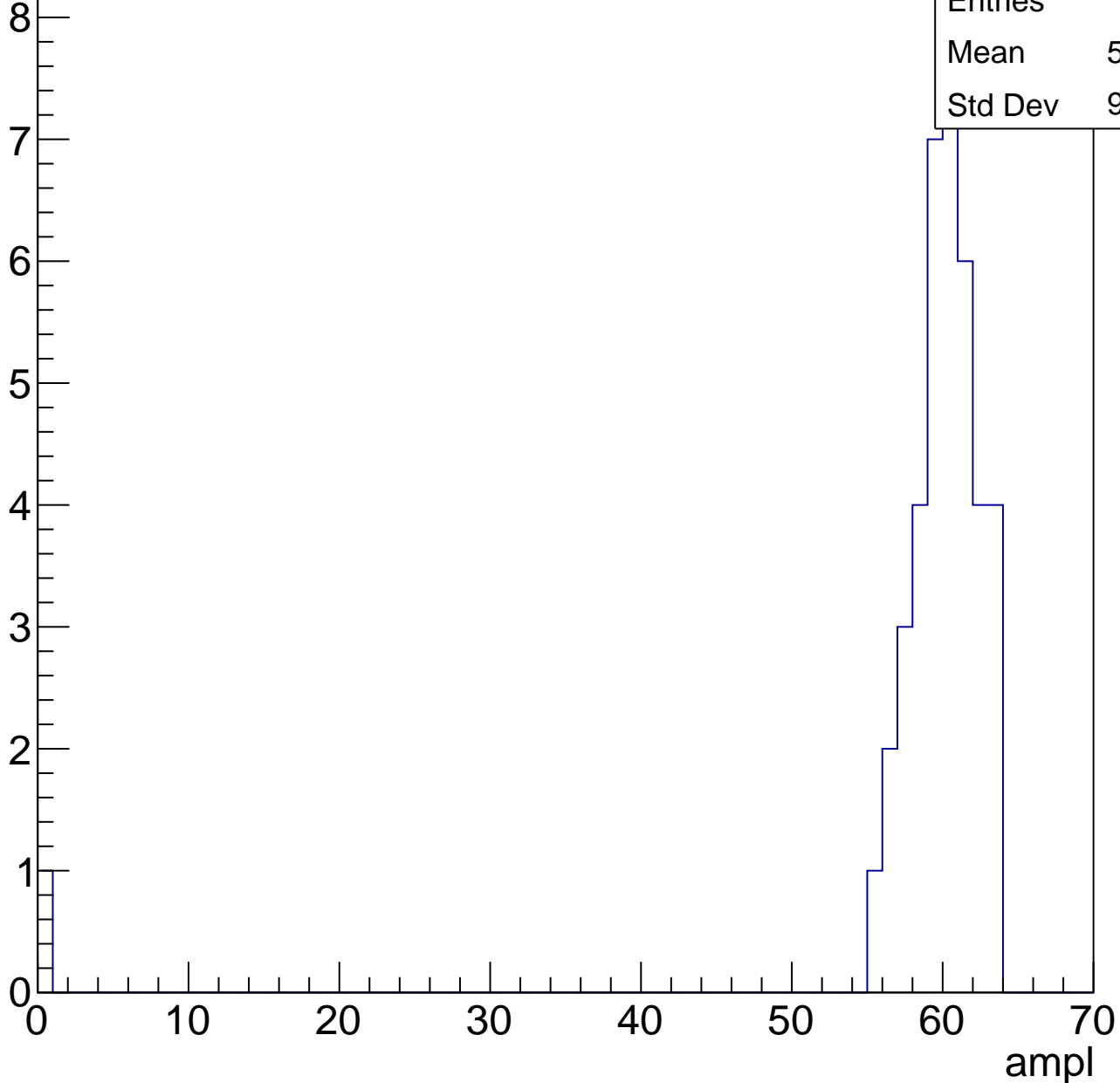


# B0L000S, U13-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.23
Std Dev	9.538

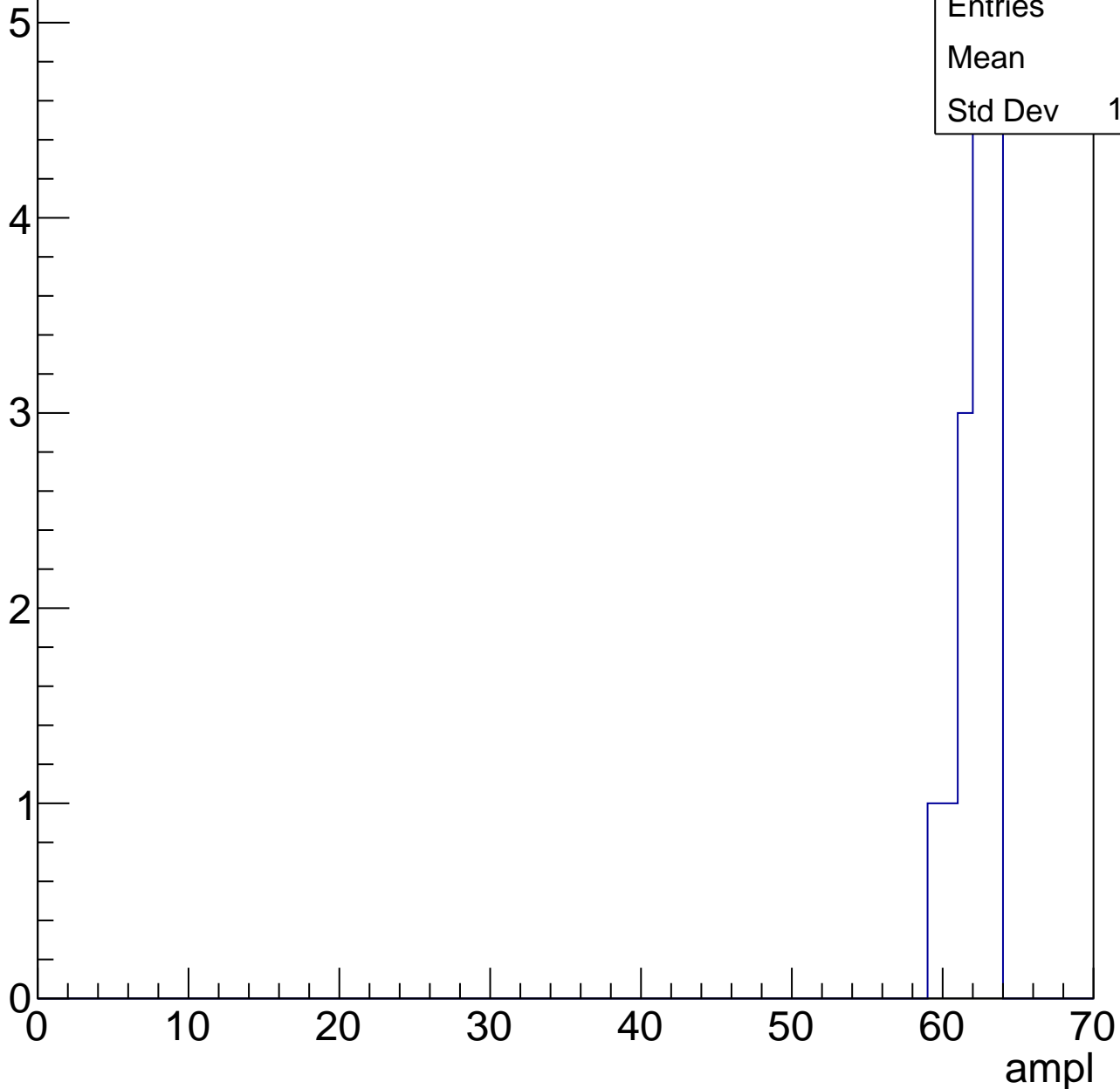


# B0L000S, U13-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	61.8
Std Dev	1.166



# B0L000S, U13-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	27
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B0L000S, U13-ch12, adc0

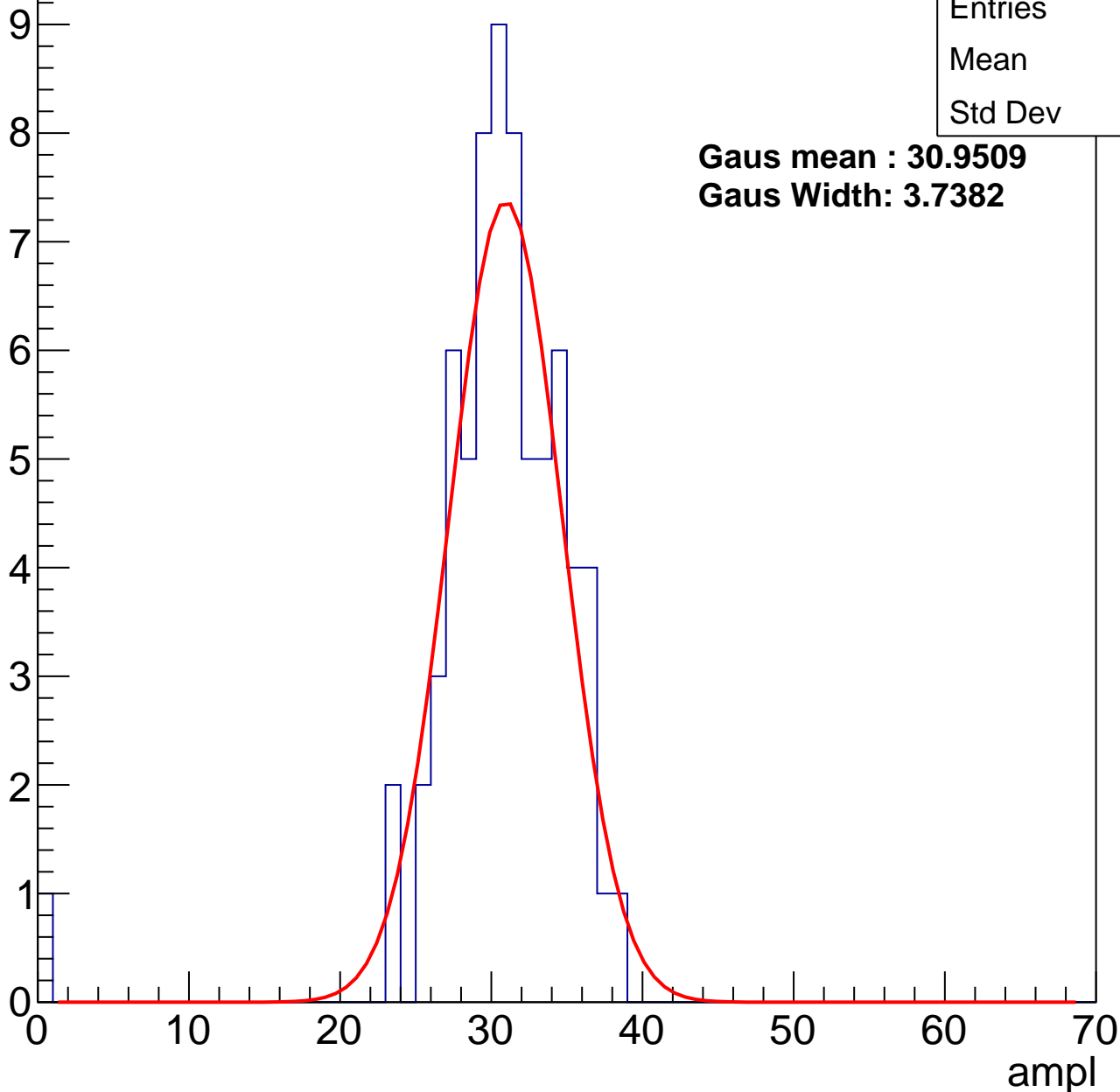
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.2
Std Dev	4.93

**Gaus mean : 30.9509**

**Gaus Width: 3.7382**



# B0L000S, U13-ch12, adc1

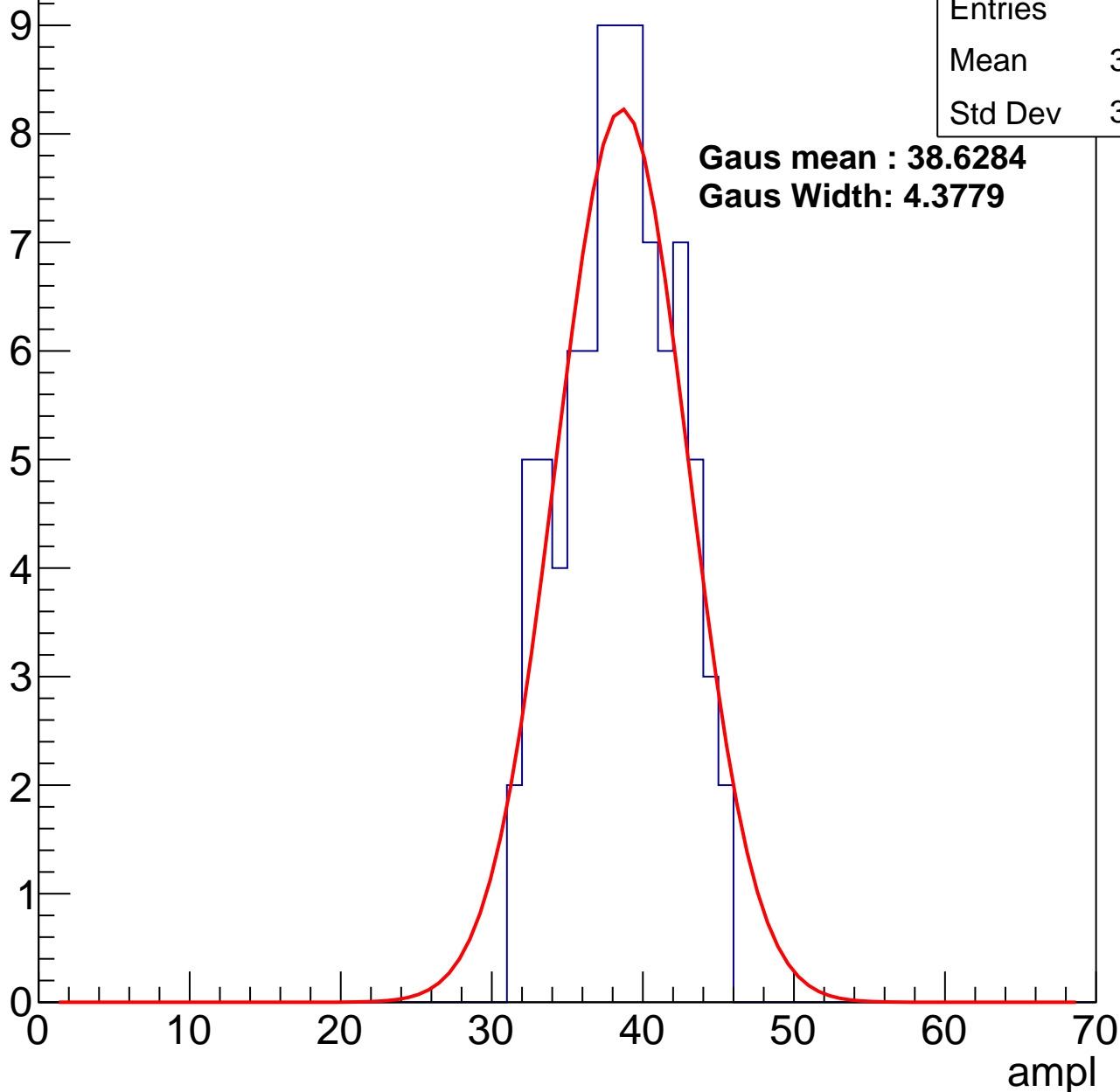
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	38.02
Std Dev	3.578

**Gaus mean : 38.6284**

**Gaus Width: 4.3779**



# B0L000S, U13-ch12, adc2

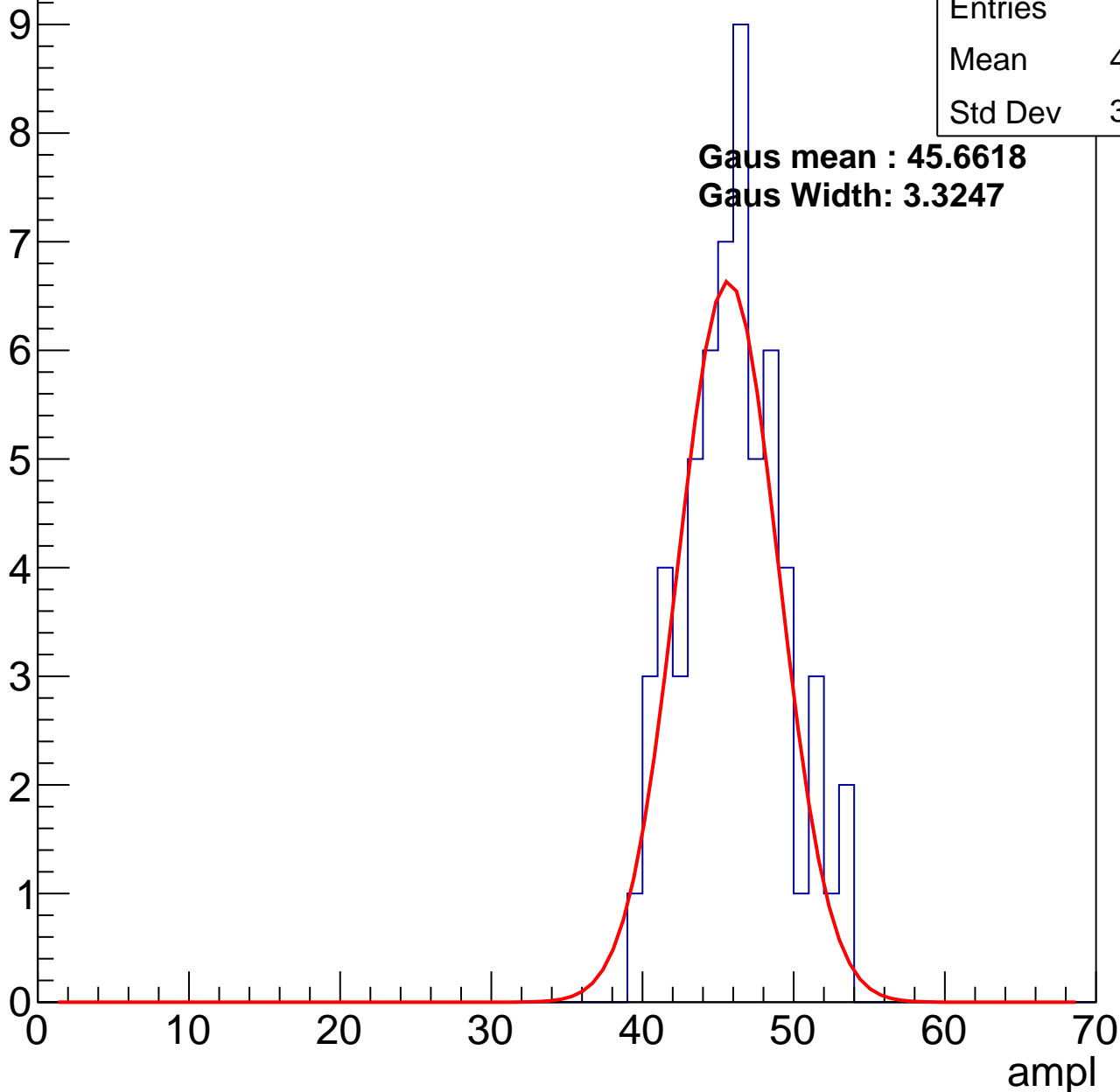
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	45.62
Std Dev	3.322

**Gaus mean : 45.6618**

**Gaus Width: 3.3247**

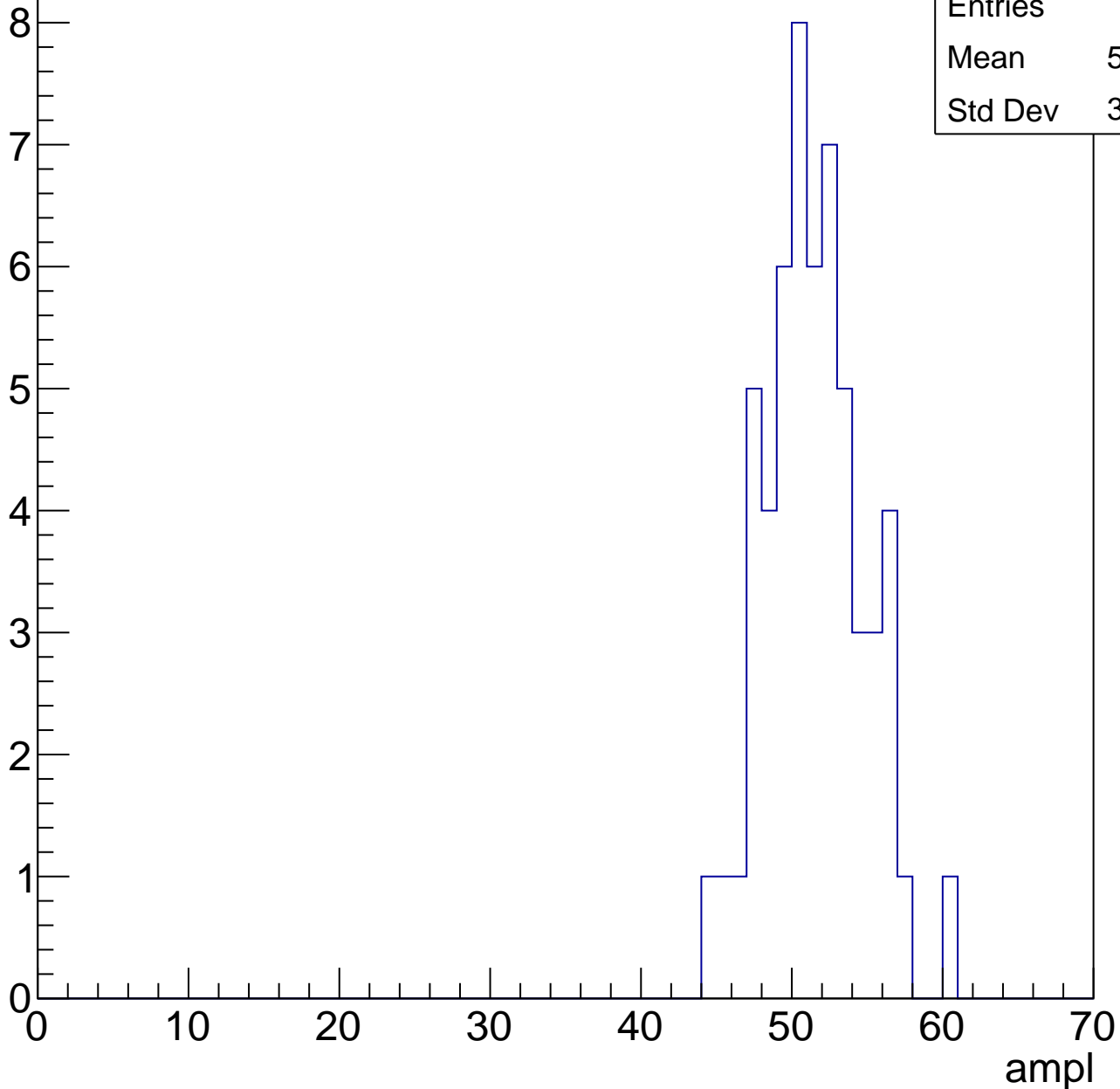


# B0L000S, U13-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	51.05
Std Dev	3.209

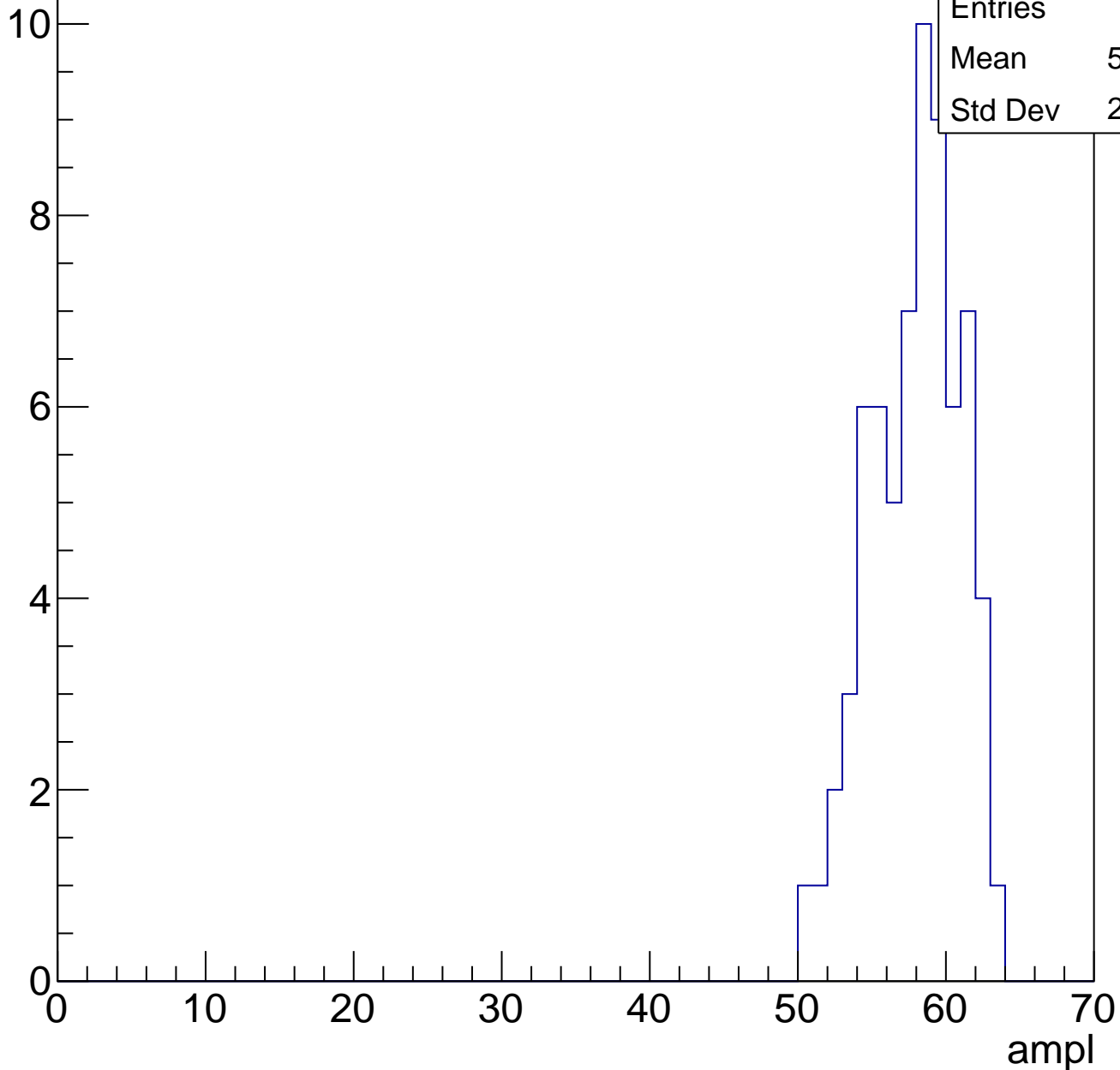


# B0L000S, U13-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	57.44
Std Dev	2.972

Entry

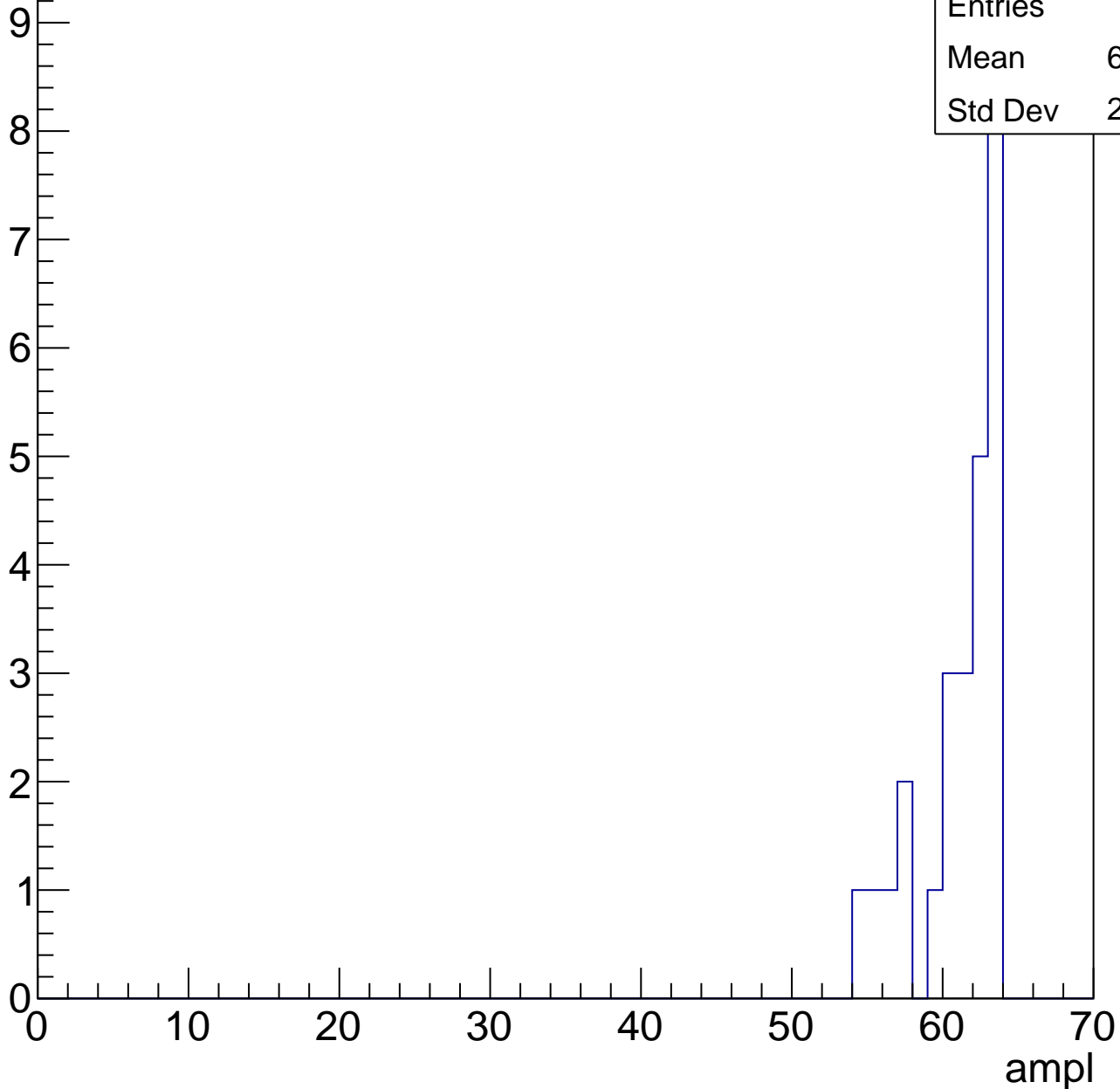


# B0L000S, U13-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

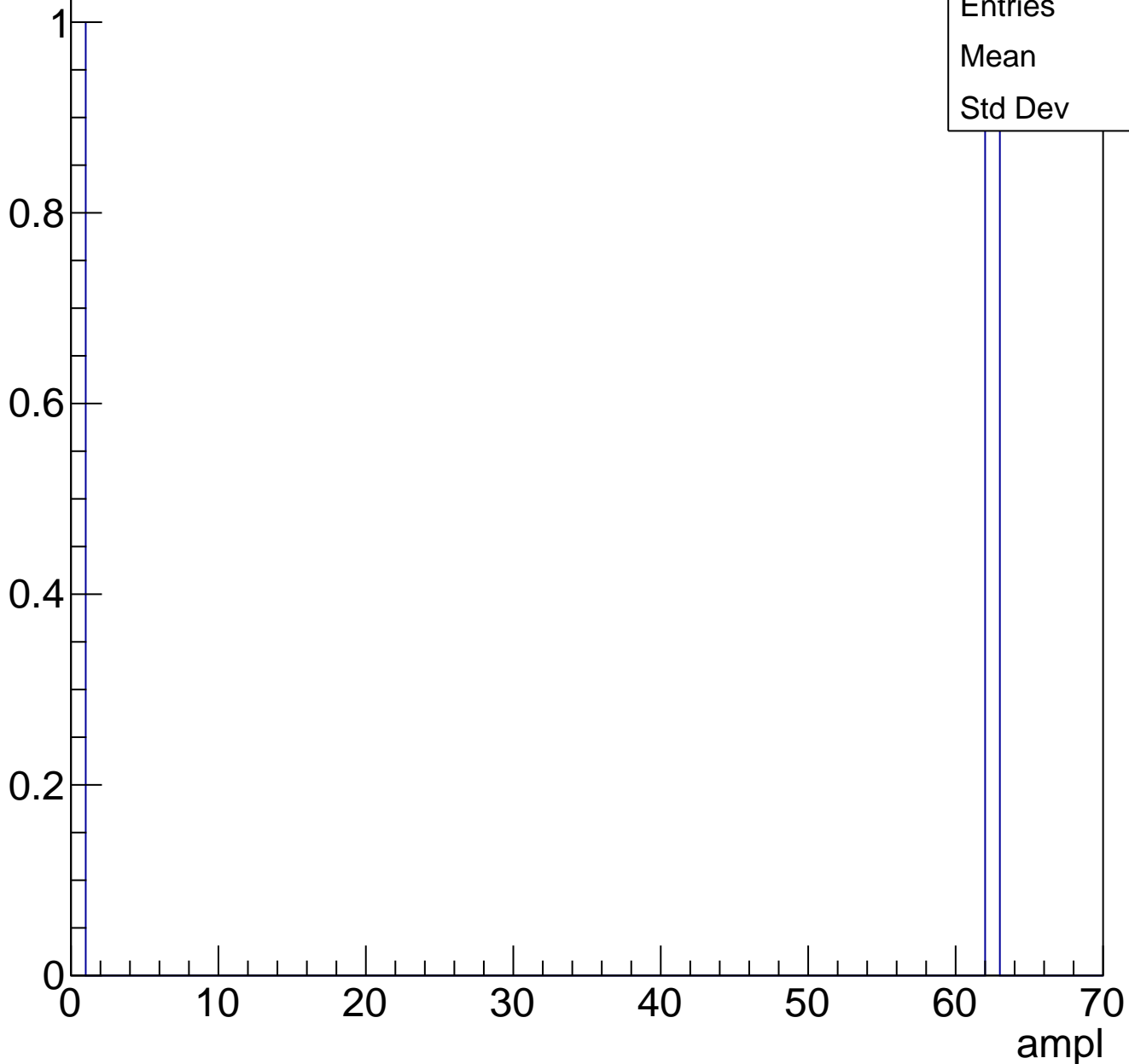
Entries	26
Mean	60.69
Std Dev	2.686



# B0L000S, U13-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U13-ch13, adc0

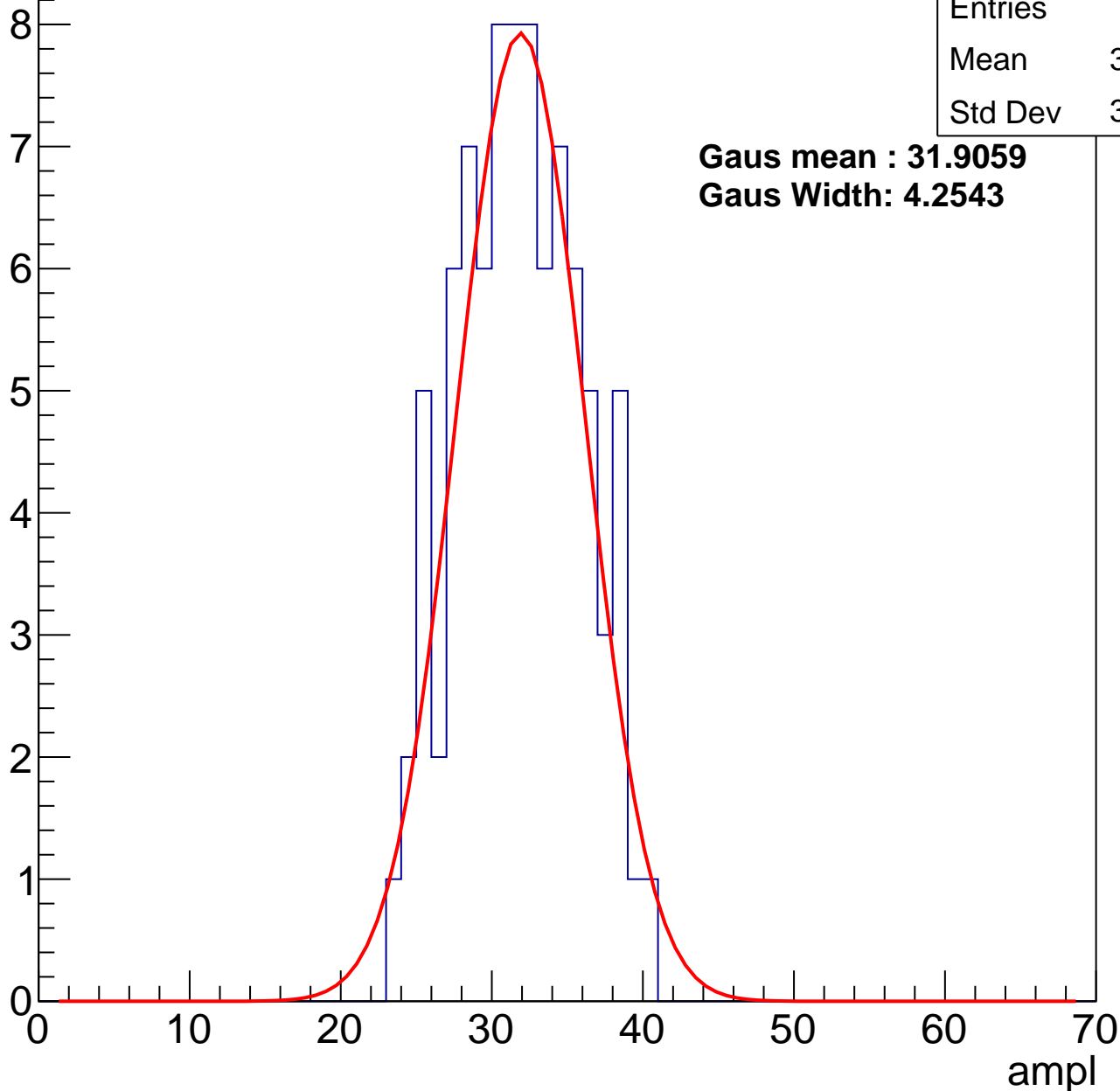
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	31.38
Std Dev	3.989

**Gaus mean : 31.9059**

**Gaus Width: 4.2543**



# B0L000S, U13-ch13, adc1

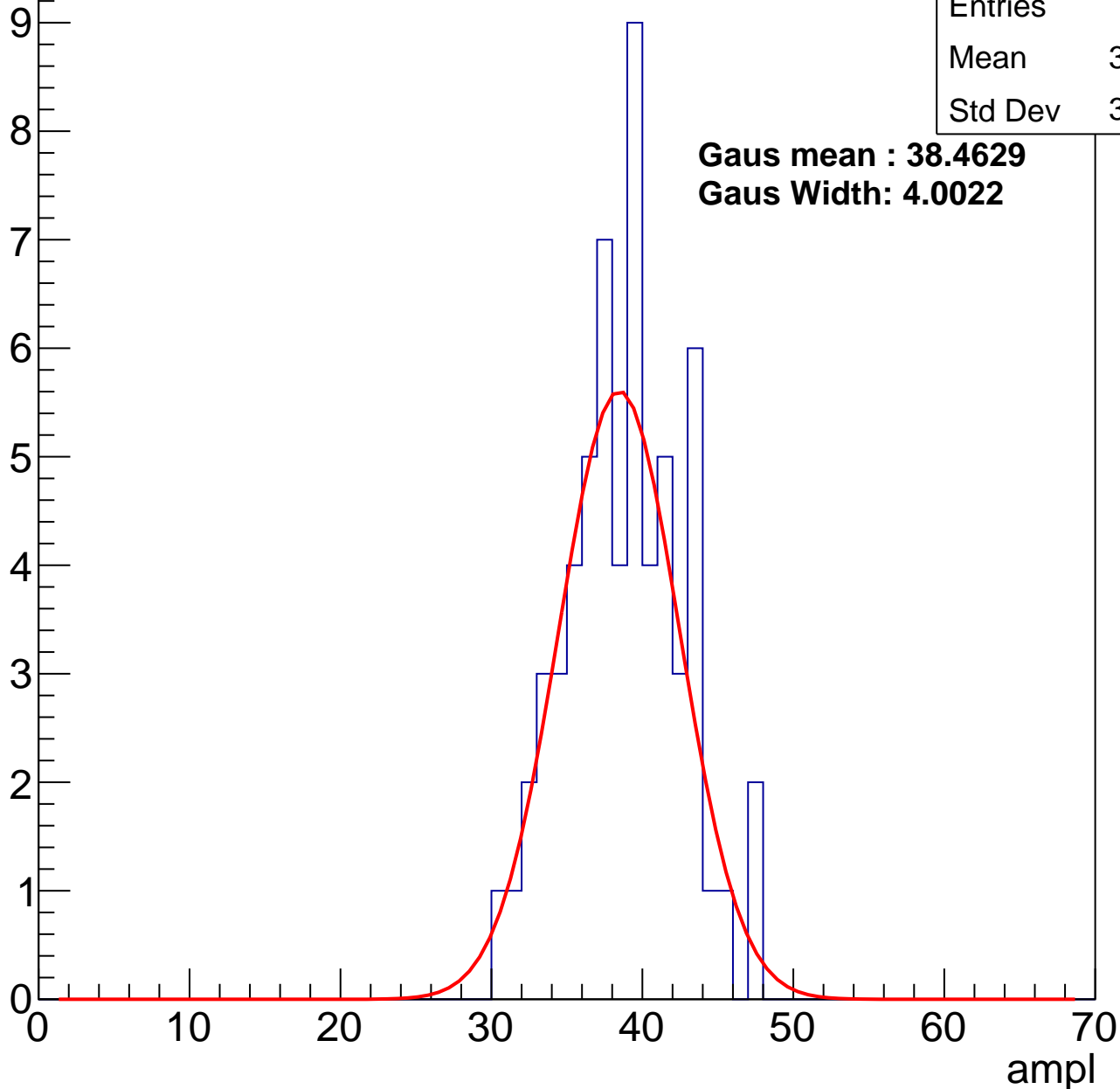
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	38.36
Std Dev	3.794

**Gaus mean : 38.4629**

**Gaus Width: 4.0022**



# B0L000S, U13-ch13, adc2

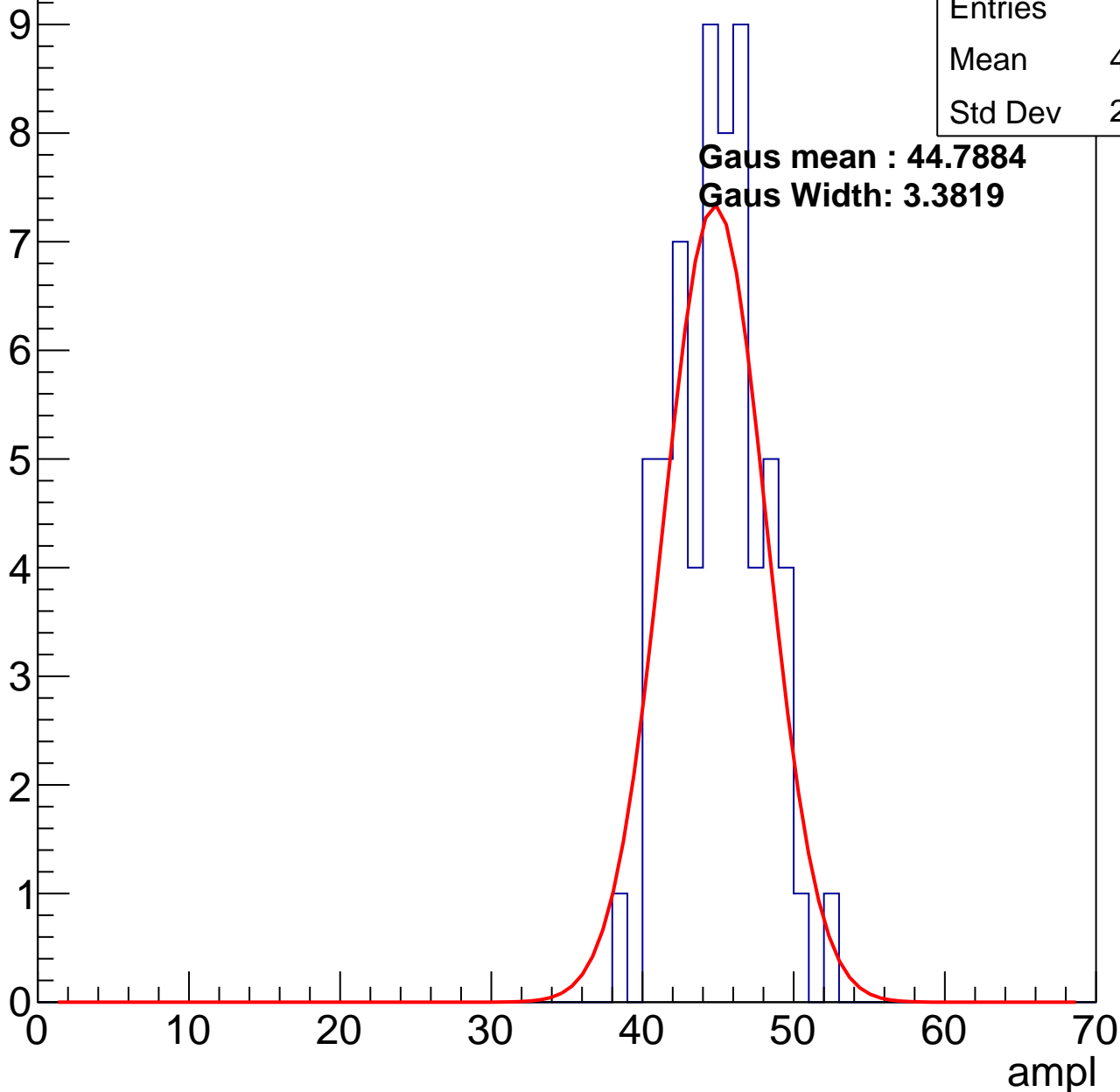
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.52
Std Dev	2.916

**Gaus mean : 44.7884**

**Gaus Width: 3.3819**

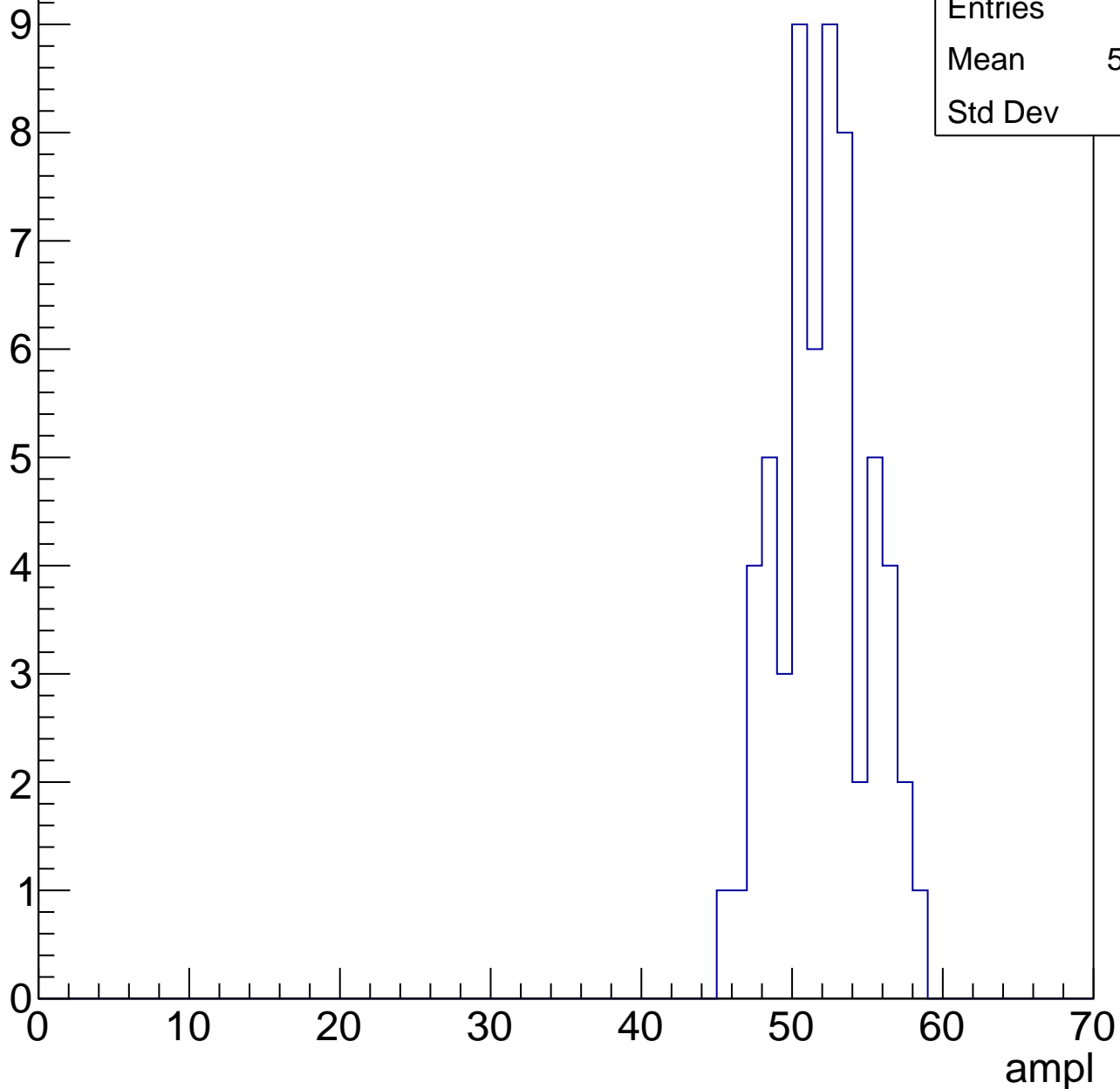


# B0L000S, U13-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.55
Std Dev	2.98

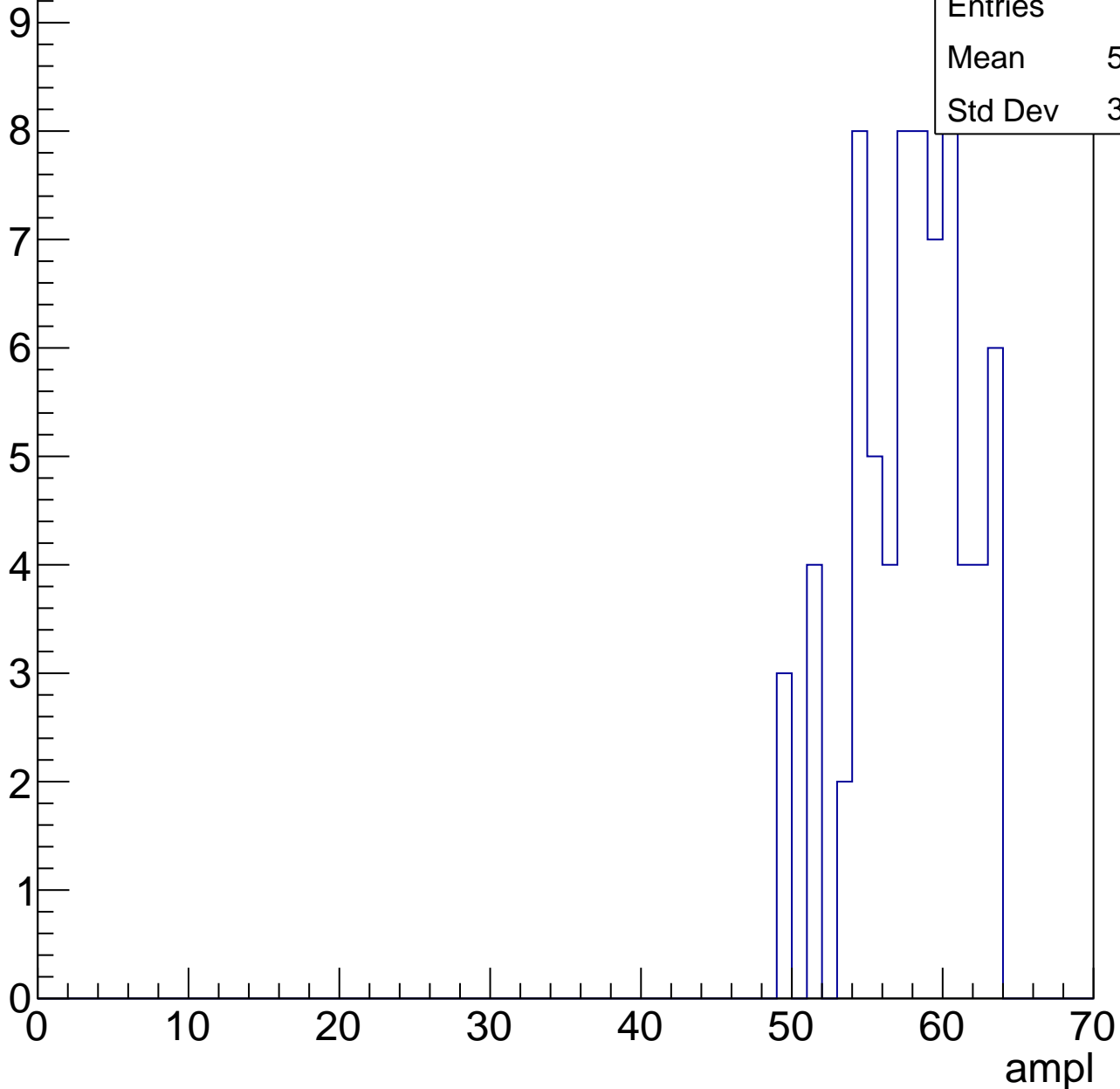


# B0L000S, U13-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

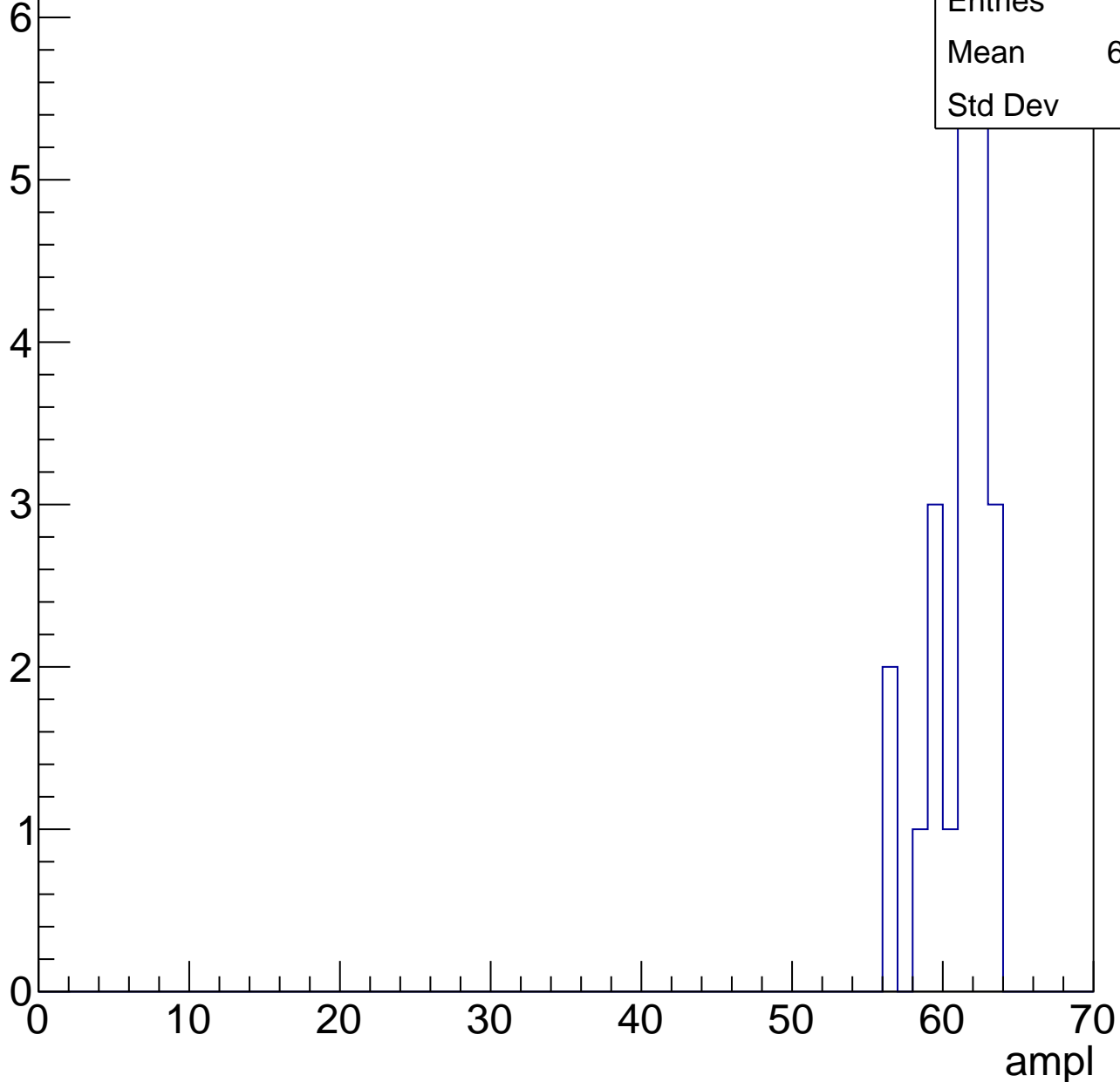
Entries	72
Mean	57.38
Std Dev	3.634



# B0L000S, U13-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U13-ch14, adc0

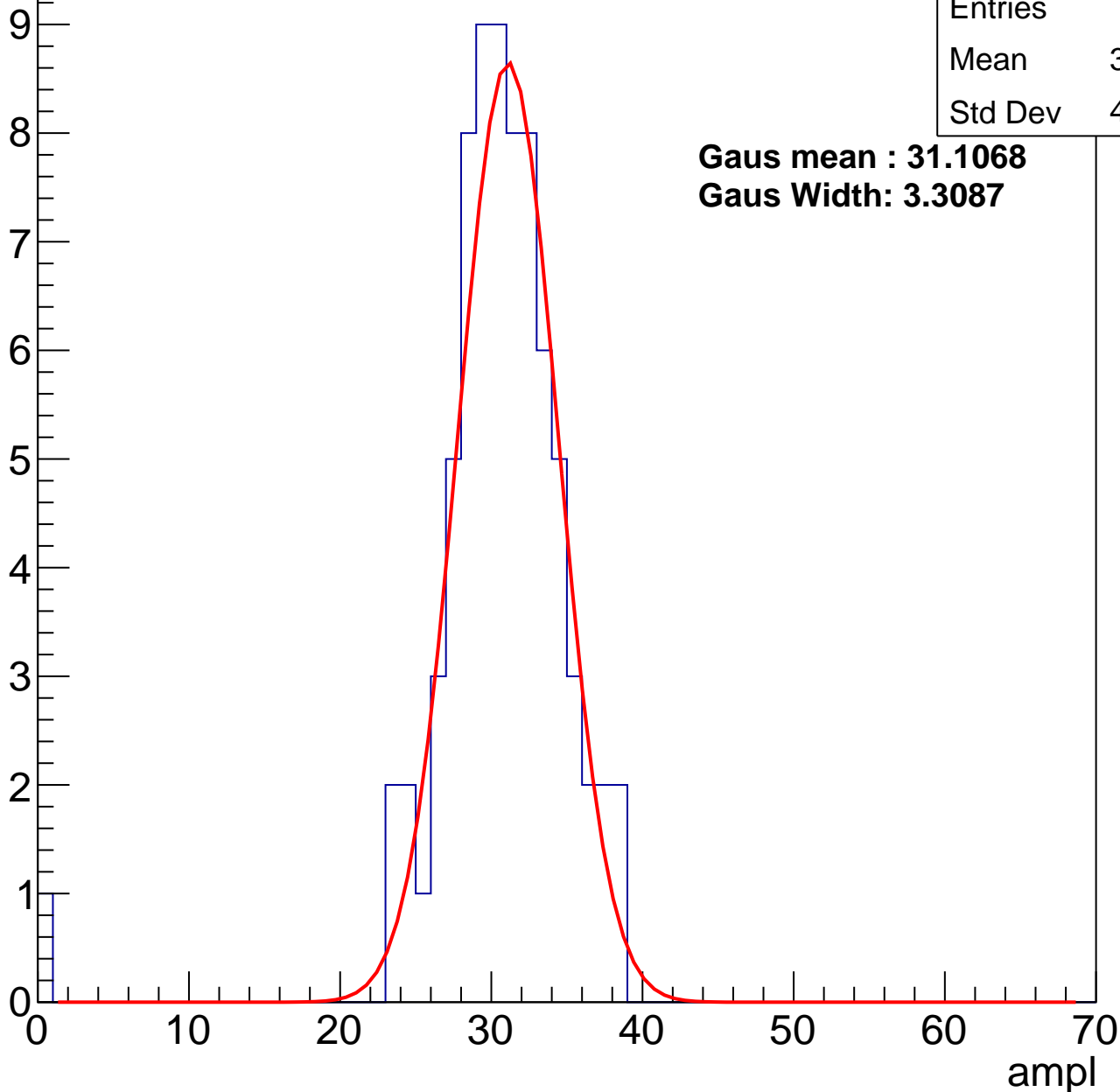
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.08
Std Dev	4.833

**Gaus mean : 31.1068**

**Gaus Width: 3.3087**



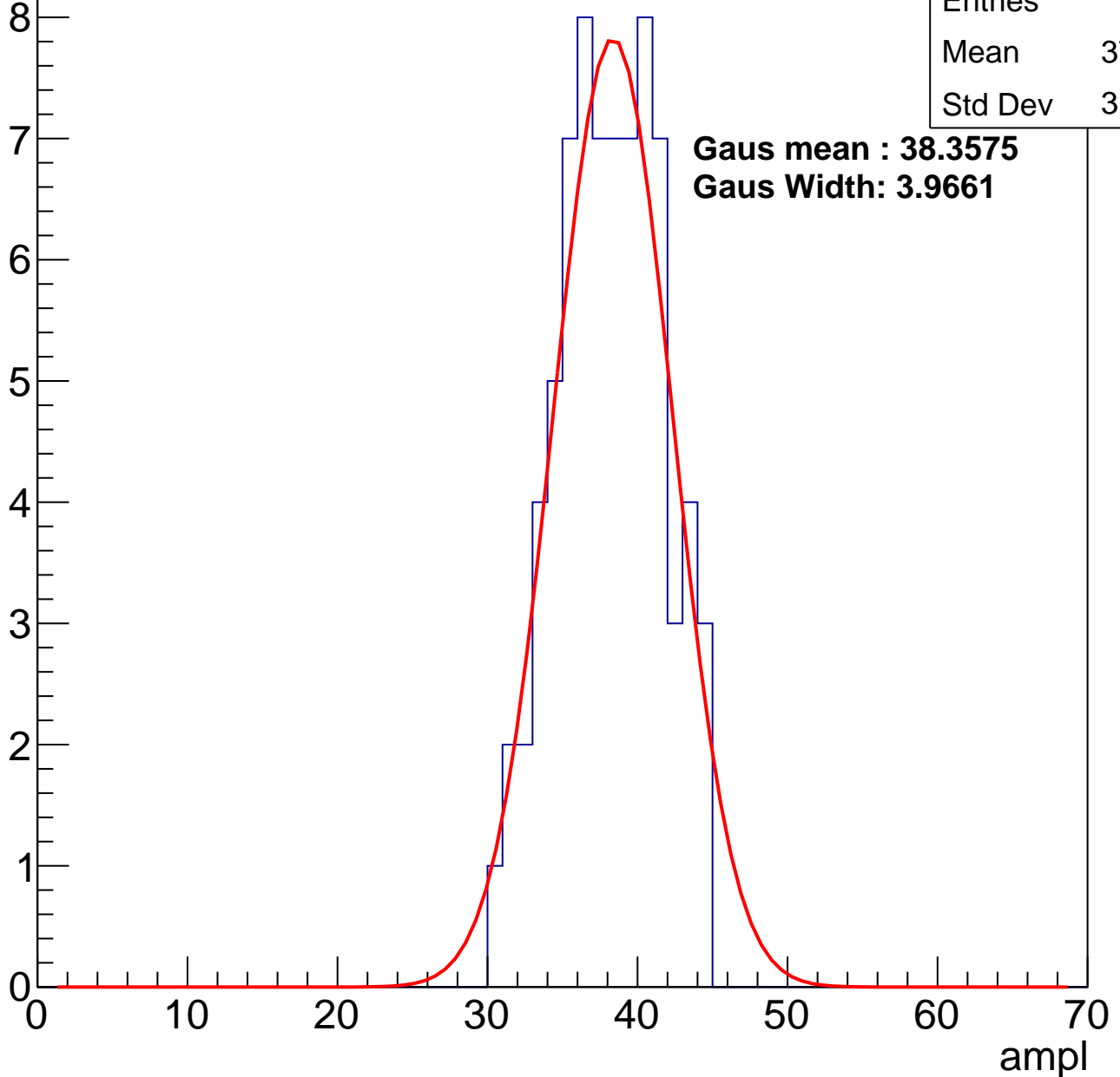
# B0L000S, U13-ch14, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.68
Std Dev	3.399

**Gaus mean : 38.3575**  
**Gaus Width: 3.9661**



# B0L000S, U13-ch14, adc2

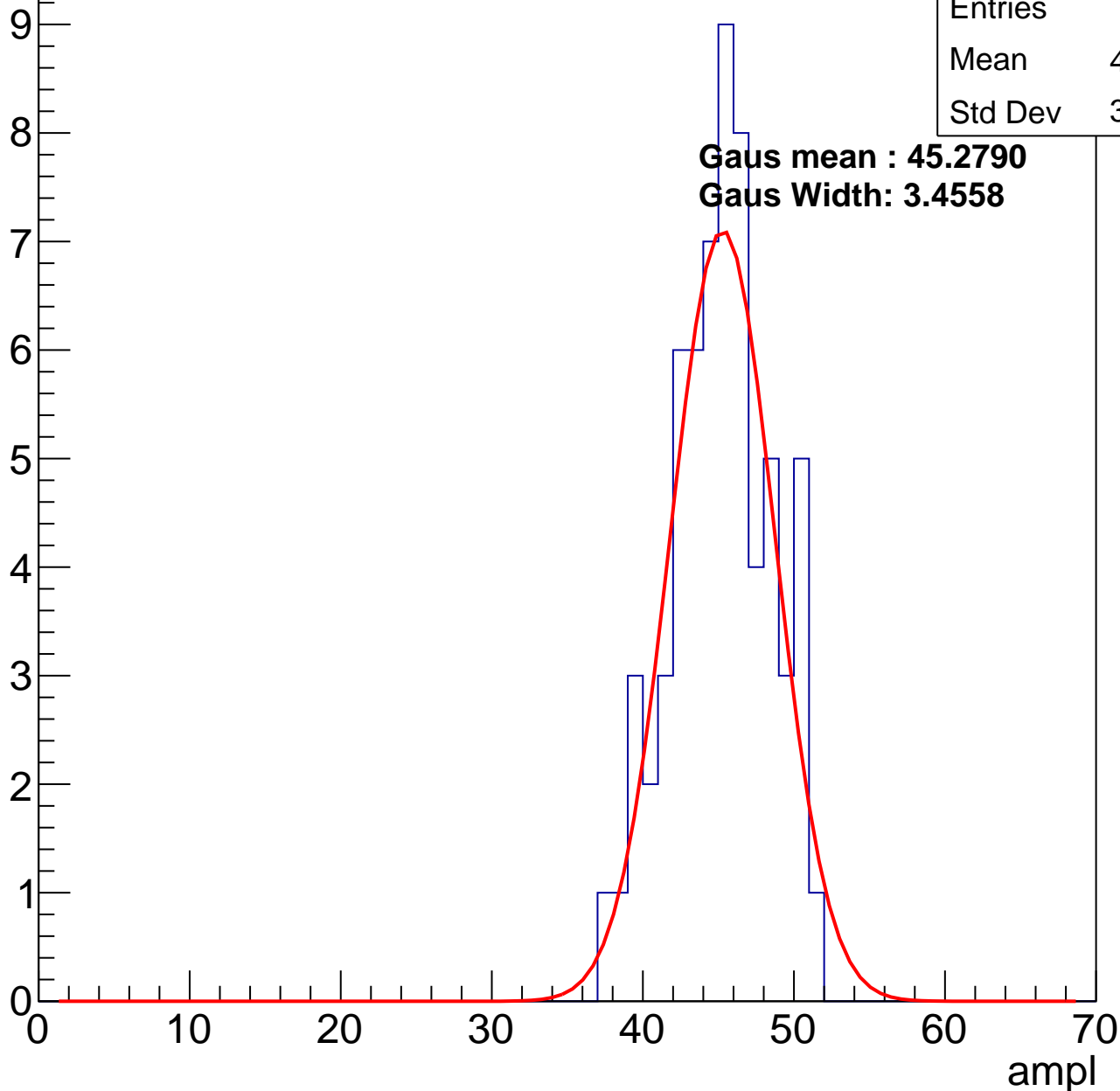
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	44.72
Std Dev	3.252

**Gaus mean : 45.2790**

**Gaus Width: 3.4558**

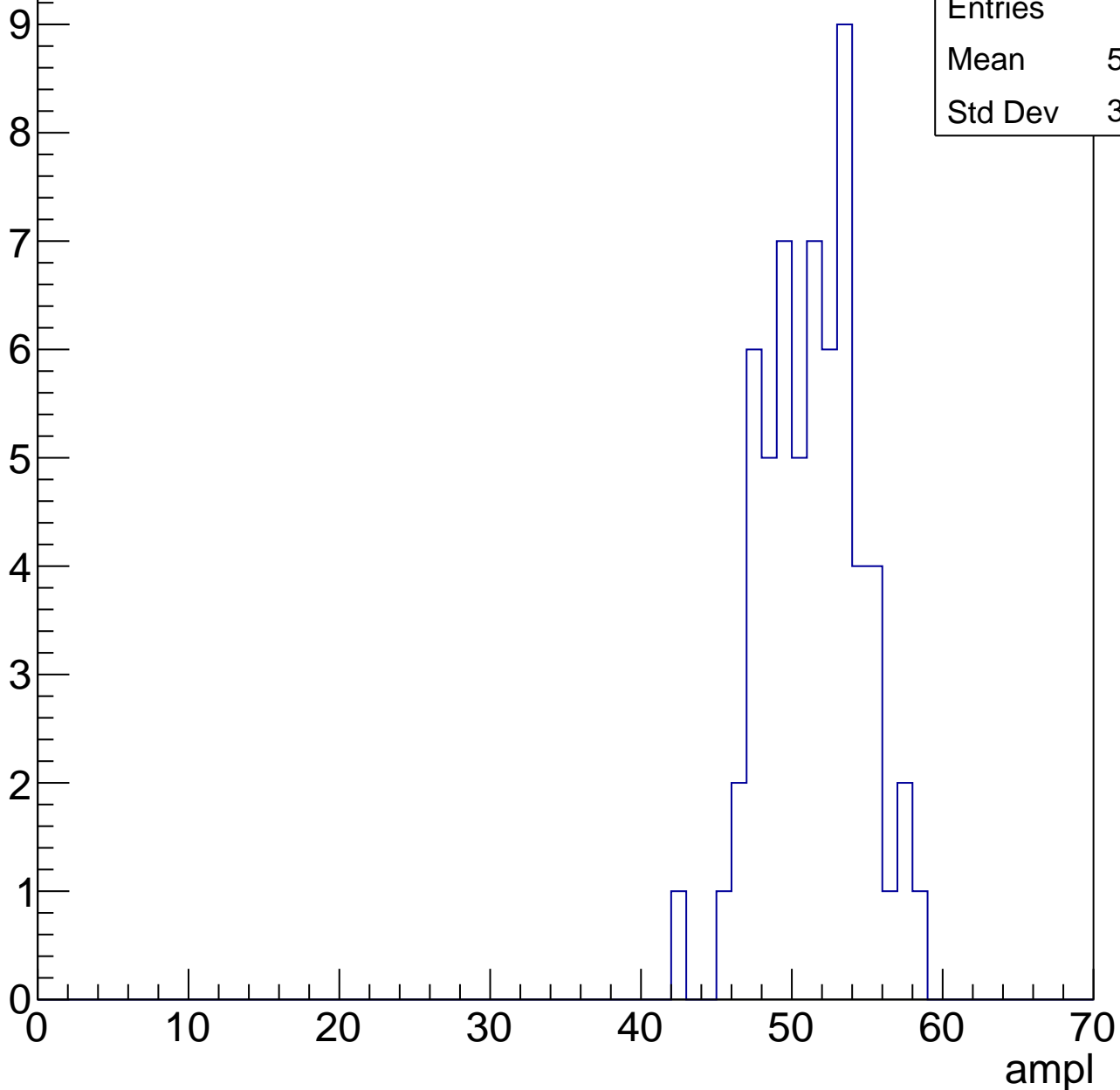


# B0L000S, U13-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	50.89
Std Dev	3.209

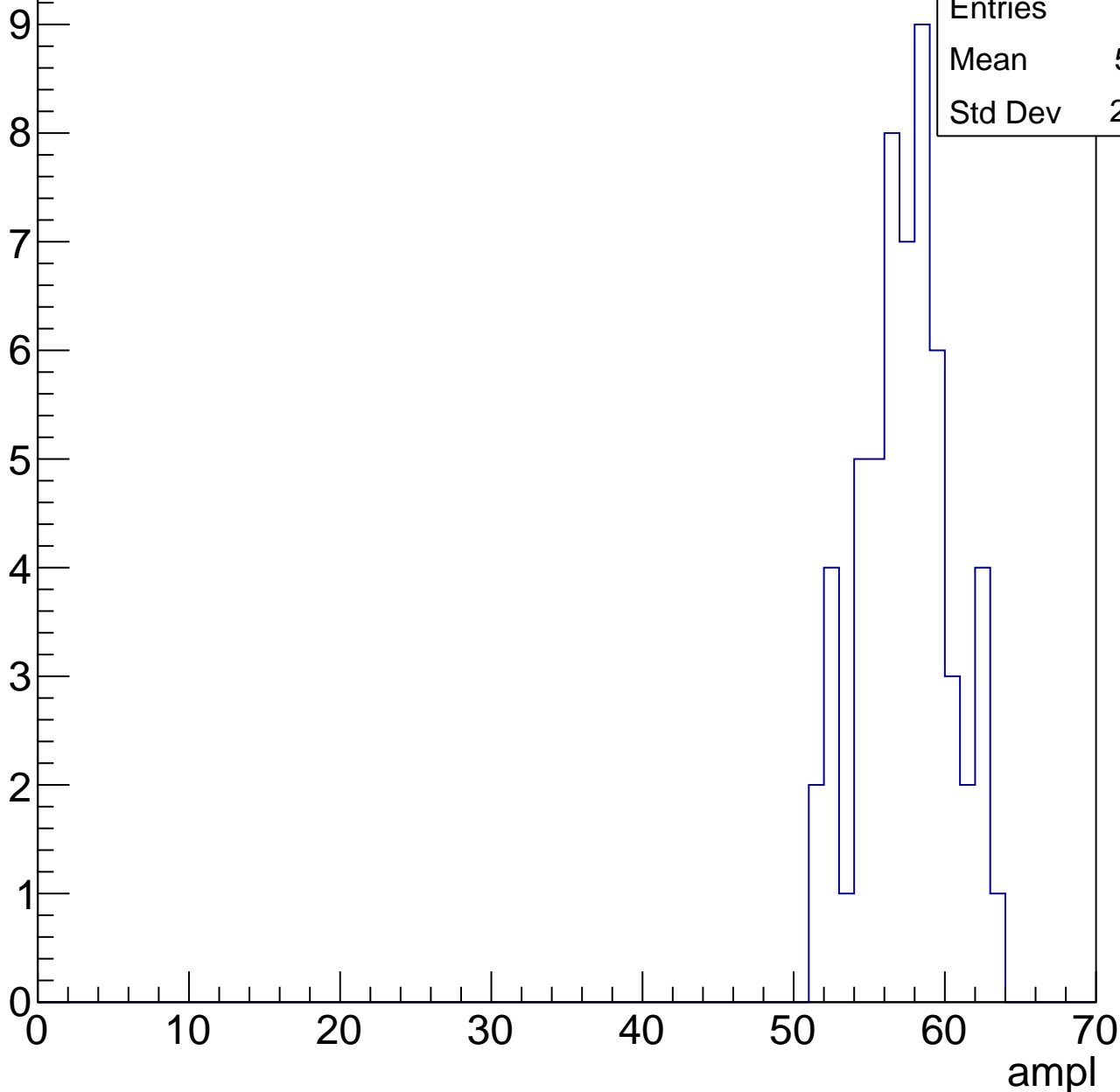


# B0L000S, U13-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.91
Std Dev	2.928

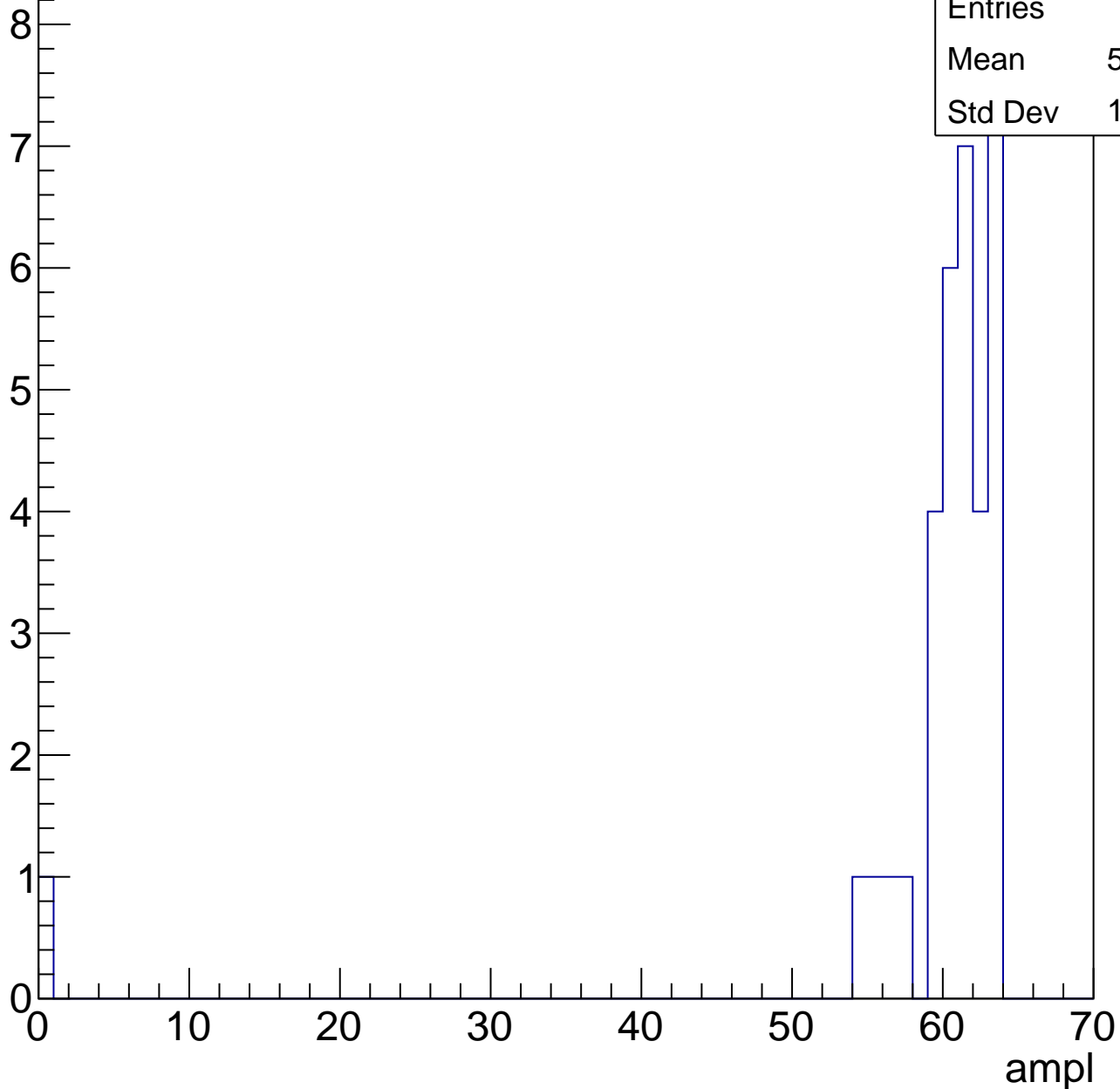


# B0L000S, U13-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.74
Std Dev	10.47



# B0L000S, U13-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch15, adc0

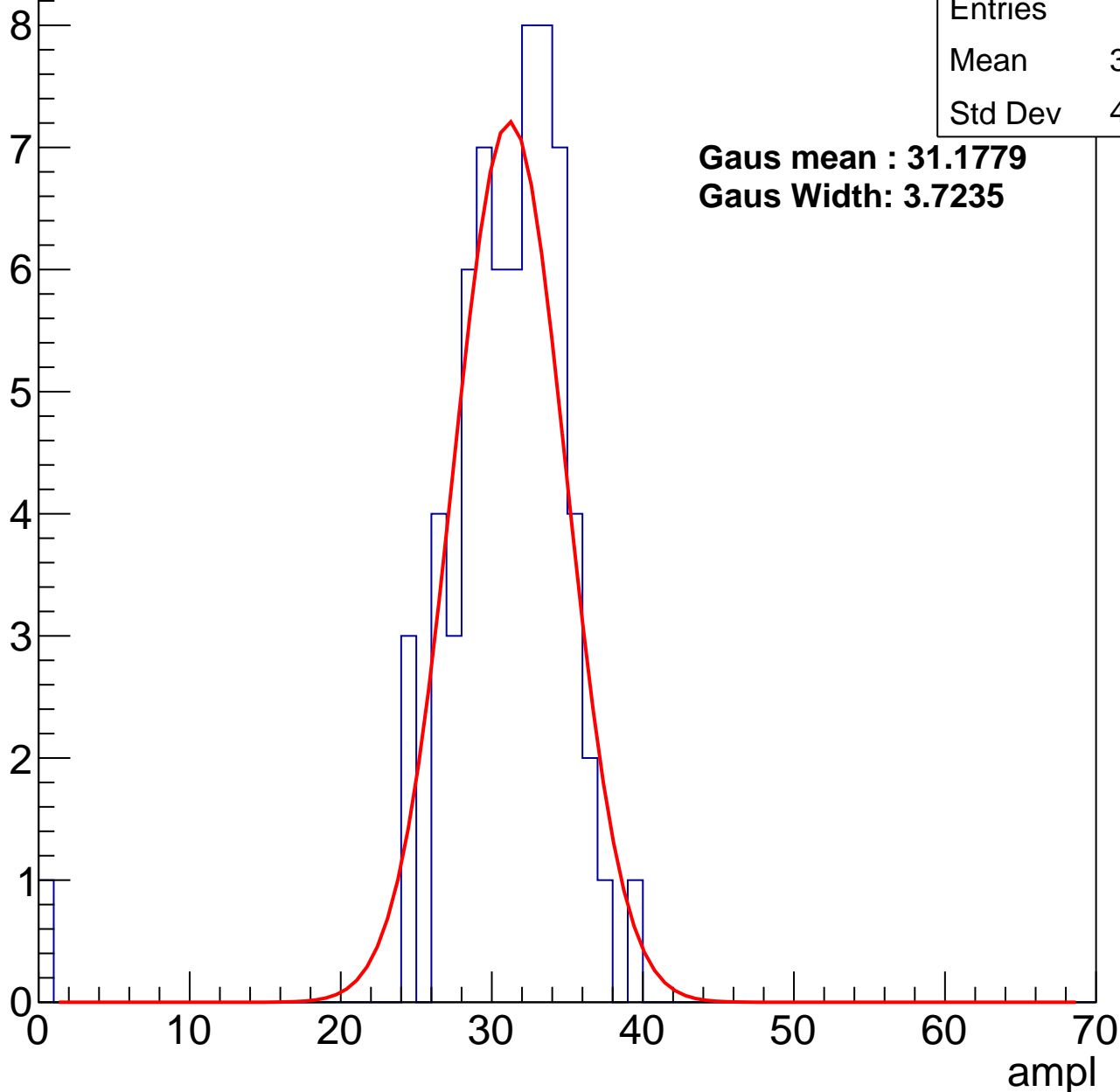
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.45
Std Dev	4.948

**Gaus mean : 31.1779**

**Gaus Width: 3.7235**



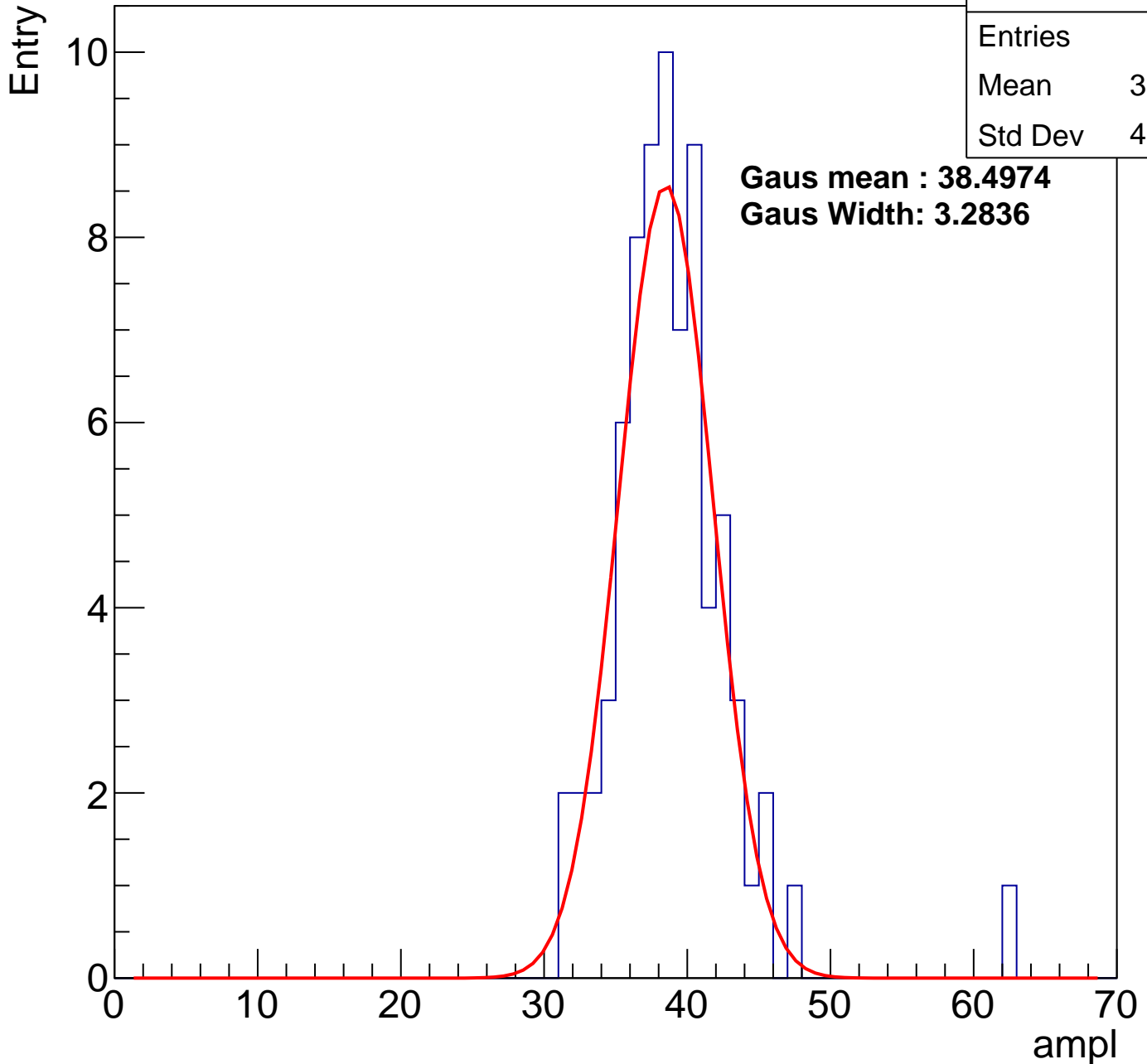
# B0L000S, U13-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	38.45
Std Dev	4.278

**Gaus mean : 38.4974**

**Gaus Width: 3.2836**



# B0L000S, U13-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	45.67
Std Dev	3.356

**Gaus mean : 46.1089**

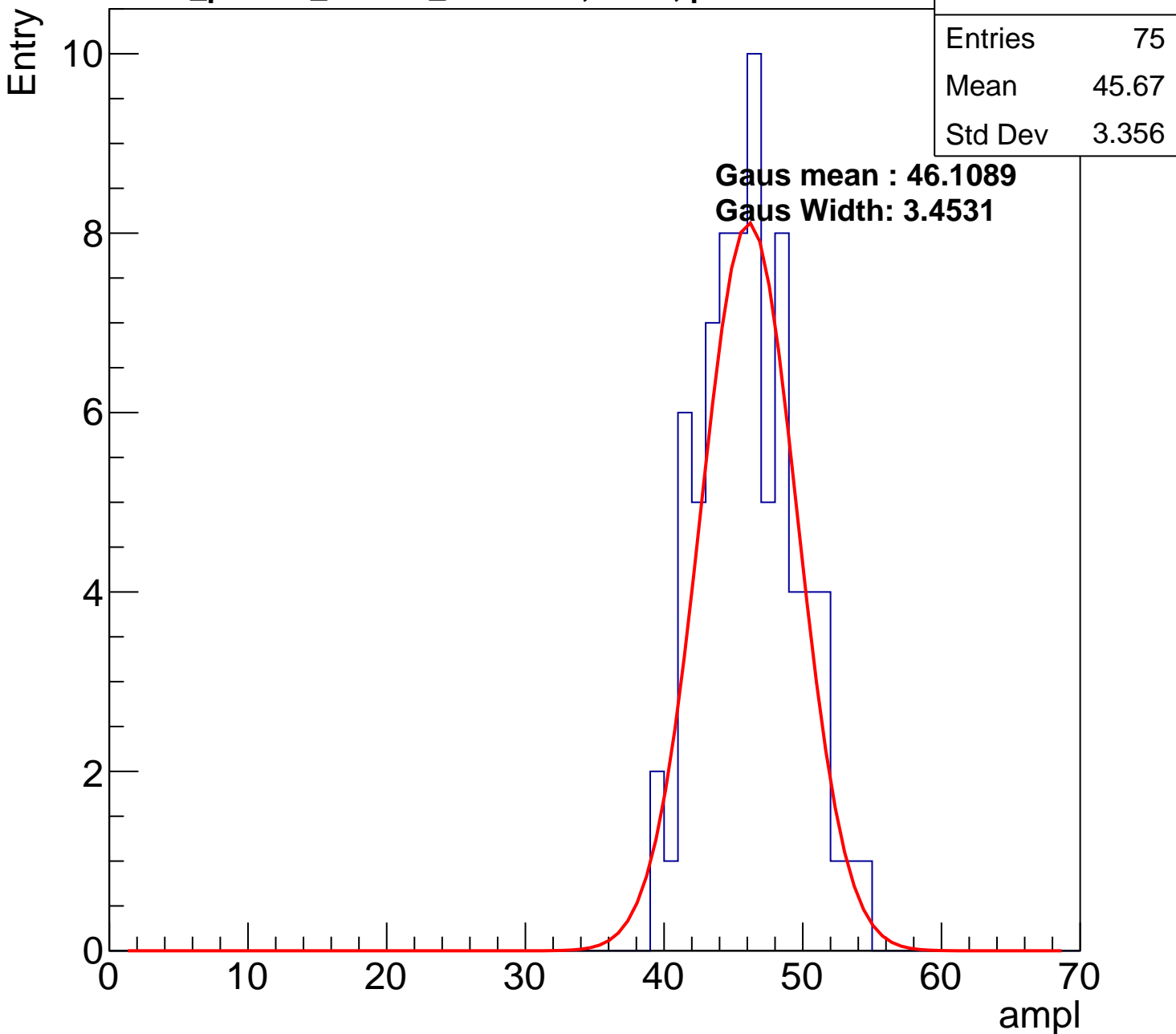
**Gaus Width: 3.4531**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

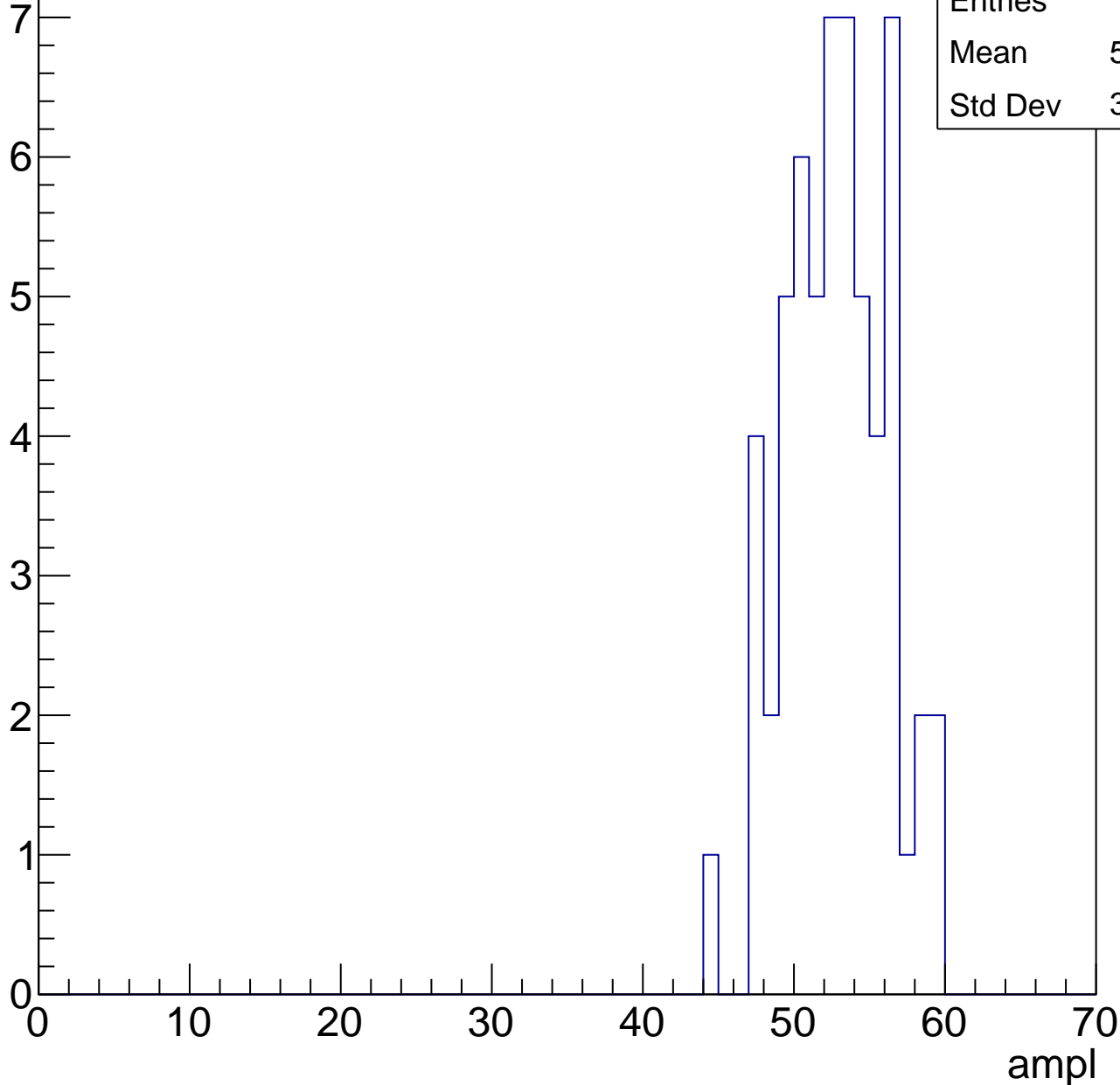


# B0L000S, U13-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

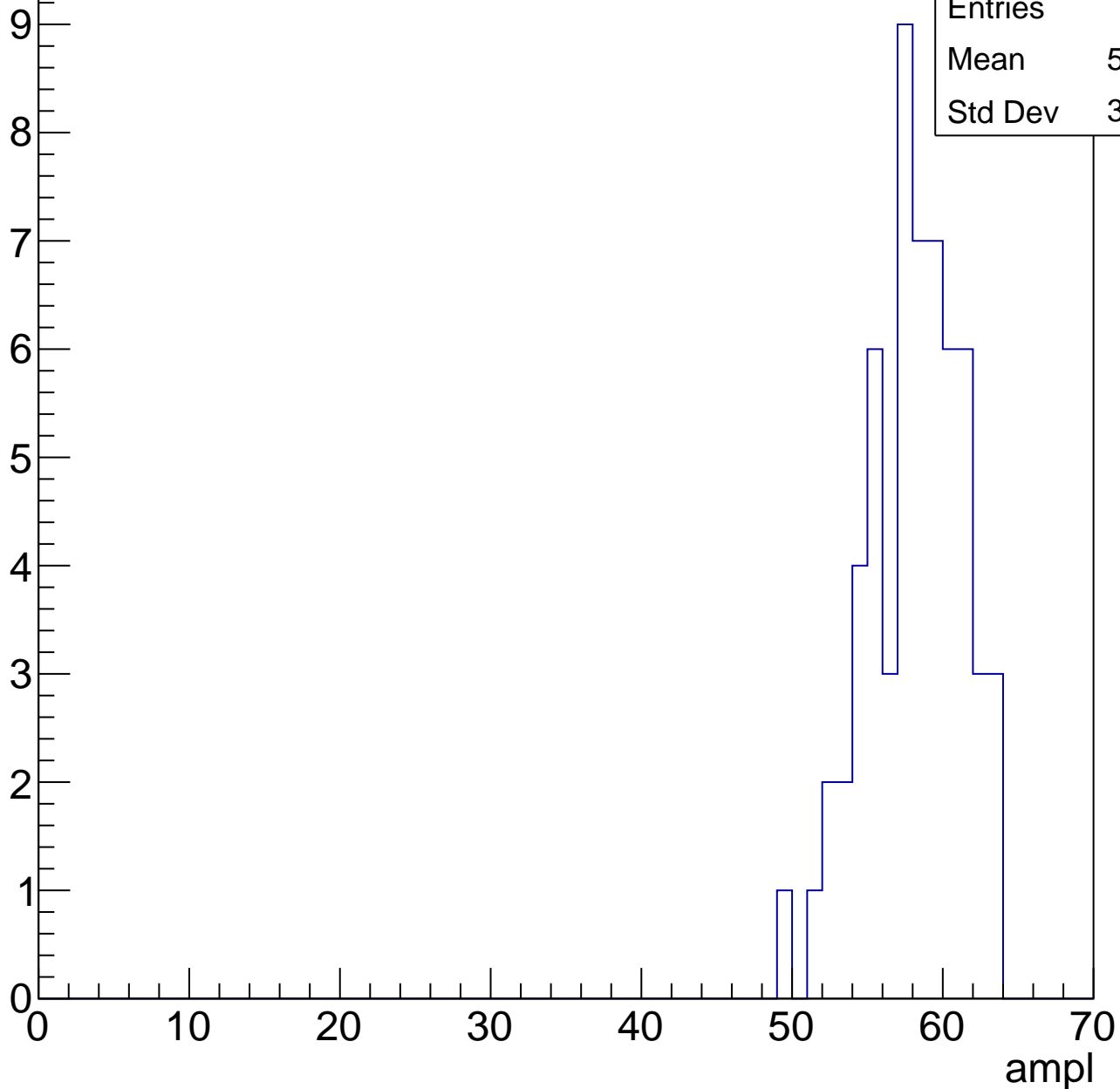
Entries	58
Mean	52.34
Std Dev	3.304



# B0L000S, U13-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



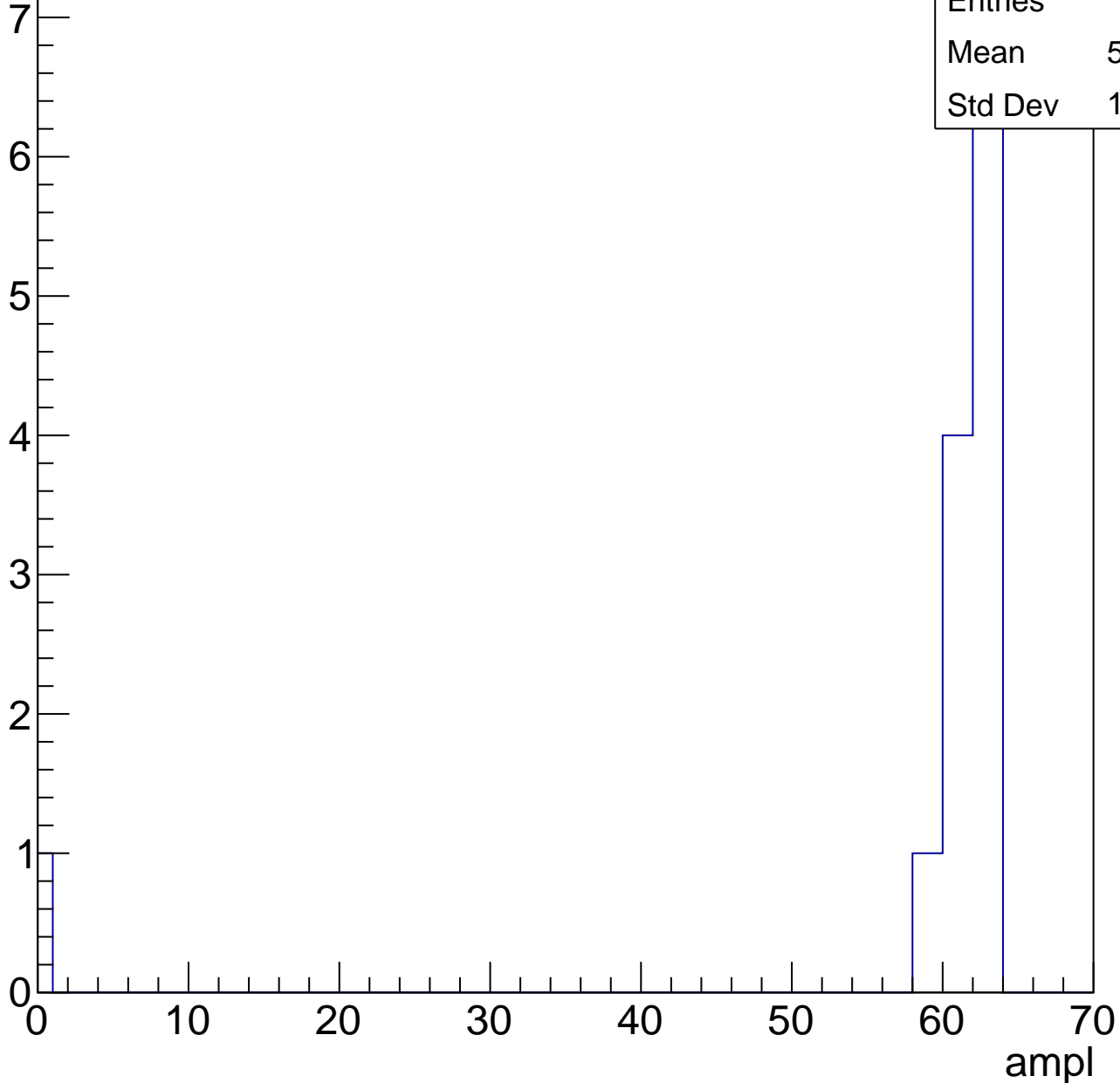
Entries	60
Mean	57.62
Std Dev	3.136

# B0L000S, U13-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	59.04
Std Dev	12.13



# B0L000S, U13-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch16, adc0

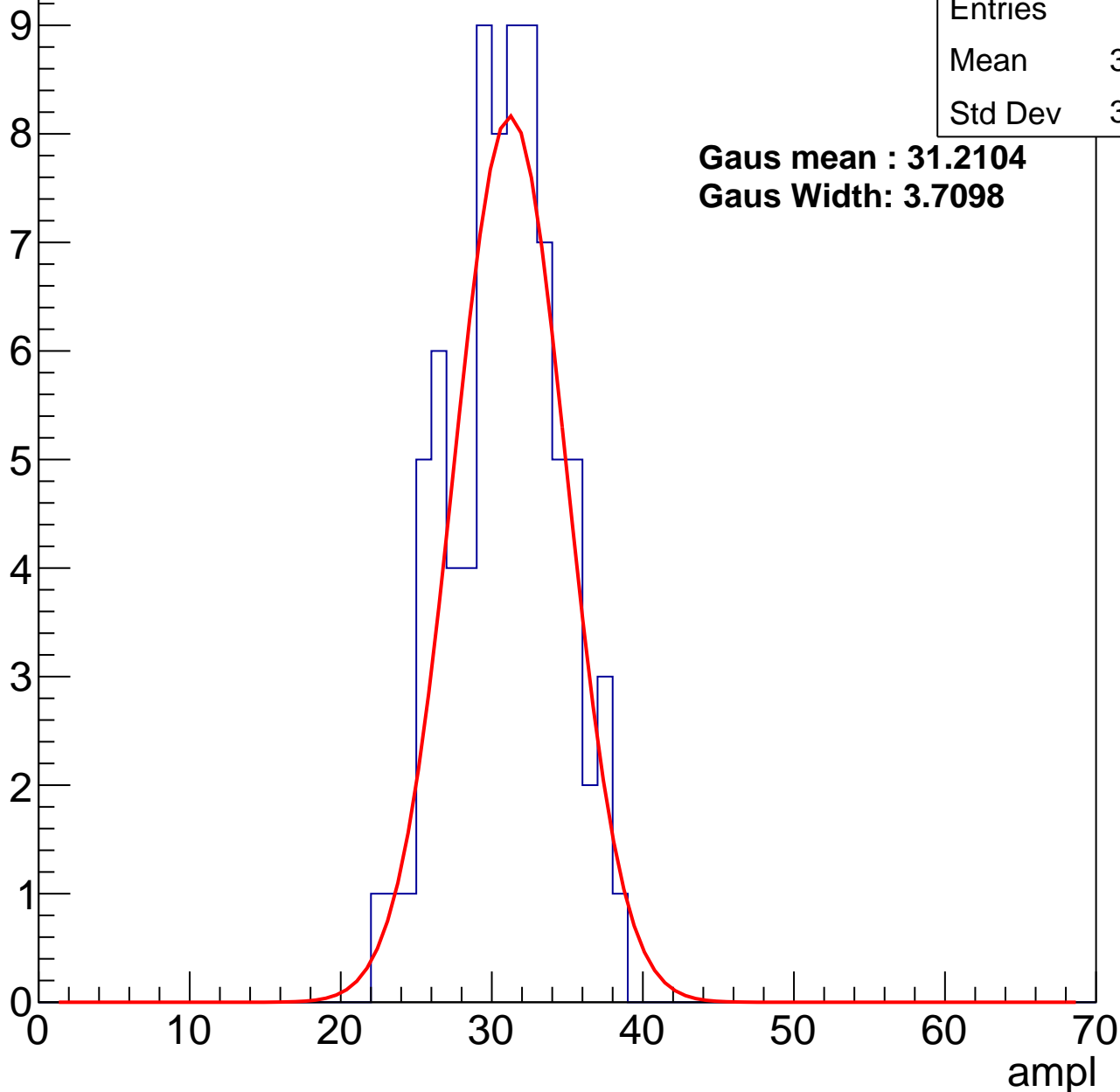
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	30.44
Std Dev	3.556

**Gaus mean : 31.2104**

**Gaus Width: 3.7098**



# B0L000S, U13-ch16, adc1

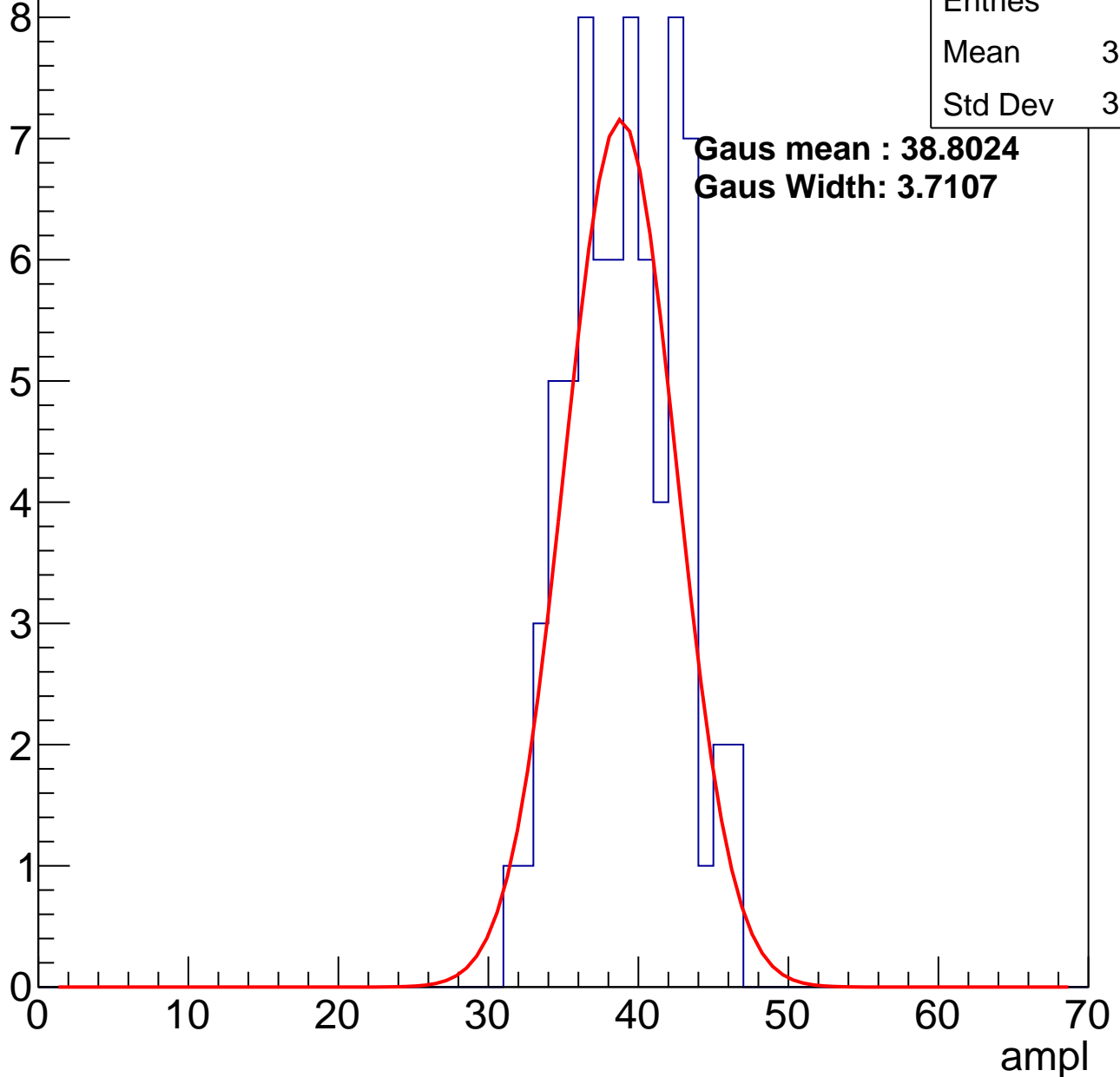
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	38.68
Std Dev	3.553

**Gaus mean : 38.8024**

**Gaus Width: 3.7107**



# B0L000S, U13-ch16, adc2

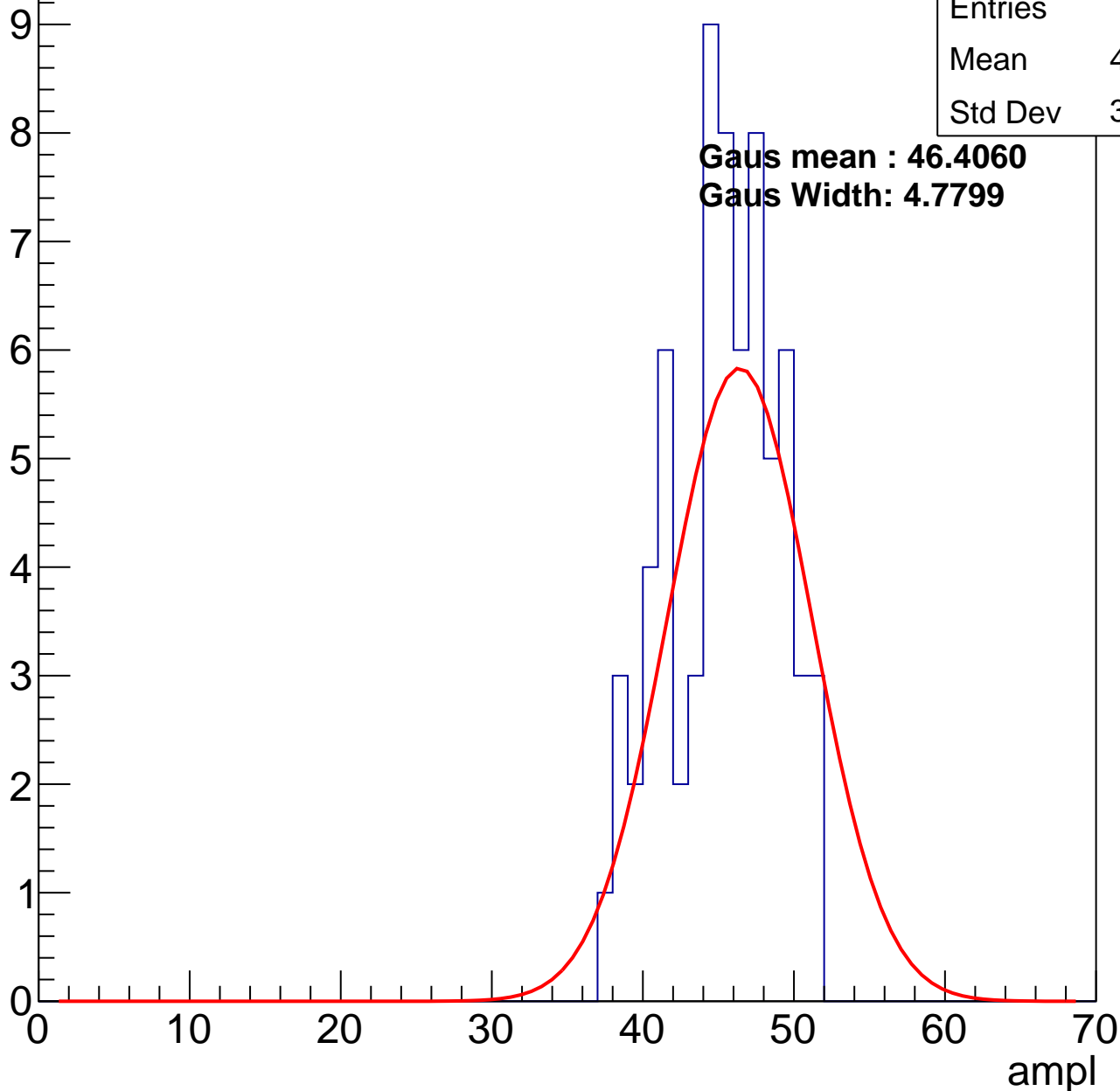
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.83
Std Dev	3.567

**Gaus mean : 46.4060**

**Gaus Width: 4.7799**

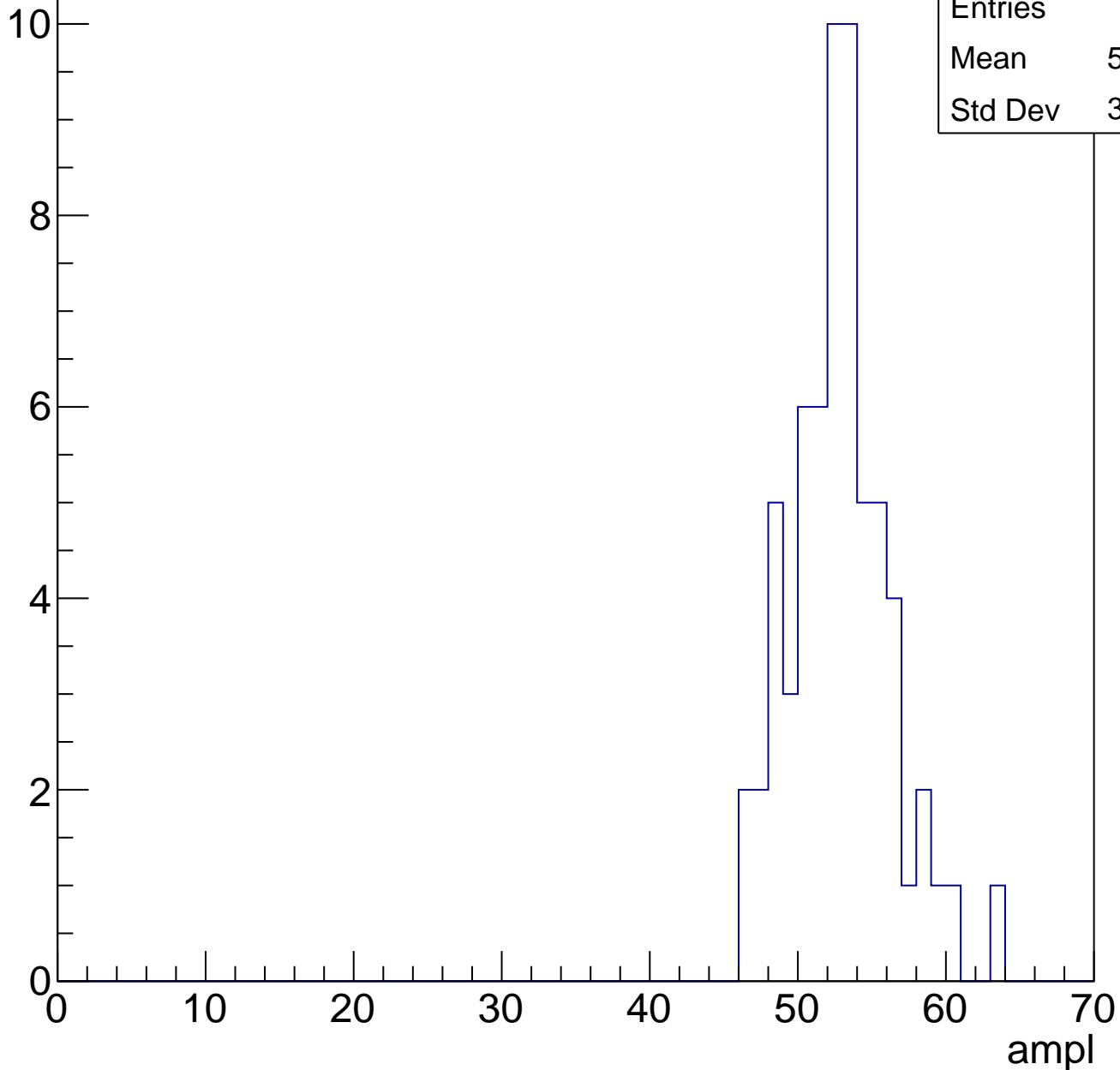


# B0L000S, U13-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	52.39
Std Dev	3.352

Entry

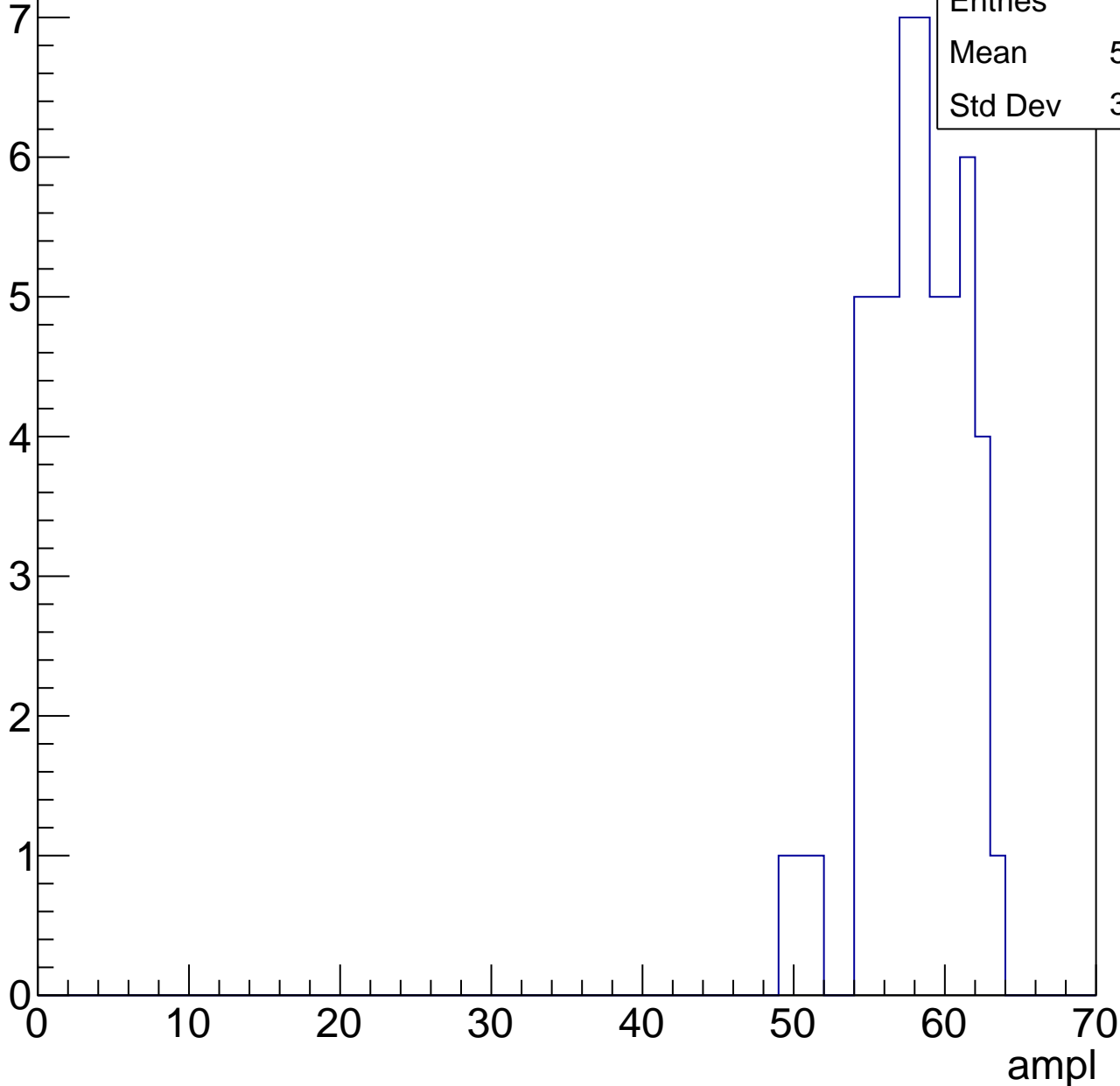


# B0L000S, U13-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.58
Std Dev	3.086

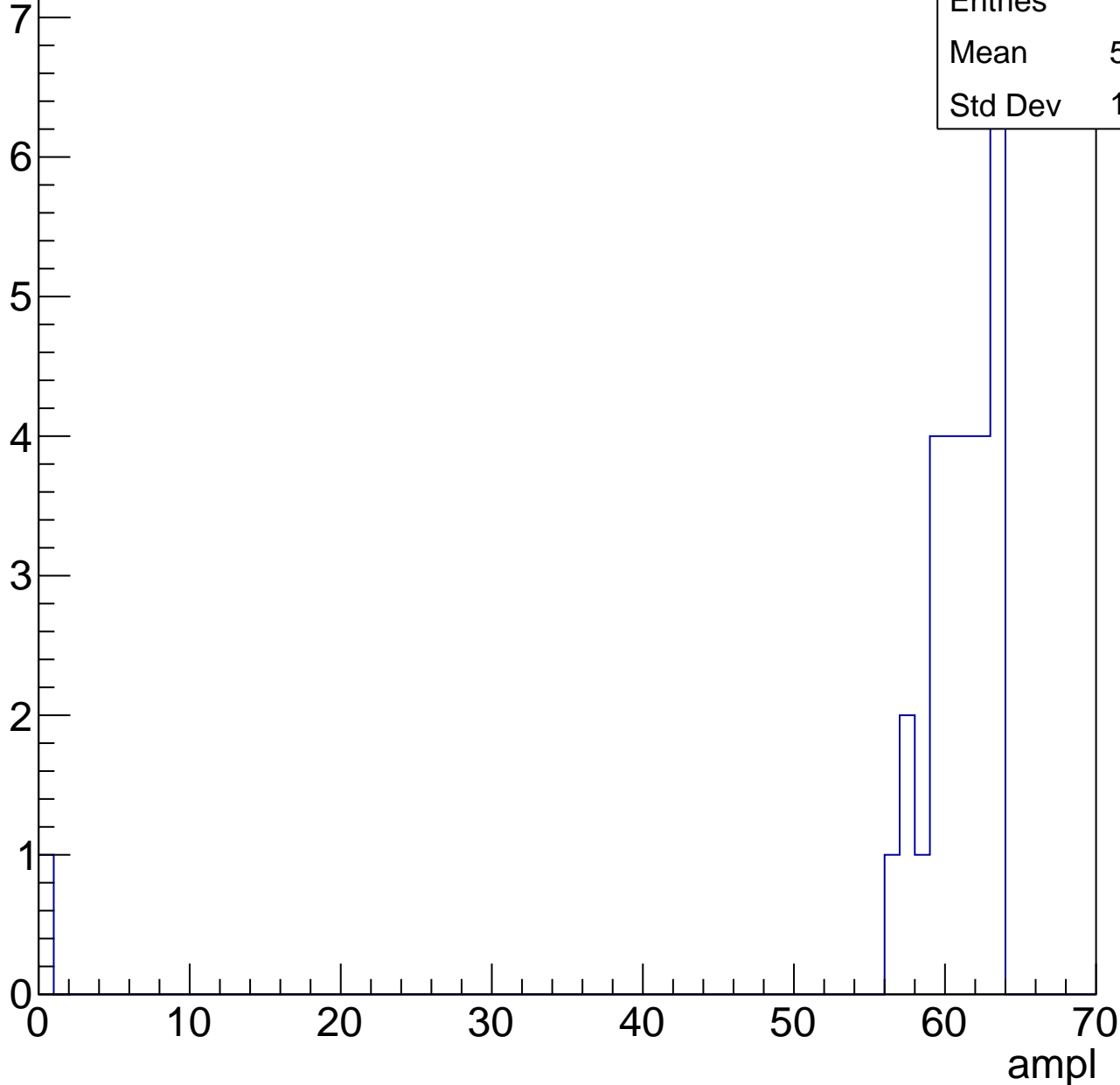


# B0L000S, U13-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.46
Std Dev	11.43



# B0L000S, U13-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch17, adc0

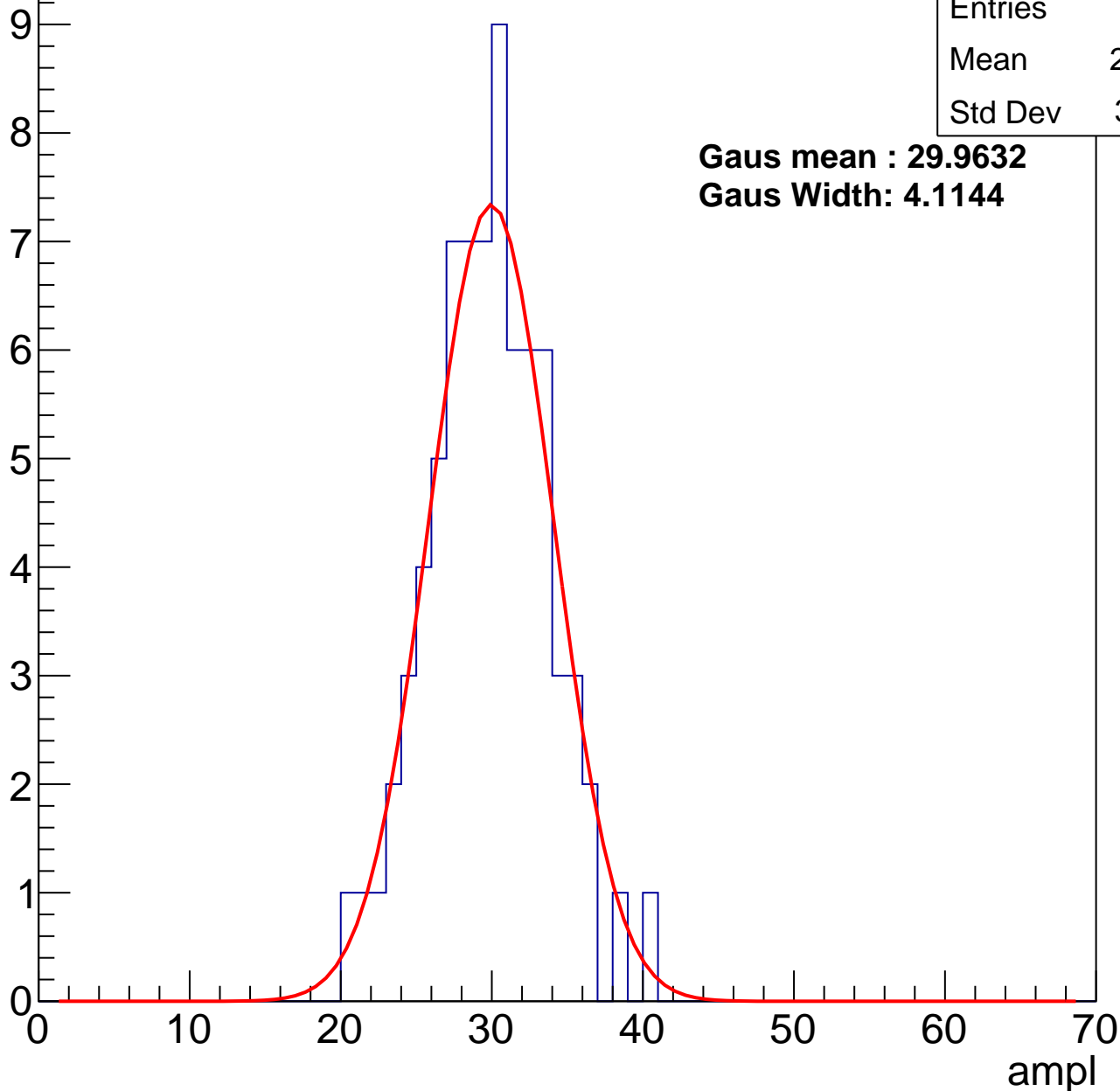
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.36
Std Dev	3.901

**Gaus mean : 29.9632**

**Gaus Width: 4.1144**



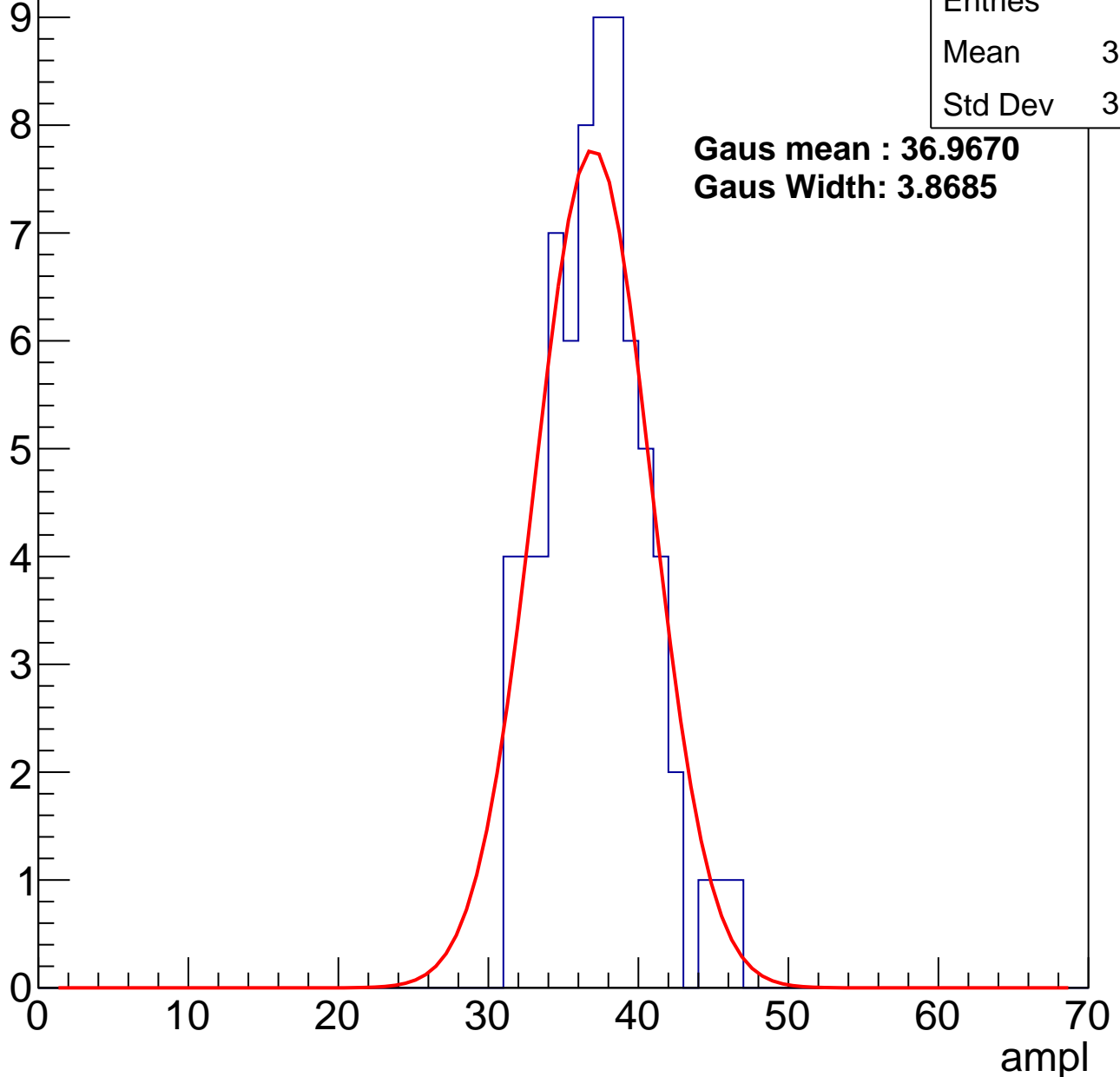
# B0L000S, U13-ch17, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.79
Std Dev	3.327

**Gaus mean : 36.9670**  
**Gaus Width: 3.8685**



# B0L000S, U13-ch17, adc2

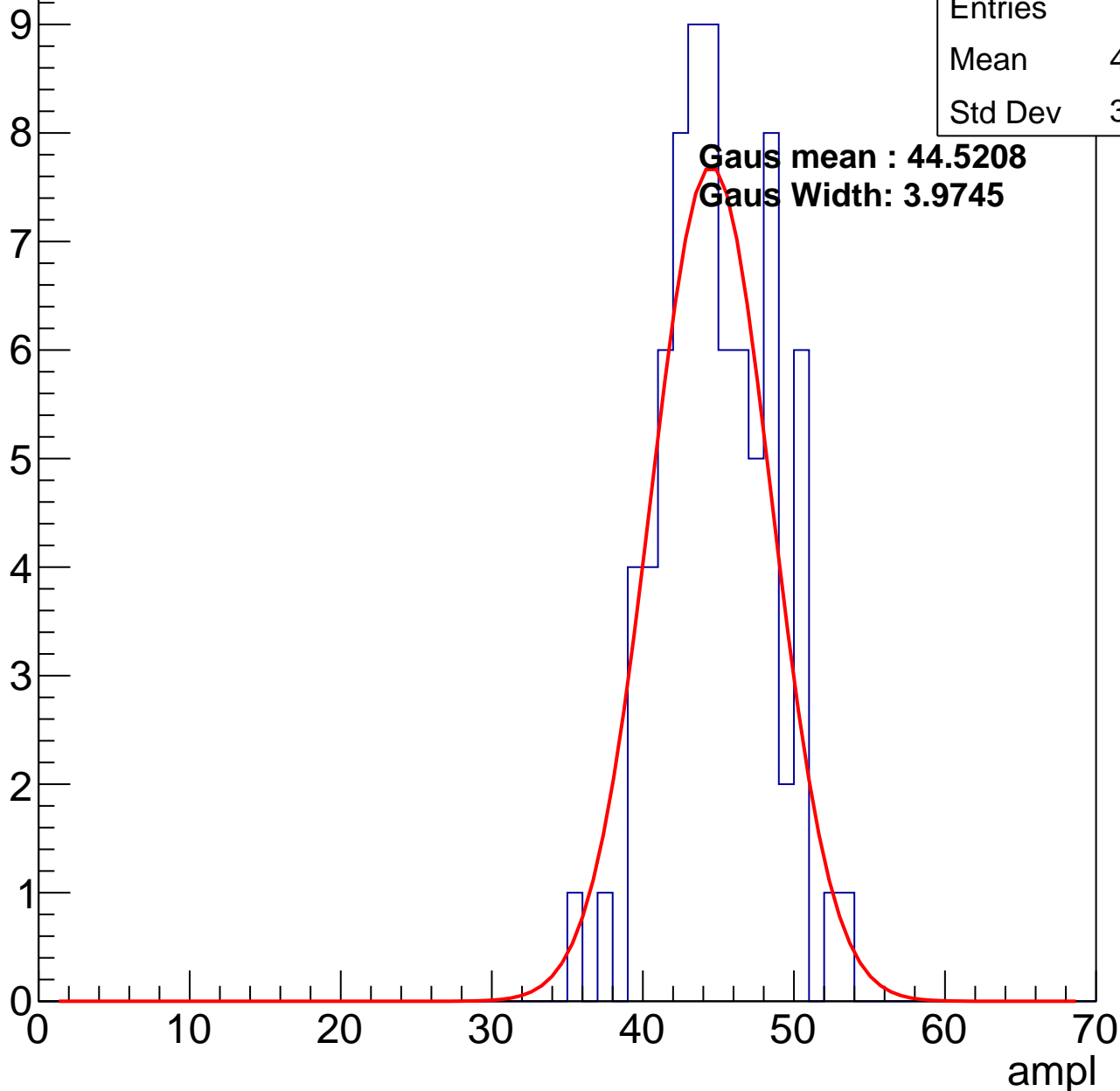
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	44.43
Std Dev	3.584

**Gaus mean : 44.5208**

**Gaus Width: 3.9745**

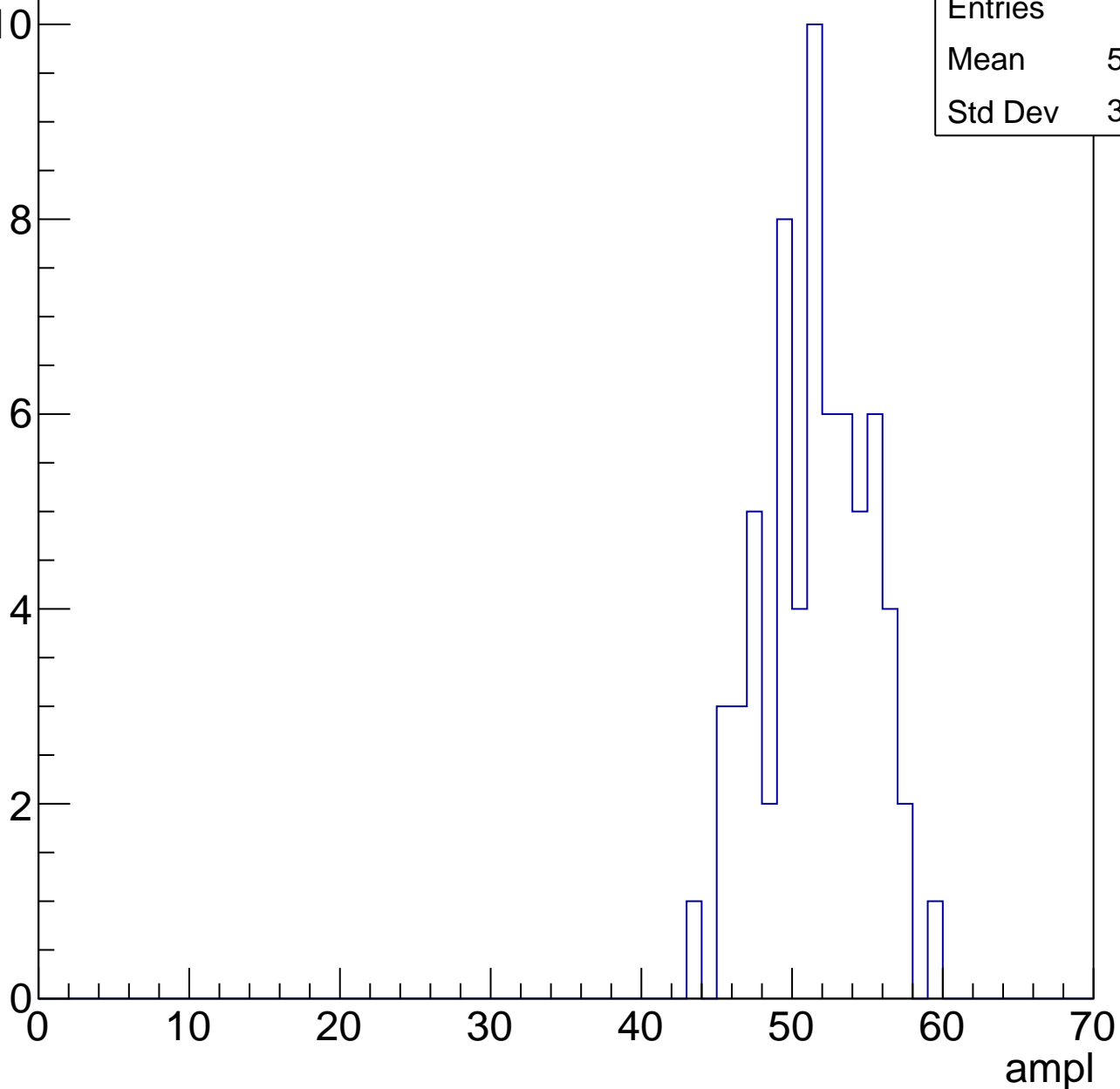


# B0L000S, U13-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	51.15
Std Dev	3.452

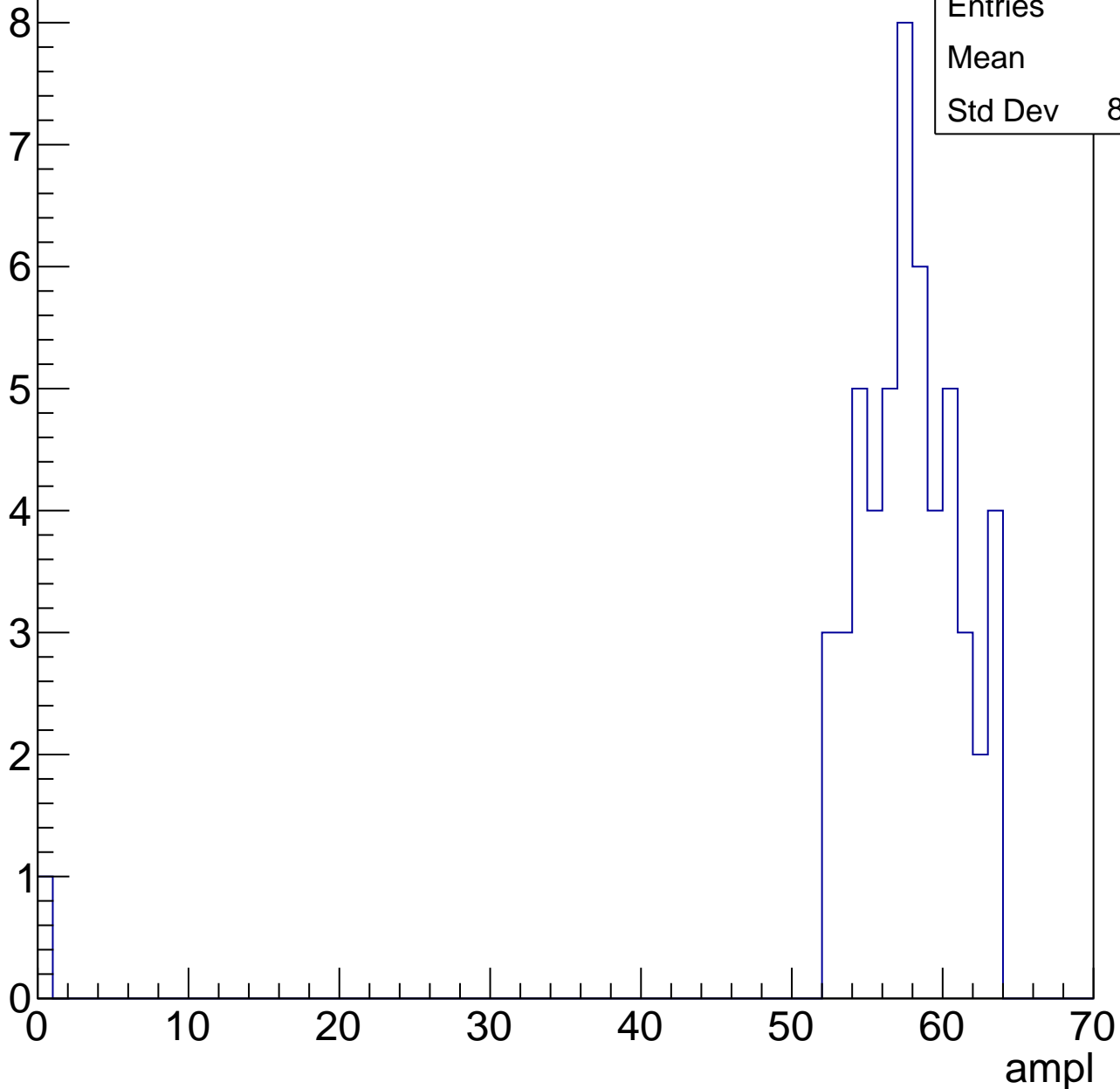


# B0L000S, U13-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.3
Std Dev	8.379

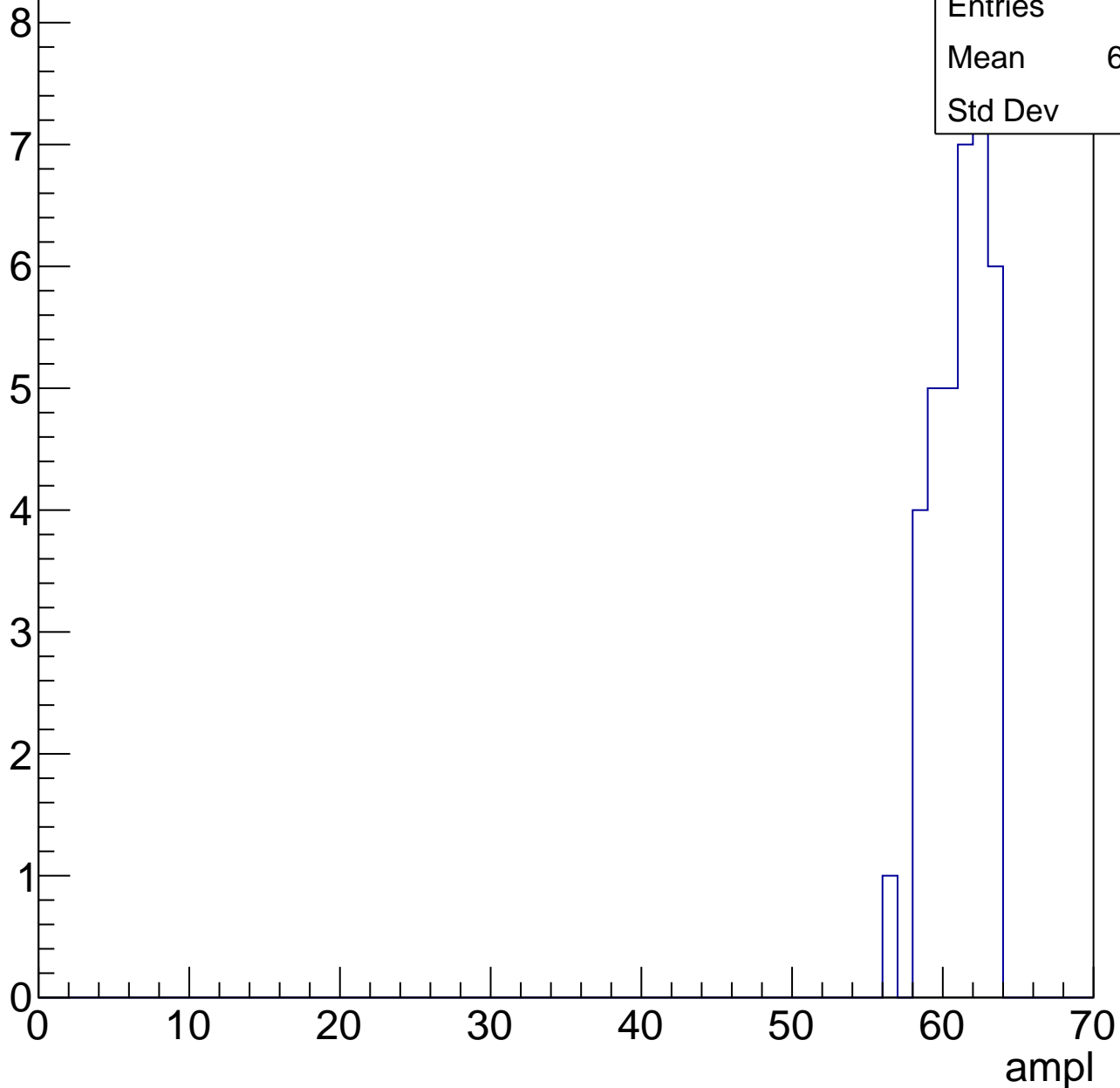


# B0L000S, U13-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	60.67
Std Dev	1.78



# B0L000S, U13-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U13-ch18, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	29.74
Std Dev	5.918

**Gaus mean : 30.5709**

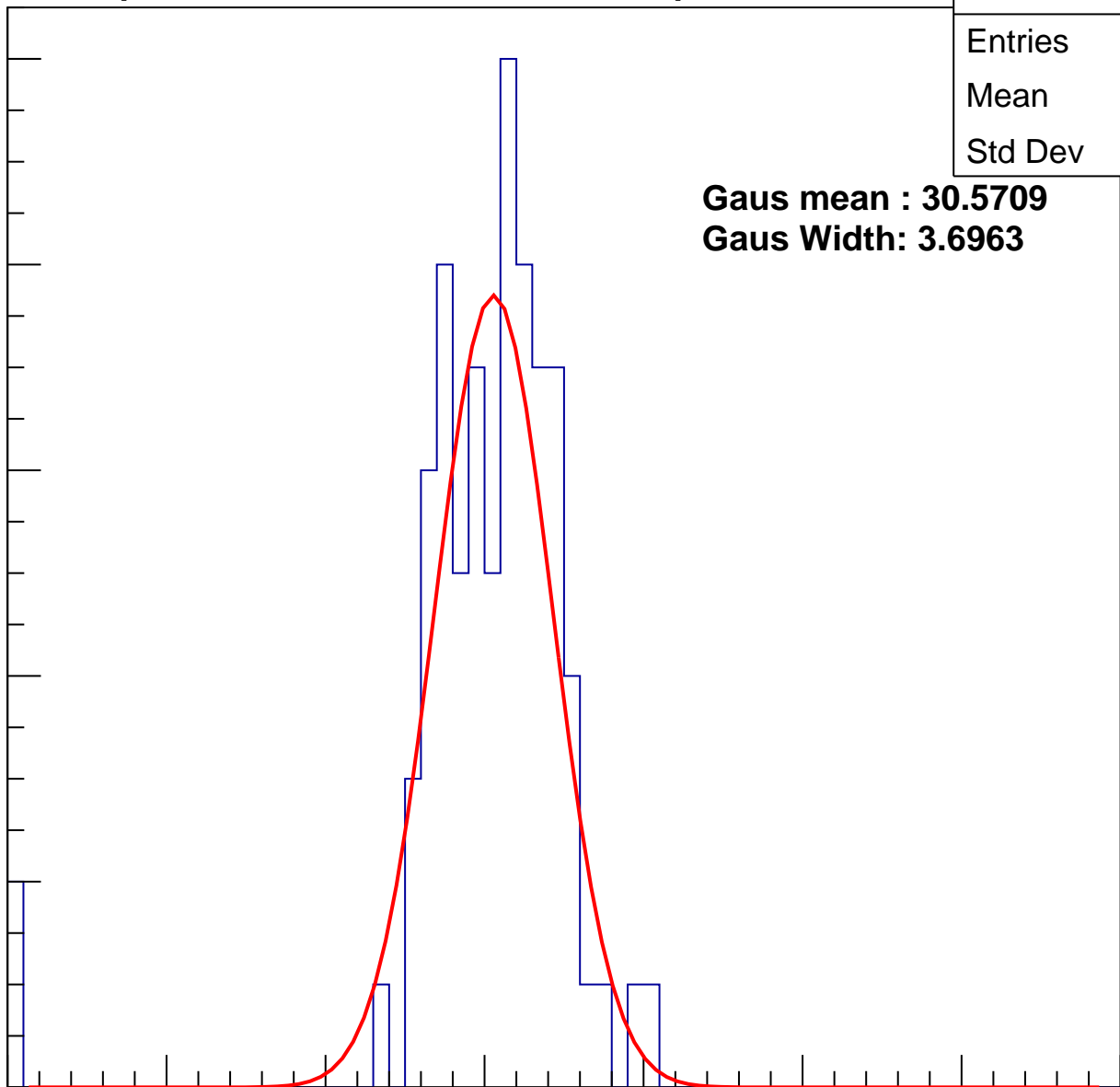
**Gaus Width: 3.6963**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch18, adc1

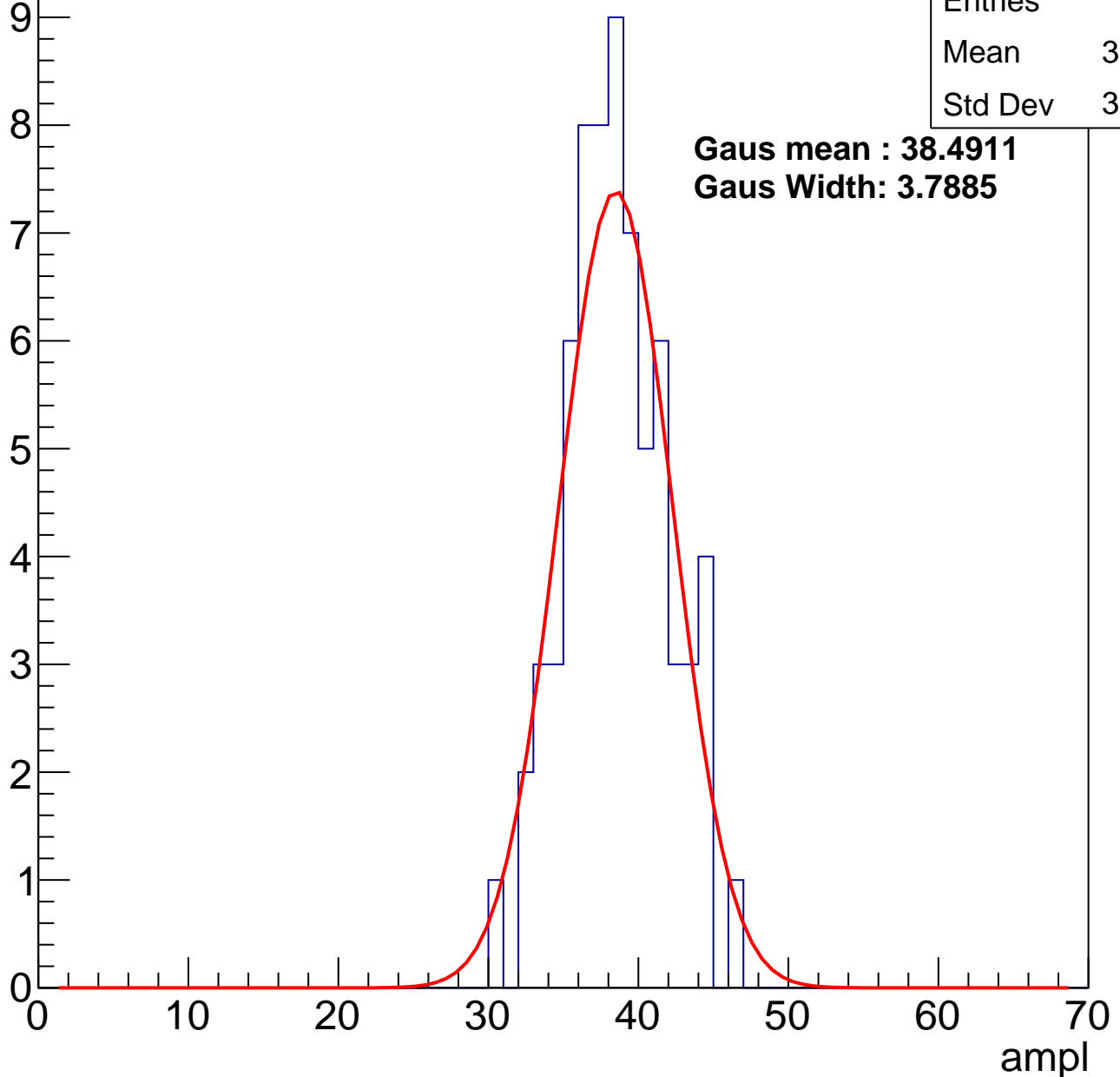
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	38.07
Std Dev	3.329

**Gaus mean : 38.4911**

**Gaus Width: 3.7885**



# B0L000S, U13-ch18, adc2

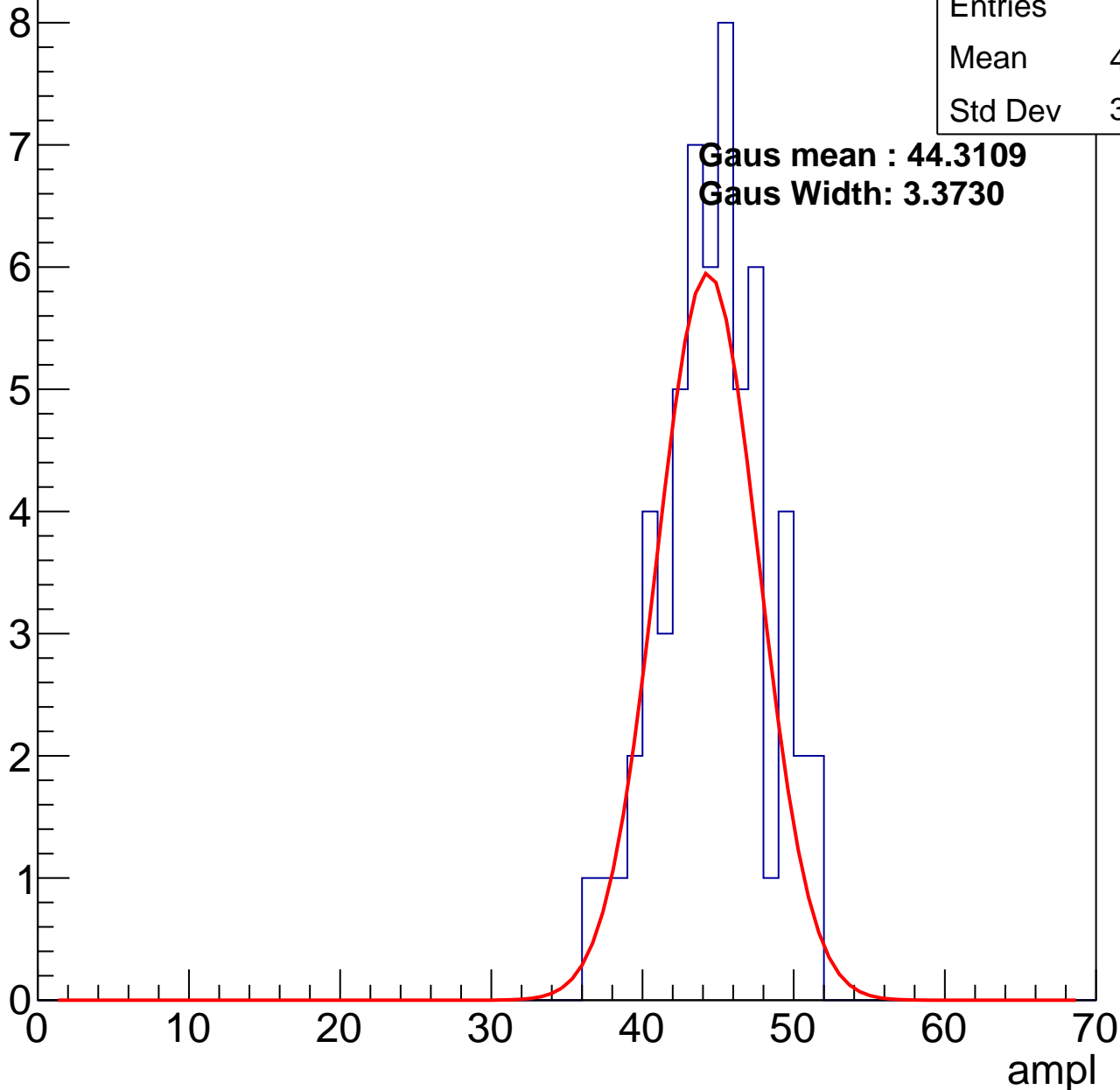
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.22
Std Dev	3.429

**Gaus mean : 44.3109**

**Gaus Width: 3.3730**

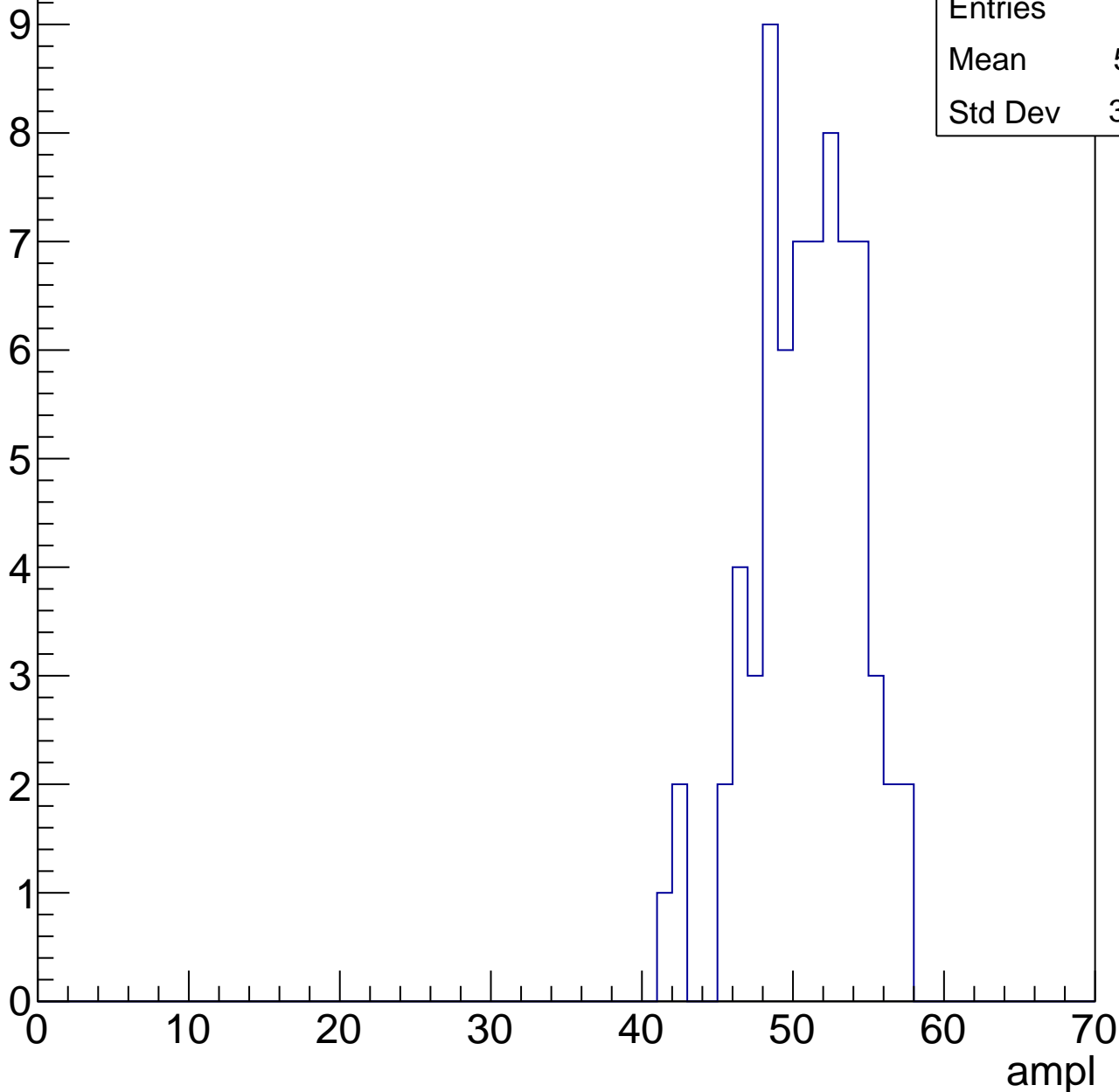


# B0L000S, U13-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.41
Std Dev	3.462

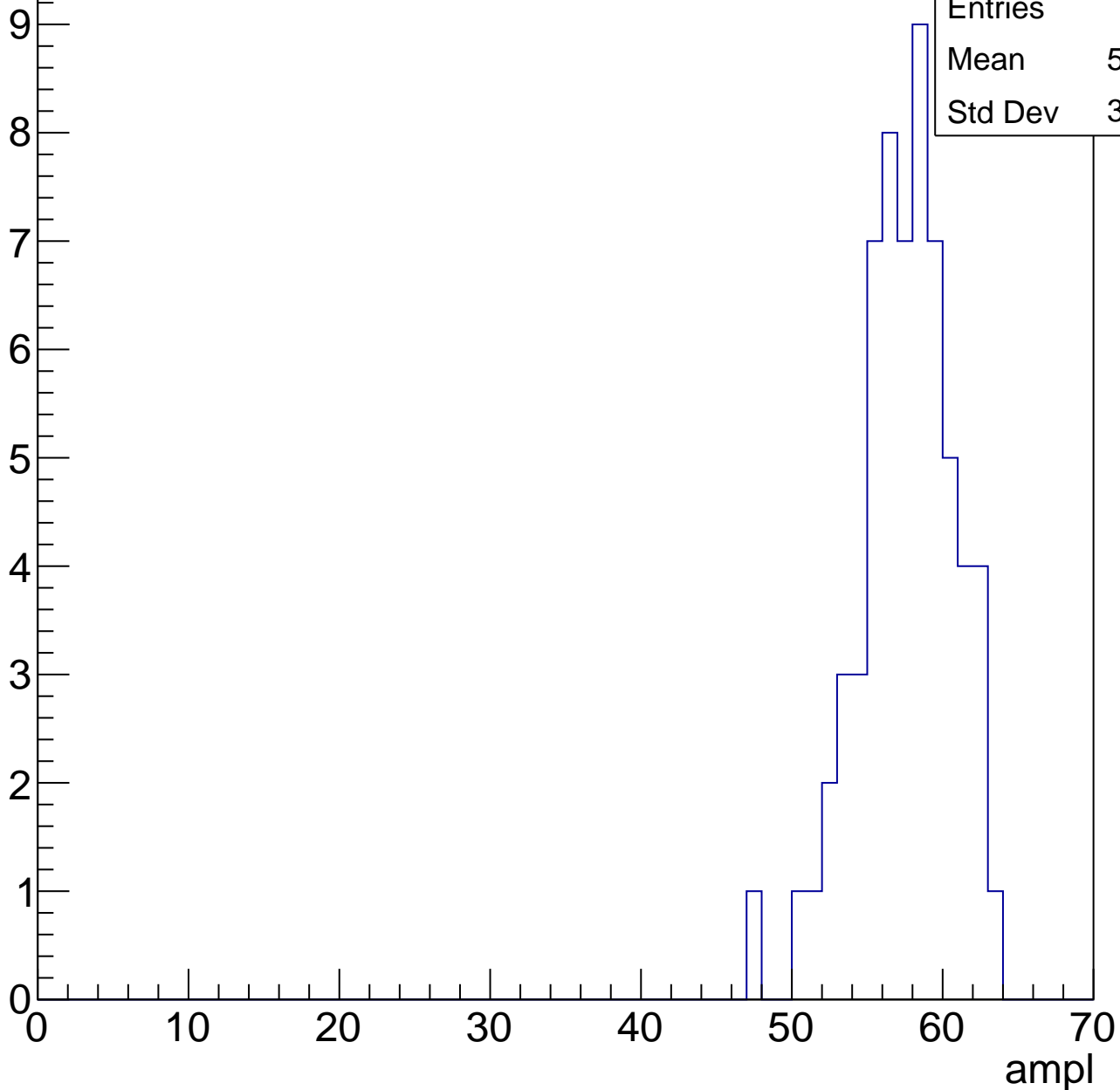


# B0L000S, U13-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	57.06
Std Dev	3.157

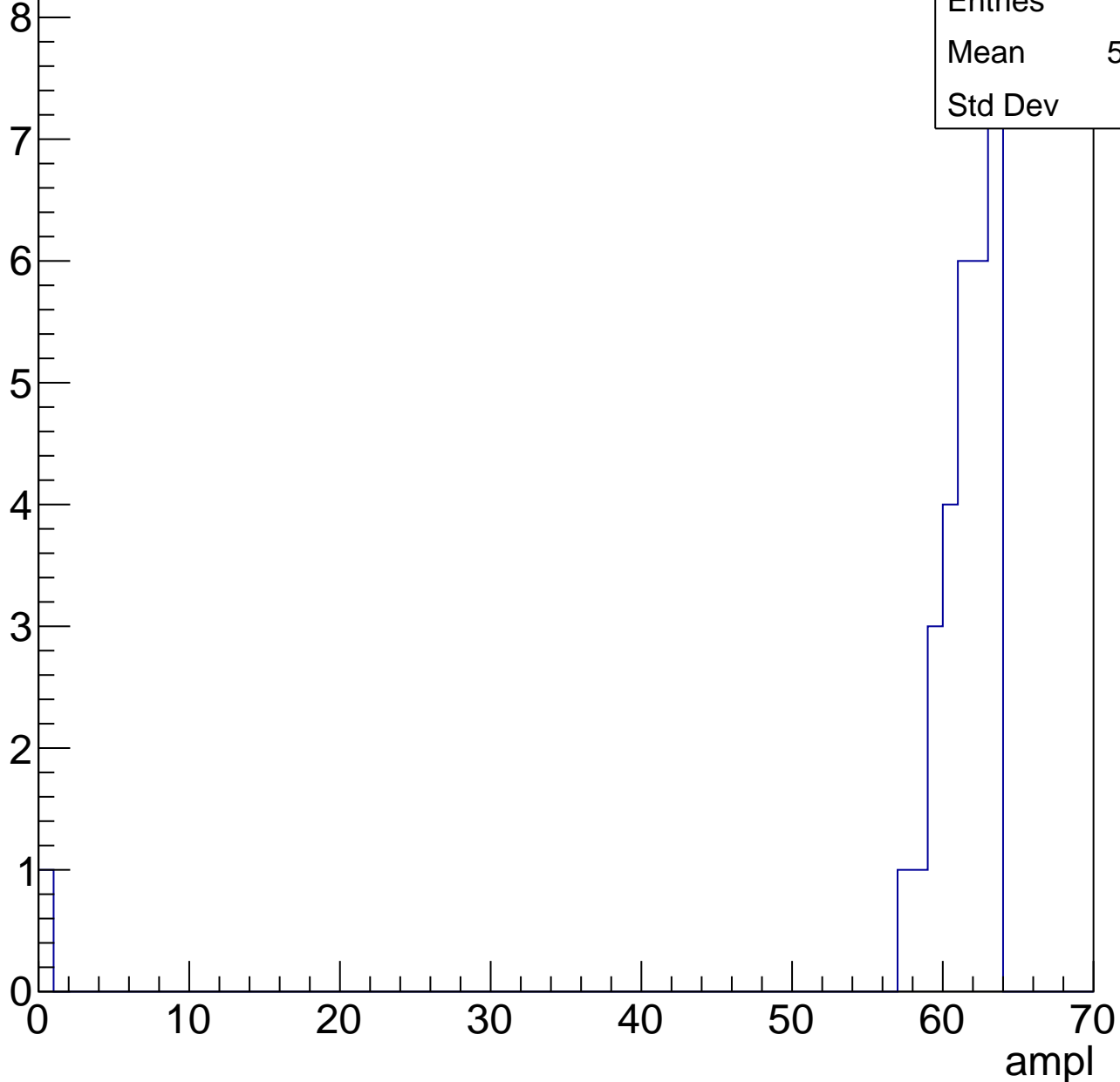


# B0L000S, U13-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

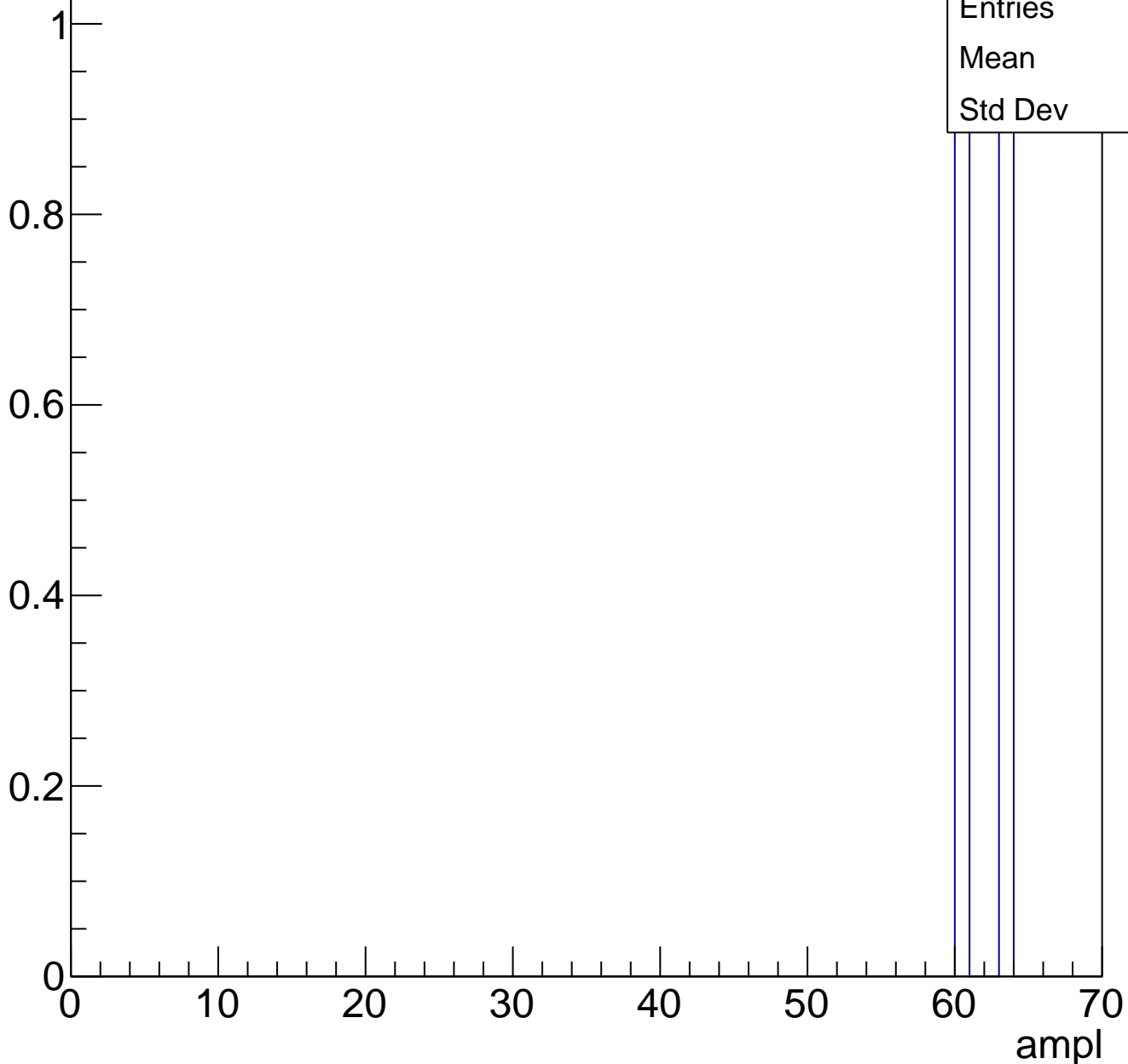
Entries	30
Mean	59.13
Std Dev	11.1



# B0L000S, U13-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7
Std Dev	9.899

# B0L000S, U13-ch19, adc0

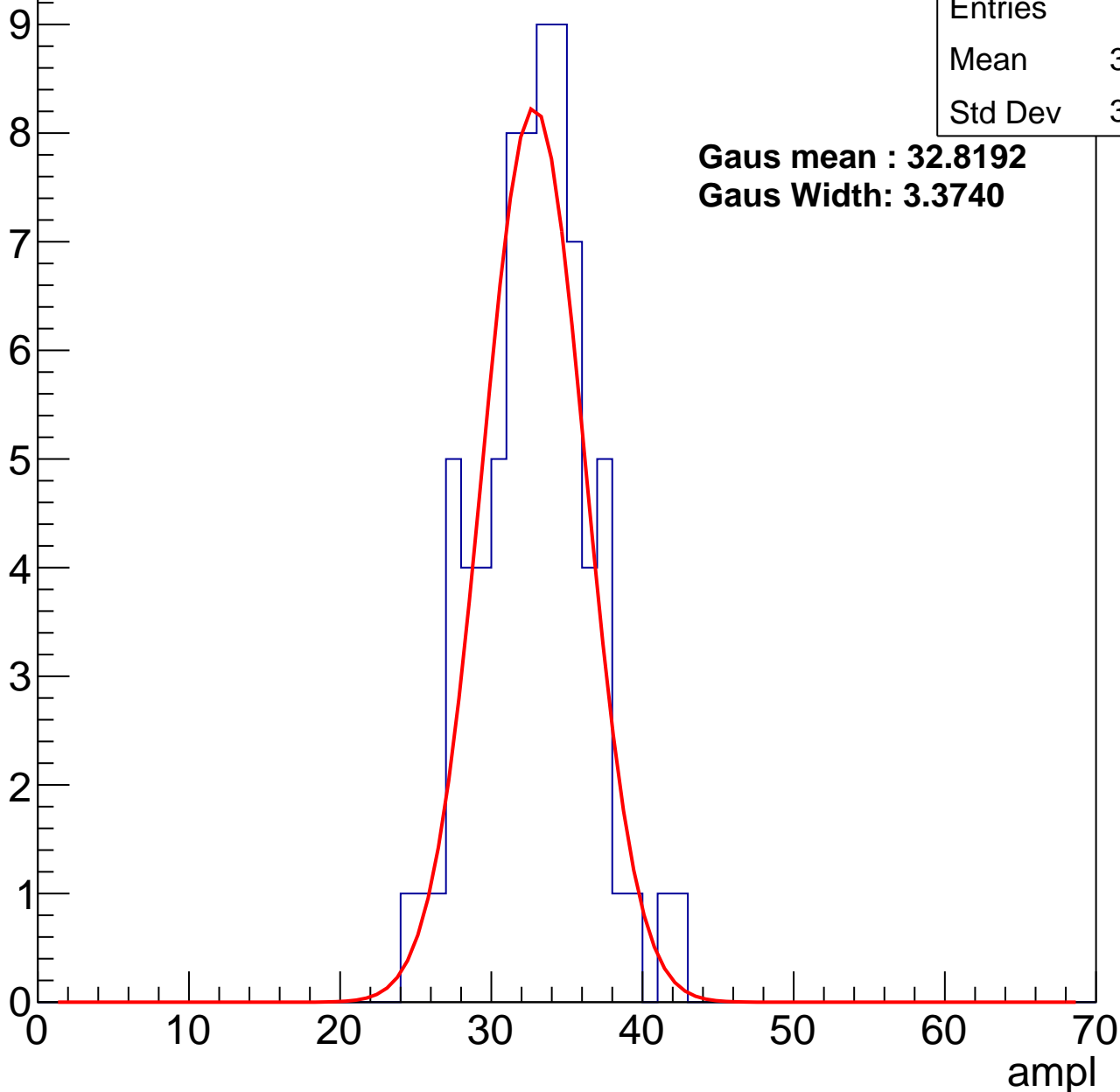
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	32.39
Std Dev	3.566

**Gaus mean : 32.8192**

**Gaus Width: 3.3740**



# B0L000S, U13-ch19, adc1

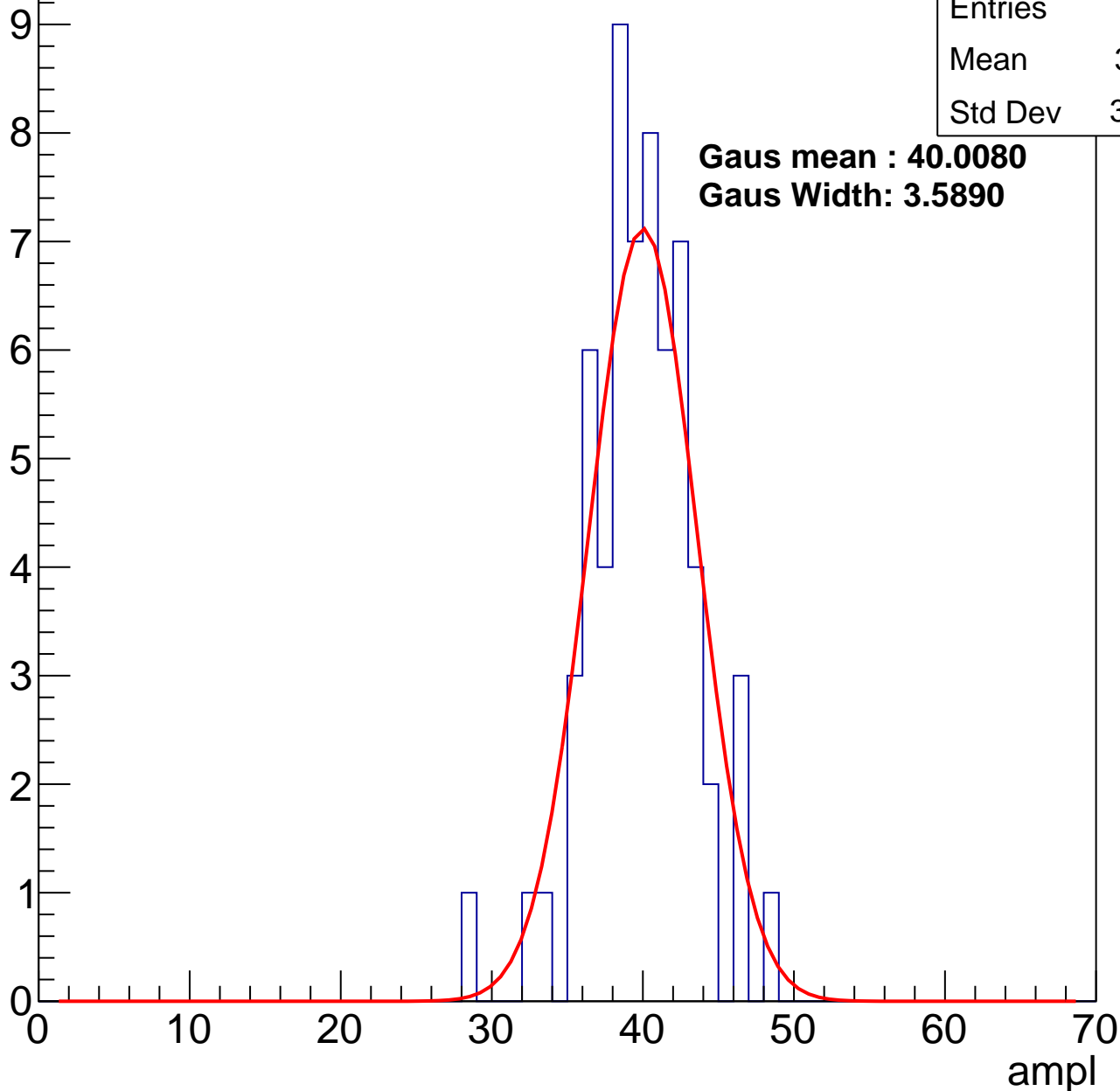
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	39.41
Std Dev	3.472

**Gaus mean : 40.0080**

**Gaus Width: 3.5890**



# B0L000S, U13-ch19, adc2

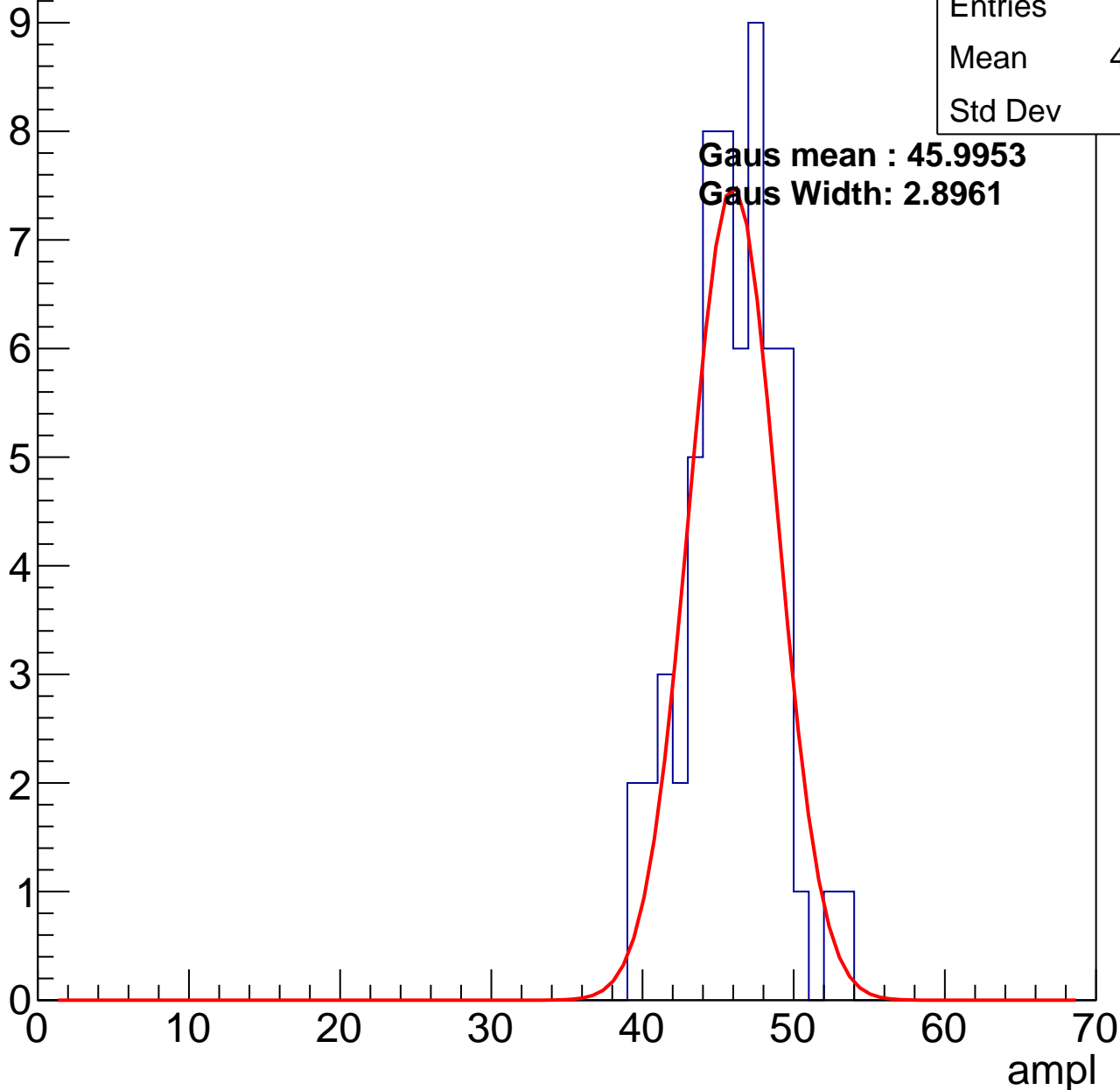
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	45.47
Std Dev	2.98

**Gaus mean : 45.9953**

**Gaus Width: 2.8961**



# B0L000S, U13-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	52.18
Std Dev	3.761

Entry

10

8

6

4

2

0

0

10

20

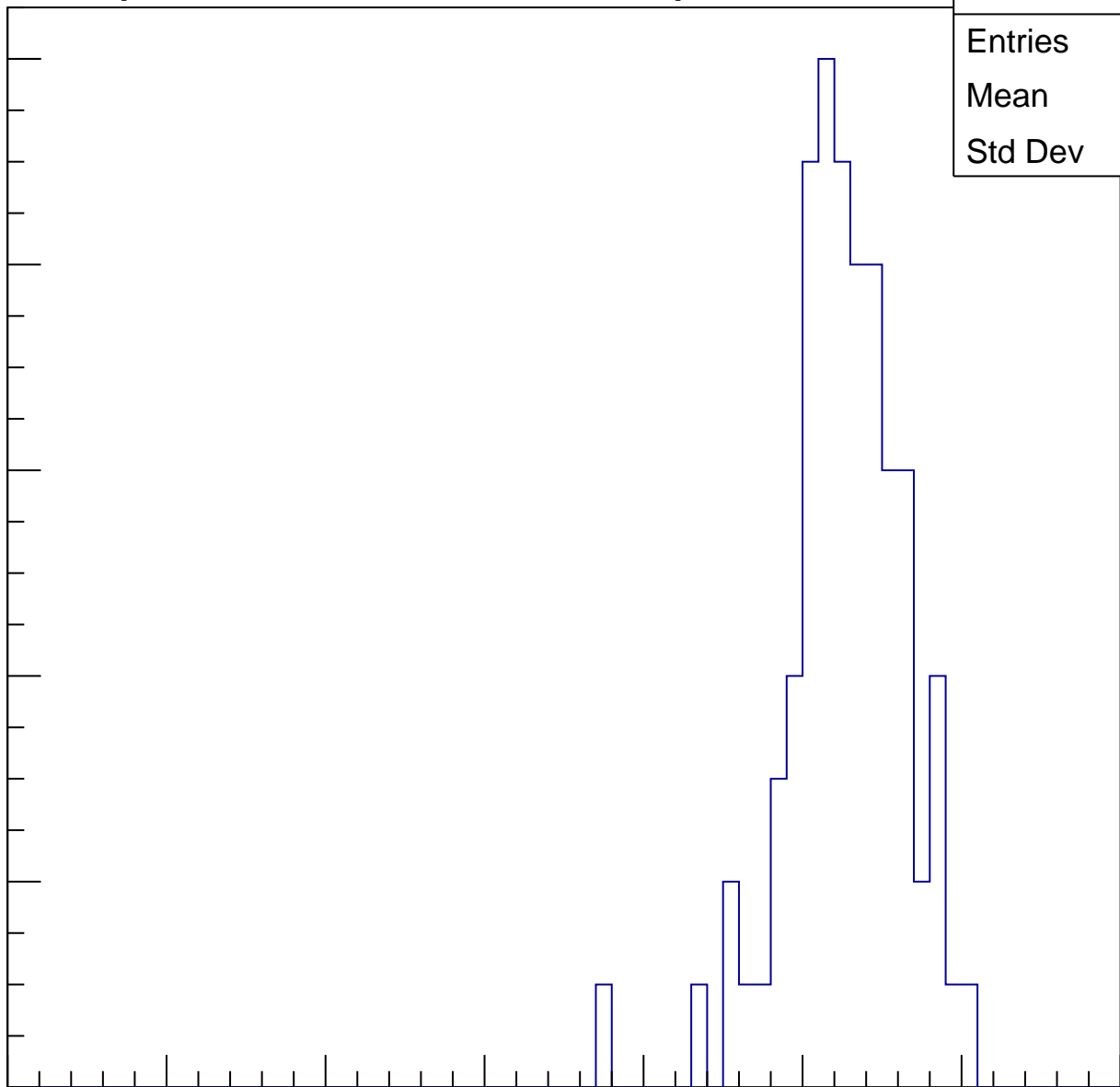
30

40

50

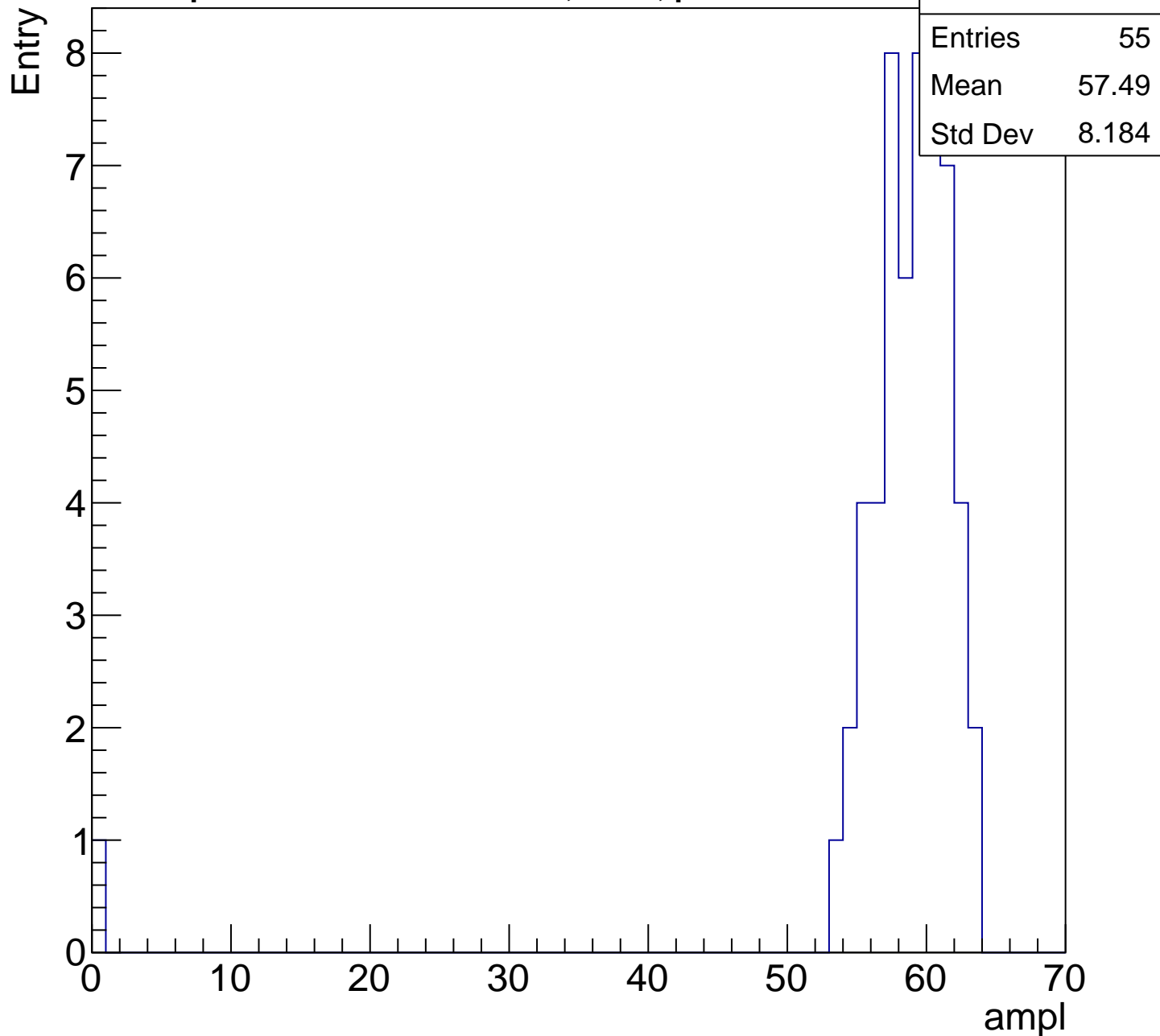
60

ampl



# B0L000S, U13-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

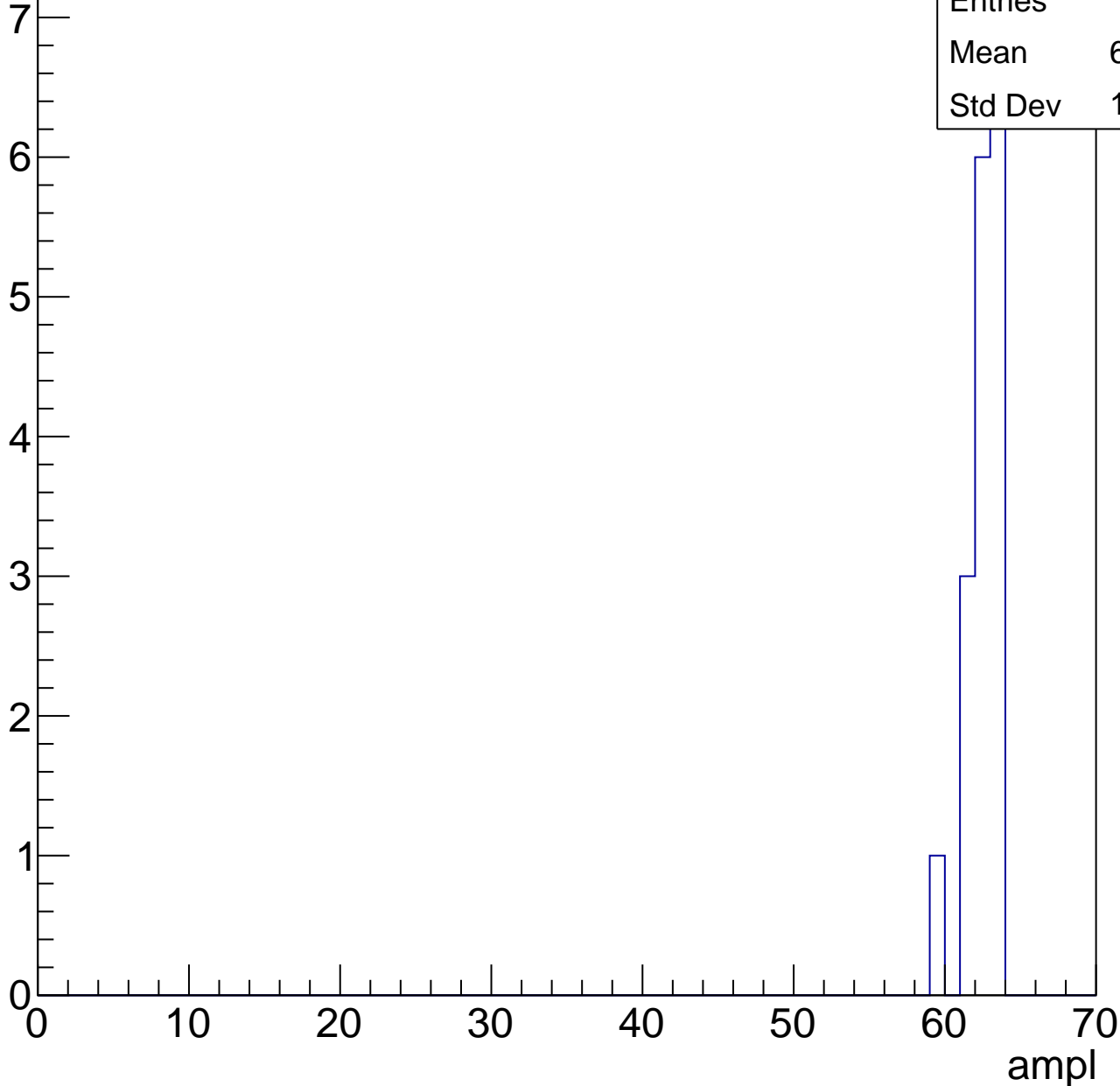


# B0L000S, U13-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	62.06
Std Dev	1.056



# B0L000S, U13-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch20, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	32.93
Std Dev	3.307

**Gaus mean : 33.4495**

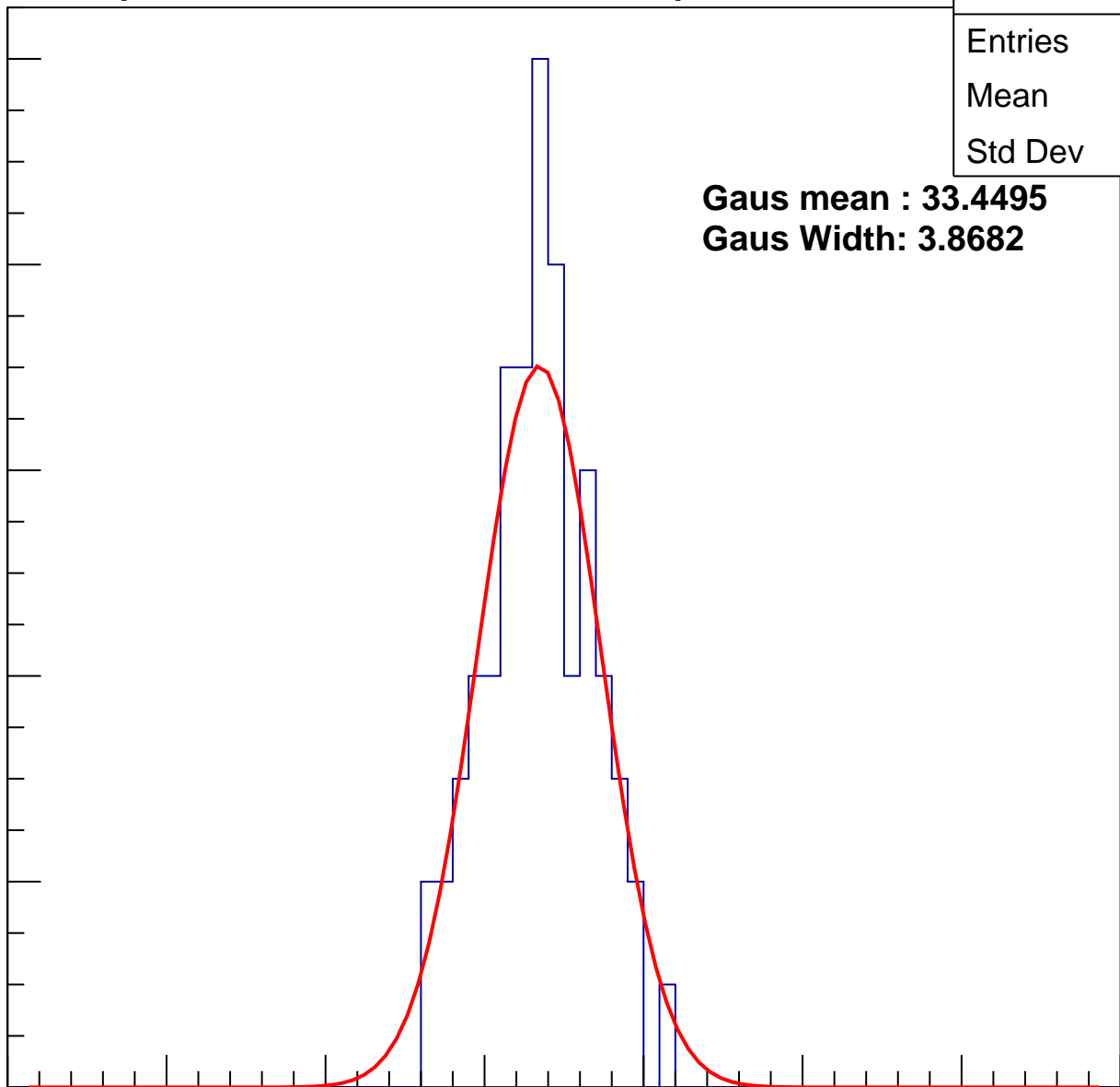
**Gaus Width: 3.8682**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch20, adc1

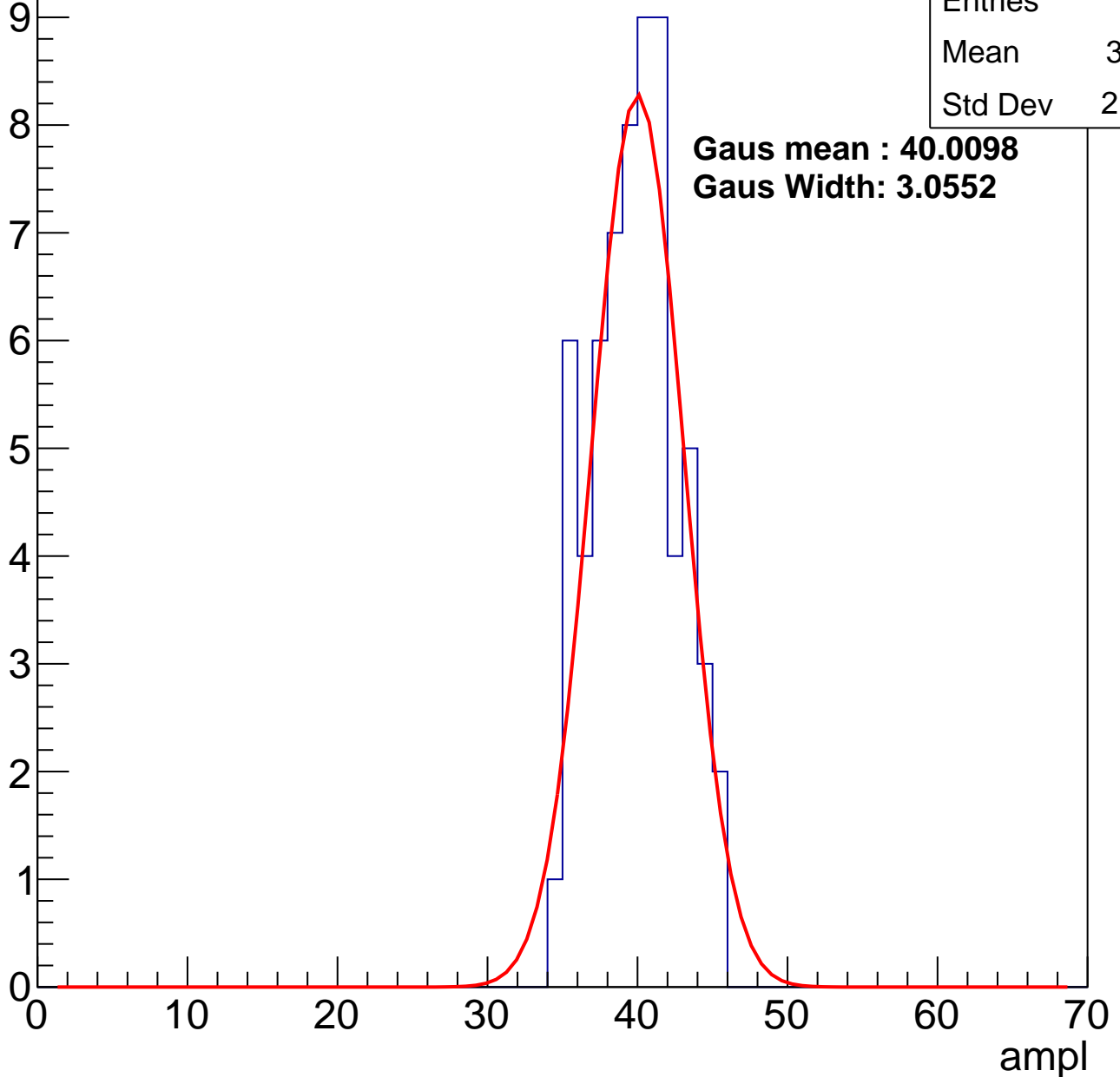
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	39.41
Std Dev	2.754

**Gaus mean : 40.0098**

**Gaus Width: 3.0552**



# B0L000S, U13-ch20, adc2

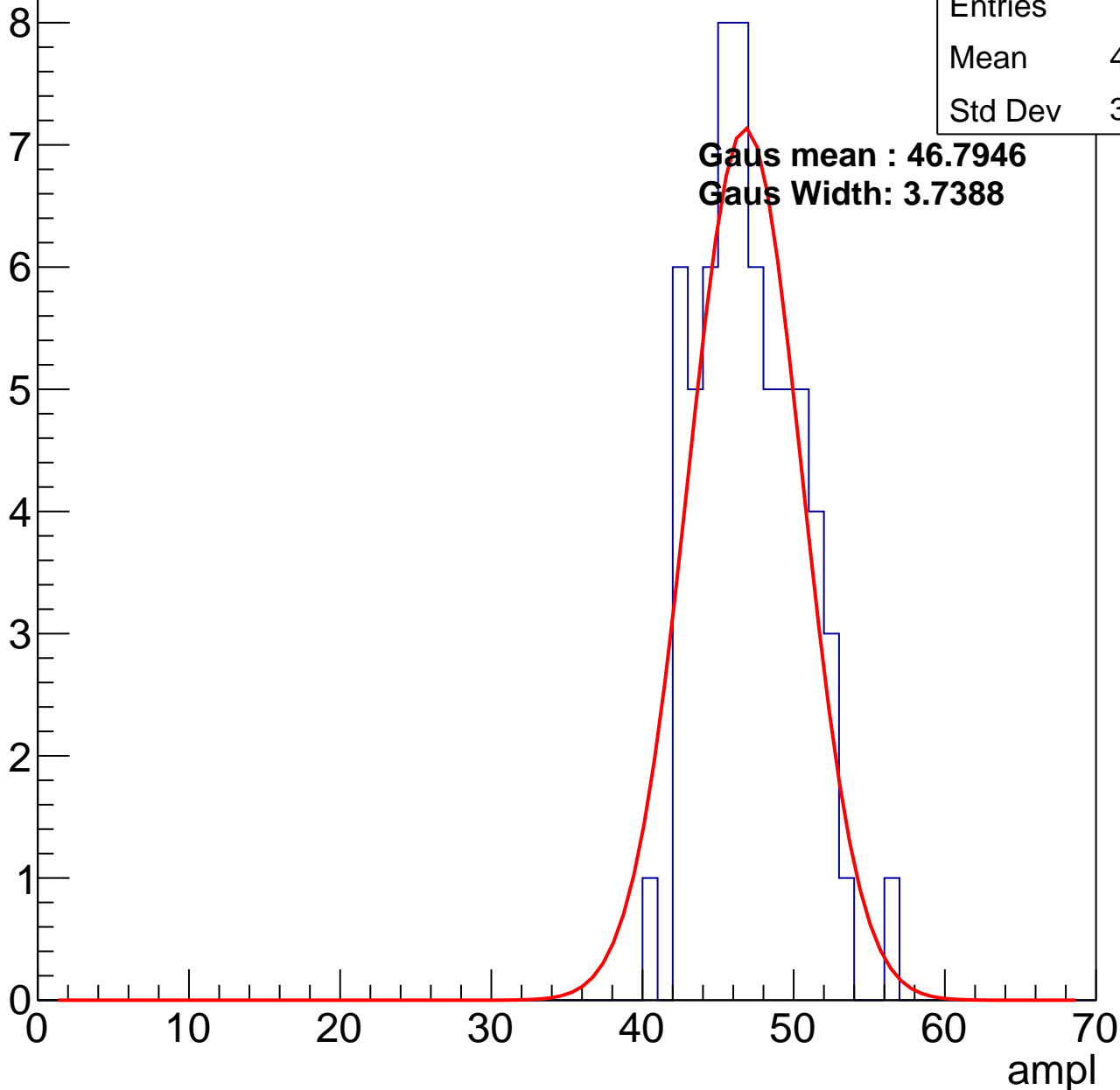
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	46.64
Std Dev	3.285

**Gaus mean : 46.7946**

**Gaus Width: 3.7388**

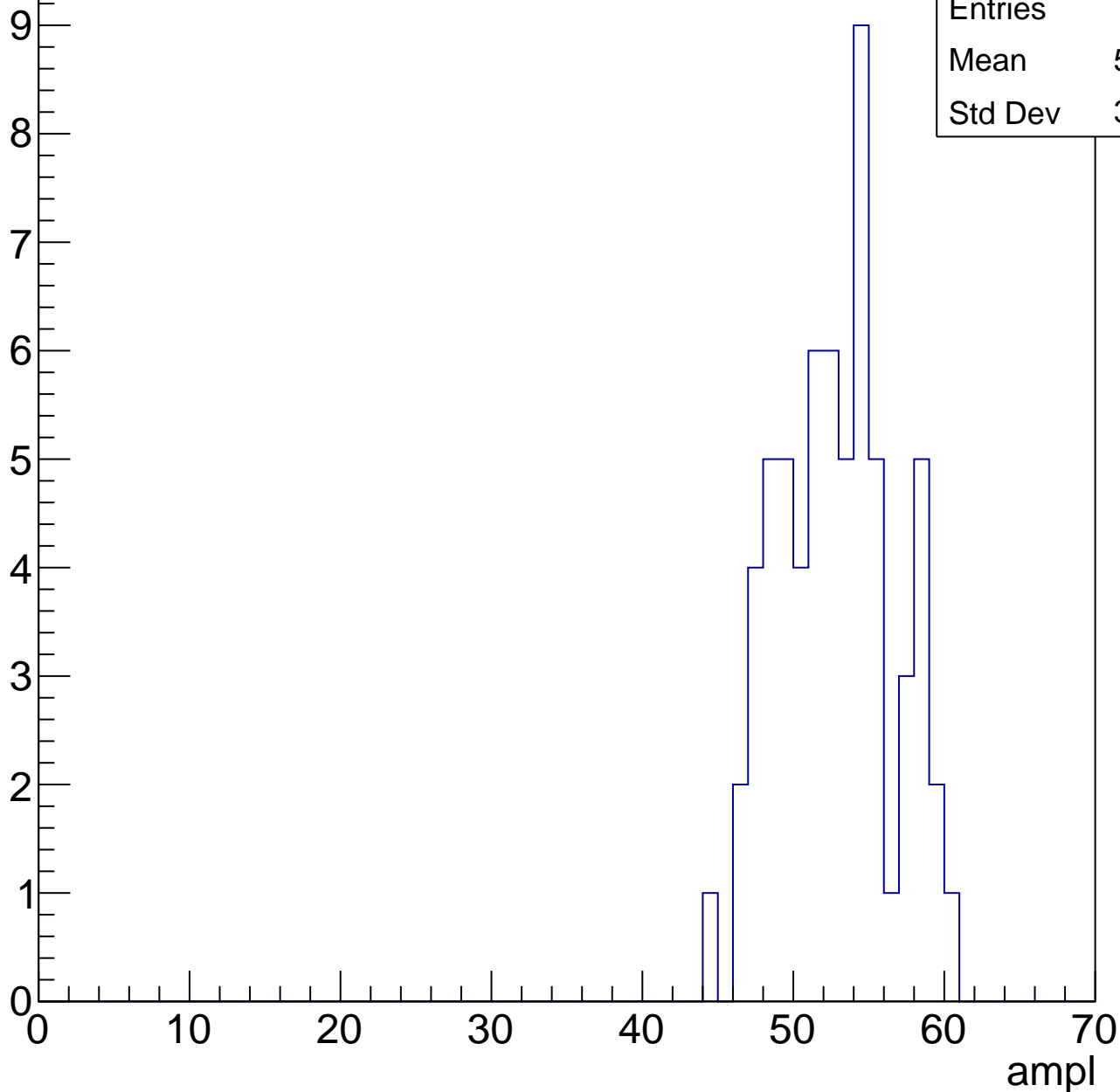


# B0L000S, U13-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	52.31
Std Dev	3.741

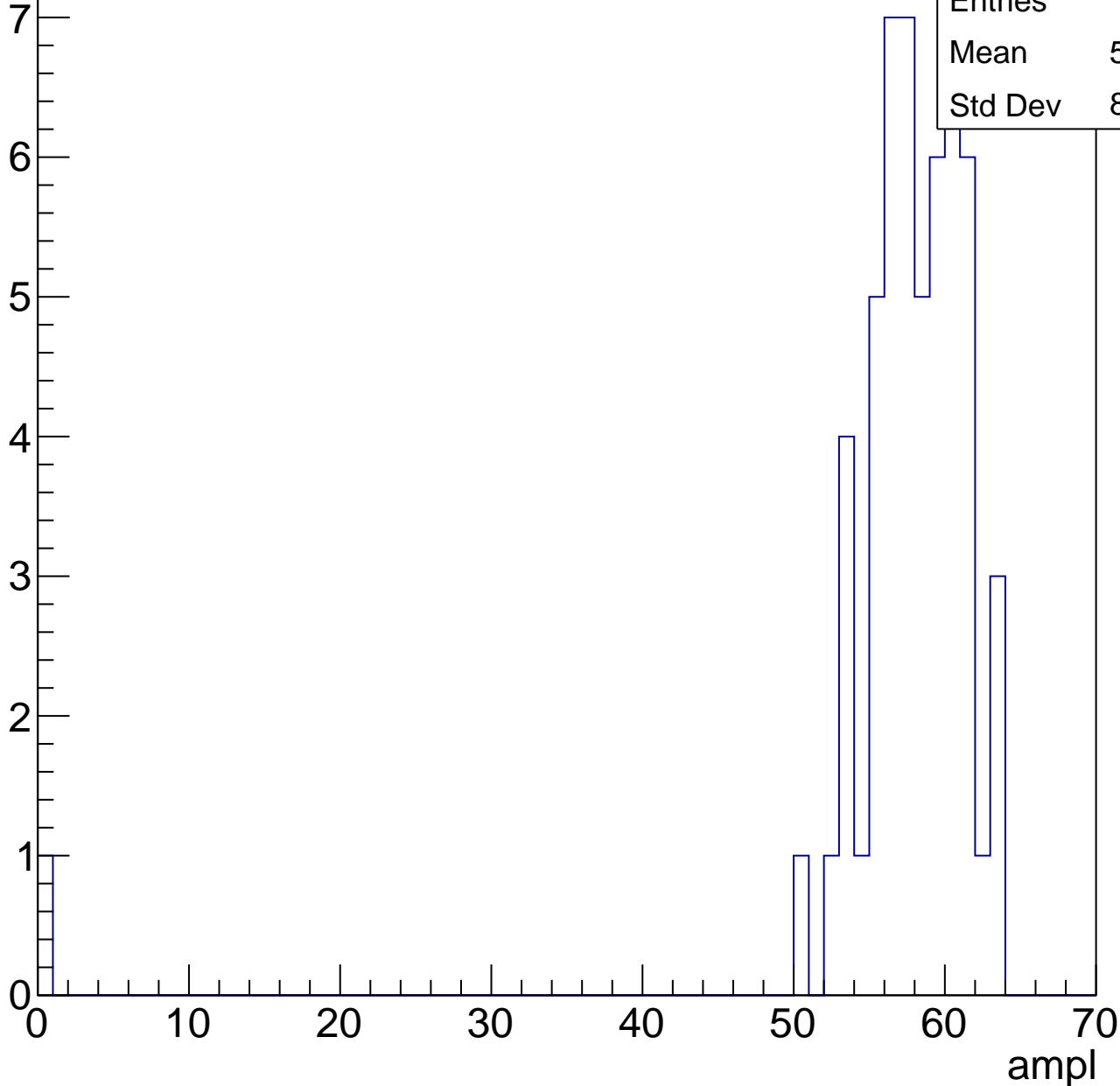


# B0L000S, U13-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

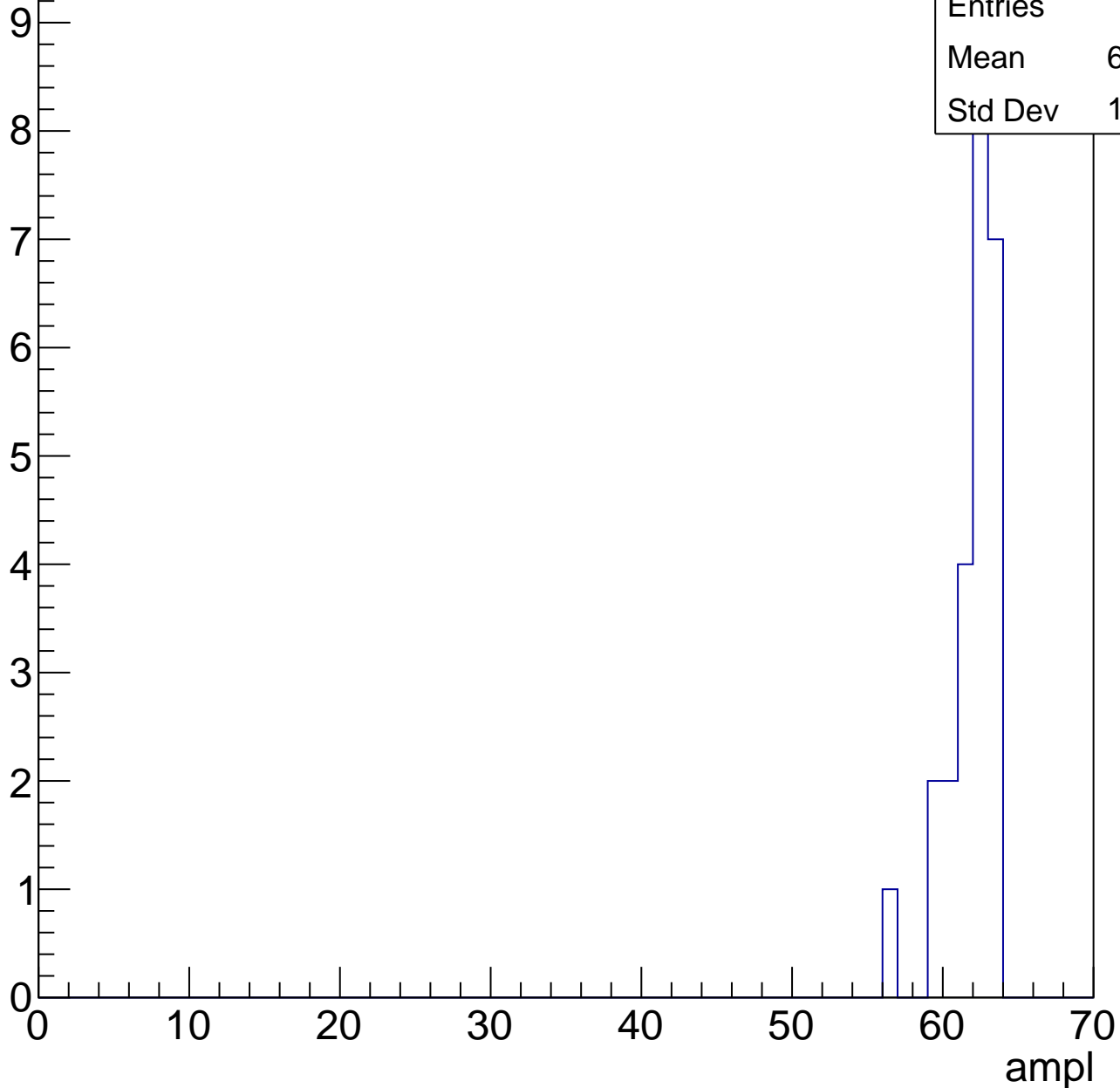
Entries	55
Mean	56.64
Std Dev	8.243



# B0L000S, U13-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch21, adc0

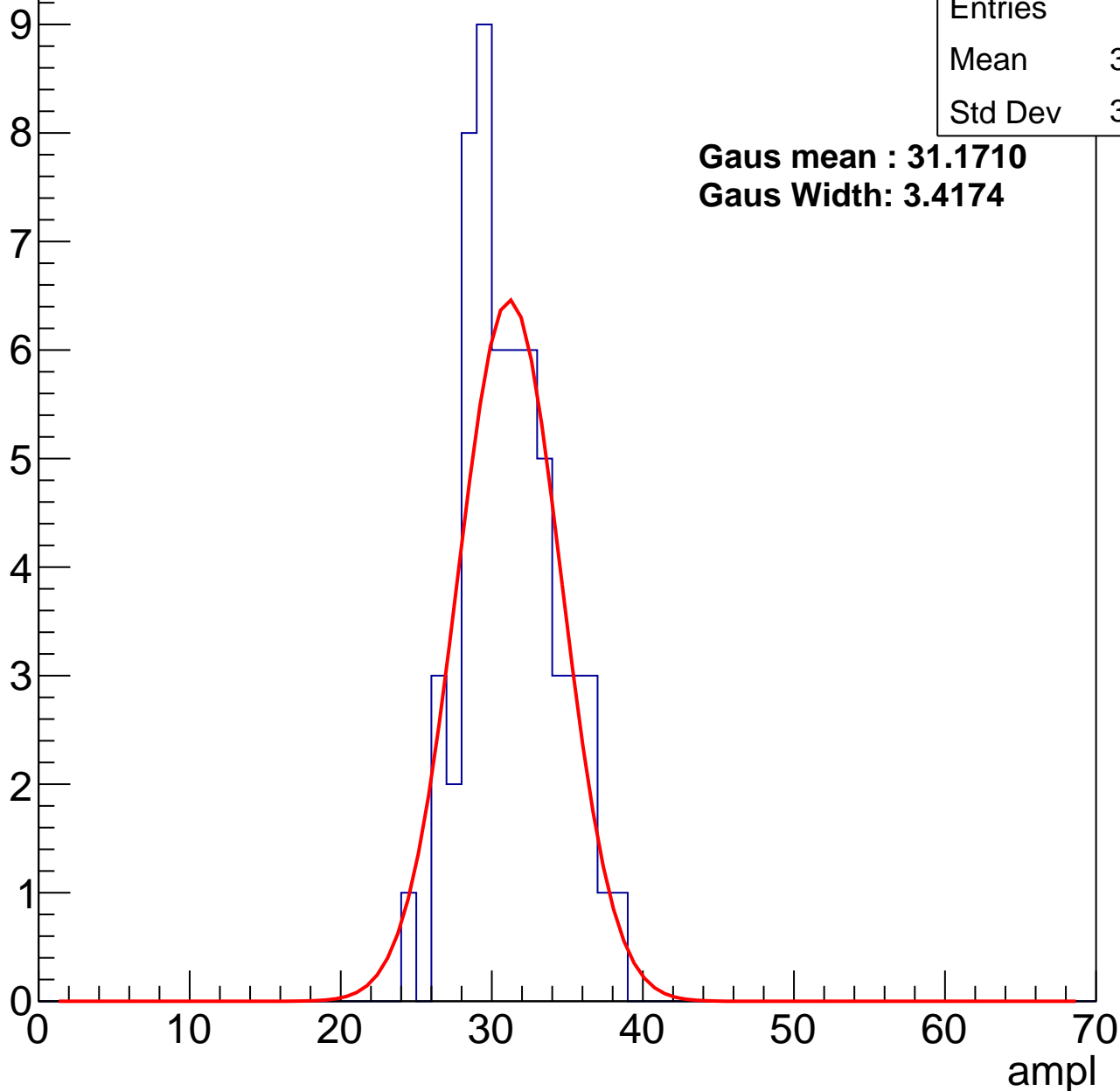
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	30.77
Std Dev	3.044

**Gaus mean : 31.1710**

**Gaus Width: 3.4174**



# B0L000S, U13-ch21, adc1

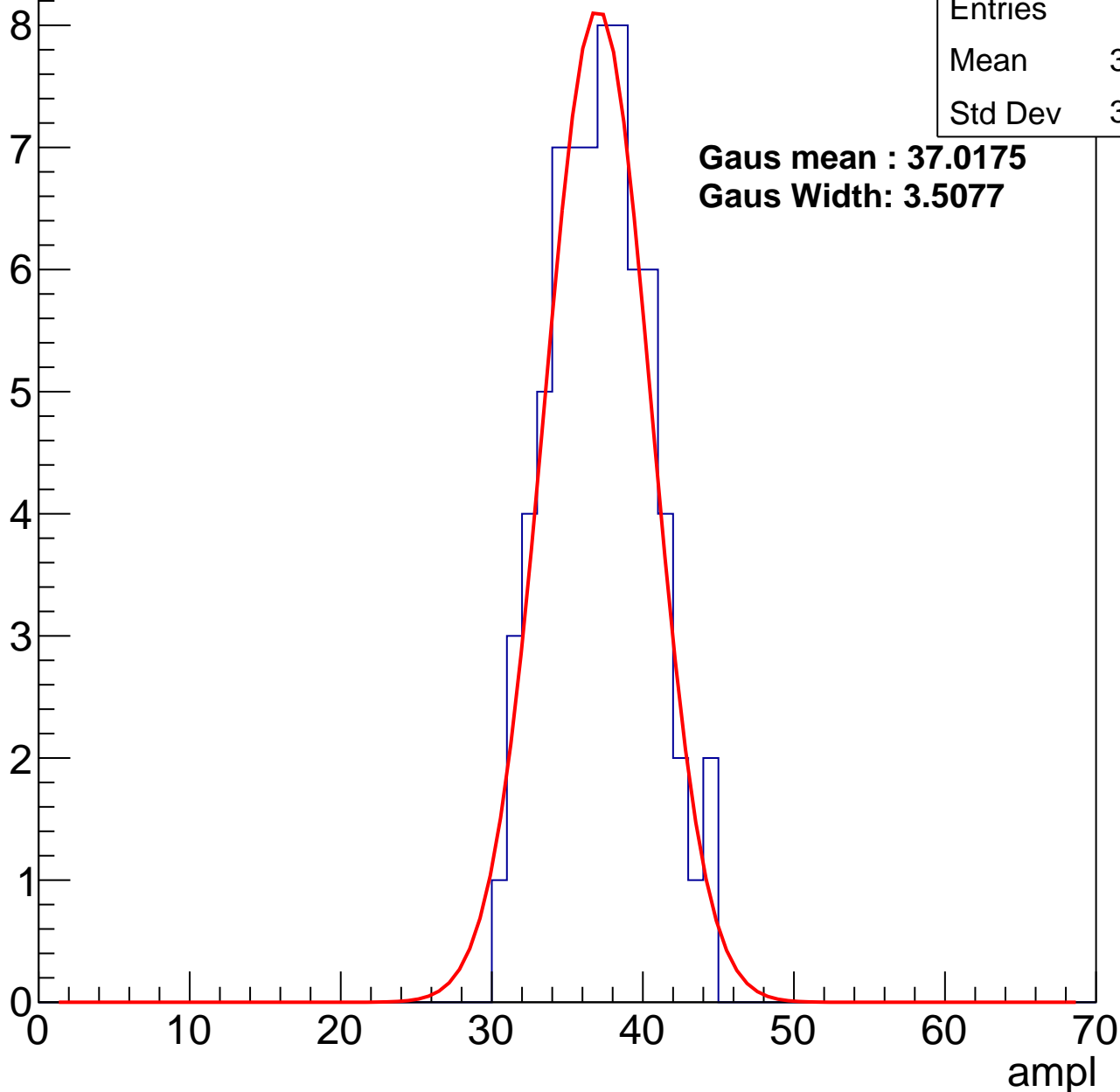
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.68
Std Dev	3.275

**Gaus mean : 37.0175**

**Gaus Width: 3.5077**



# B0L000S, U13-ch21, adc2

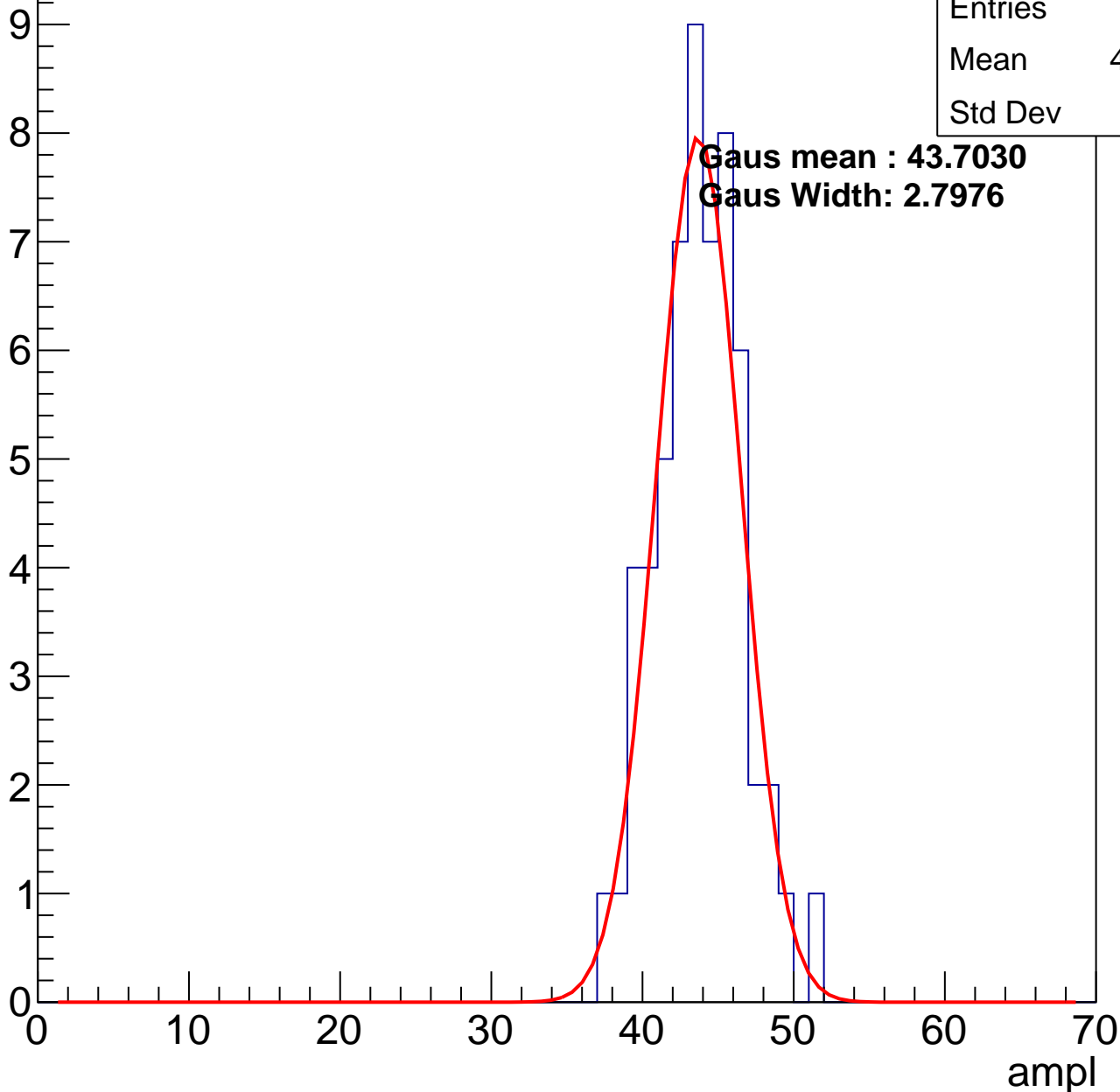
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.29
Std Dev	2.81

**Gaus mean : 43.7030**

**Gaus Width: 2.7976**



# B0L000S, U13-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

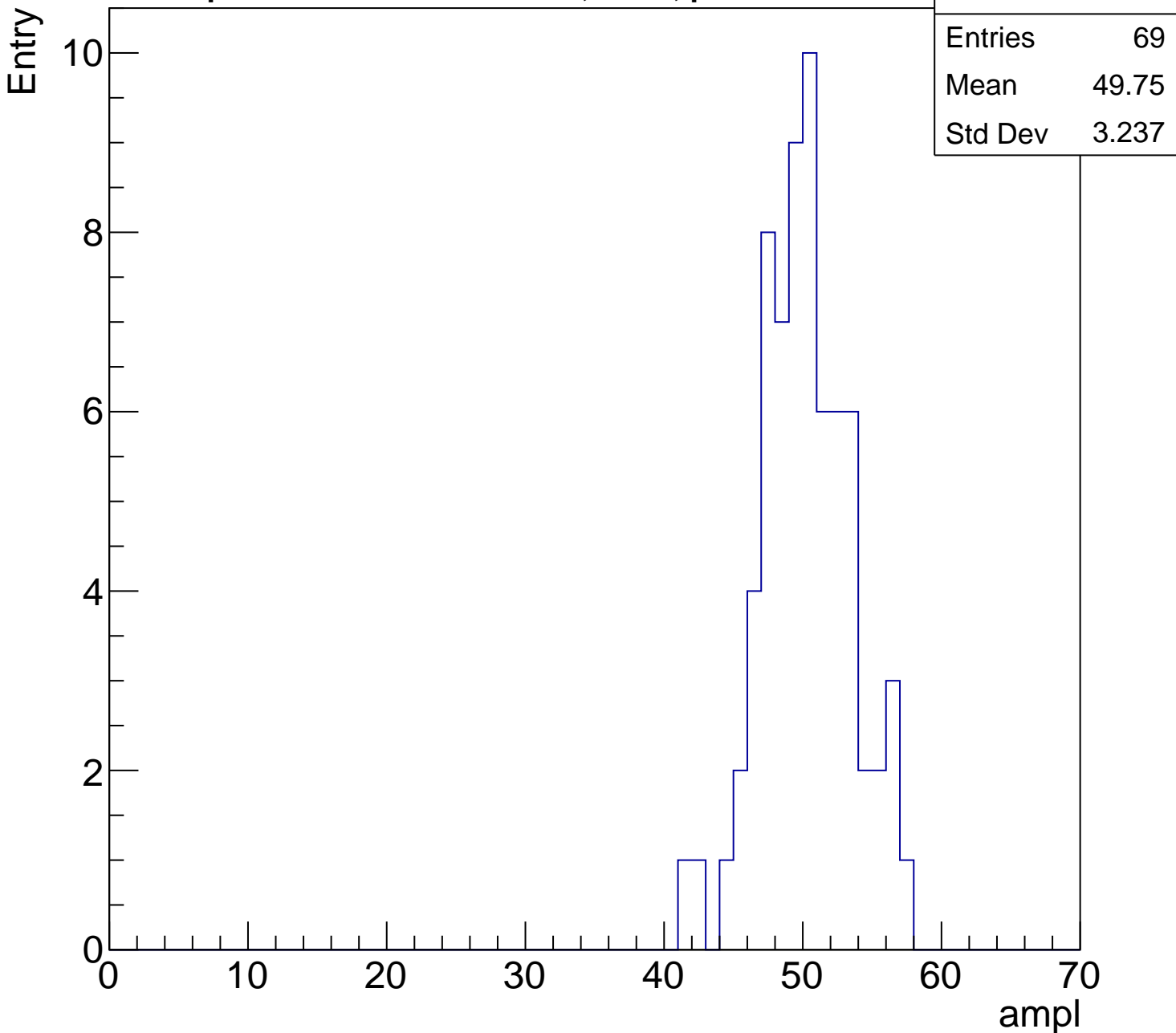
Entries	69
Mean	49.75
Std Dev	3.237

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

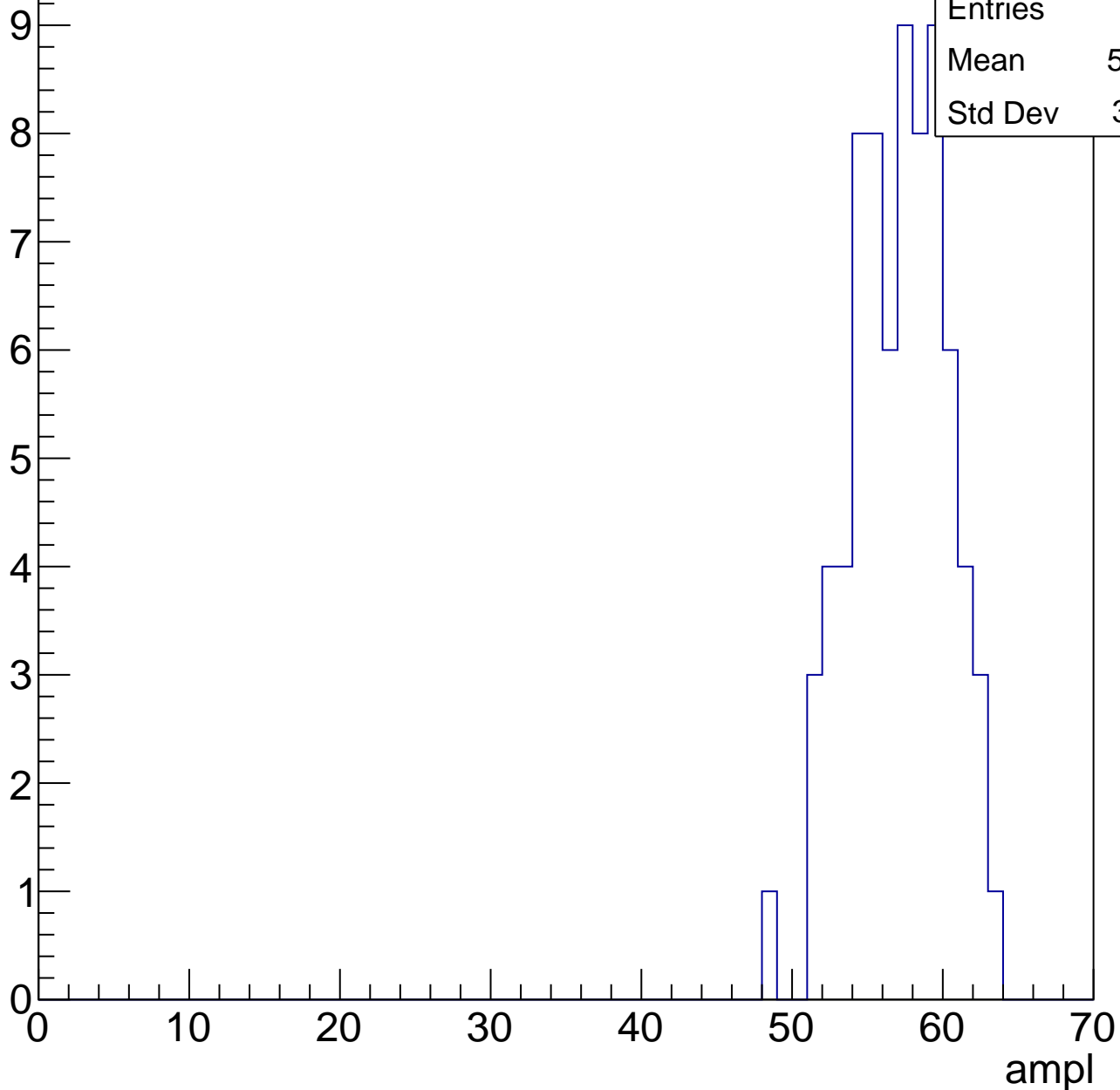
ampl



# B0L000S, U13-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

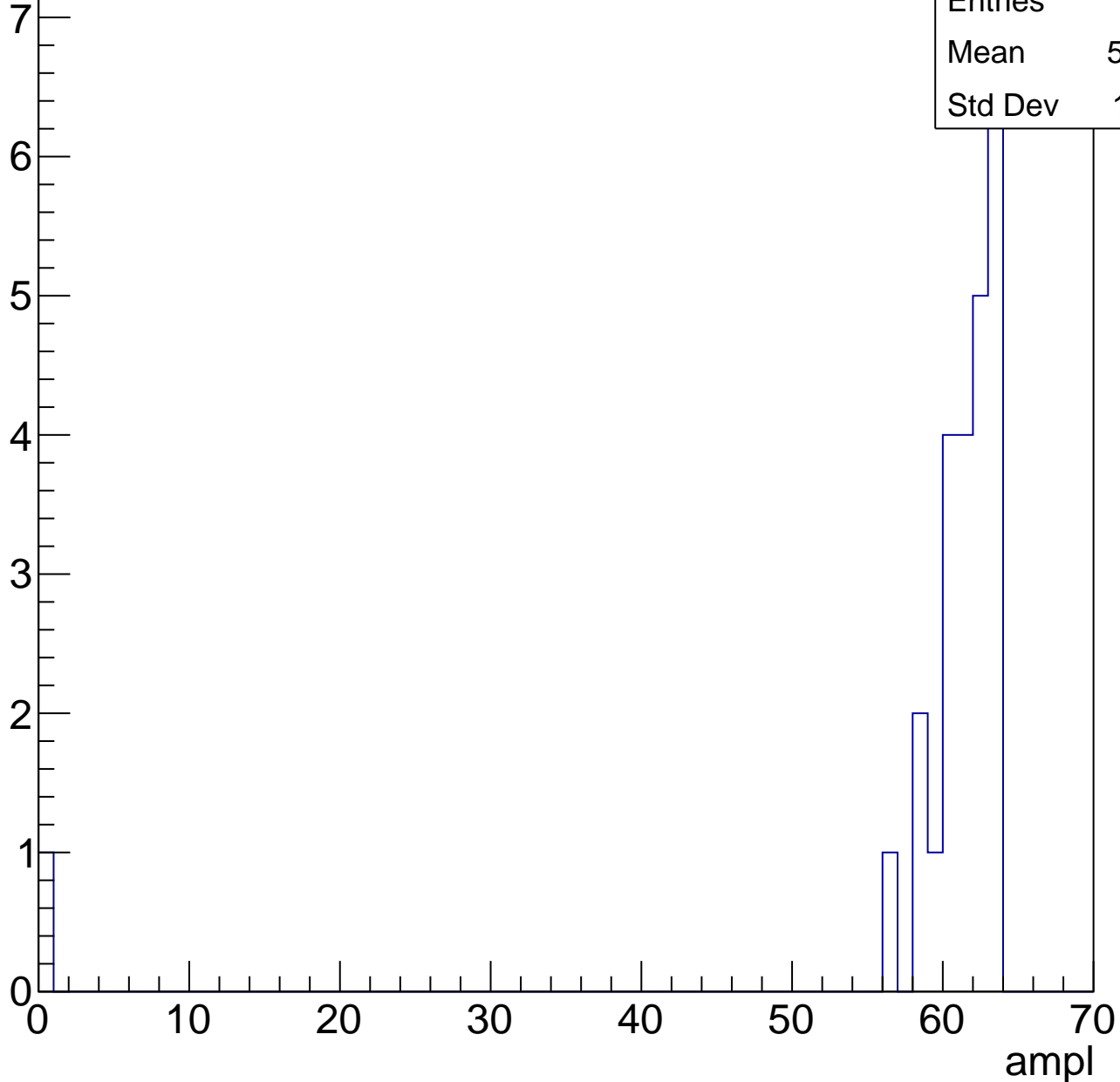


# B0L000S, U13-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

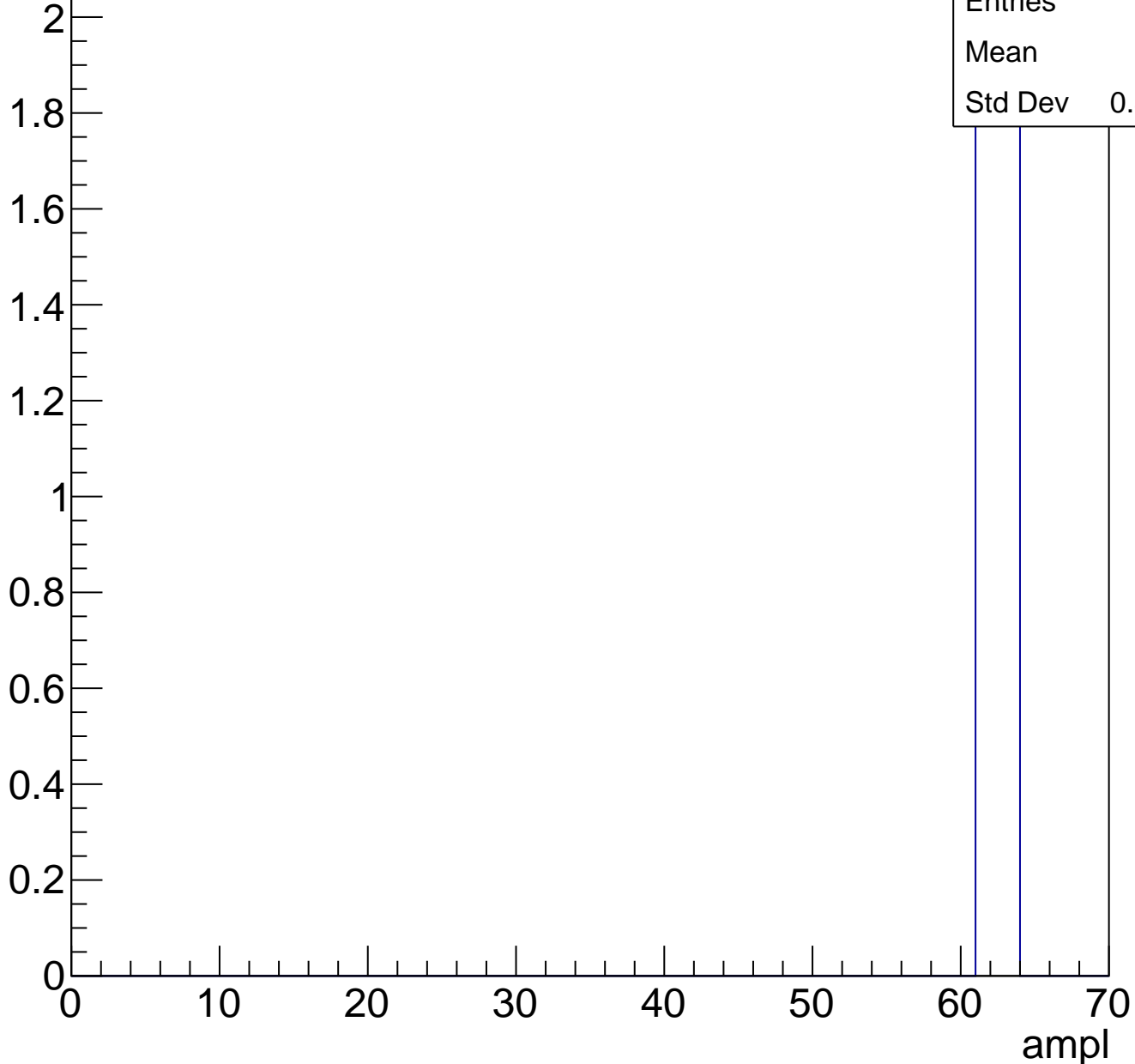
Entries	25
Mean	58.64
Std Dev	12.11



# B0L000S, U13-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

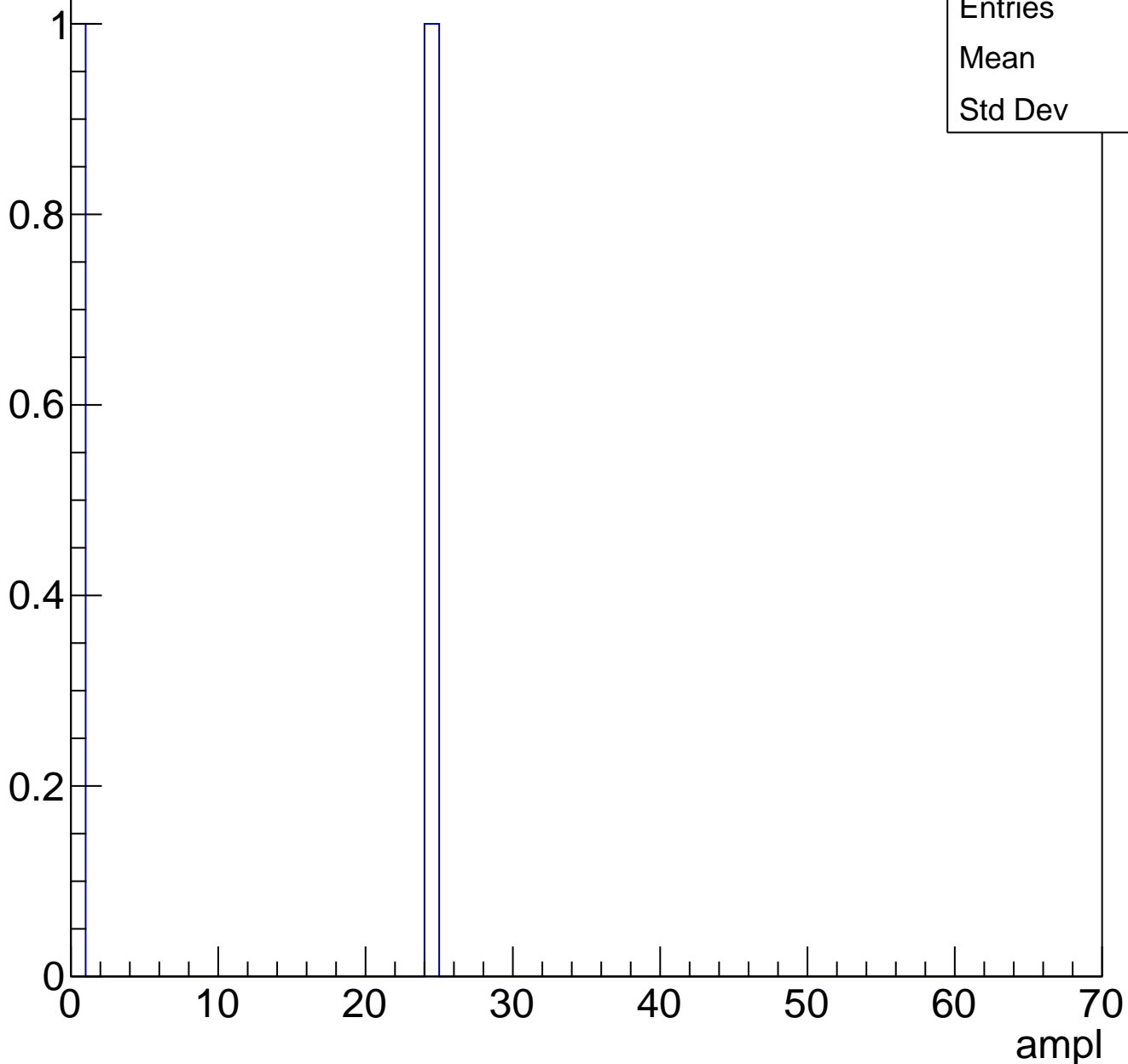




# B0L000S, U13-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12
Std Dev	12

# B0L000S, U13-ch22, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	30.14
Std Dev	3.409

**Gaus mean : 30.3793**

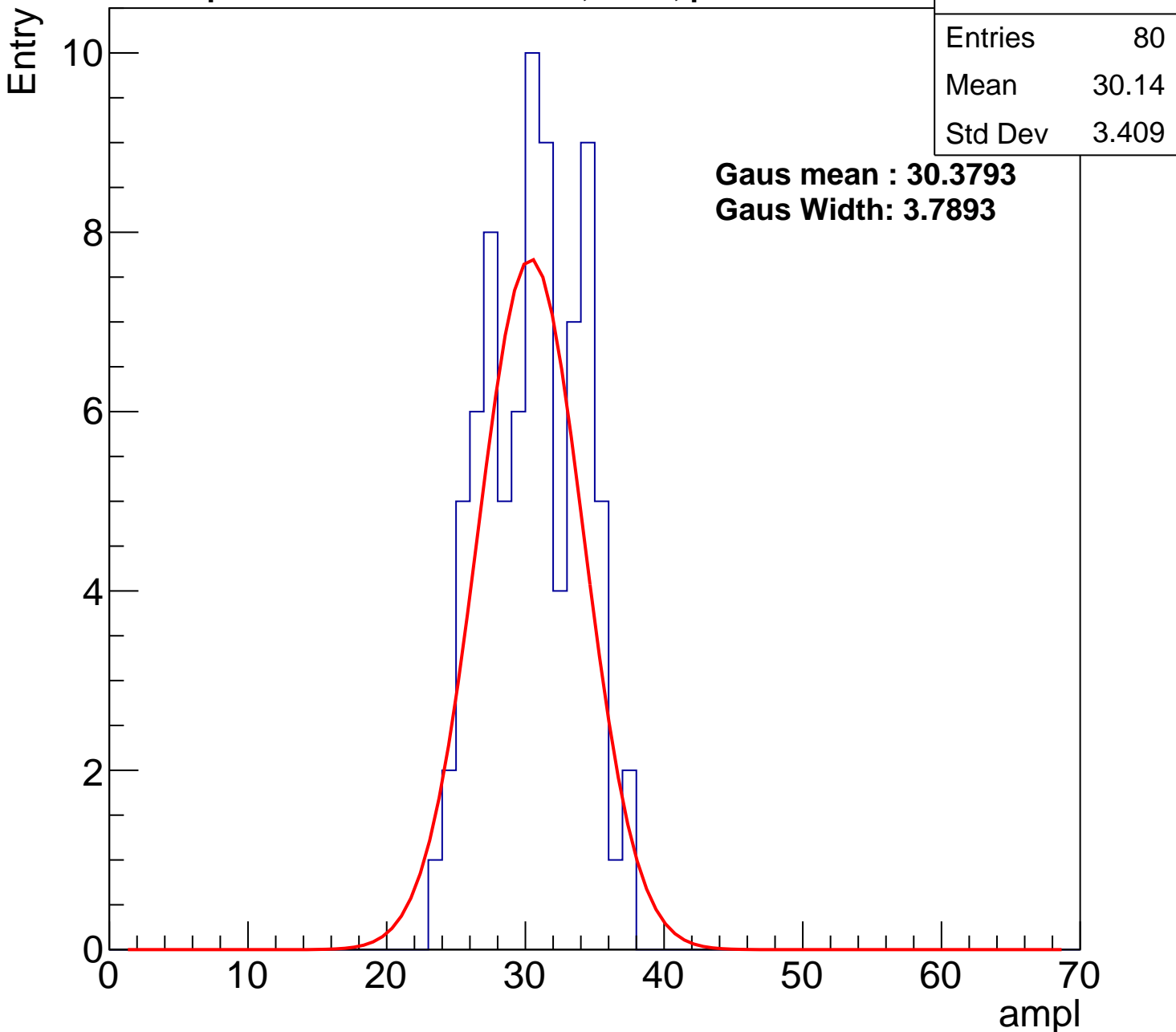
**Gaus Width: 3.7893**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch22, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	37.69
Std Dev	2.971

**Gaus mean : 38.4199**

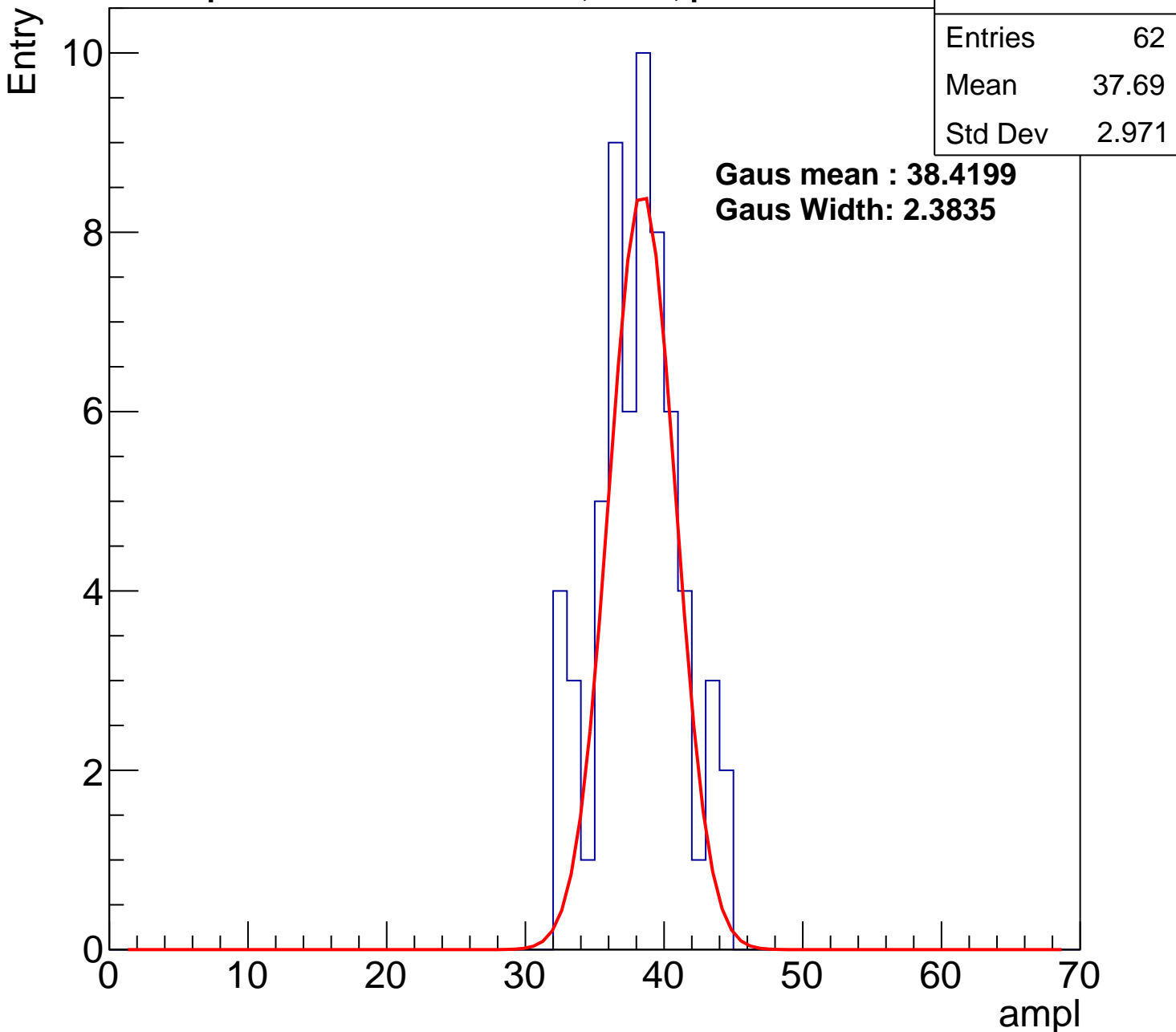
**Gaus Width: 2.3835**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch22, adc2

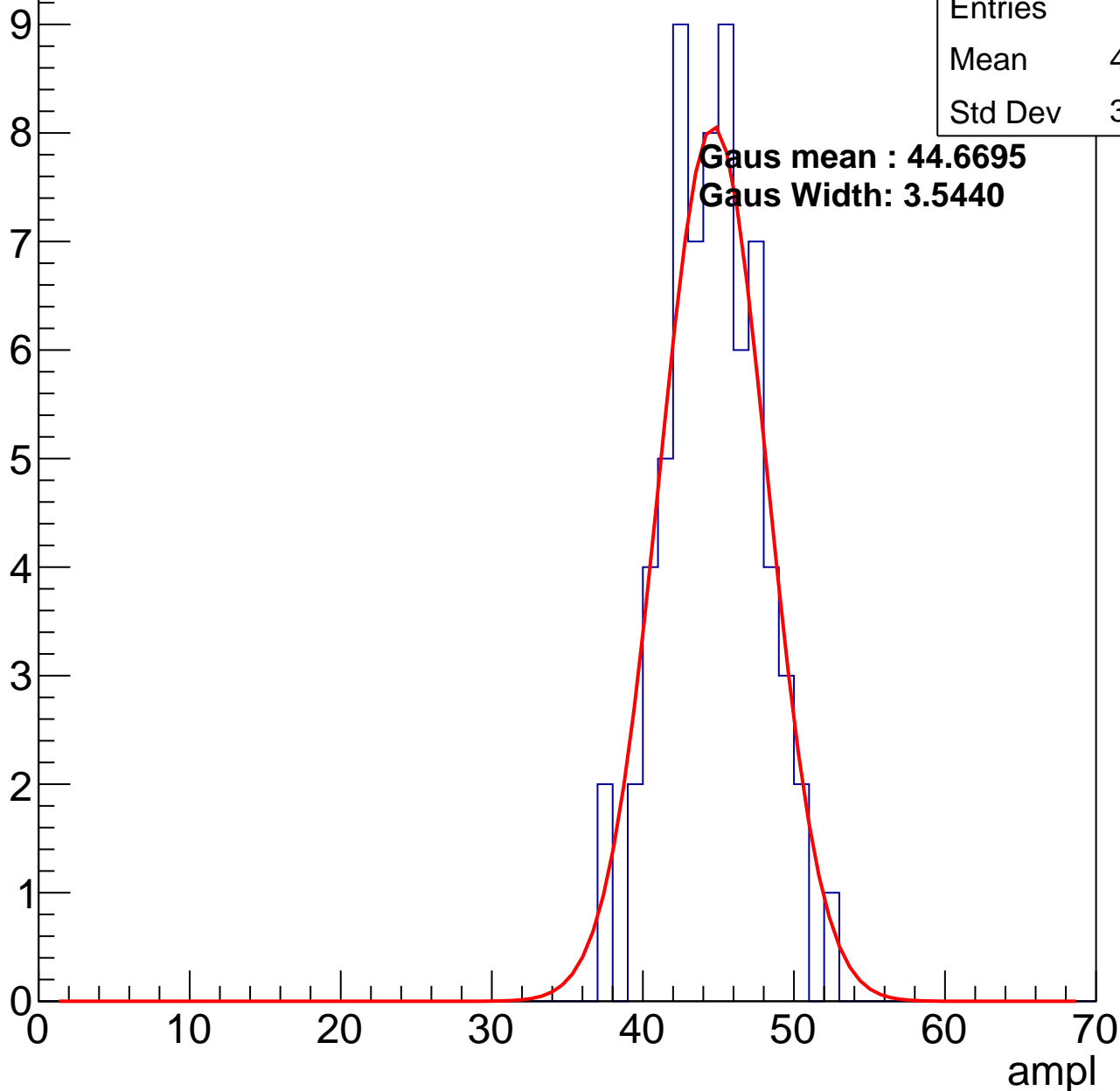
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.19
Std Dev	3.113

**Gaus mean : 44.6695**

**Gaus Width: 3.5440**

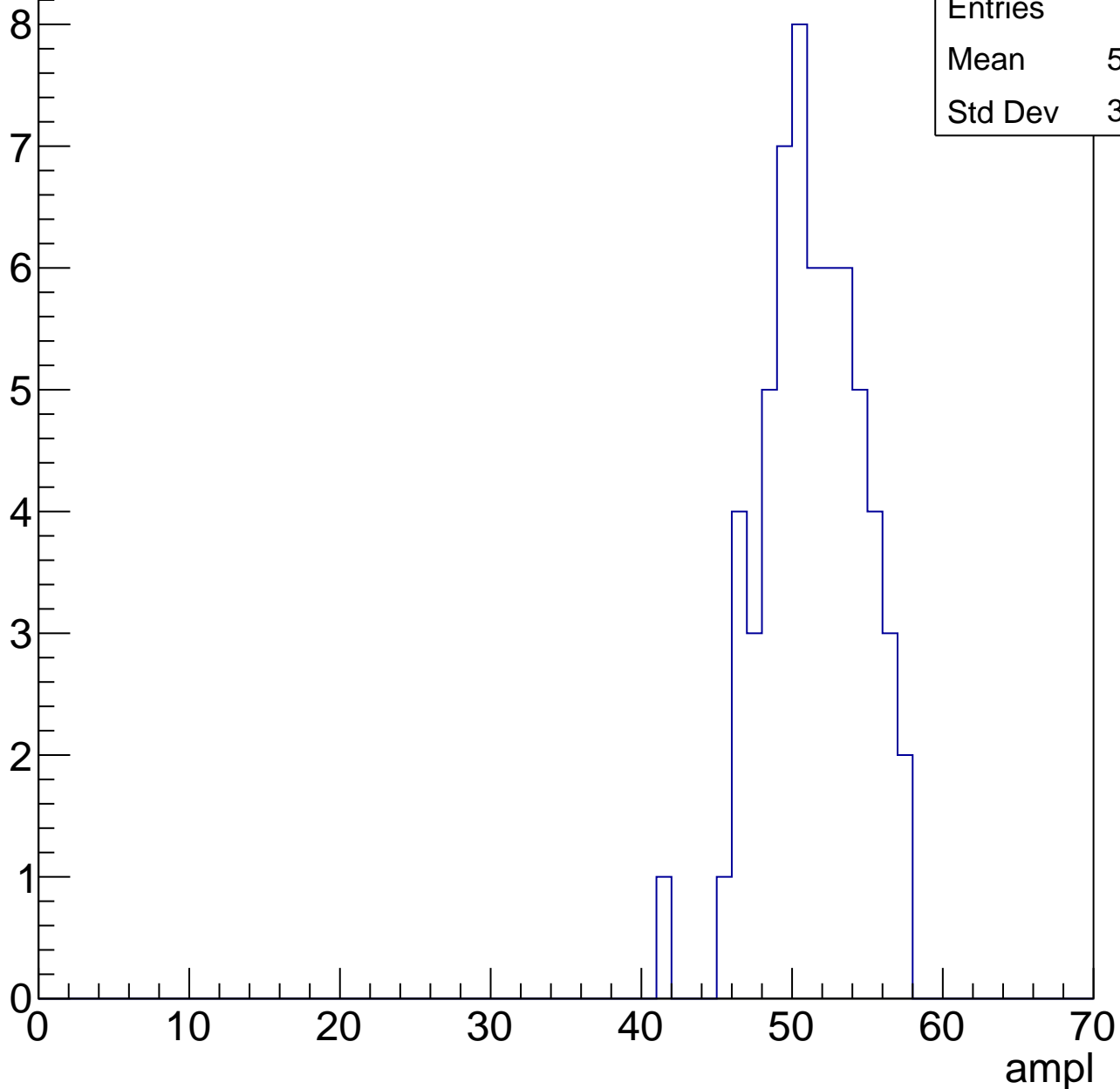


# B0L000S, U13-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	50.85
Std Dev	3.263

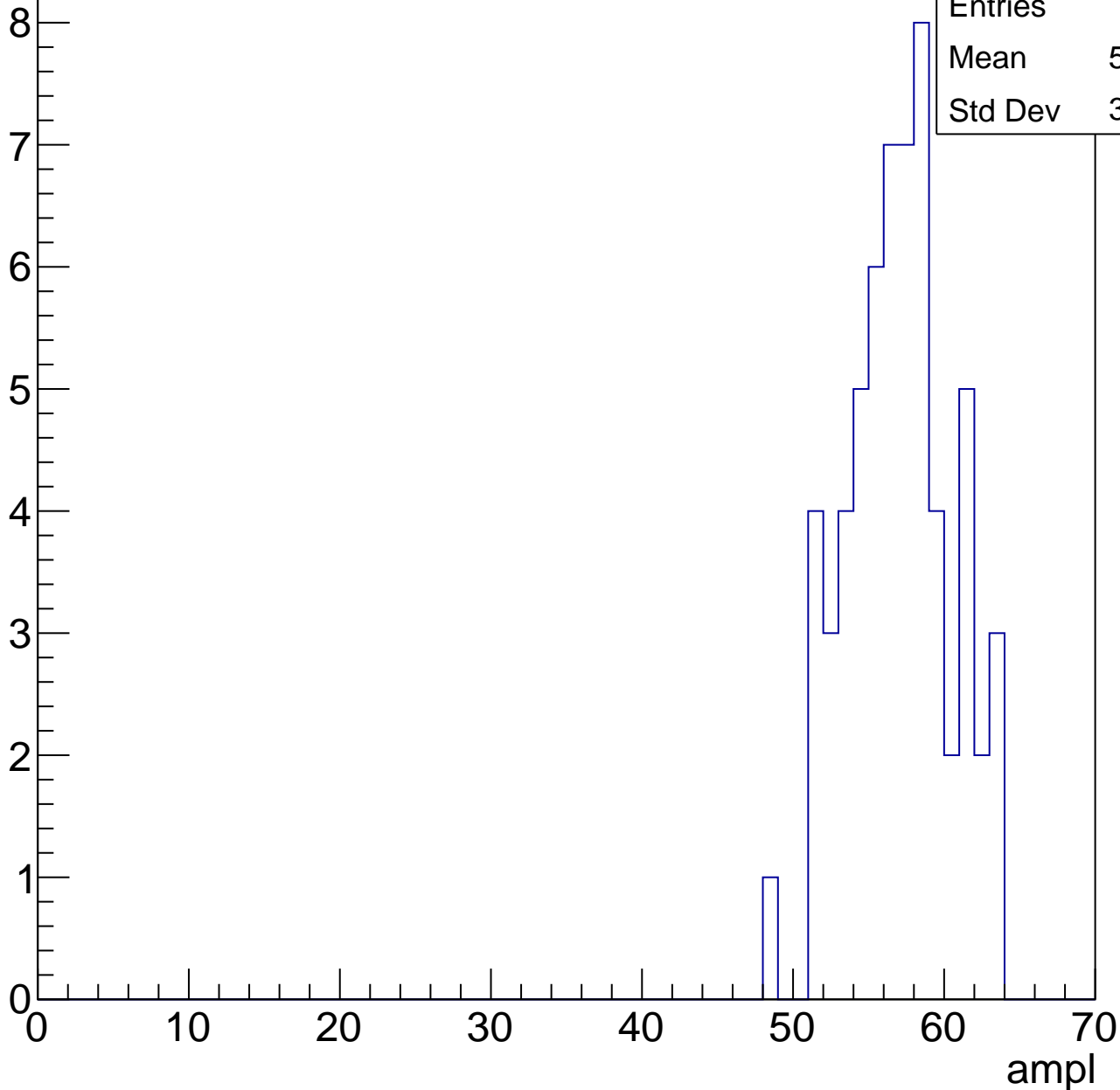


# B0L000S, U13-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

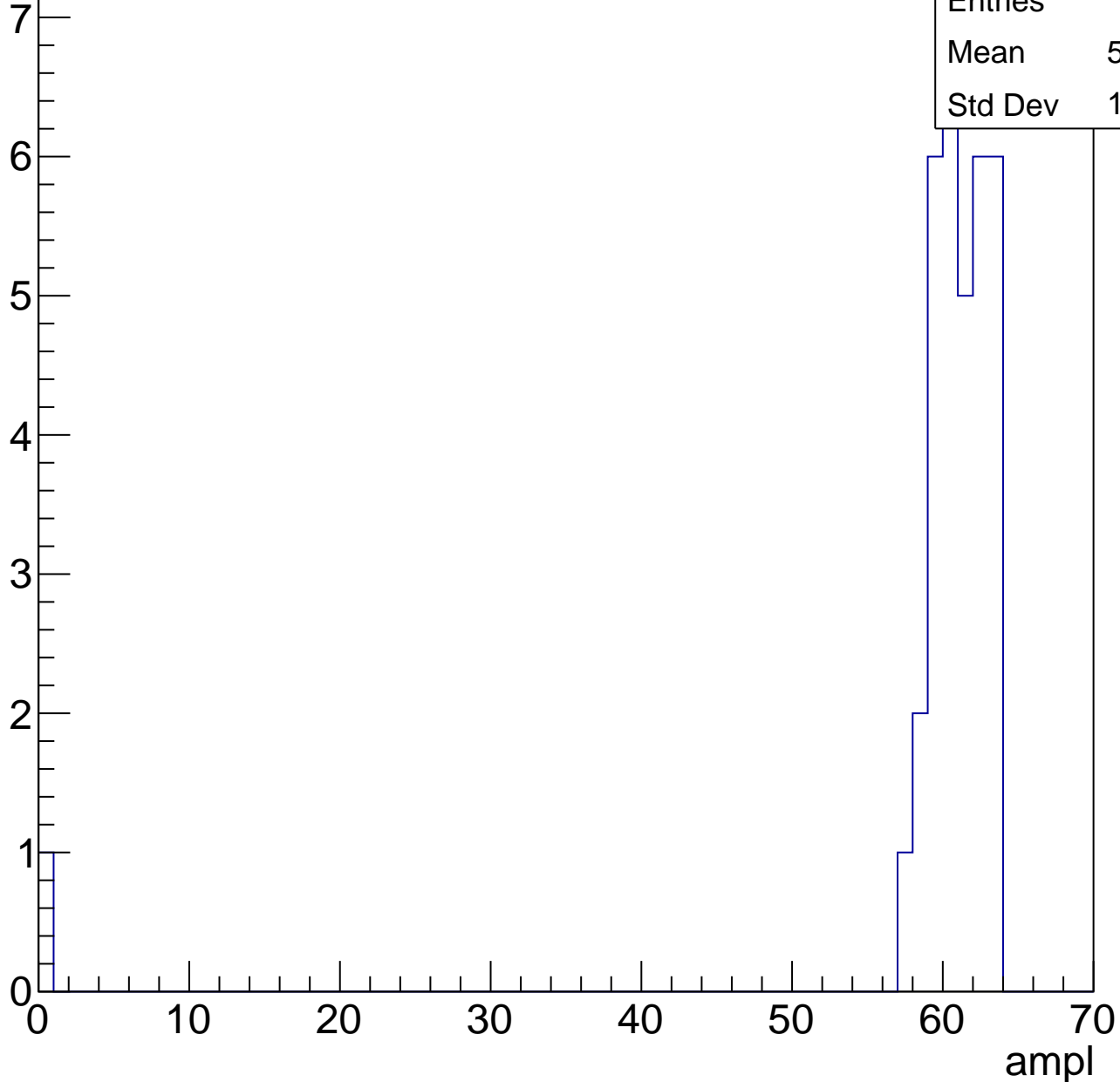
Entries	61
Mean	56.54
Std Dev	3.405



# B0L000S, U13-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

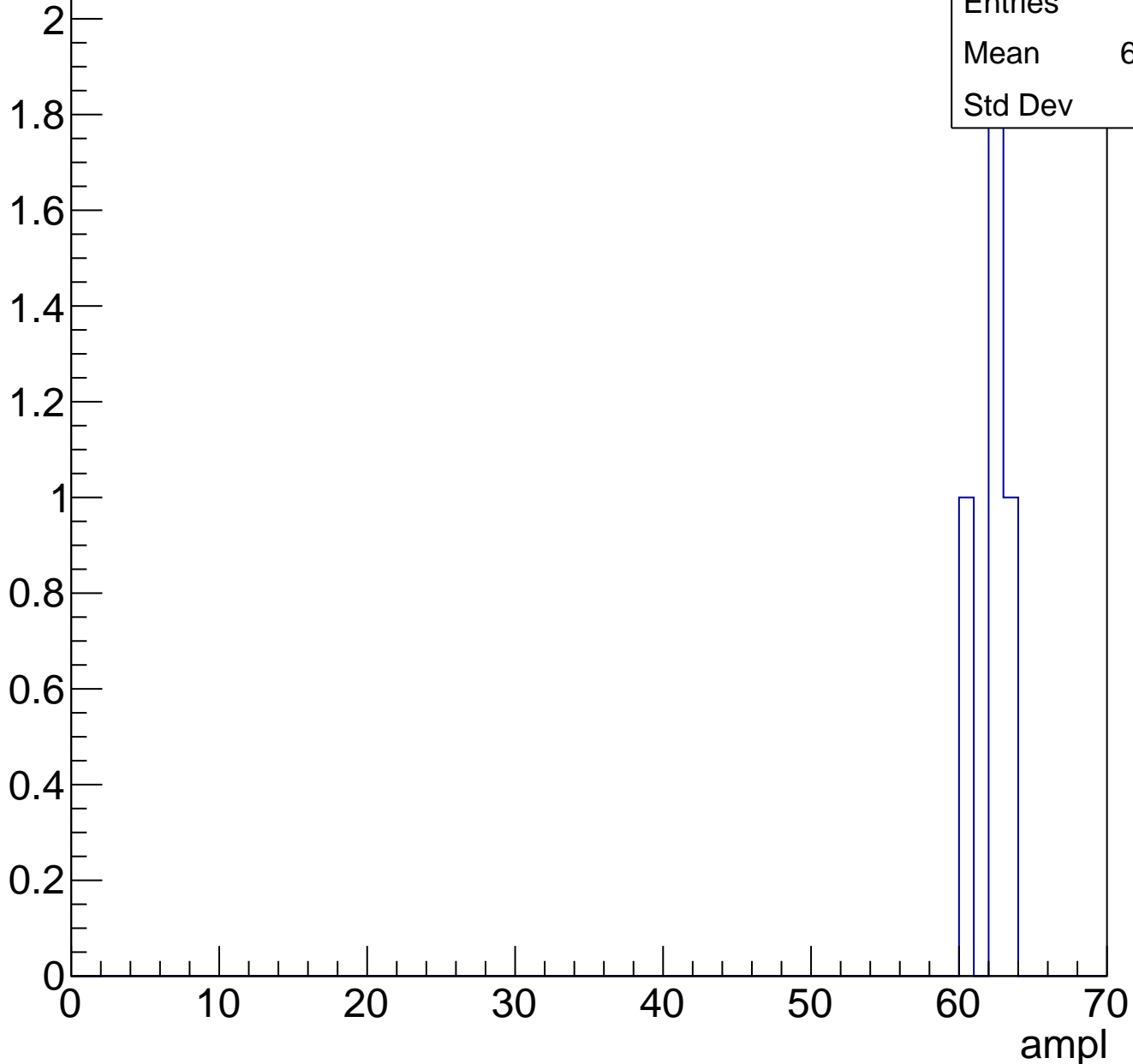
Entry



# B0L000S, U13-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

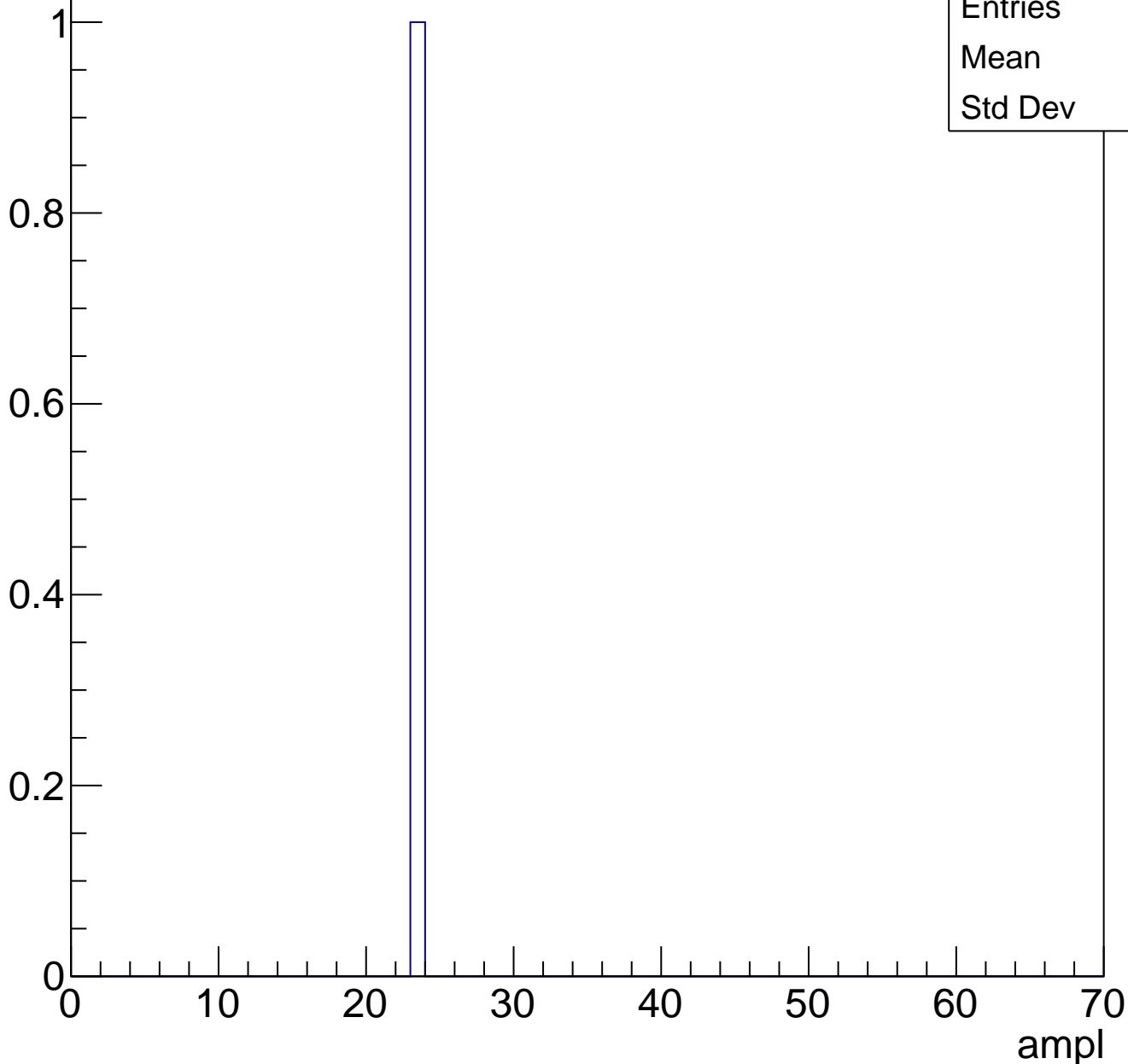




# B0L000S, U13-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch23, adc0

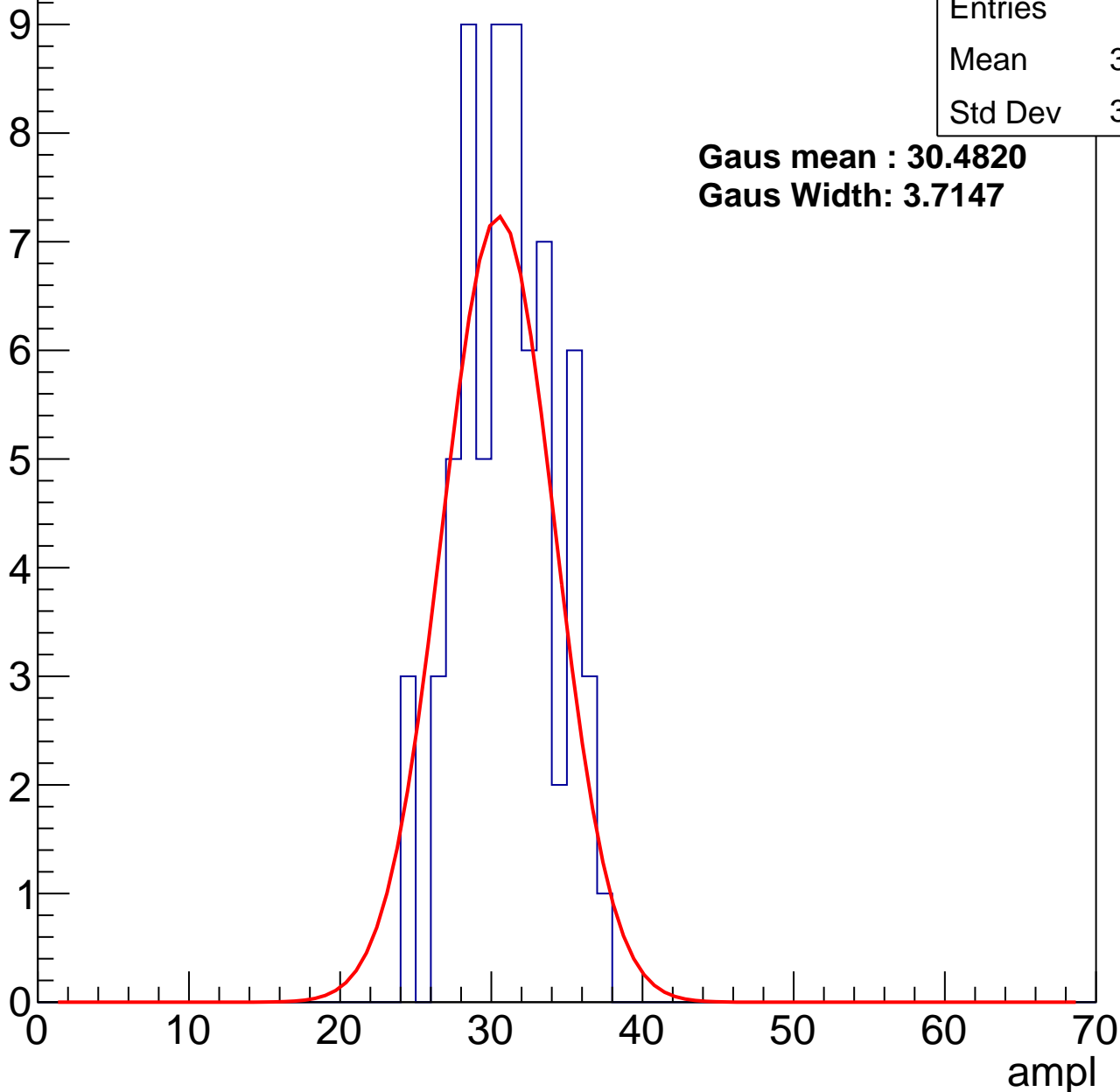
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.54
Std Dev	3.108

**Gaus mean : 30.4820**

**Gaus Width: 3.7147**



# B0L000S, U13-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	37.79
Std Dev	3.127

**Gaus mean : 38.0334**

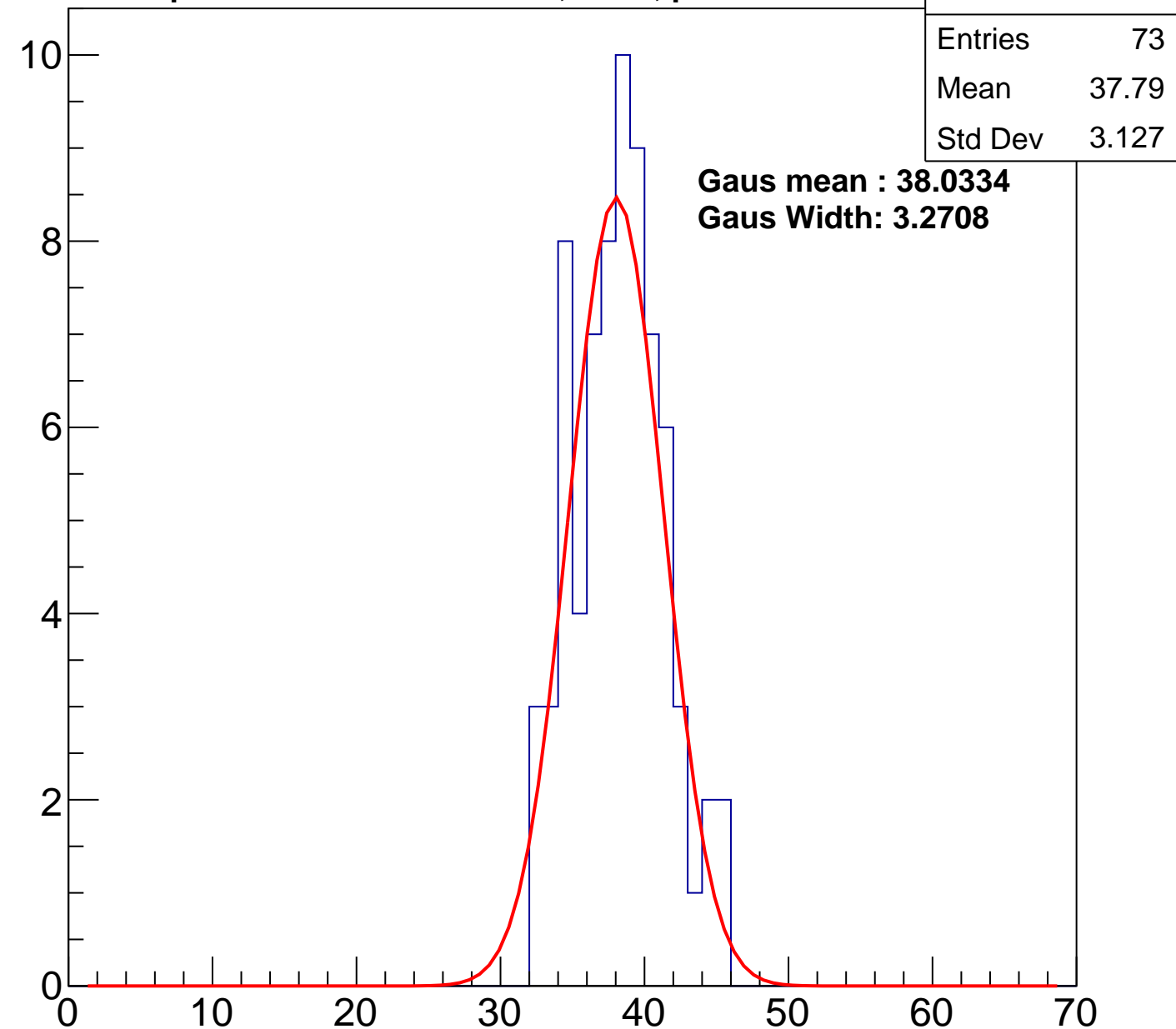
**Gaus Width: 3.2708**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch23, adc2

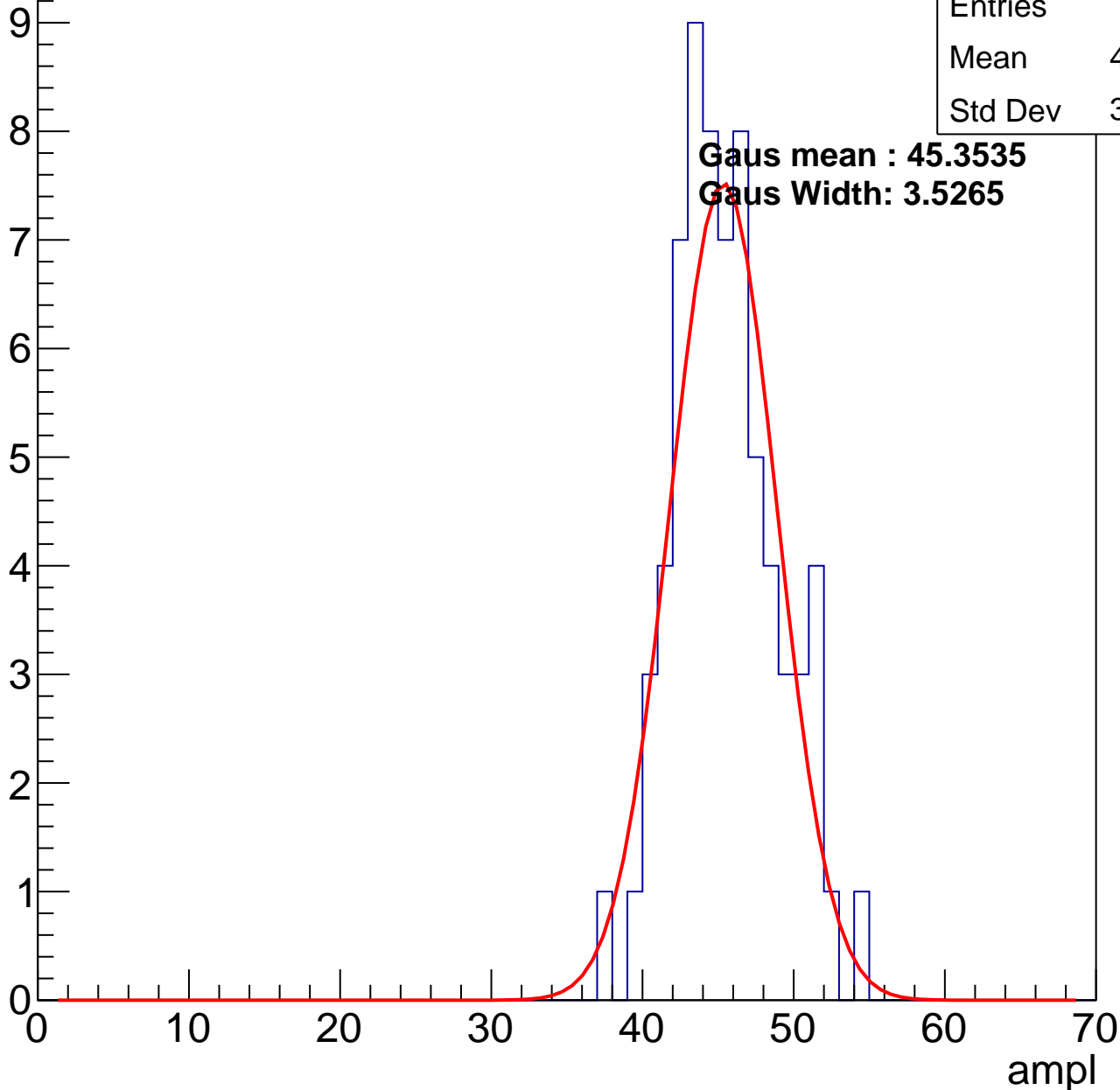
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	45.07
Std Dev	3.419

**Gaus mean : 45.3535**

**Gaus Width: 3.5265**

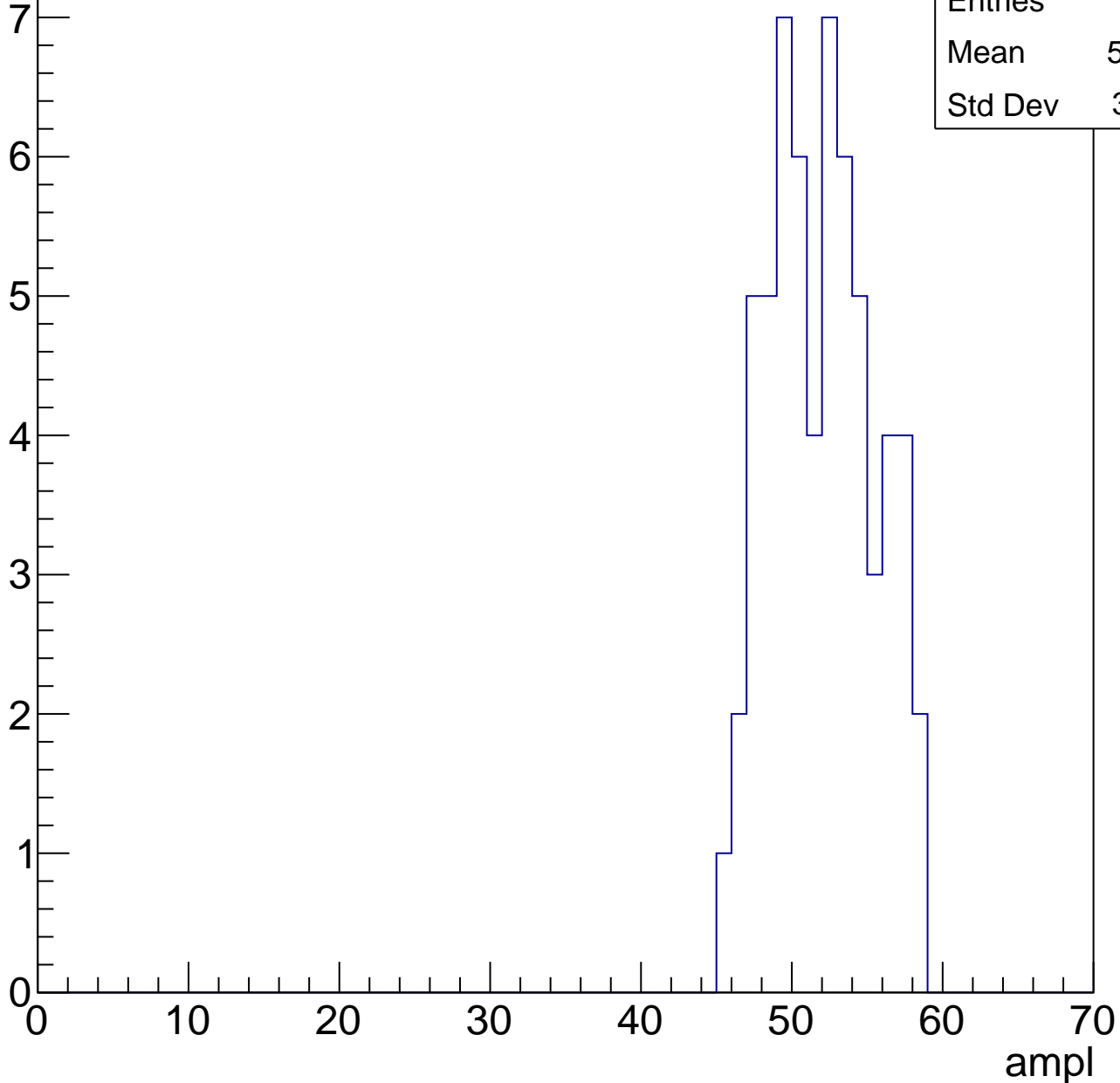


# B0L000S, U13-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	51.54
Std Dev	3.371



# B0L000S, U13-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	57.47
Std Dev	3.432

Entry

10

8

6

4

2

0

0

10

20

30

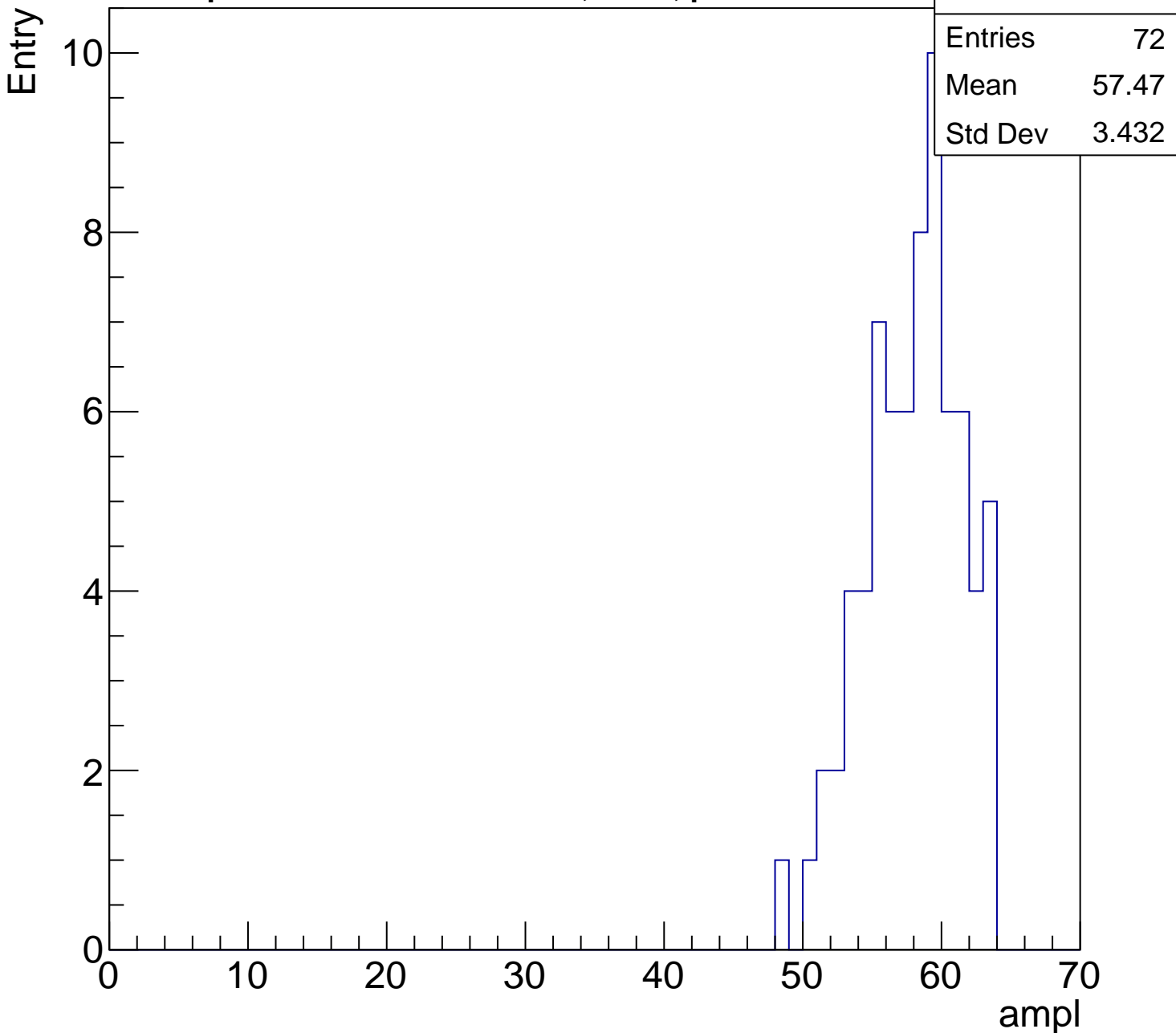
40

50

60

70

ampl

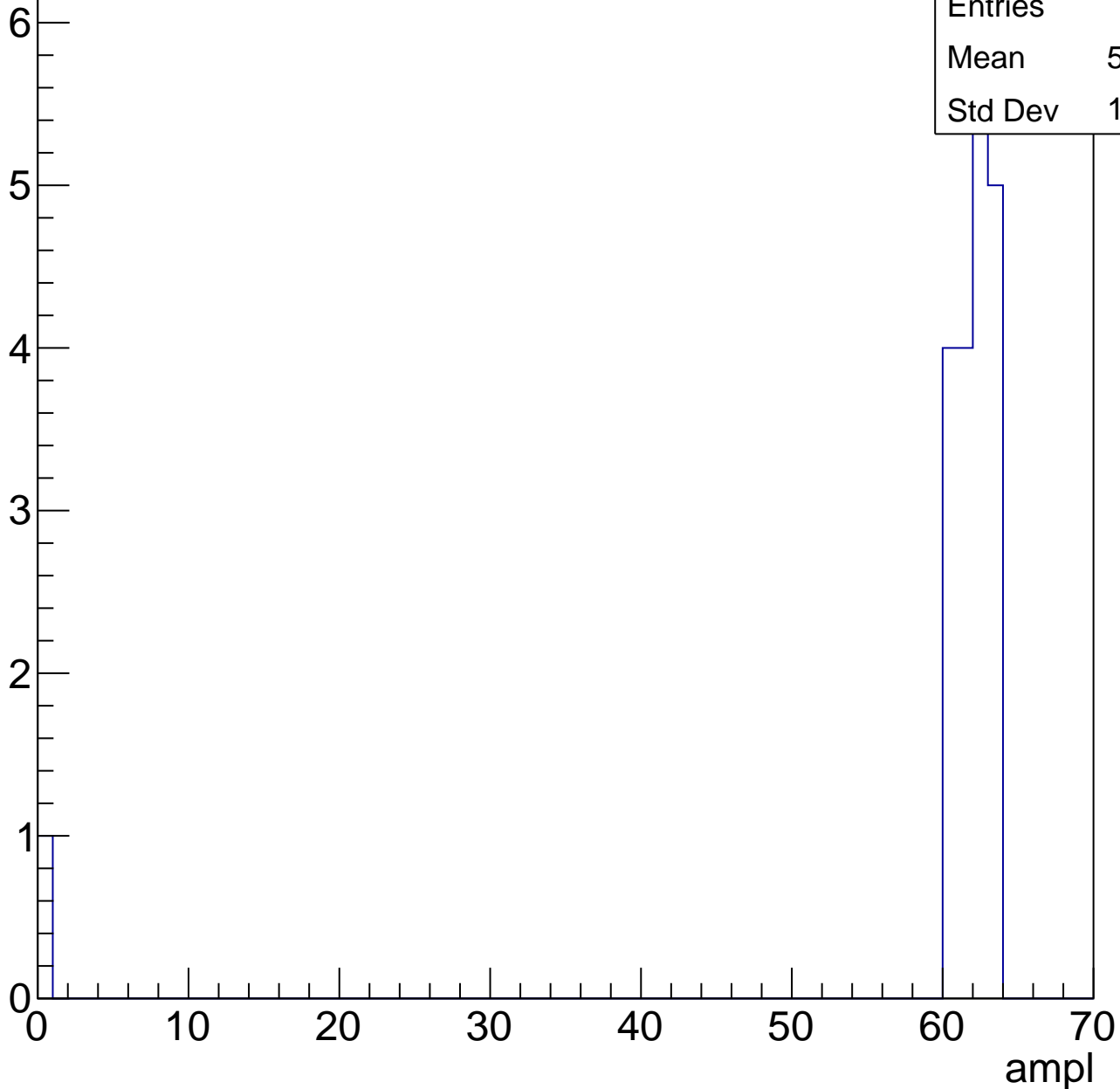


# B0L000S, U13-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	58.55
Std Dev	13.47



# B0L000S, U13-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U13-ch24, adc0

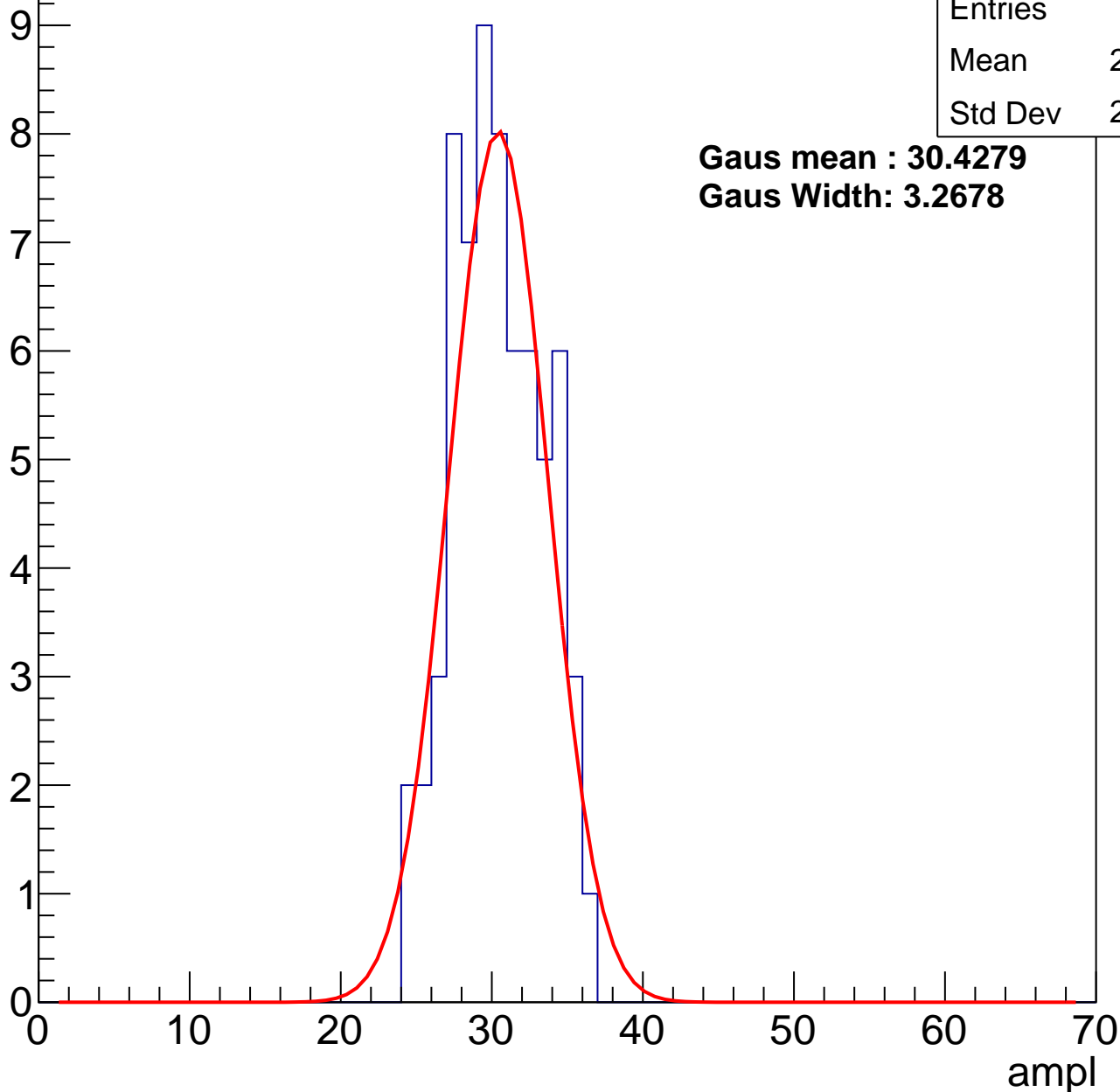
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.95
Std Dev	2.915

**Gaus mean : 30.4279**

**Gaus Width: 3.2678**



# B0L000S, U13-ch24, adc1

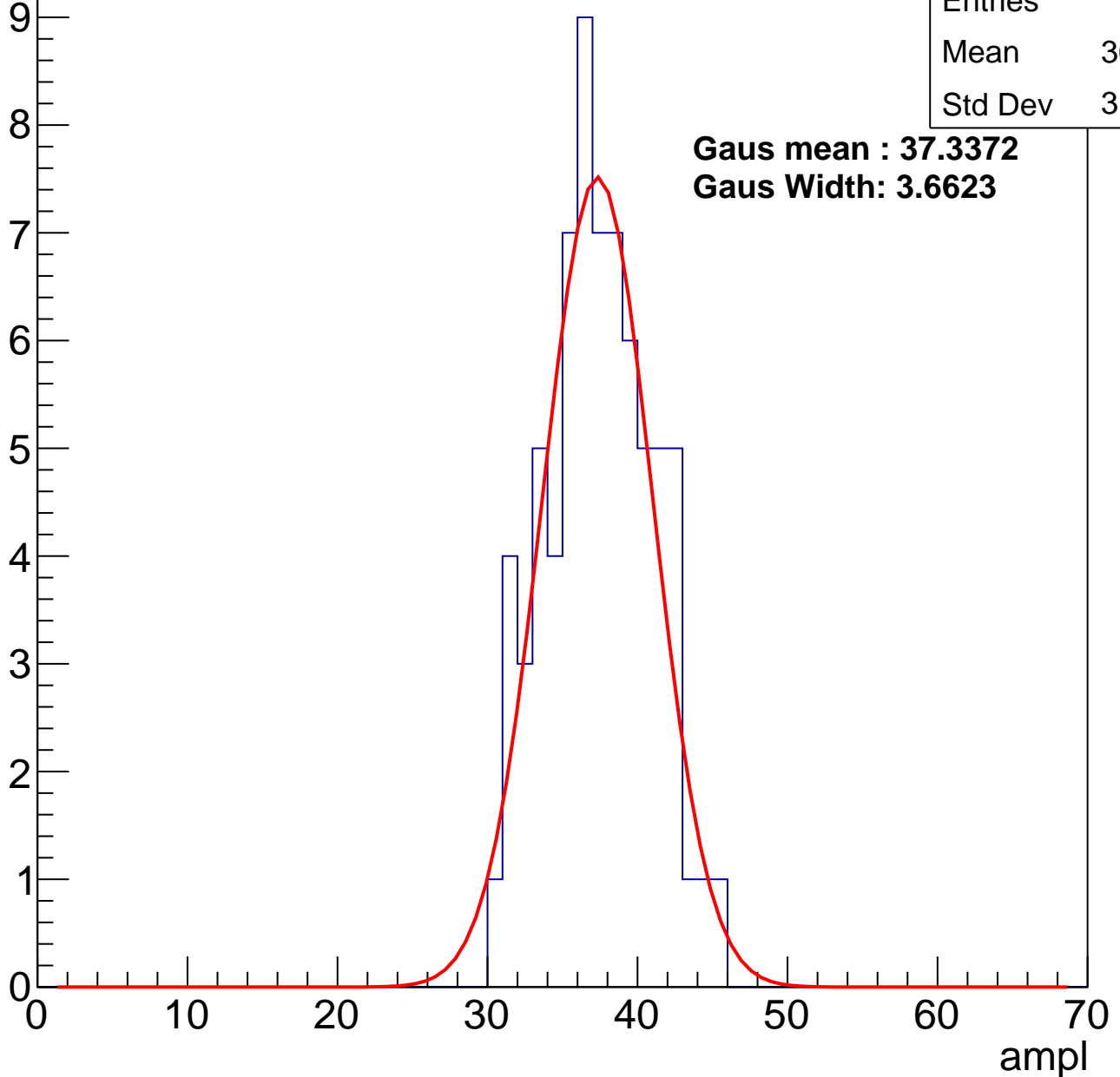
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.99
Std Dev	3.462

**Gaus mean : 37.3372**

**Gaus Width: 3.6623**



# B0L000S, U13-ch24, adc2

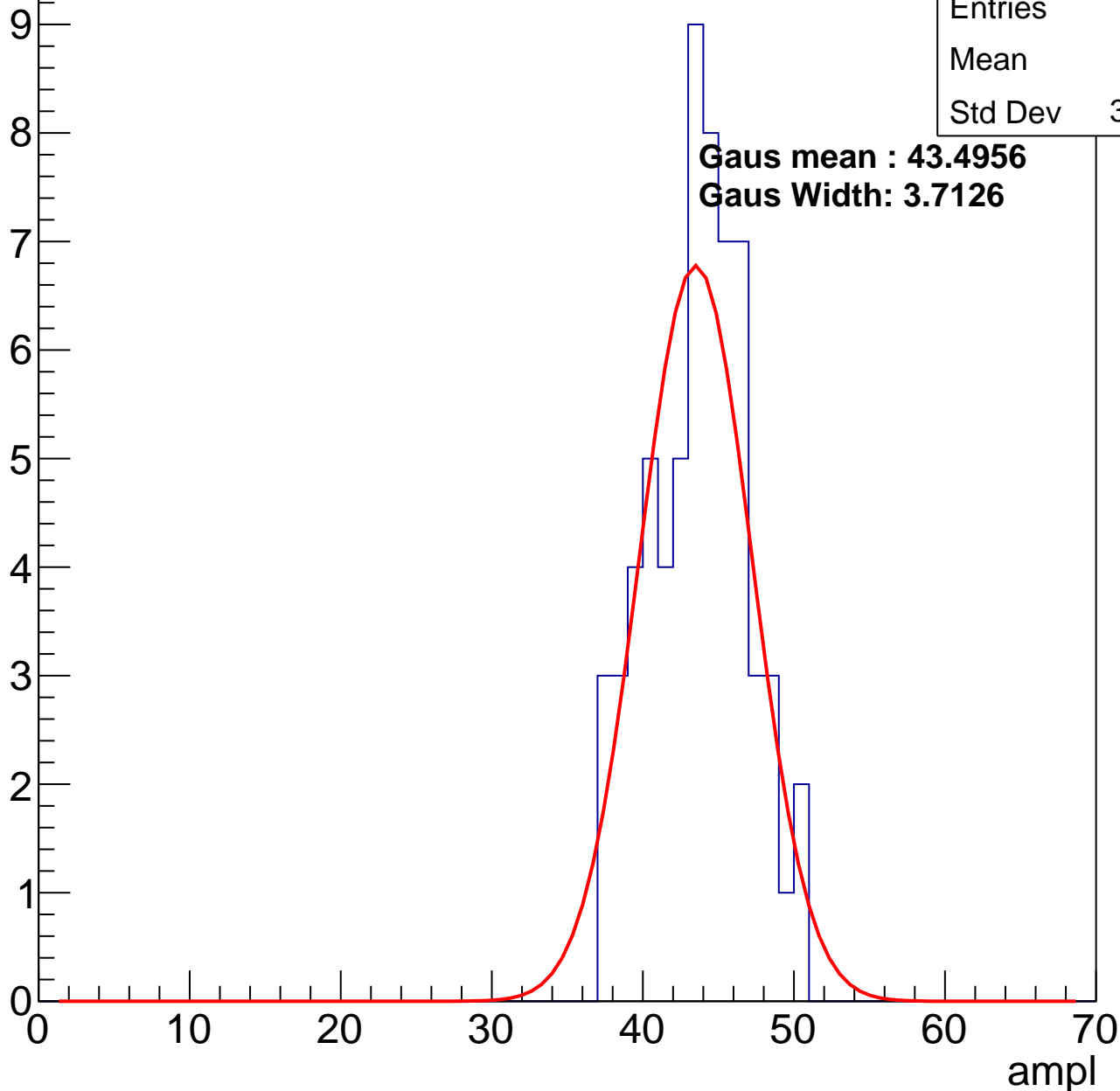
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.2
Std Dev	3.227

**Gaus mean : 43.4956**

**Gaus Width: 3.7126**

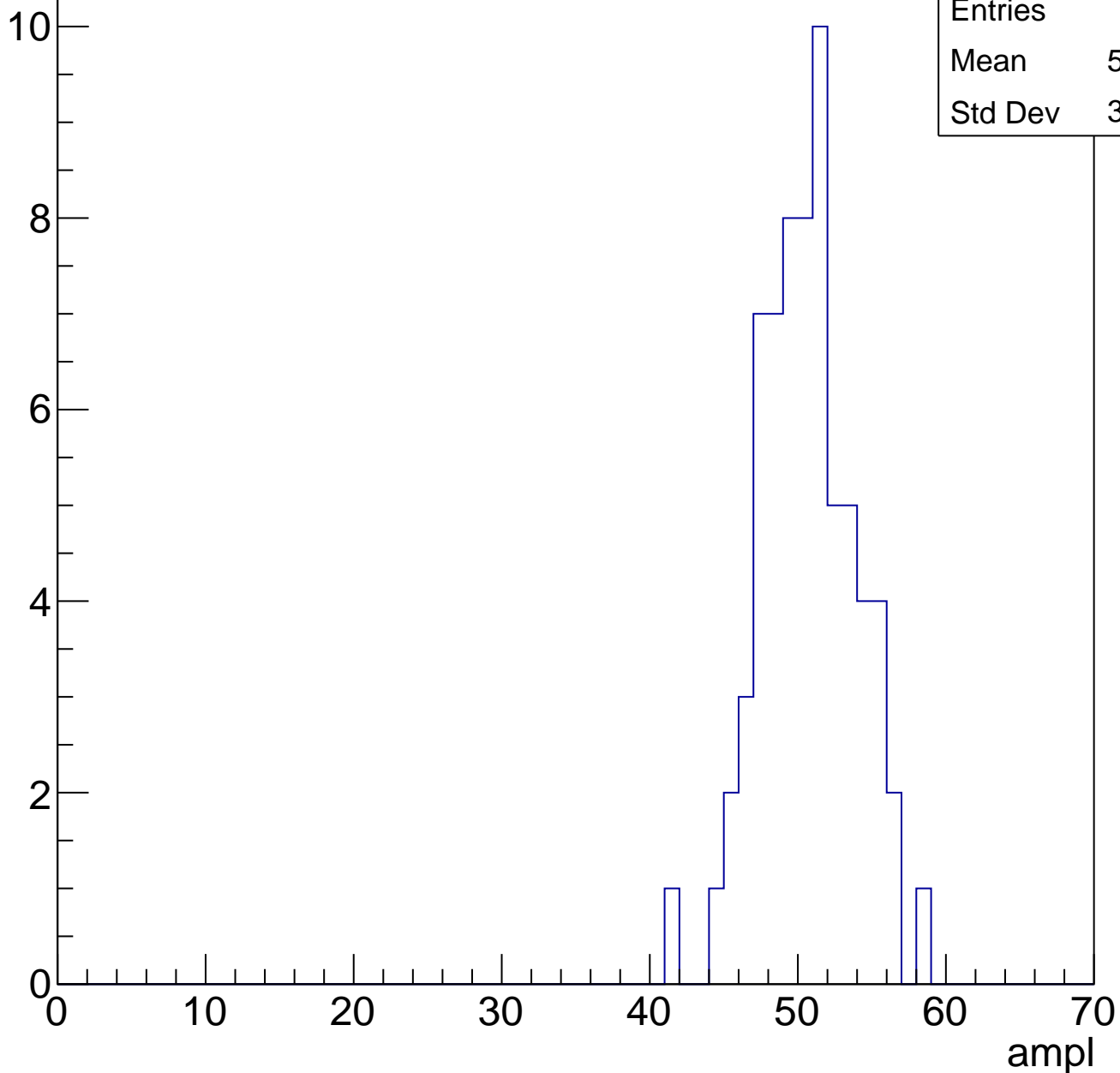


# B0L000S, U13-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.16
Std Dev	3.179

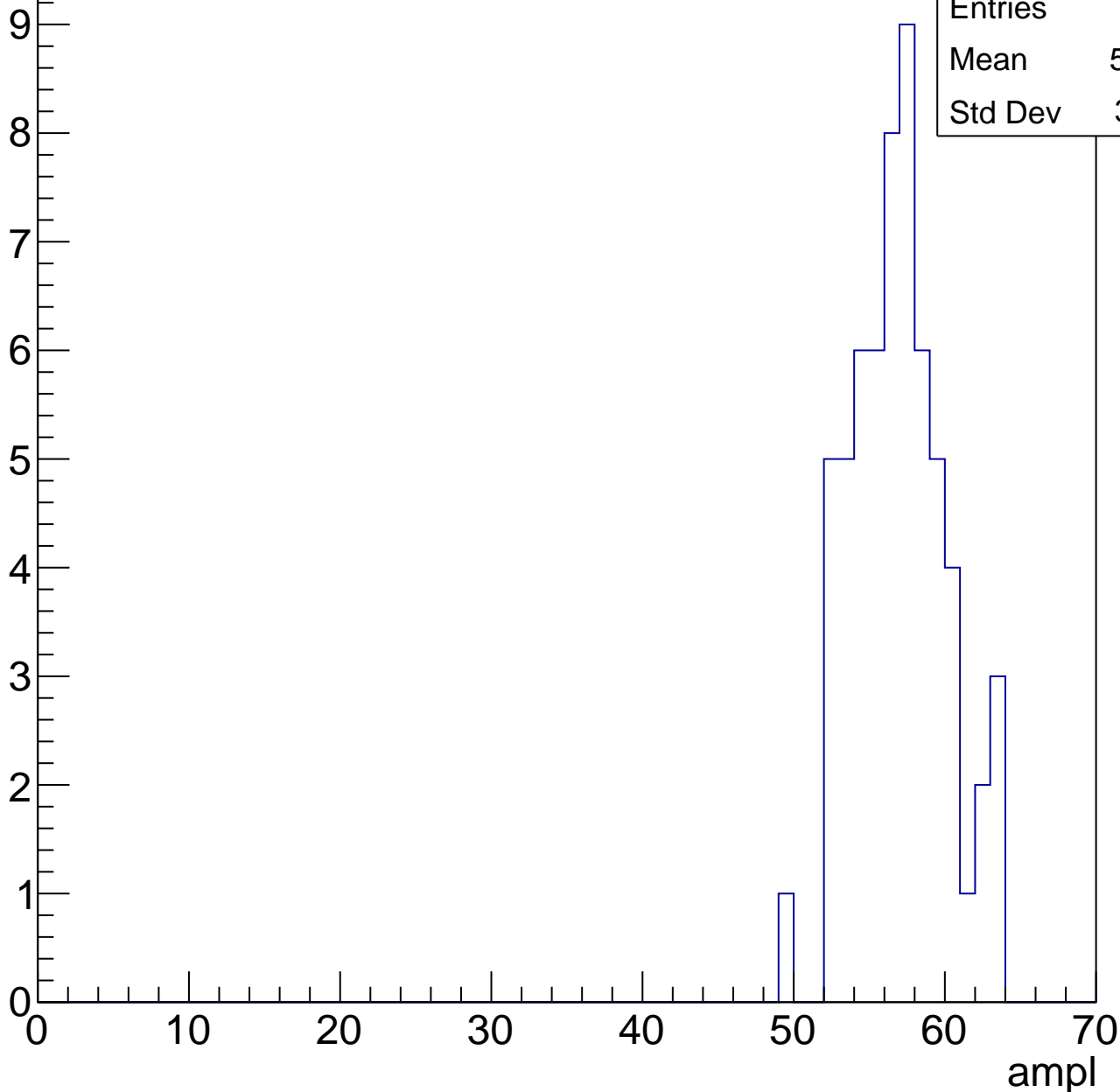


# B0L000S, U13-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.49
Std Dev	3.071

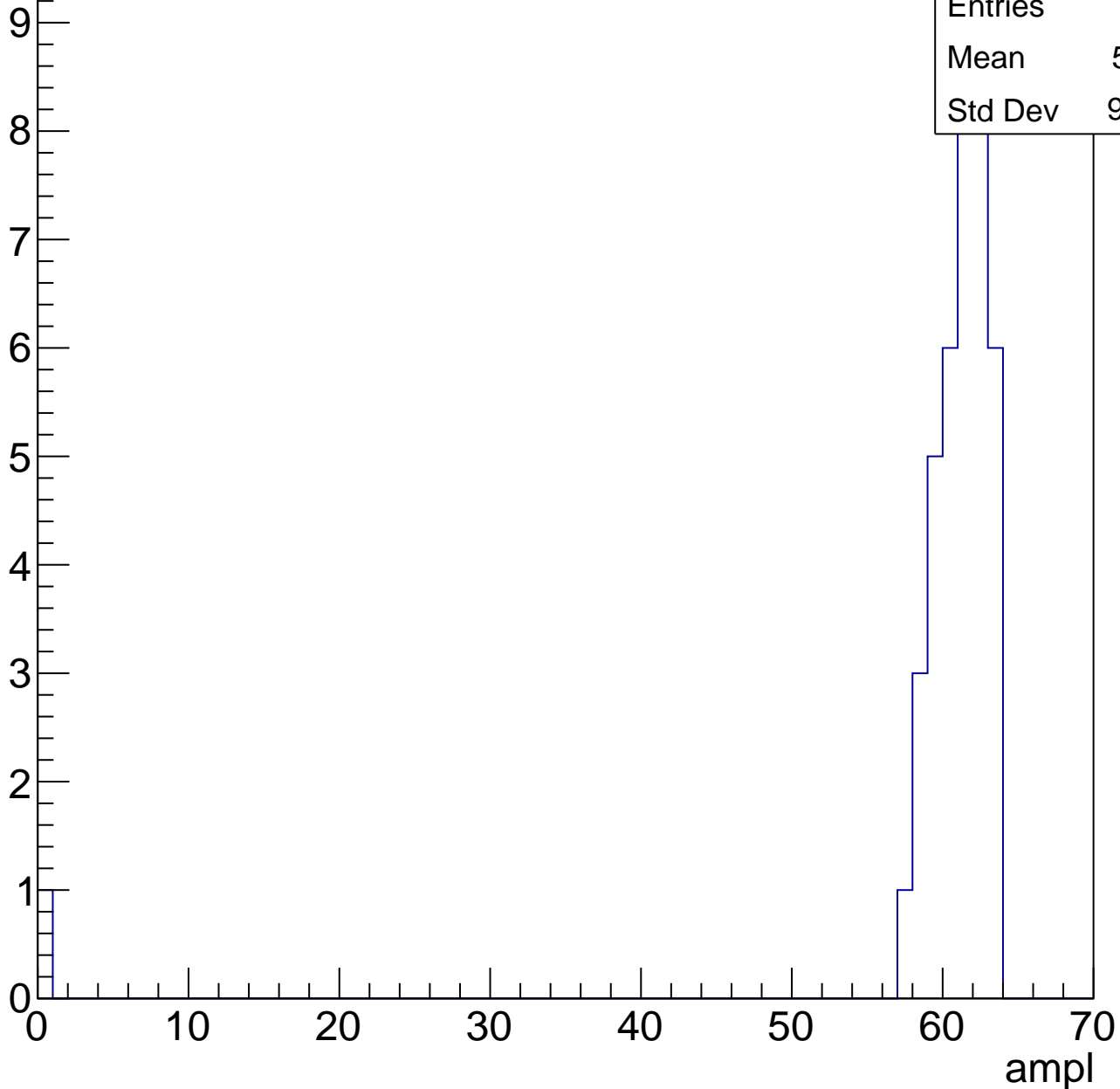


# B0L000S, U13-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	59.21
Std Dev	9.735



# B0L000S, U13-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch25, adc0

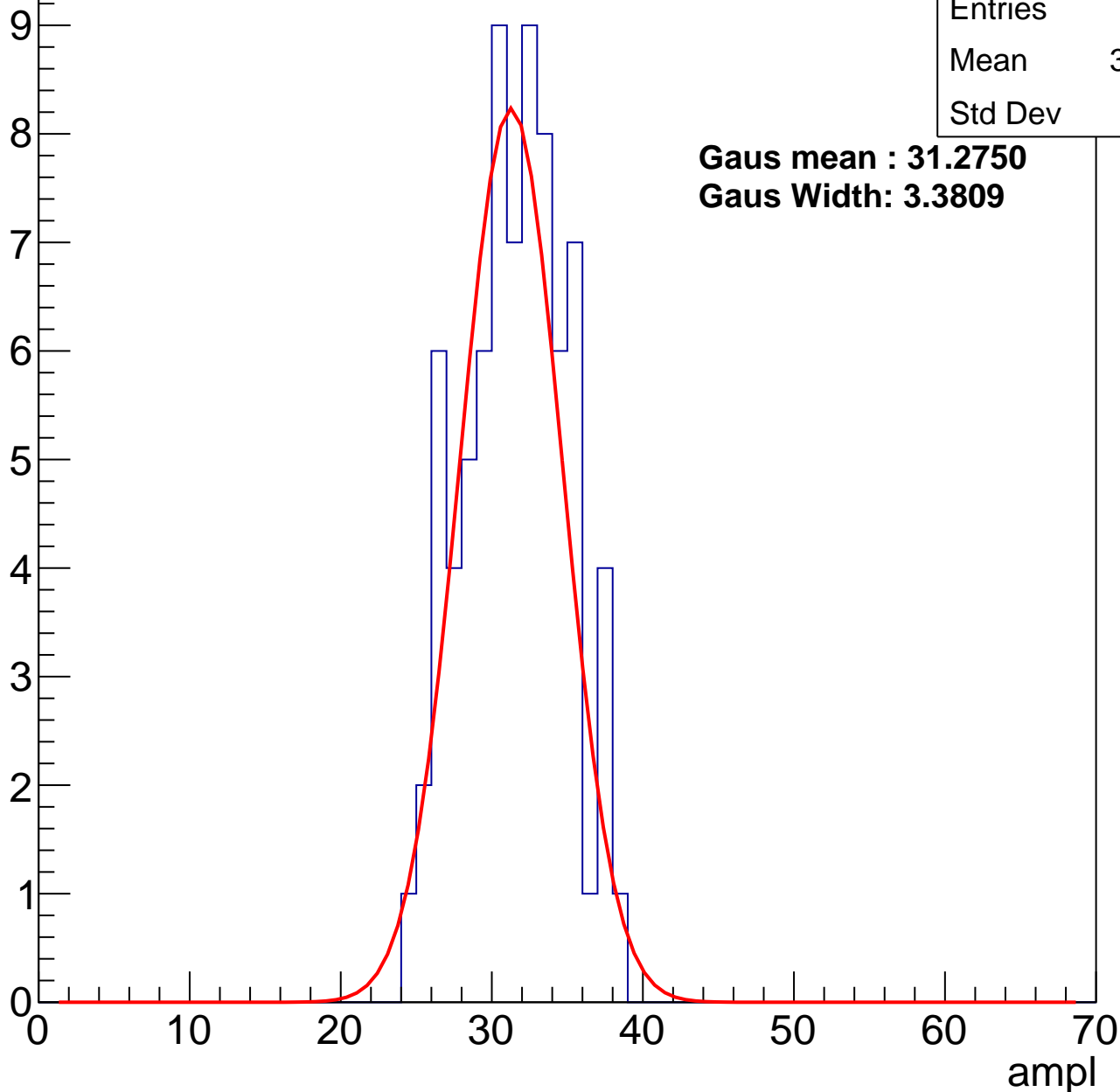
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	31.08
Std Dev	3.32

**Gaus mean : 31.2750**

**Gaus Width: 3.3809**



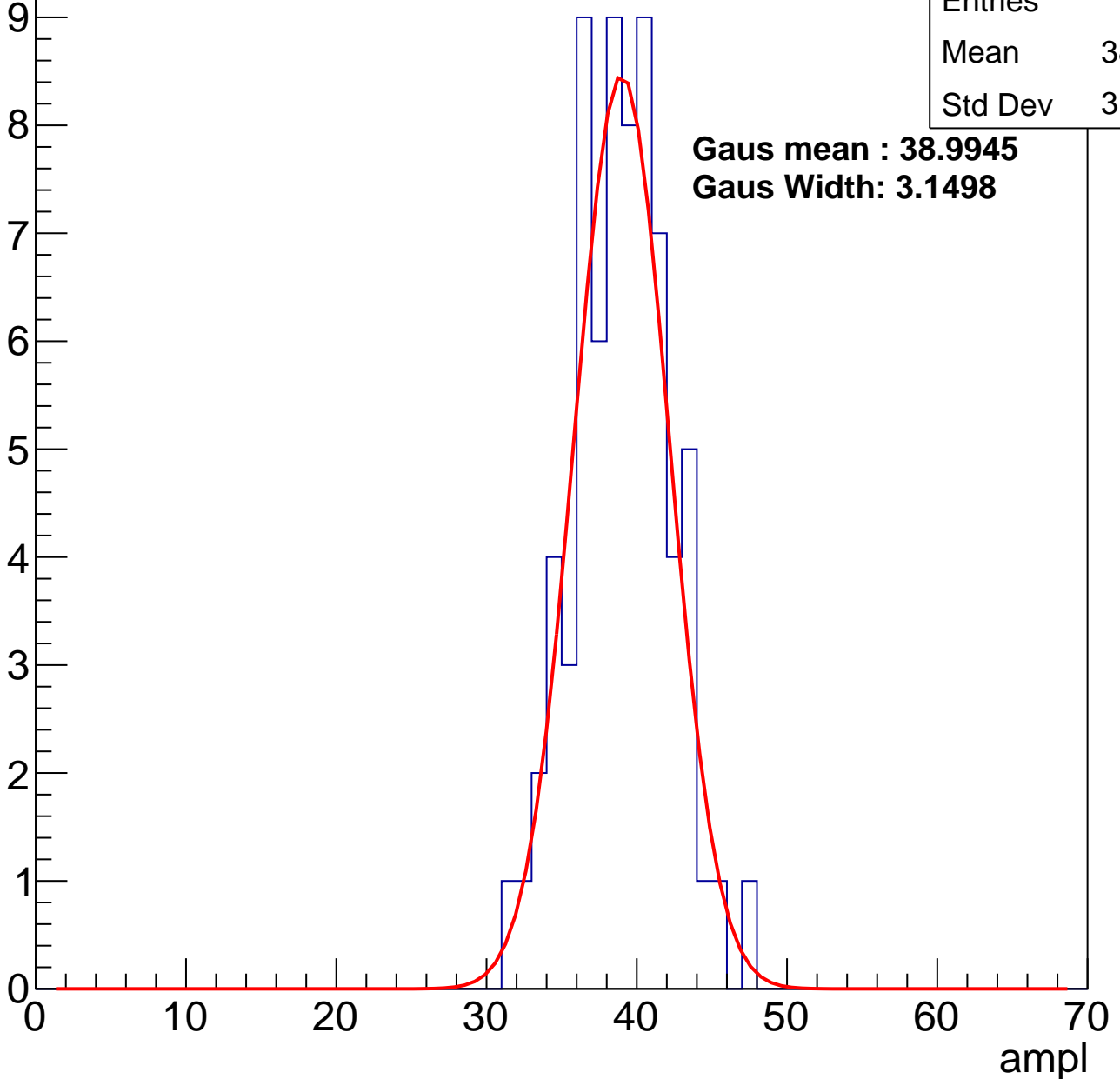
# B0L000S, U13-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	38.54
Std Dev	3.162

**Gaus mean : 38.9945**  
**Gaus Width: 3.1498**



# B0L000S, U13-ch25, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	45.3
Std Dev	3.045

**Gaus mean : 45.7428**

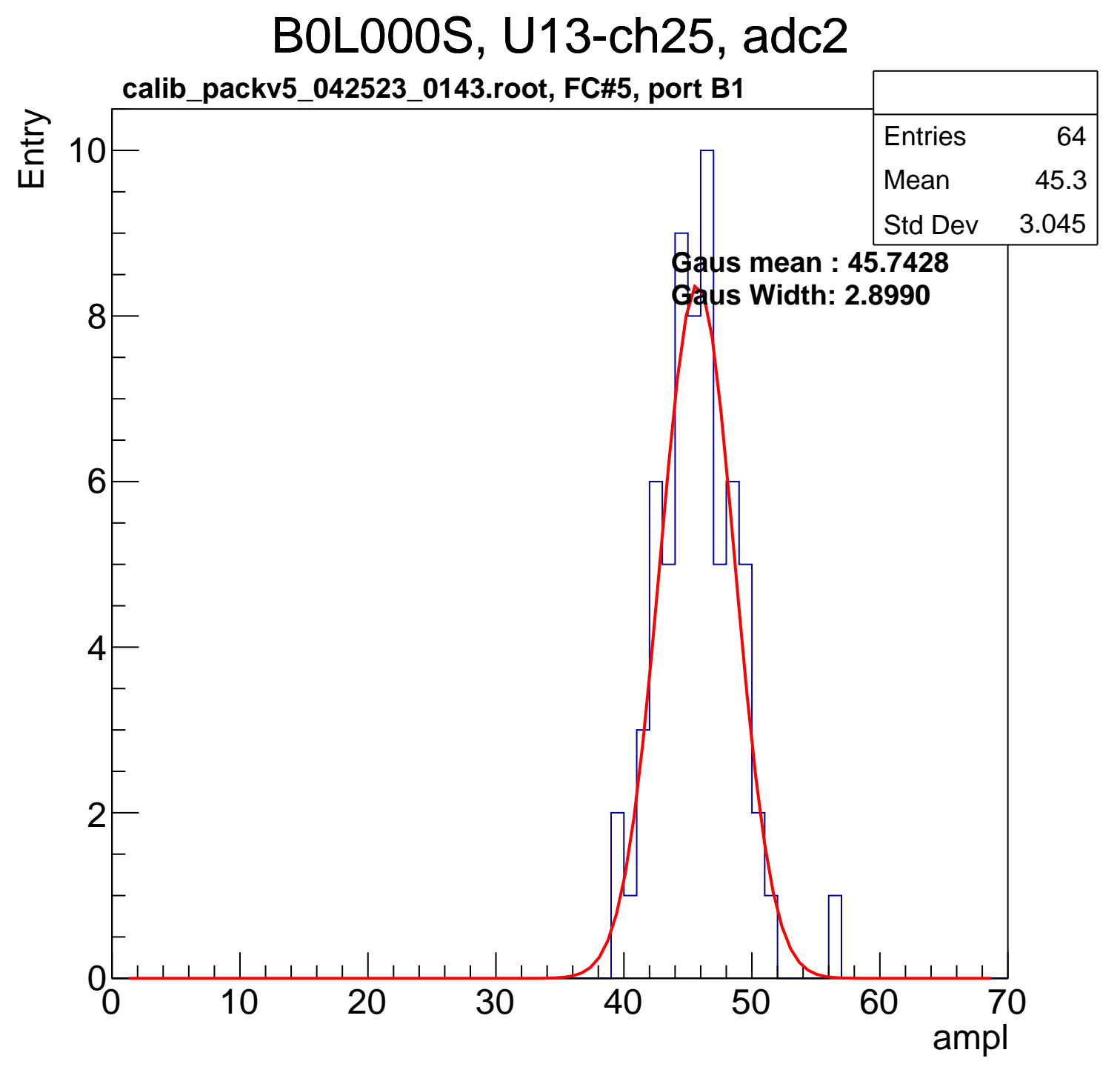
**Gaus Width: 2.8990**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

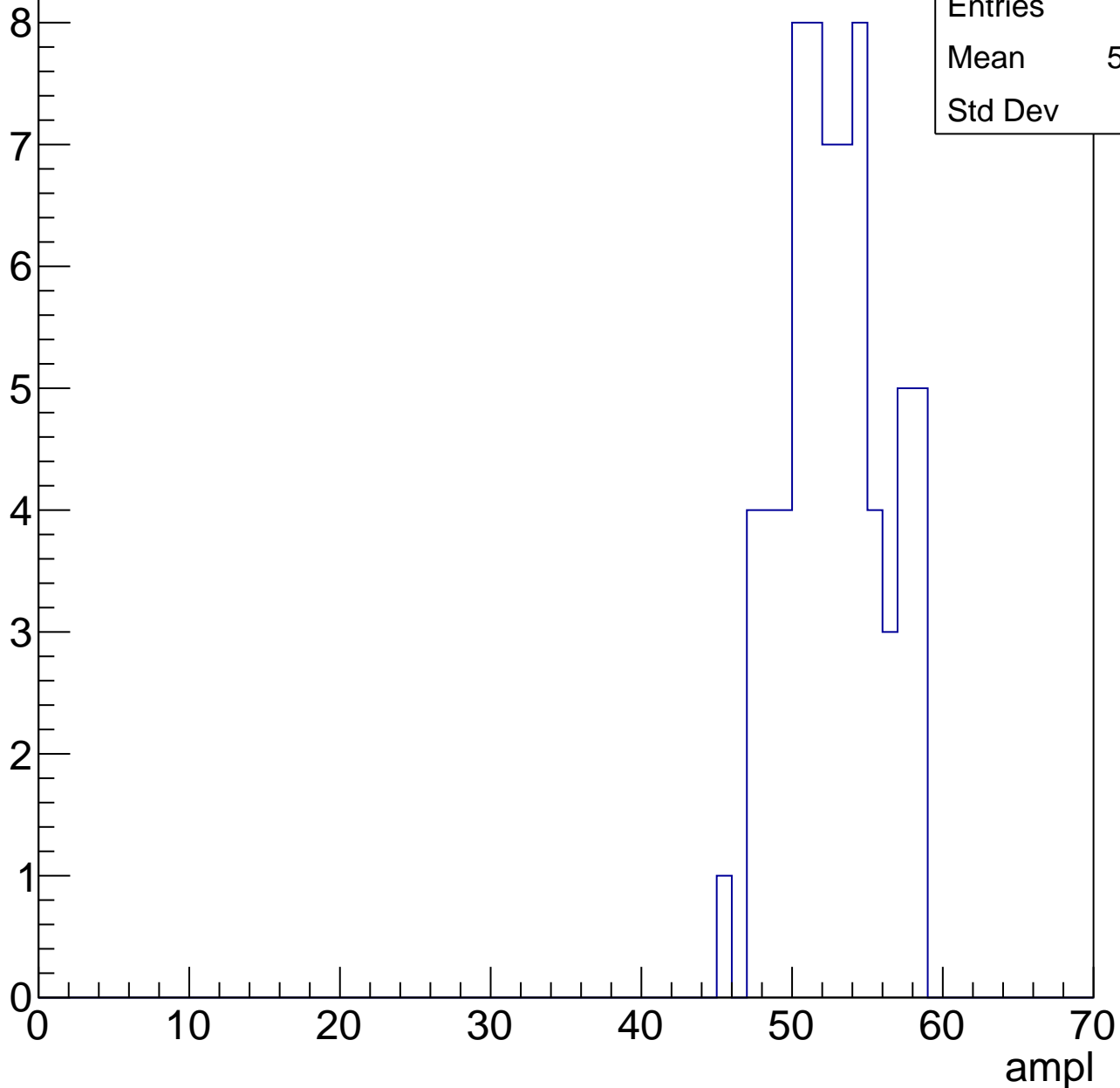


# B0L000S, U13-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	52.34
Std Dev	3.23

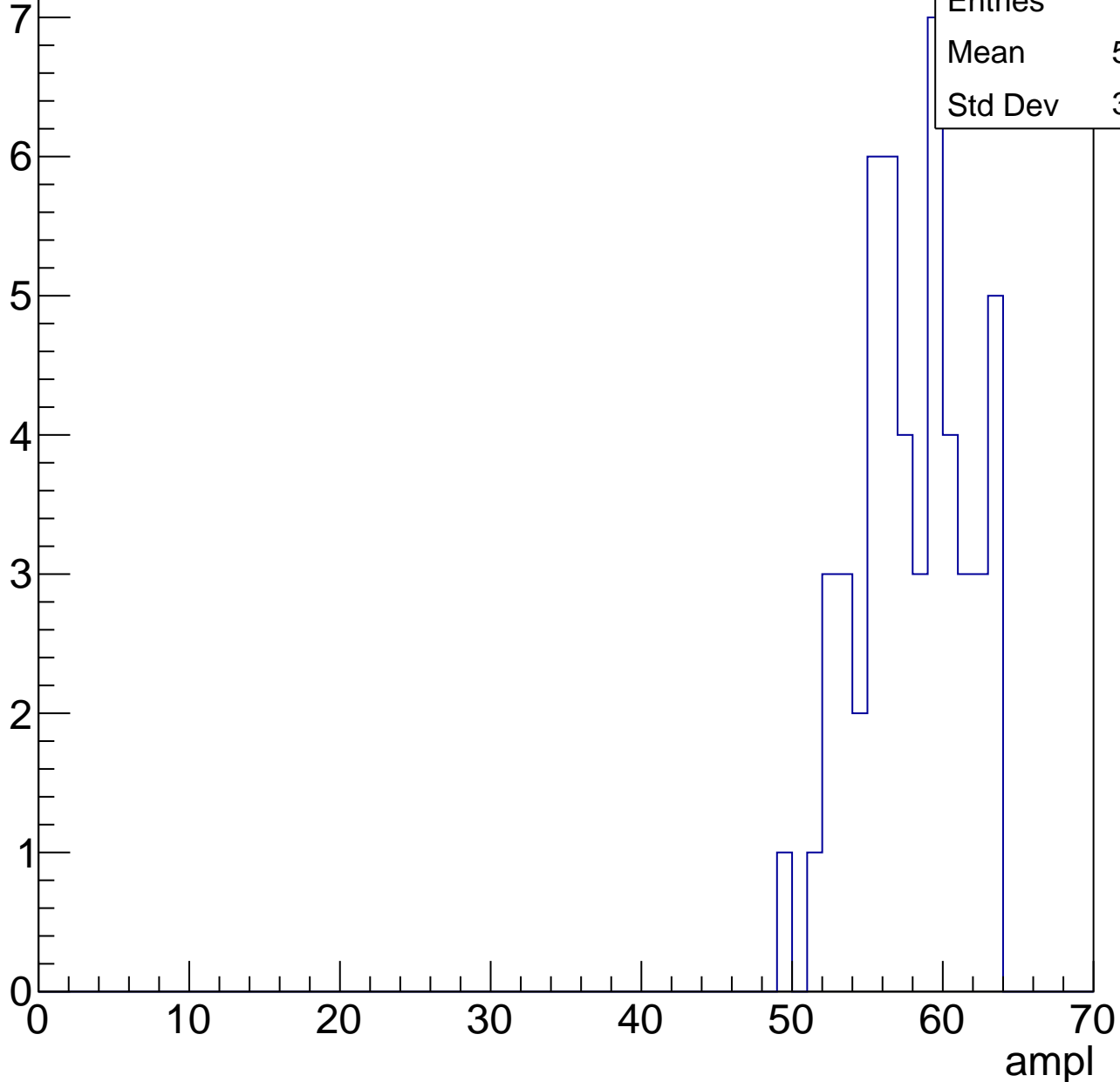


# B0L000S, U13-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	57.41
Std Dev	3.521

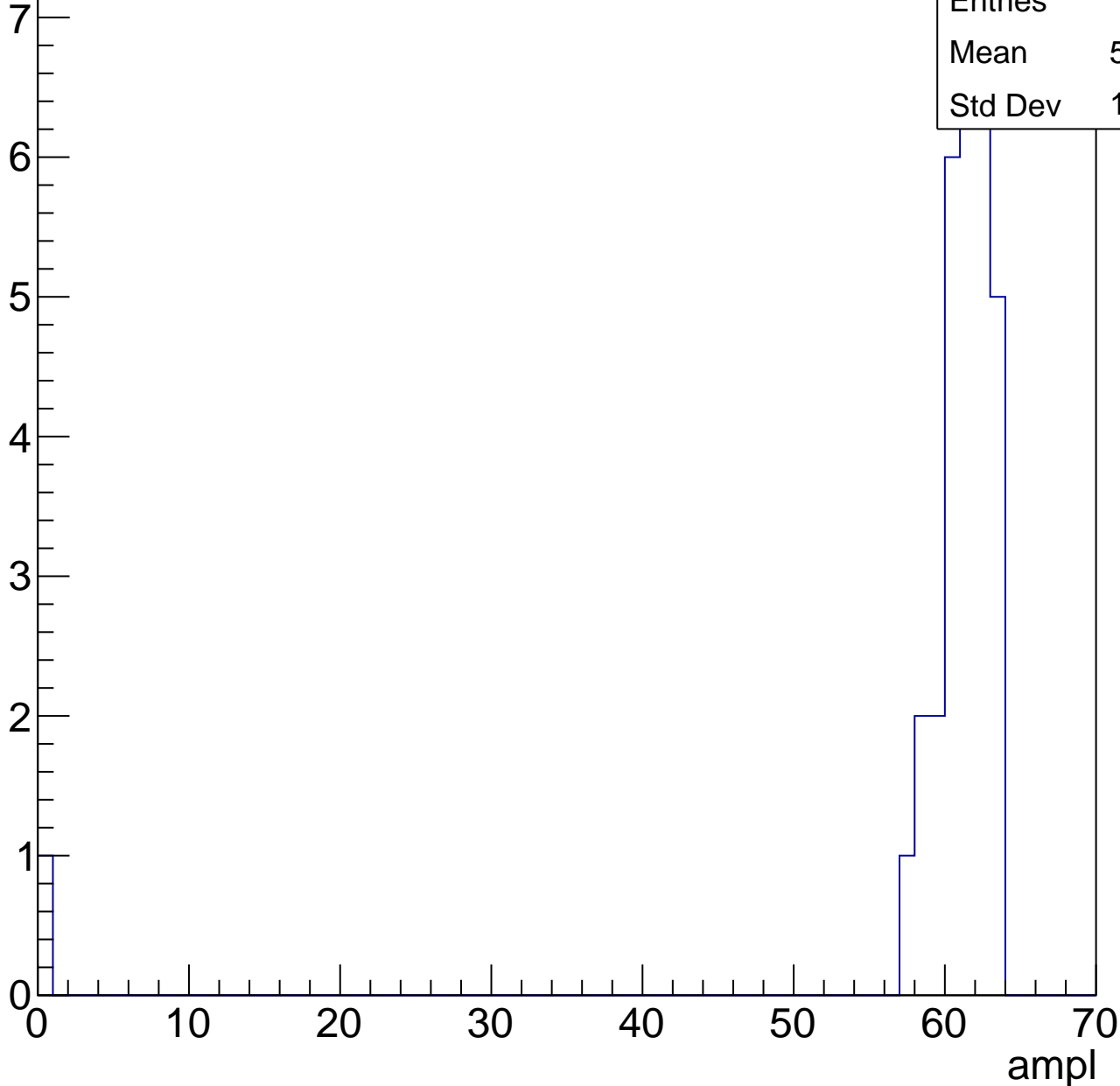


# B0L000S, U13-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	58.94
Std Dev	10.87



# B0L000S, U13-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

ampl

Entries	1
Mean	59
Std Dev	0



# B0L000S, U13-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch26, adc0

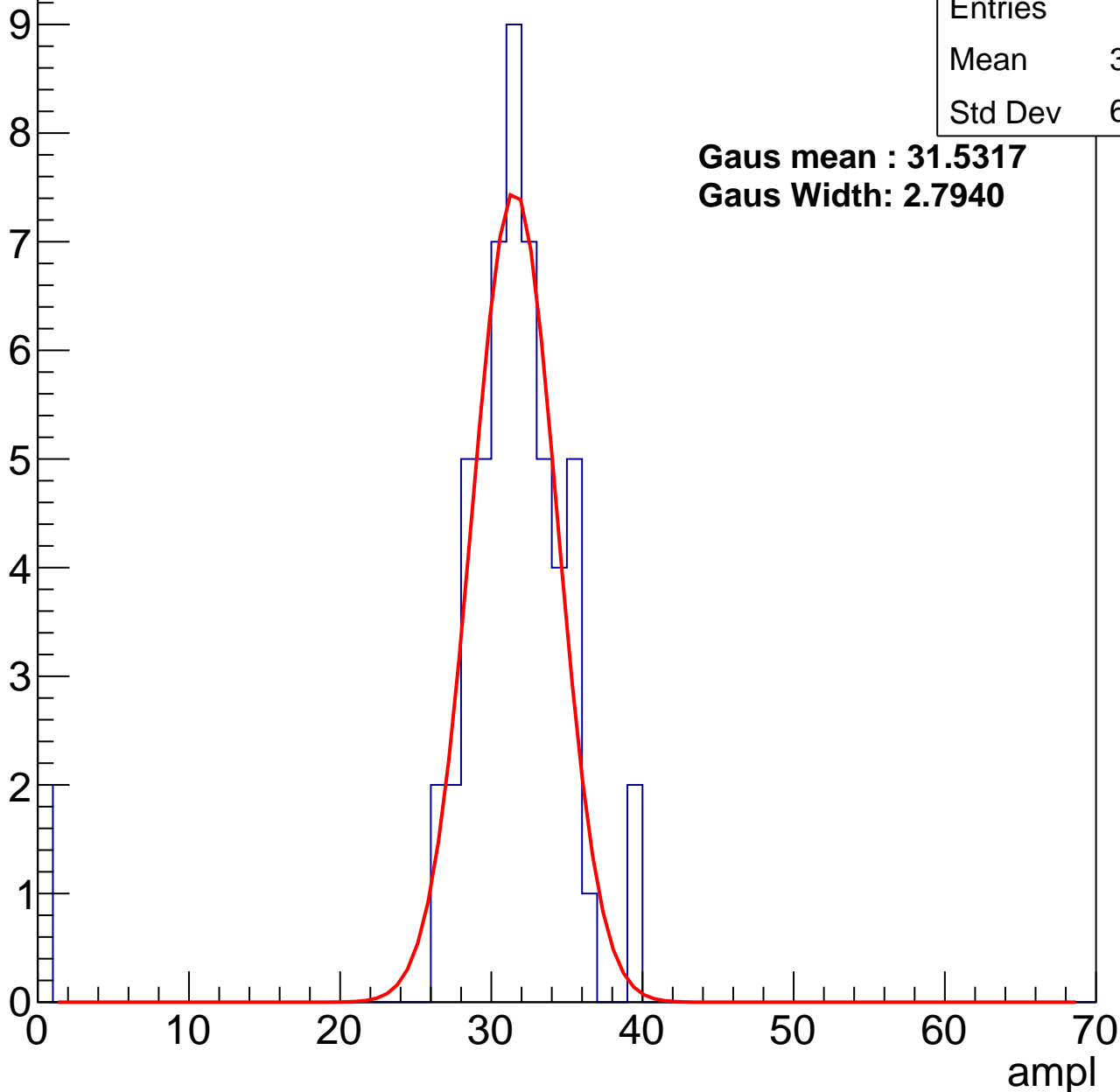
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	30.25
Std Dev	6.465

**Gaus mean : 31.5317**

**Gaus Width: 2.7940**

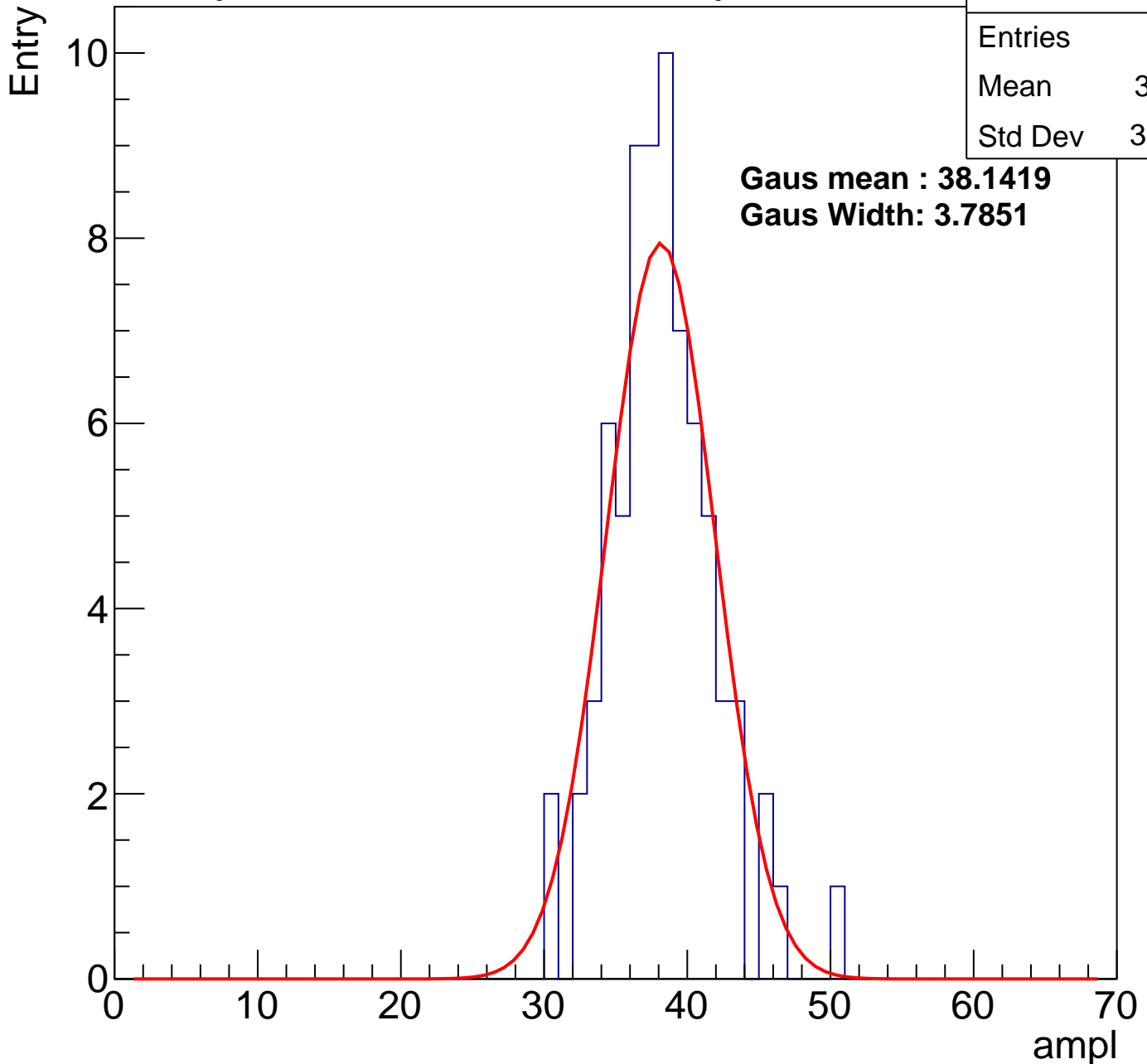


# B0L000S, U13-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	37.81
Std Dev	3.608

**Gaus mean : 38.1419**  
**Gaus Width: 3.7851**



# B0L000S, U13-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	45.11
Std Dev	3.422

**Gaus mean : 45.5062**

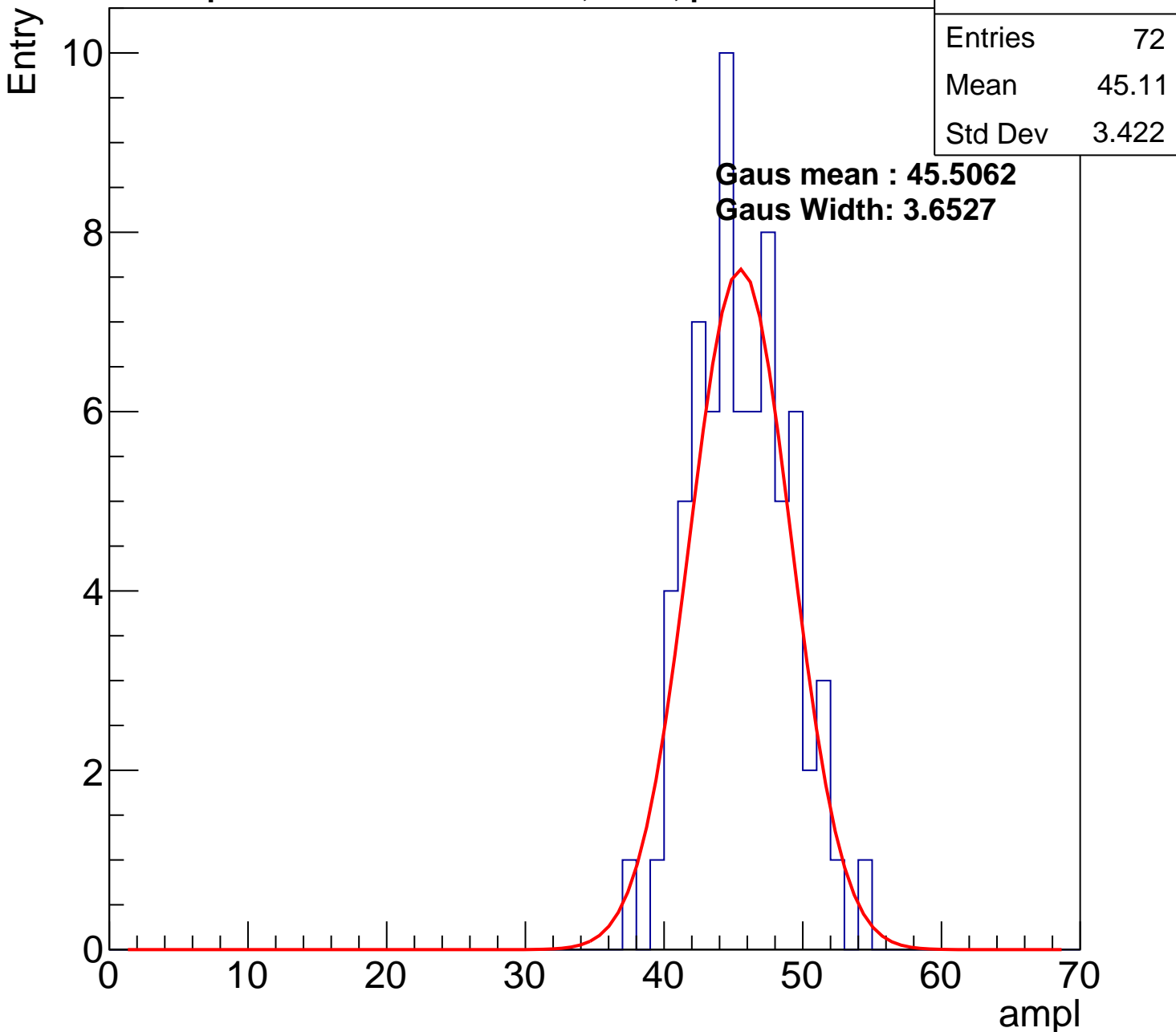
**Gaus Width: 3.6527**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

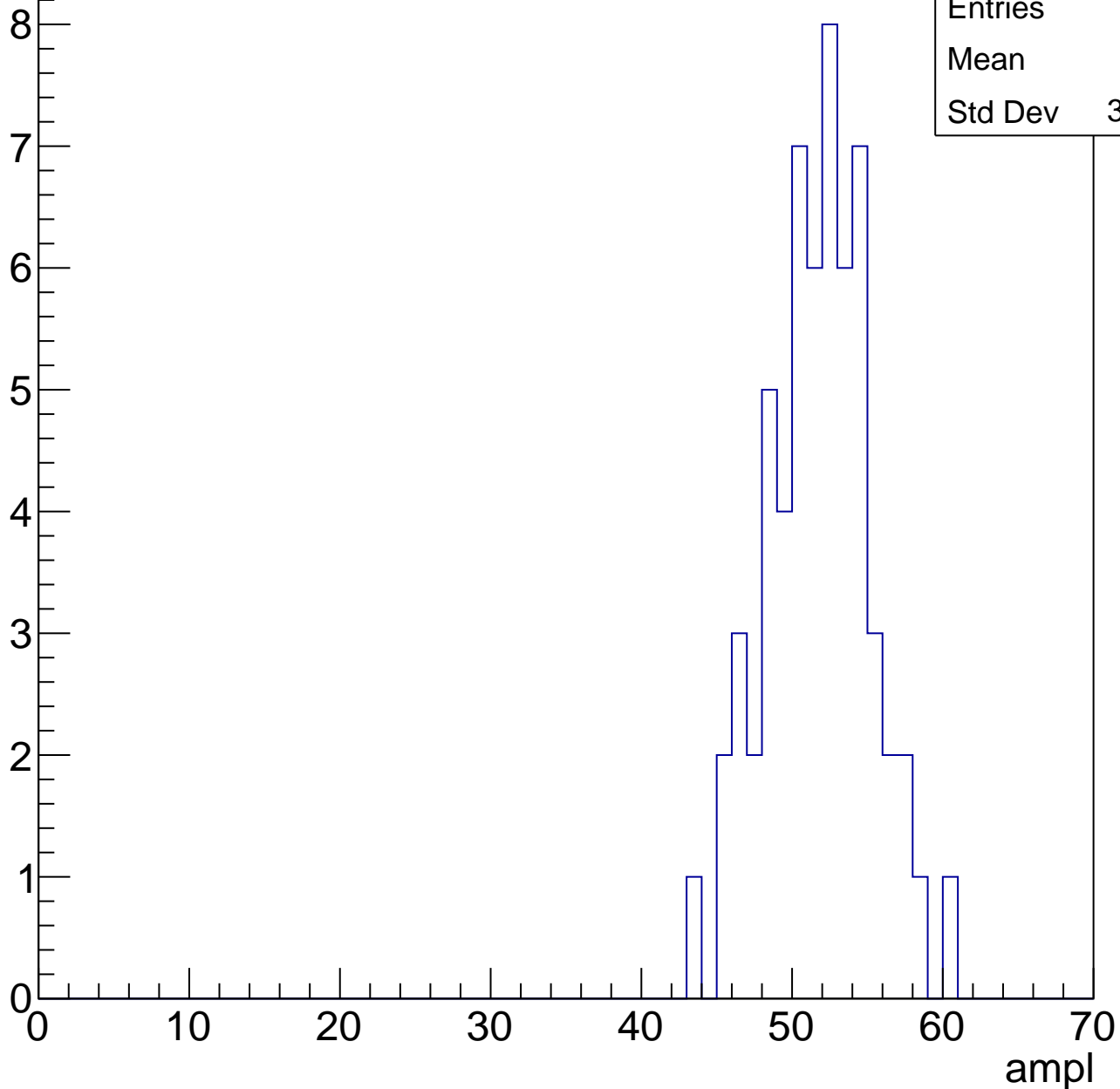


# B0L000S, U13-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

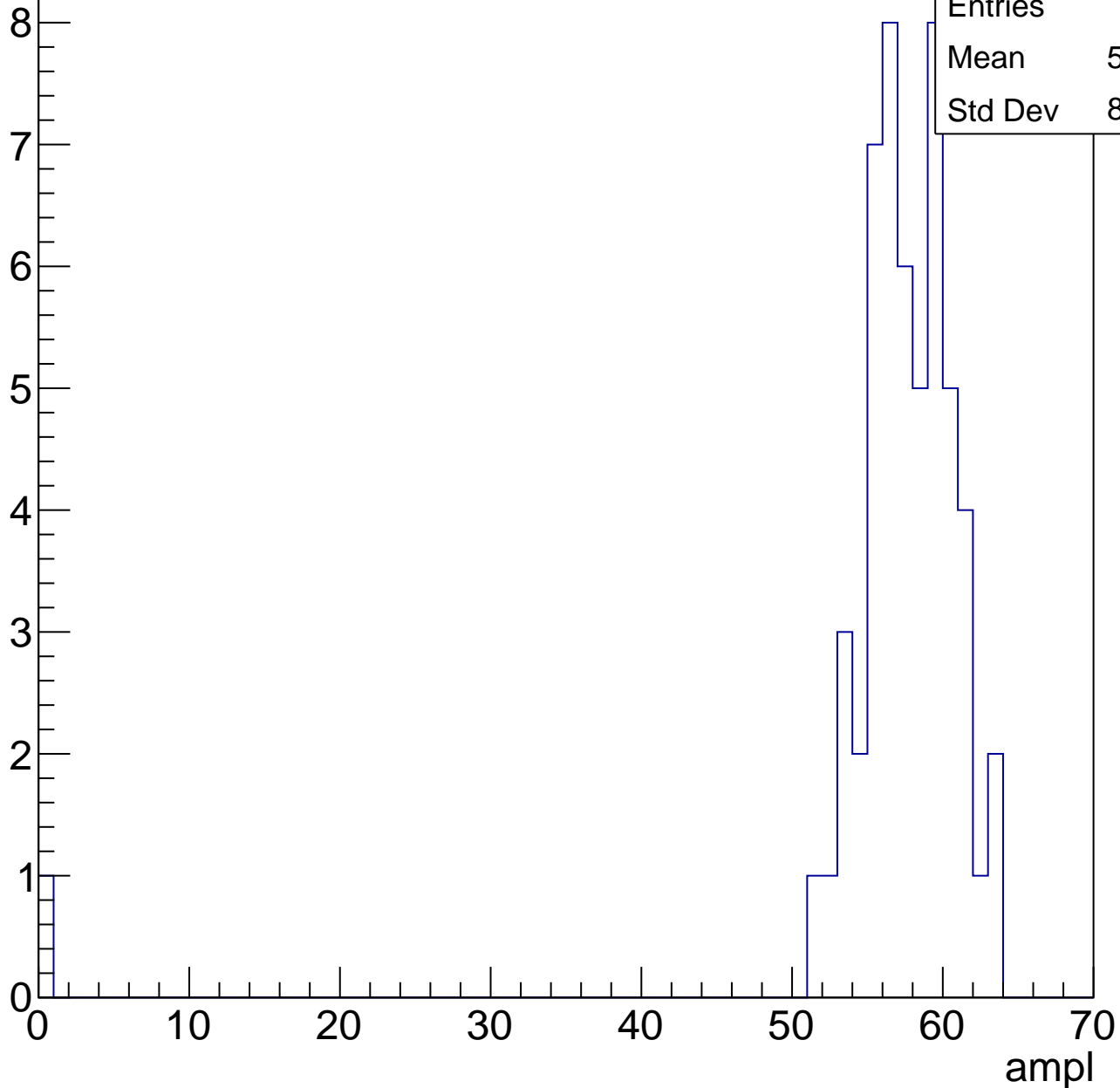
Entries	60
Mean	51.3
Std Dev	3.417



# B0L000S, U13-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

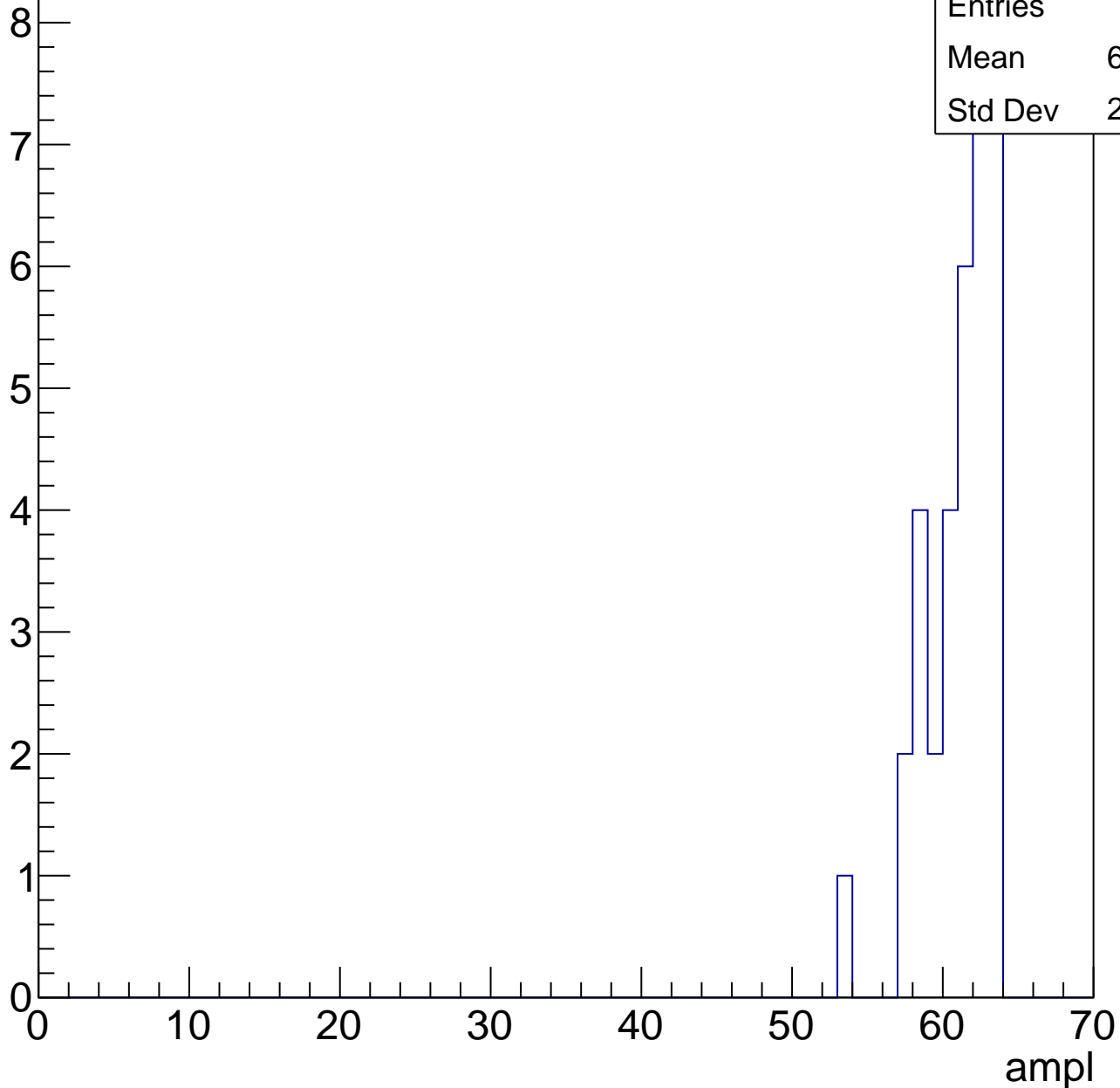


# B0L000S, U13-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	60.66
Std Dev	2.267



# B0L000S, U13-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

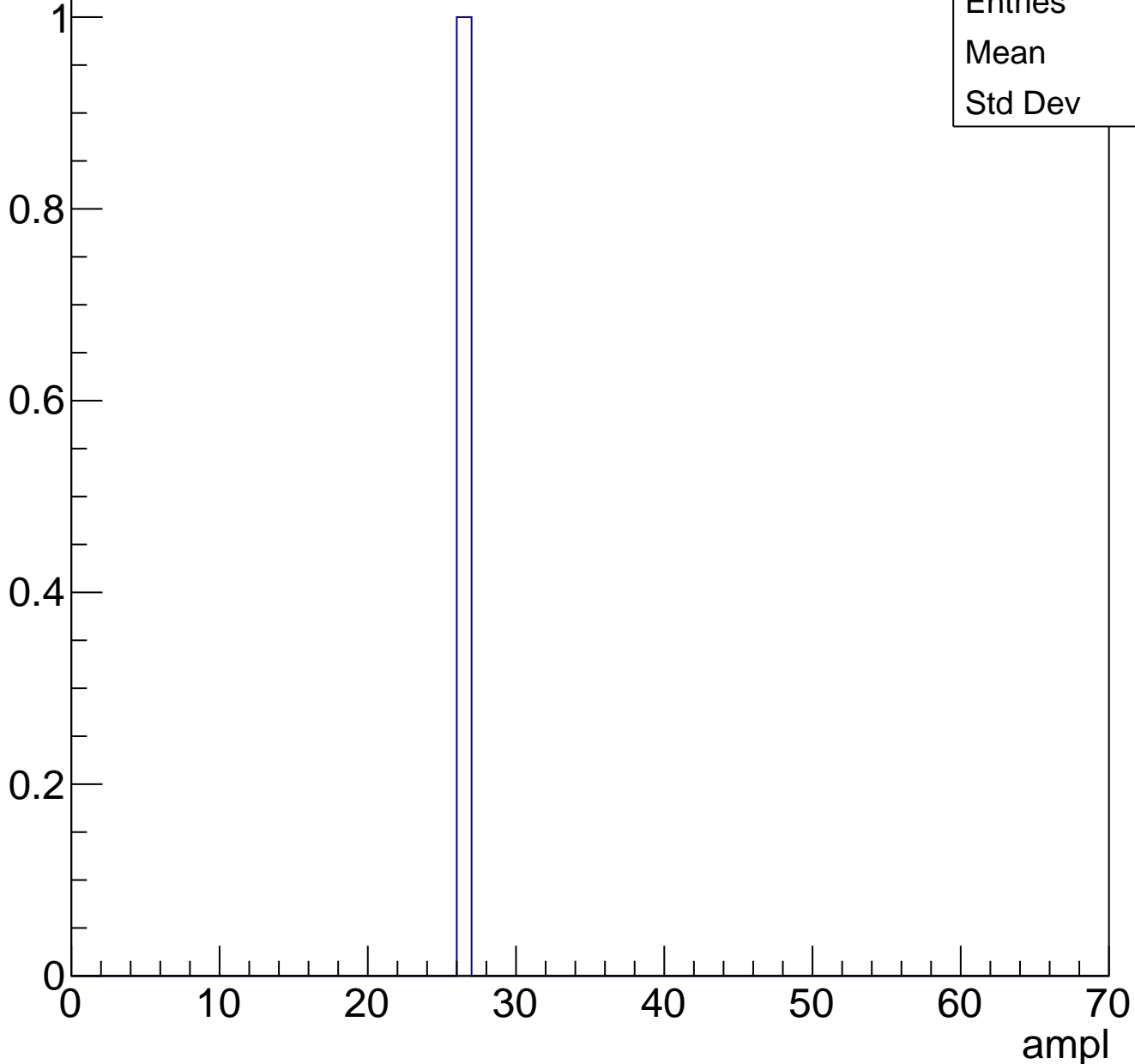




# B0L000S, U13-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	26
Std Dev	0

# B0L000S, U13-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	104
Mean	31.53
Std Dev	4.922

**Gaus mean : 32.3925**

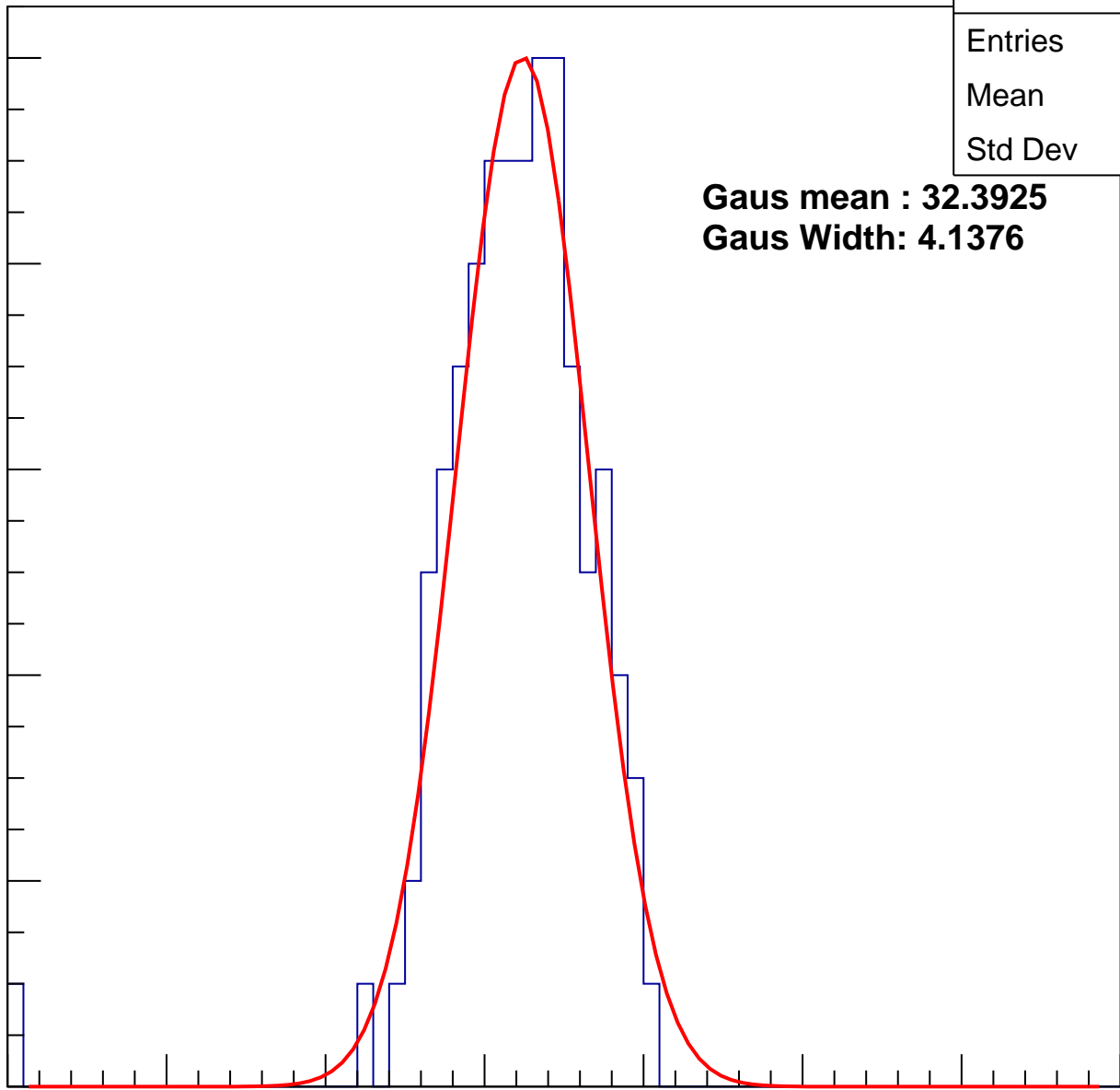
**Gaus Width: 4.1376**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch27, adc1

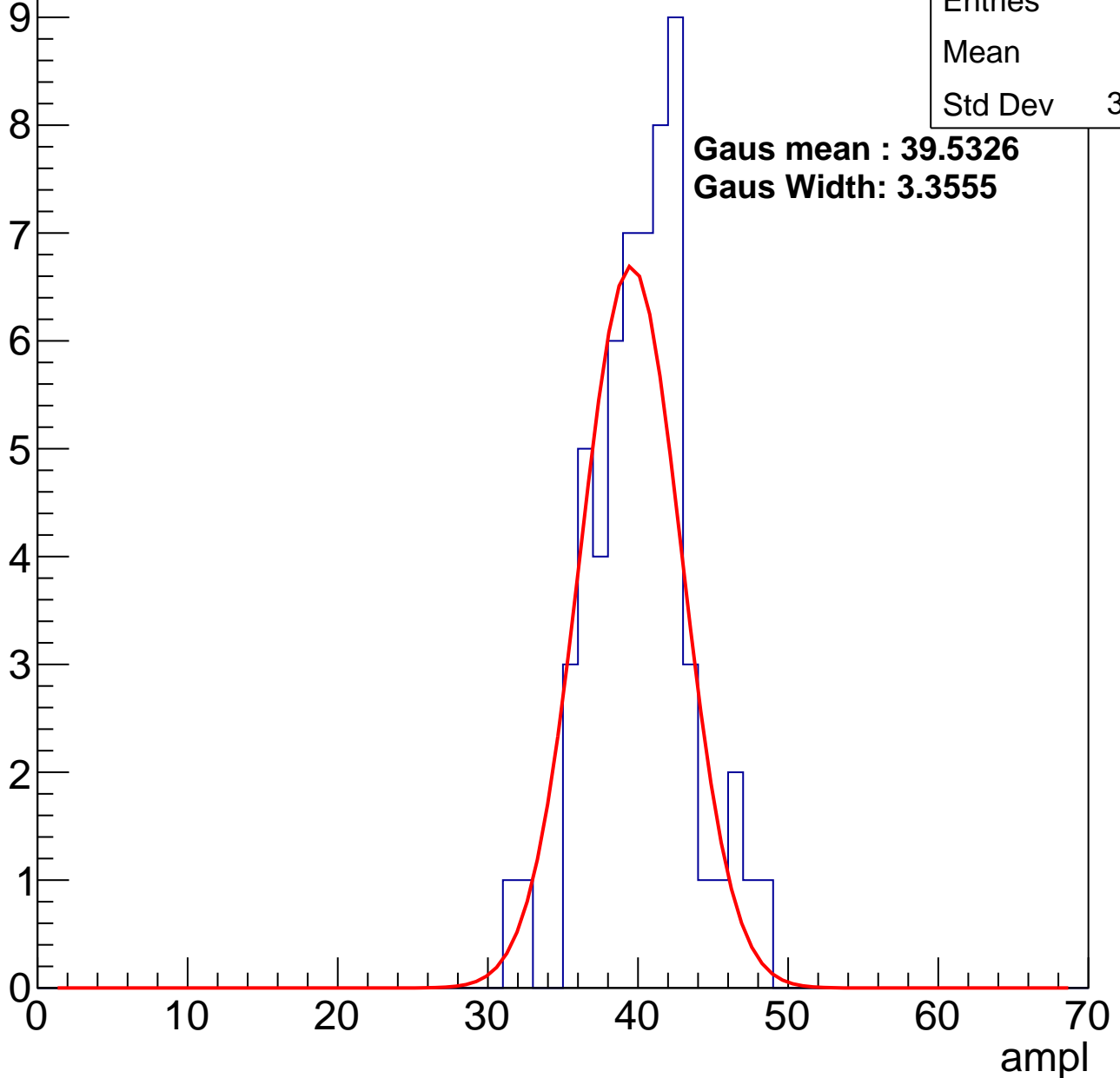
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	39.8
Std Dev	3.321

**Gaus mean : 39.5326**

**Gaus Width: 3.3555**



# B0L000S, U13-ch27, adc2

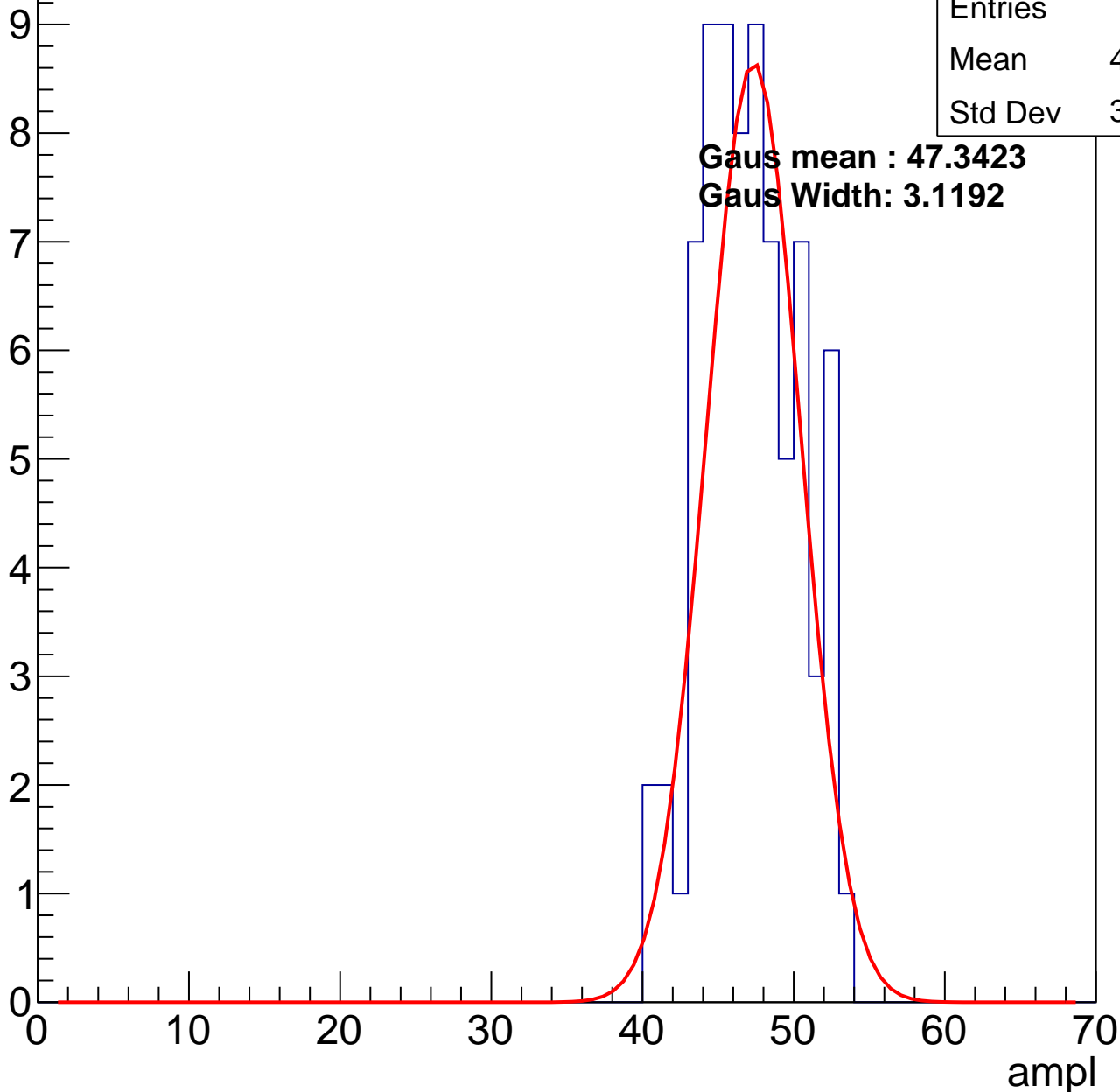
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	46.66
Std Dev	3.135

**Gaus mean : 47.3423**

**Gaus Width: 3.1192**

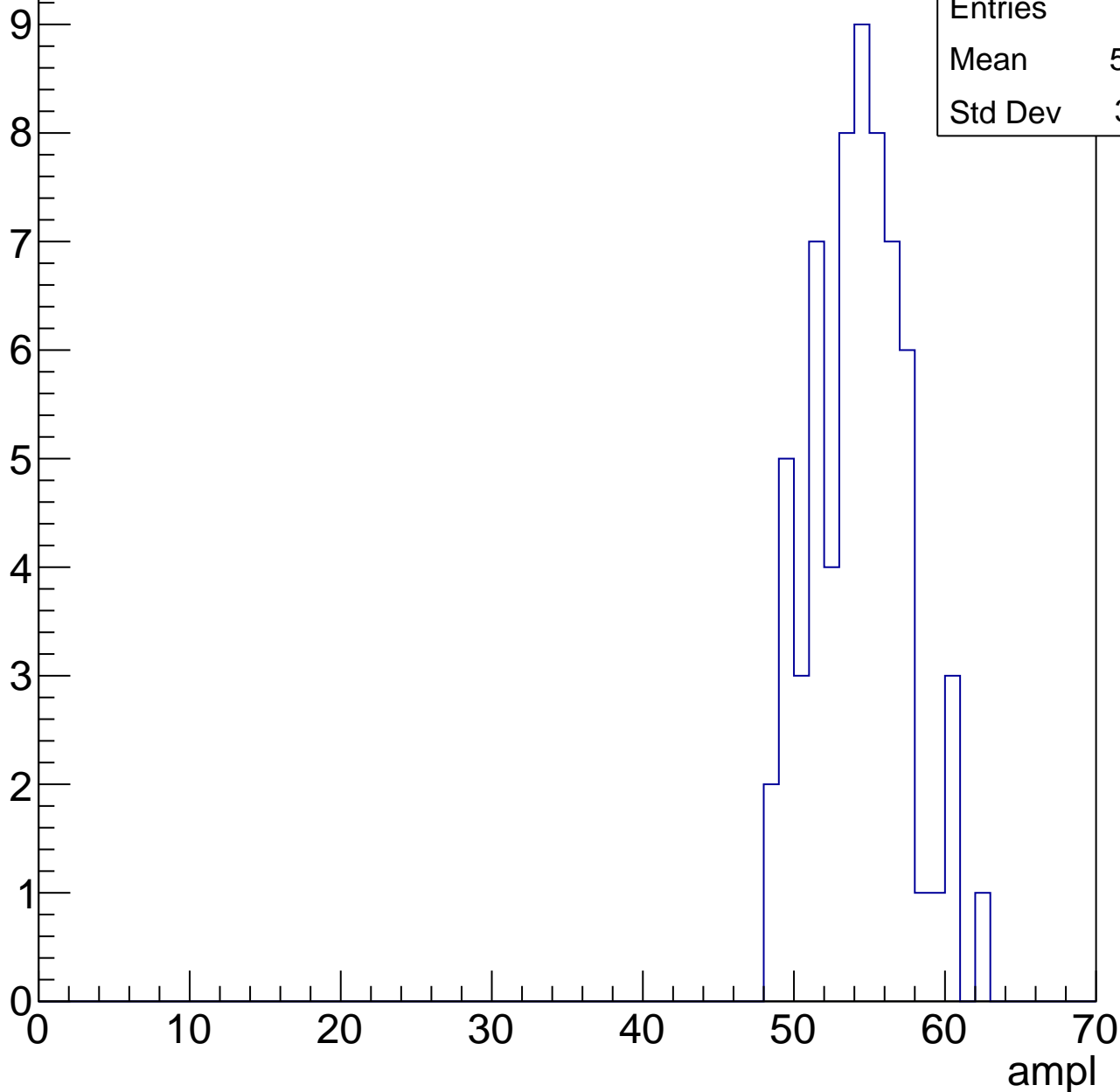


# B0L000S, U13-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	53.83
Std Dev	3.121

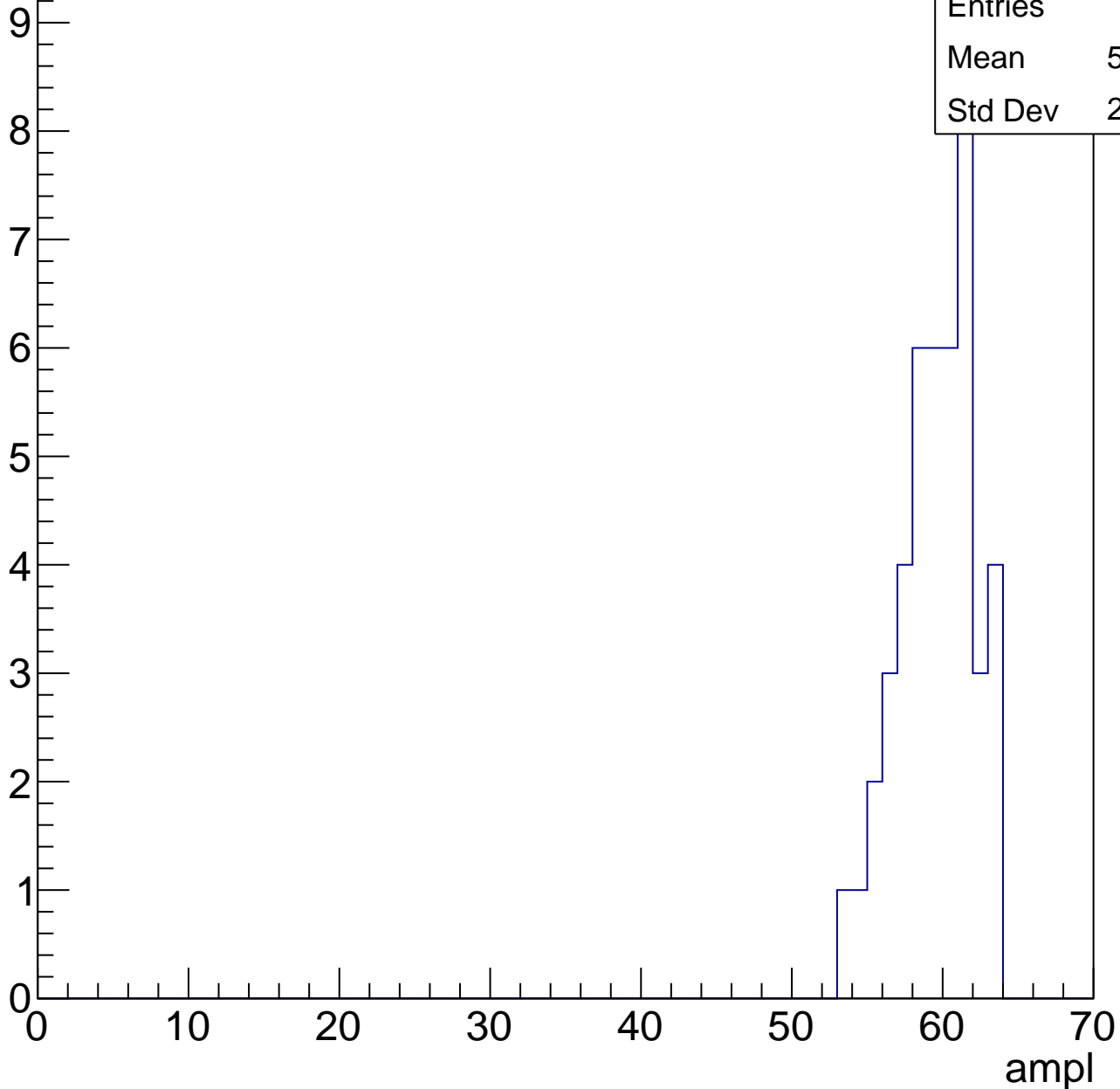


# B0L000S, U13-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

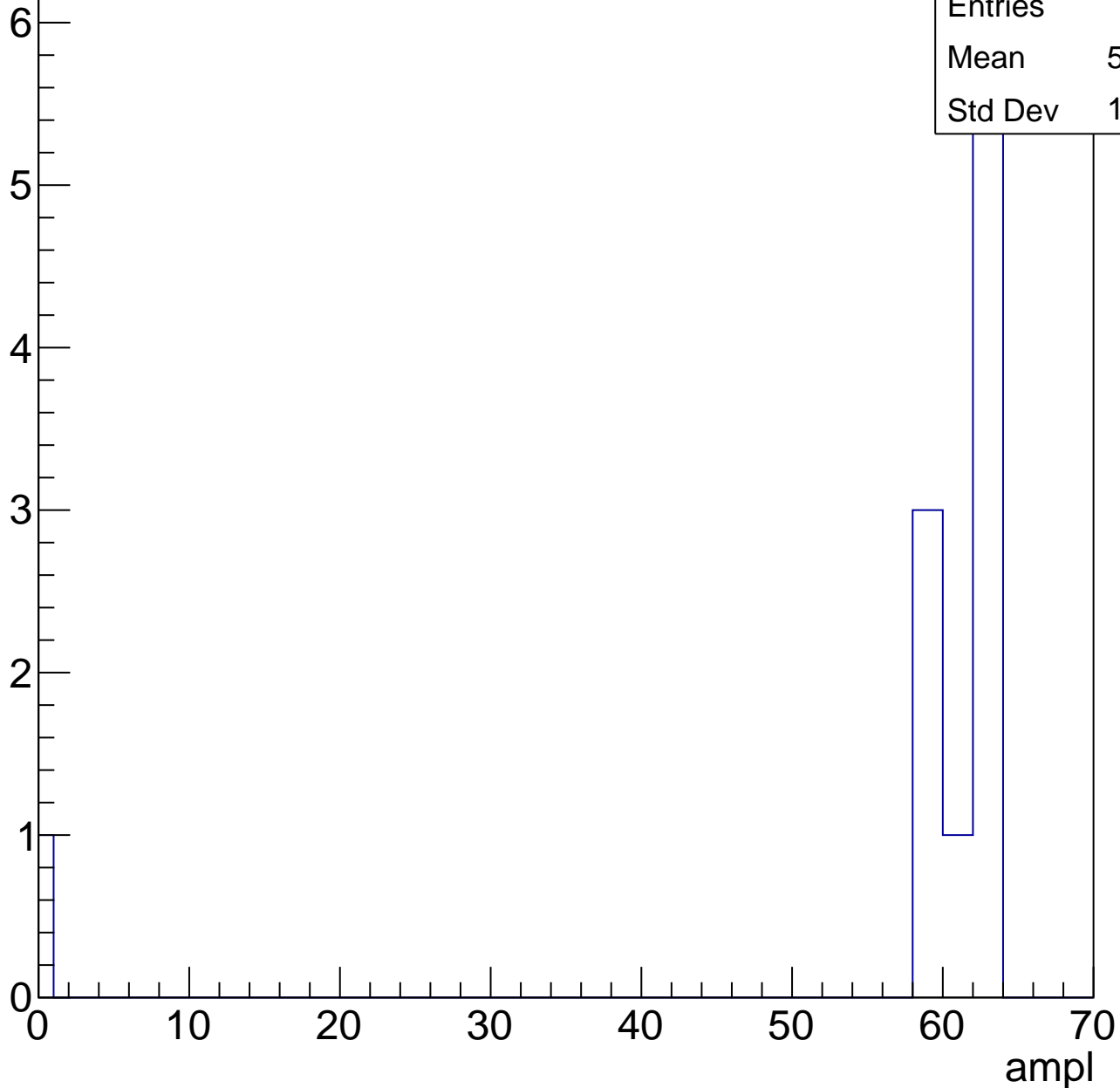
Entries	45
Mean	59.16
Std Dev	2.467



# B0L000S, U13-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl

# B0L000S, U13-ch28, adc0

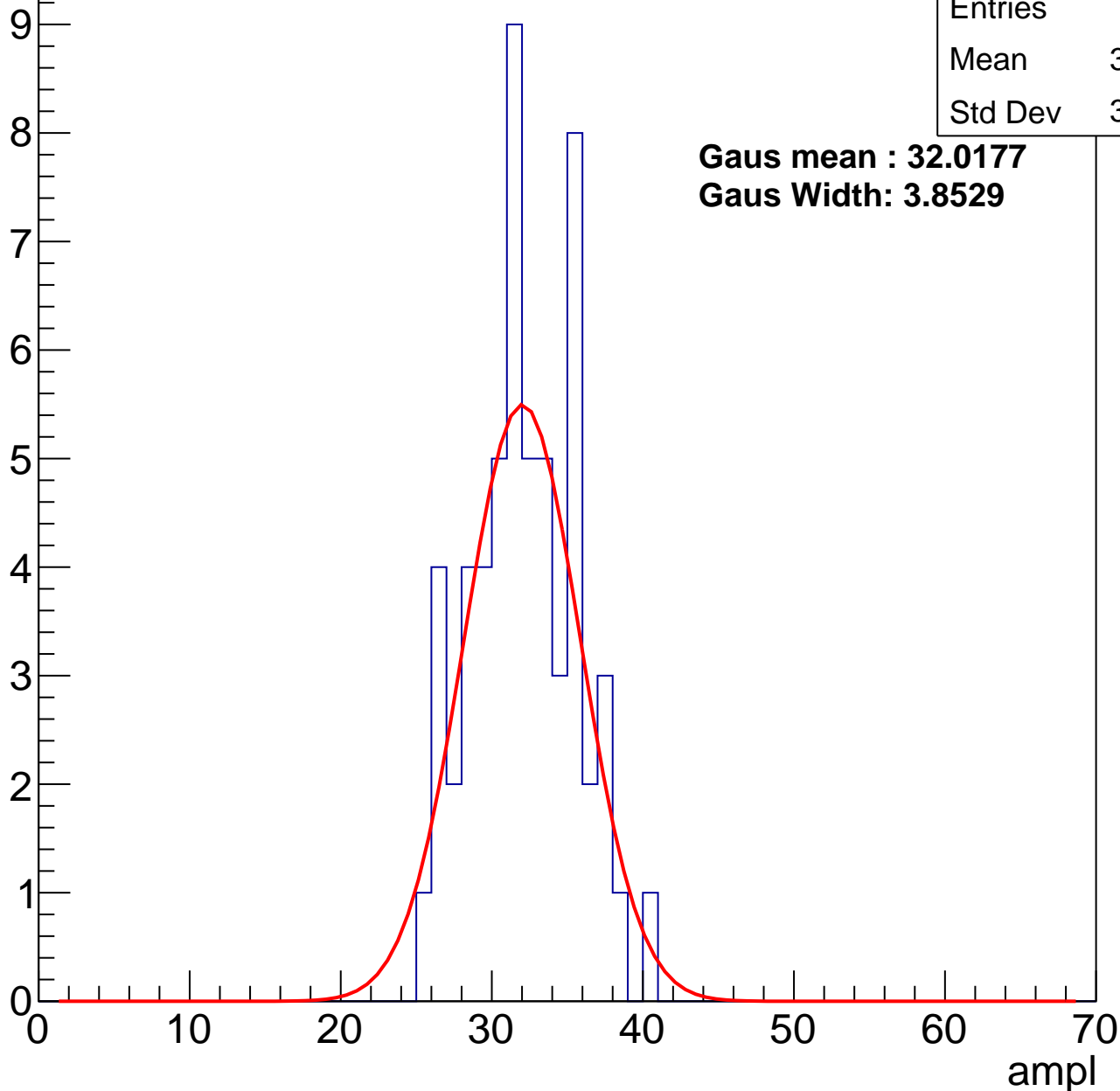
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	31.72
Std Dev	3.412

**Gaus mean : 32.0177**

**Gaus Width: 3.8529**



# B0L000S, U13-ch28, adc1

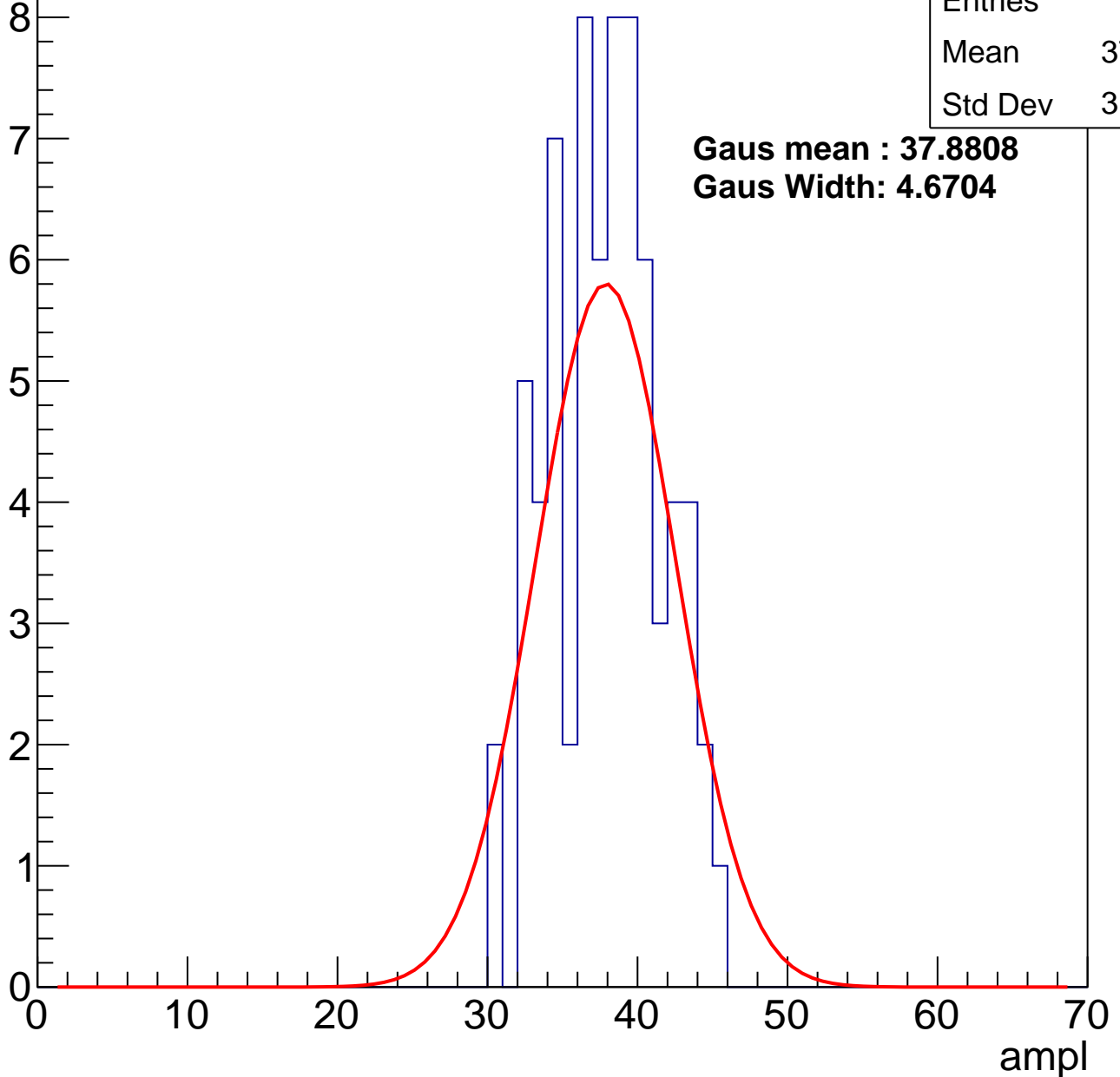
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.46
Std Dev	3.584

**Gaus mean : 37.8808**

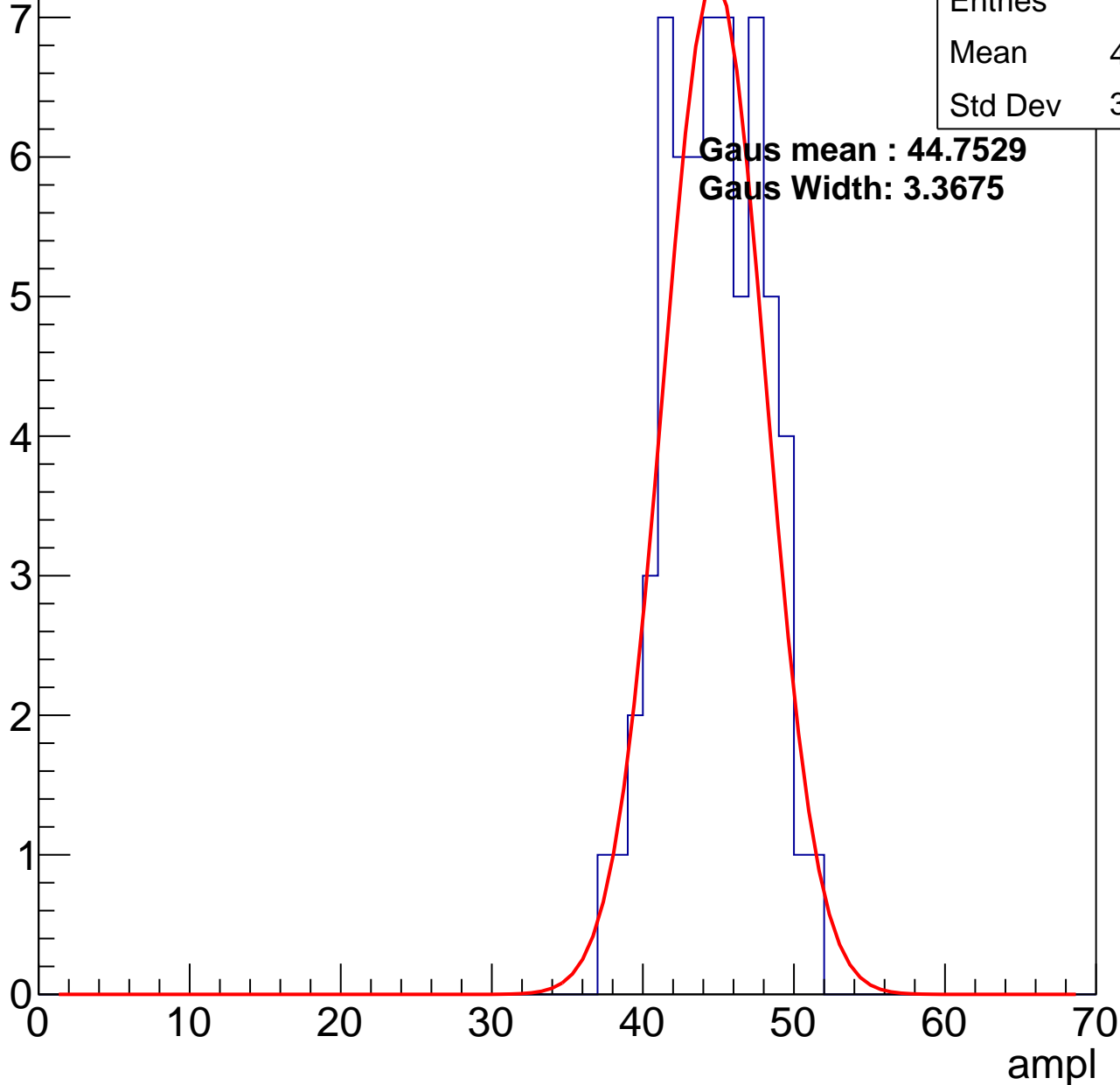
**Gaus Width: 4.6704**



# B0L000S, U13-ch28, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

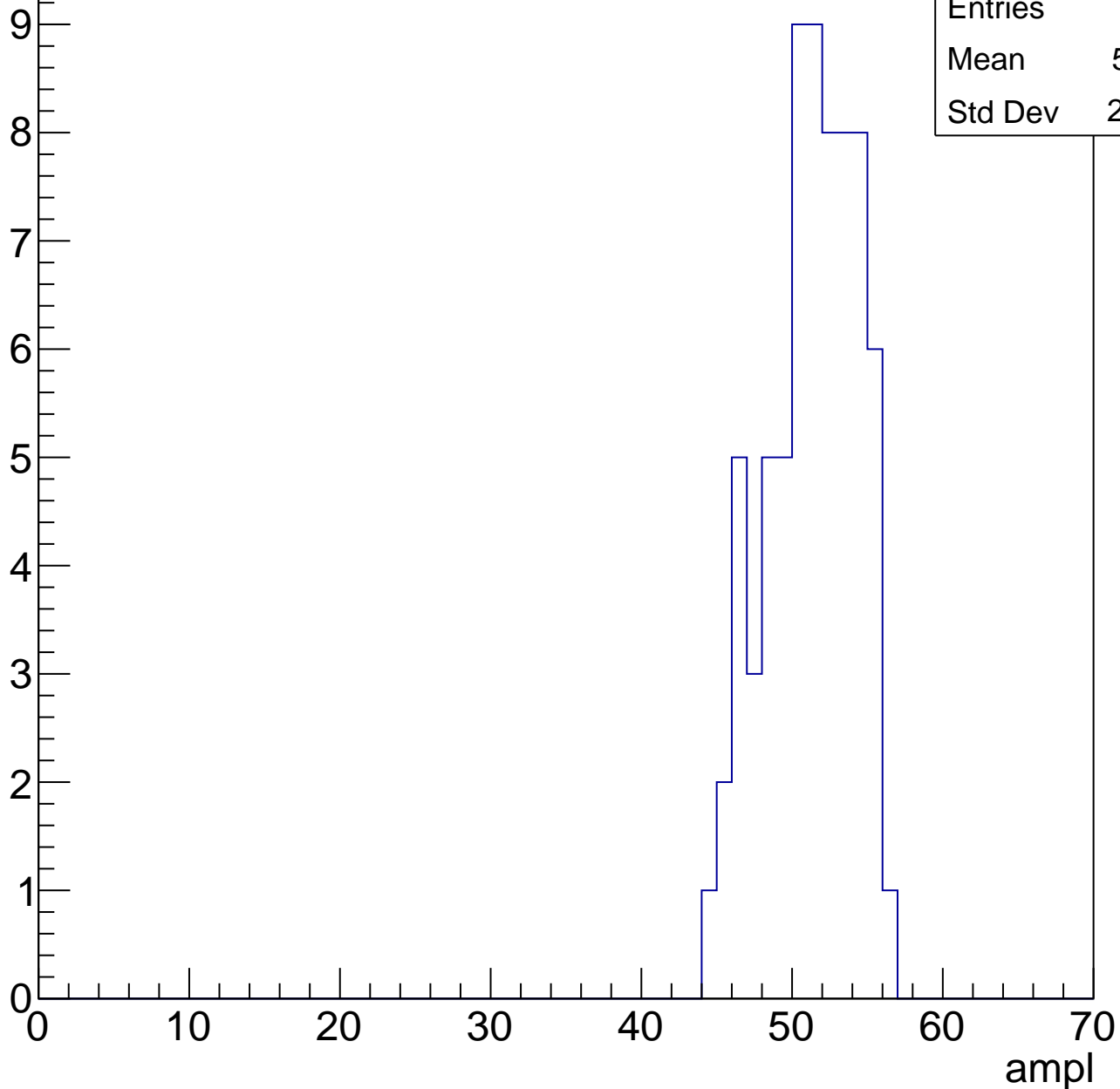
Entry



# B0L000S, U13-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



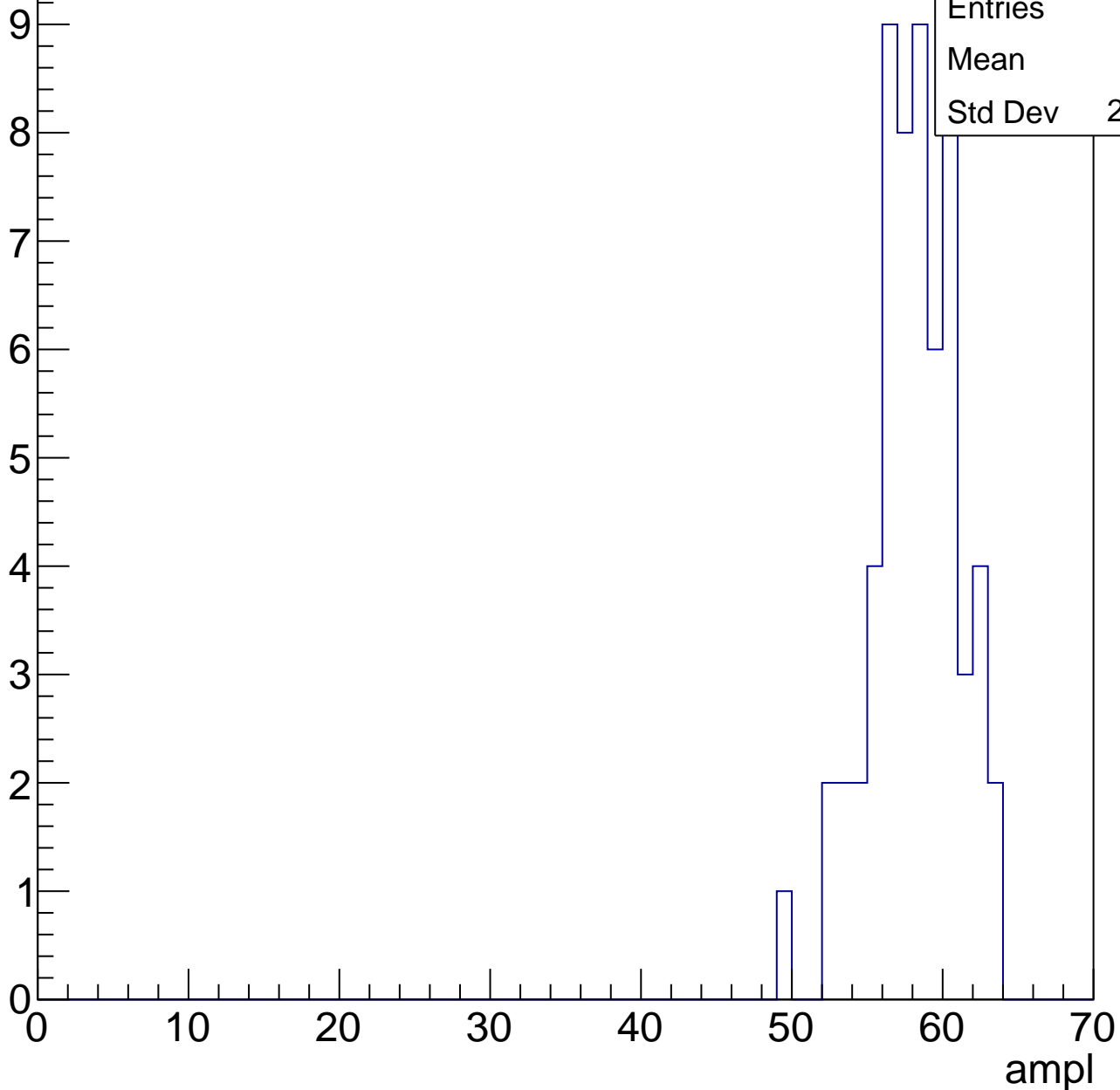
Entries	70
Mean	50.81
Std Dev	2.924

# B0L000S, U13-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

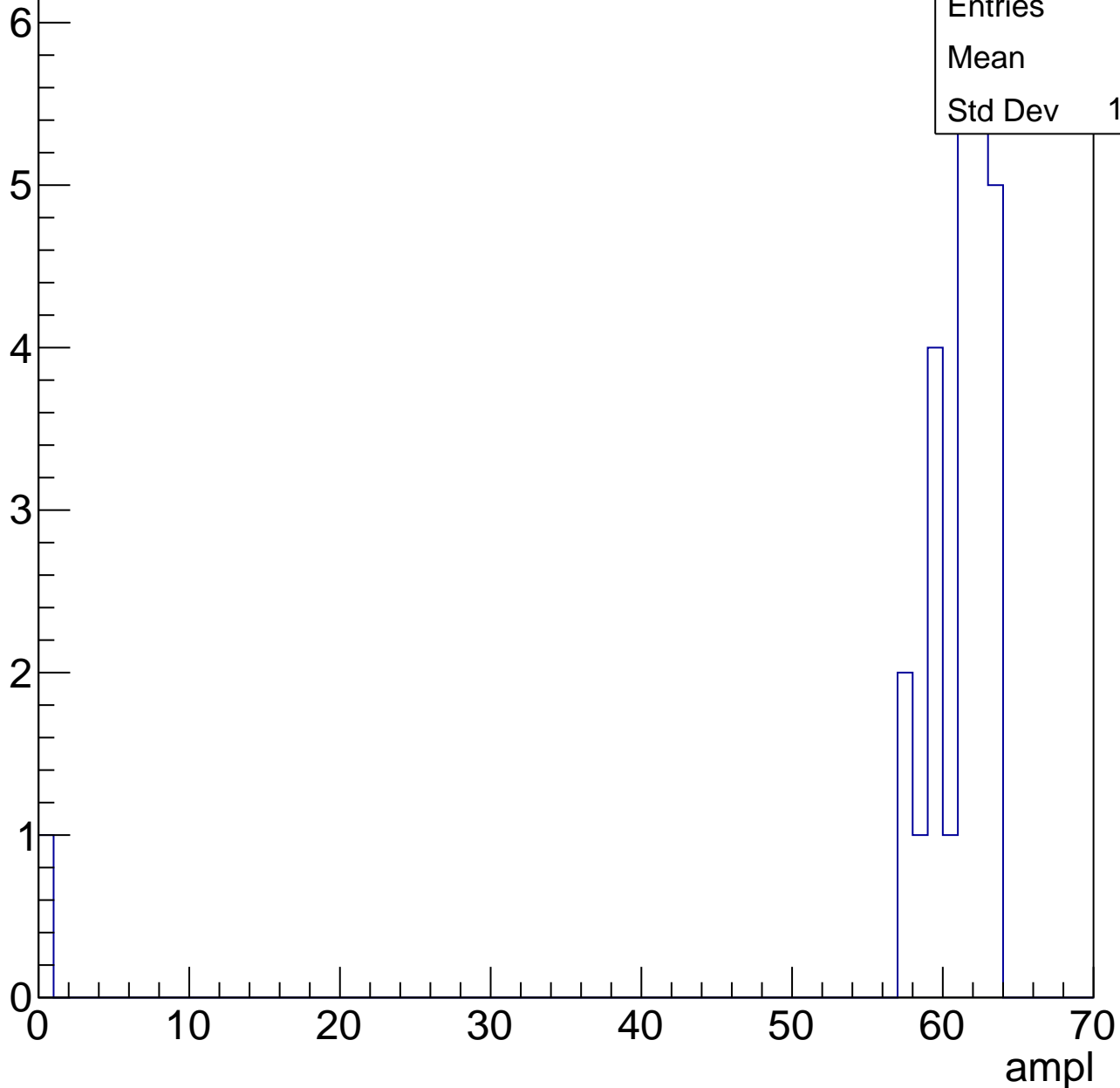
Entries	61
Mean	57.7
Std Dev	2.842



# B0L000S, U13-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch29, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	31.37
Std Dev	4.056

**Gaus mean : 31.9108**

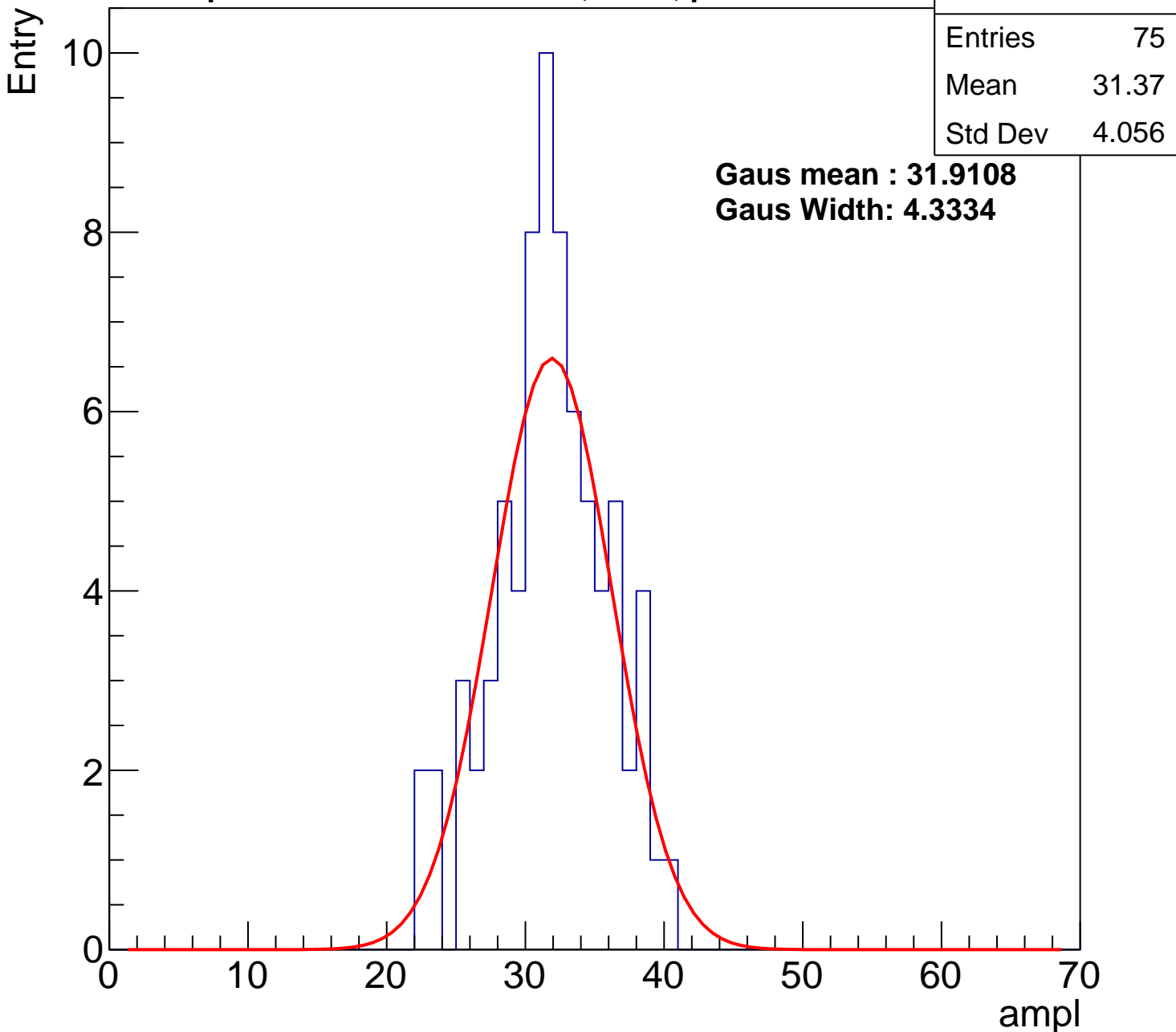
**Gaus Width: 4.3334**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



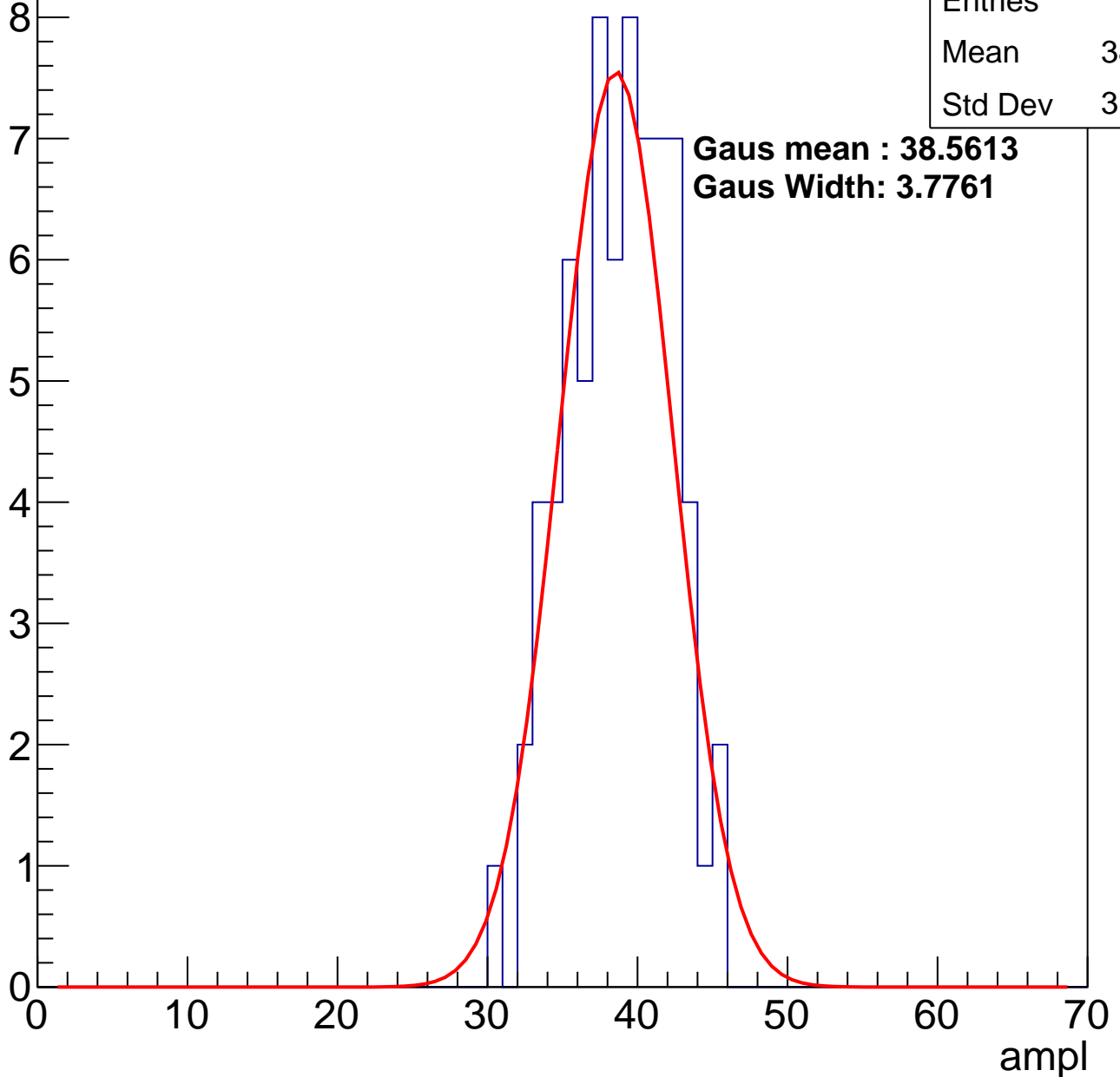
# B0L000S, U13-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	38.26
Std Dev	3.379

**Gaus mean : 38.5613**  
**Gaus Width: 3.7761**



# B0L000S, U13-ch29, adc2

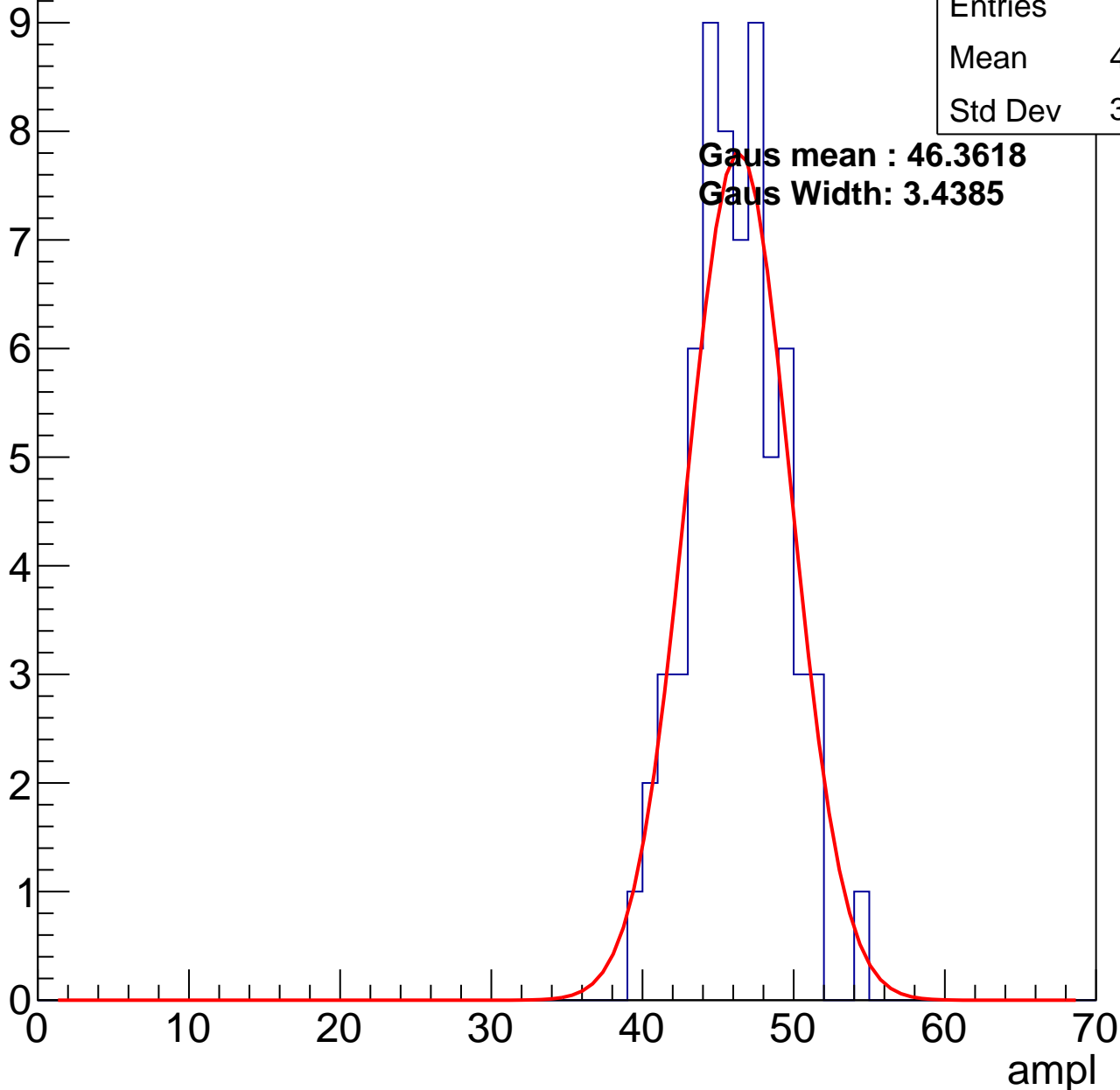
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	45.73
Std Dev	3.033

**Gaus mean : 46.3618**

**Gaus Width: 3.4385**

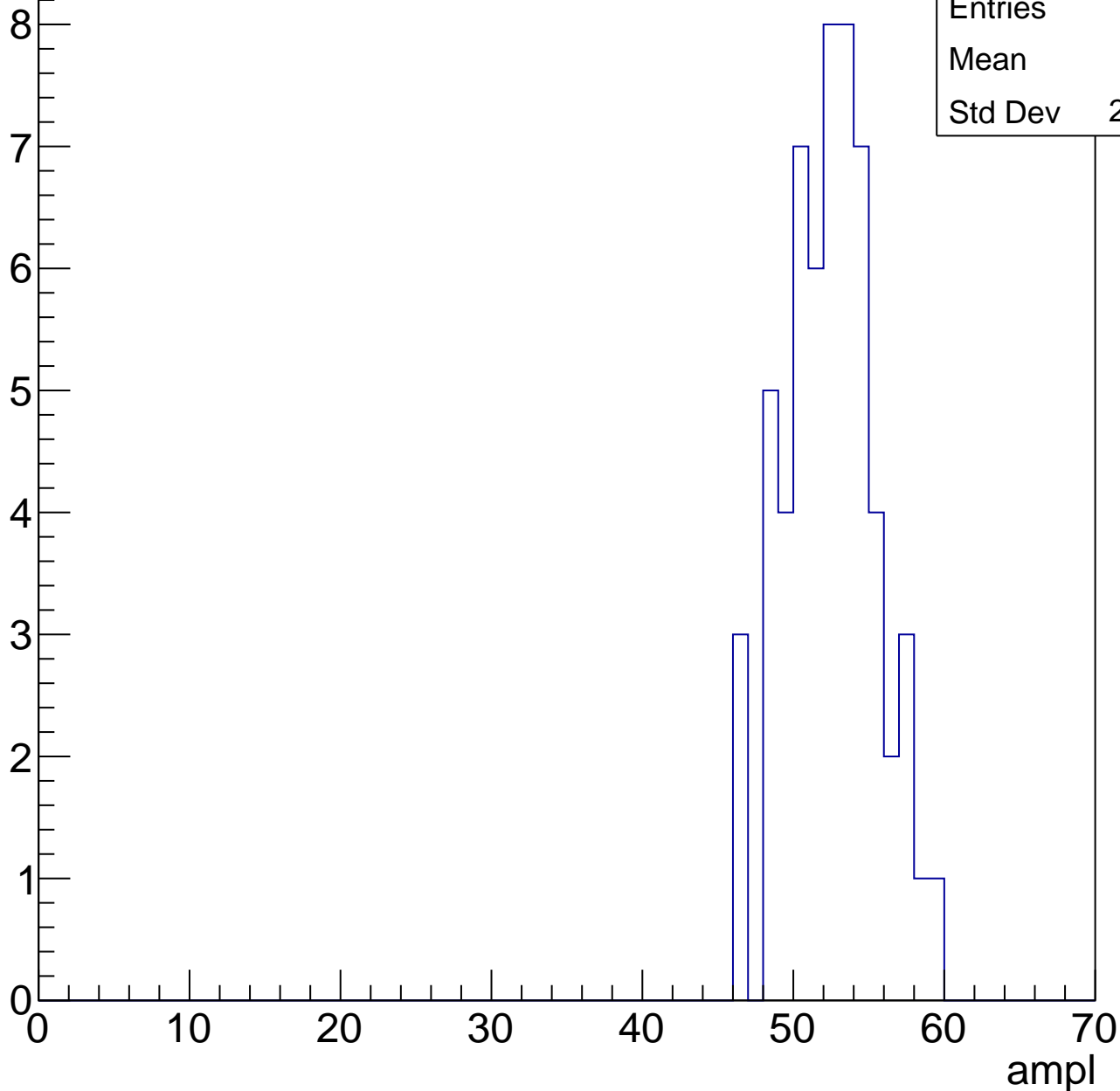


# B0L000S, U13-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	52
Std Dev	2.974



# B0L000S, U13-ch29, adc4

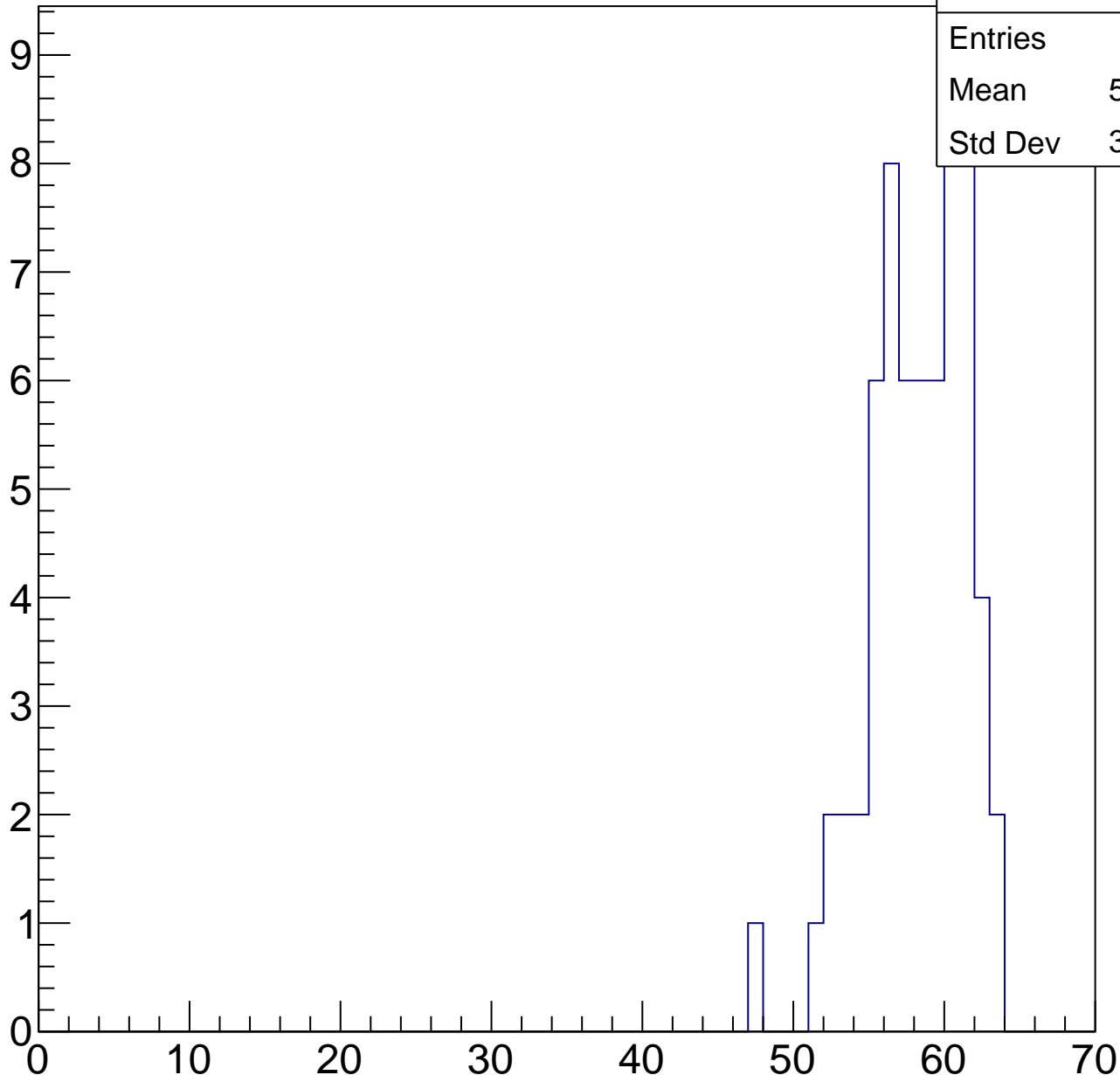
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	57.79
Std Dev	3.203

ampl

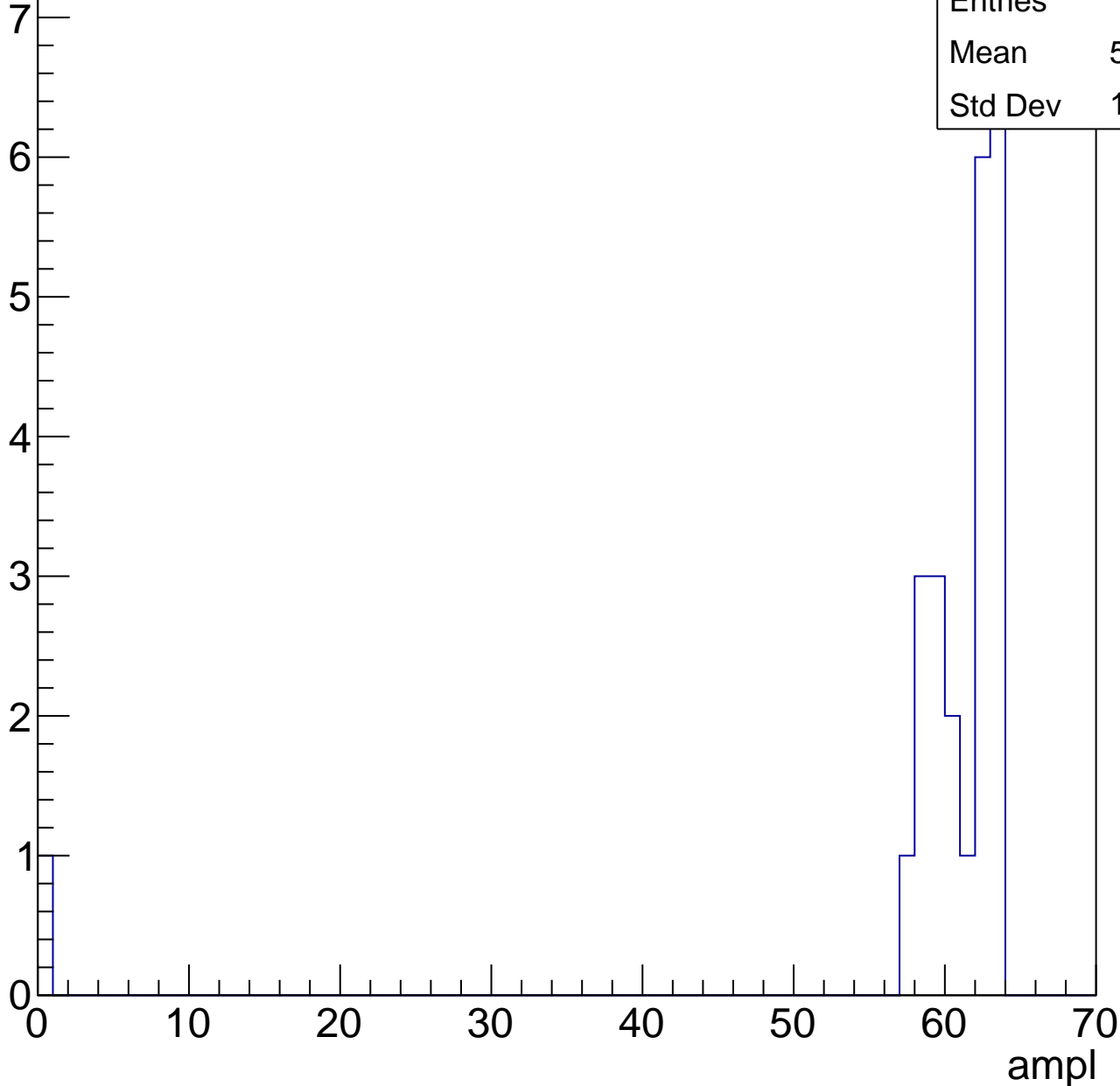


# B0L000S, U13-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	24
Mean	58.42
Std Dev	12.34



# B0L000S, U13-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	31.46
Std Dev	2.837

**Gaus mean : 31.9333**

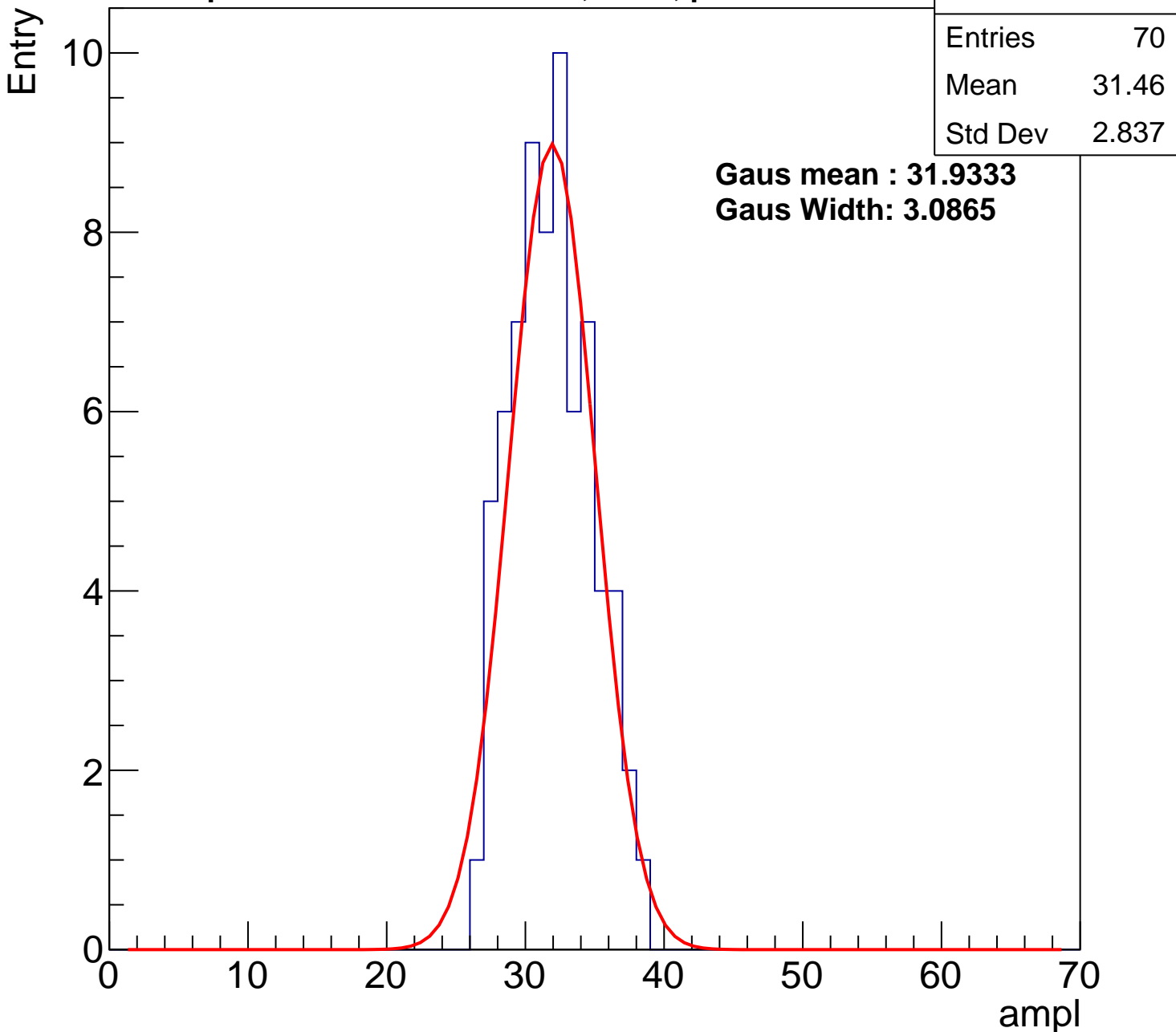
**Gaus Width: 3.0865**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch30, adc1

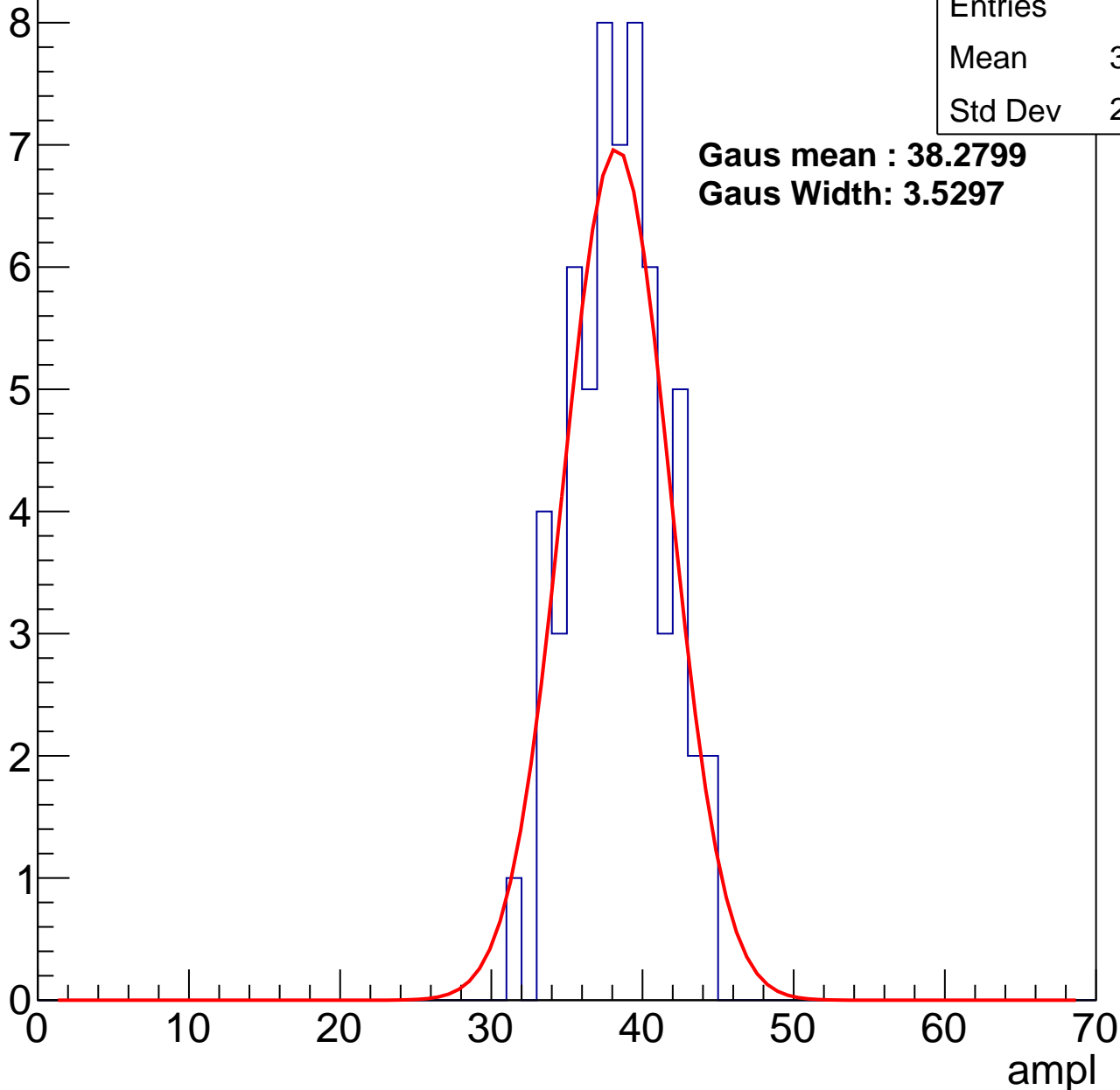
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	37.93
Std Dev	2.999

**Gaus mean : 38.2799**

**Gaus Width: 3.5297**



# B0L000S, U13-ch30, adc2

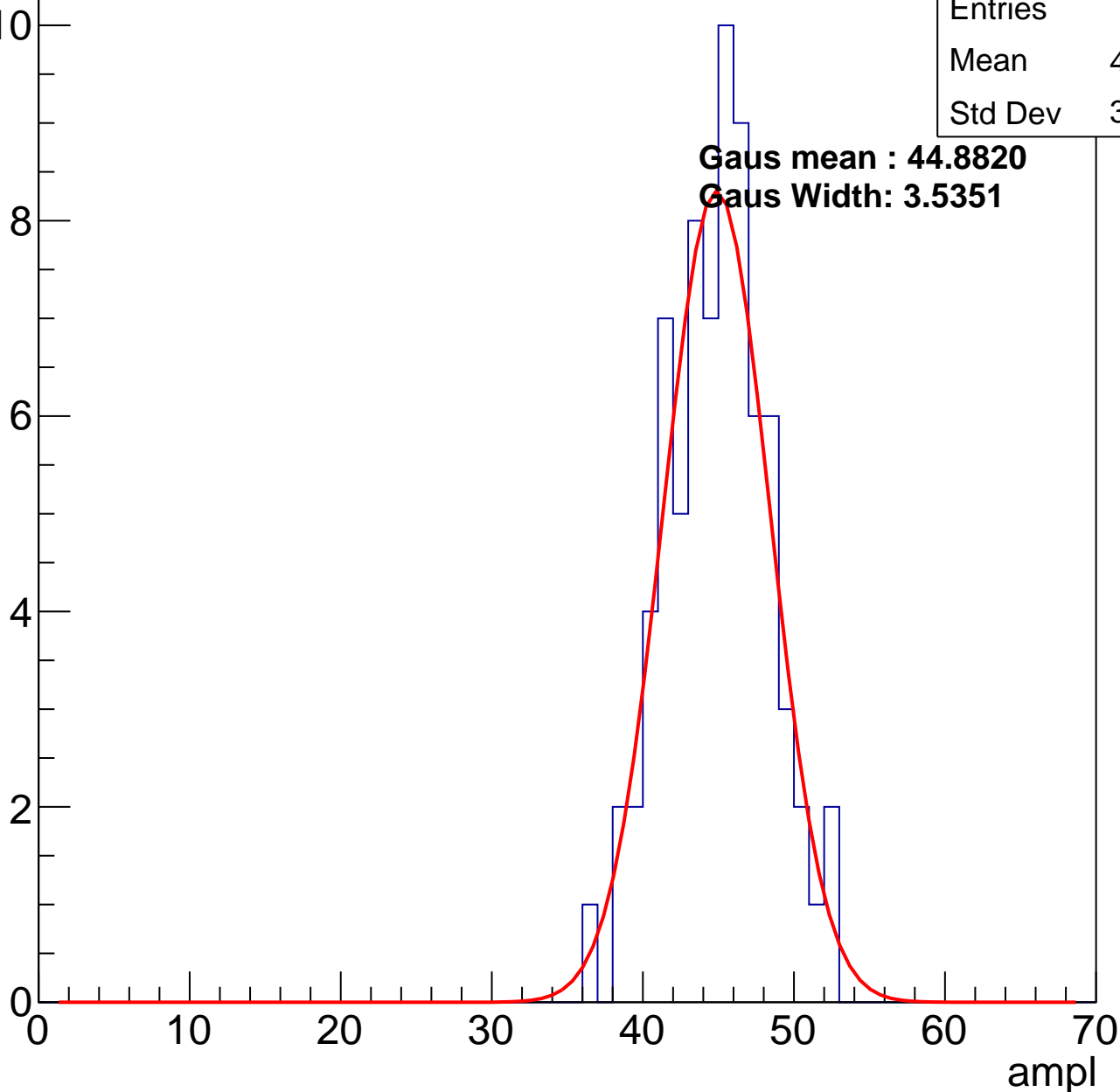
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	44.47
Std Dev	3.356

**Gaus mean : 44.8820**

**Gaus Width: 3.5351**

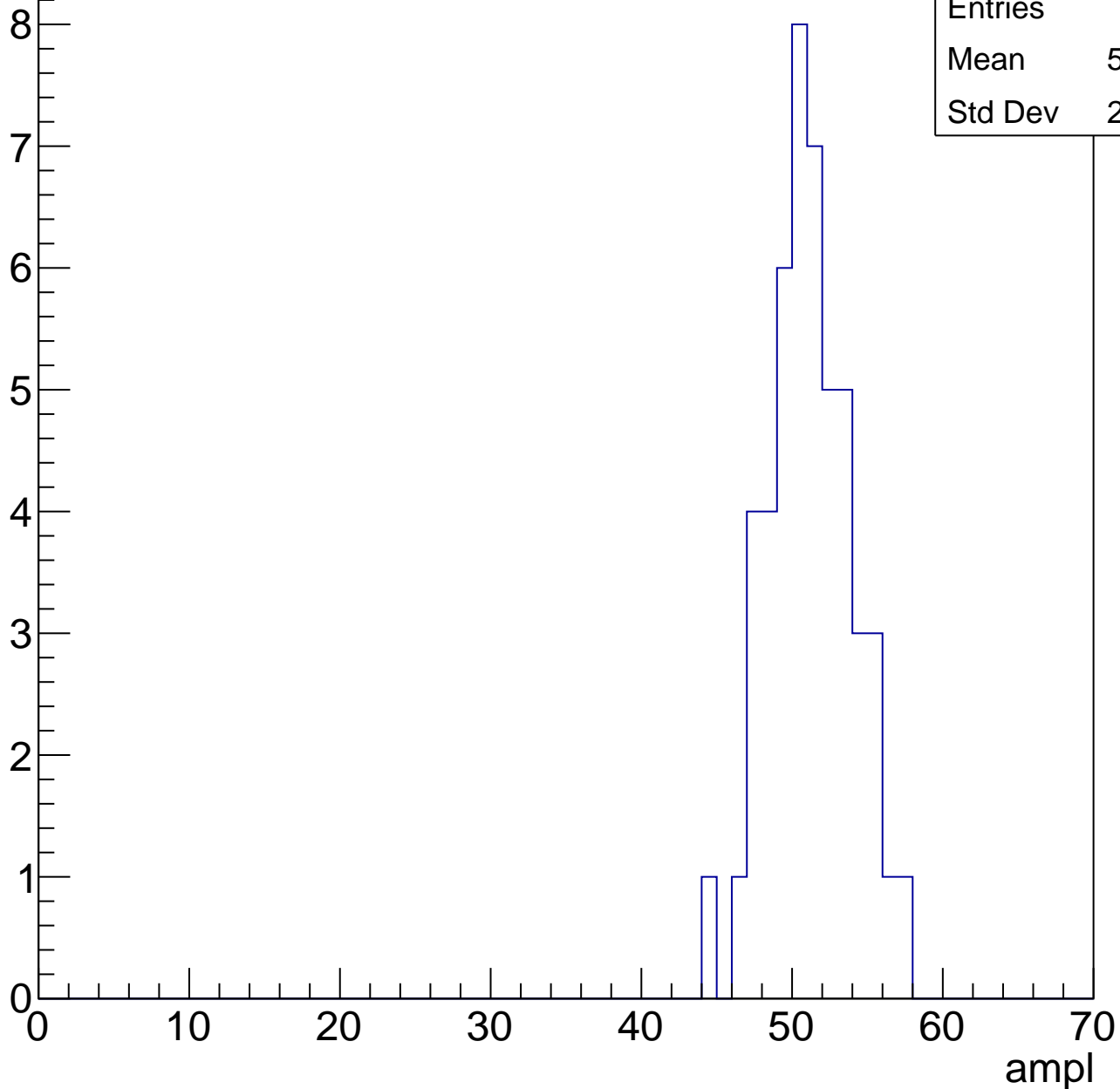


# B0L000S, U13-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	50.73
Std Dev	2.724

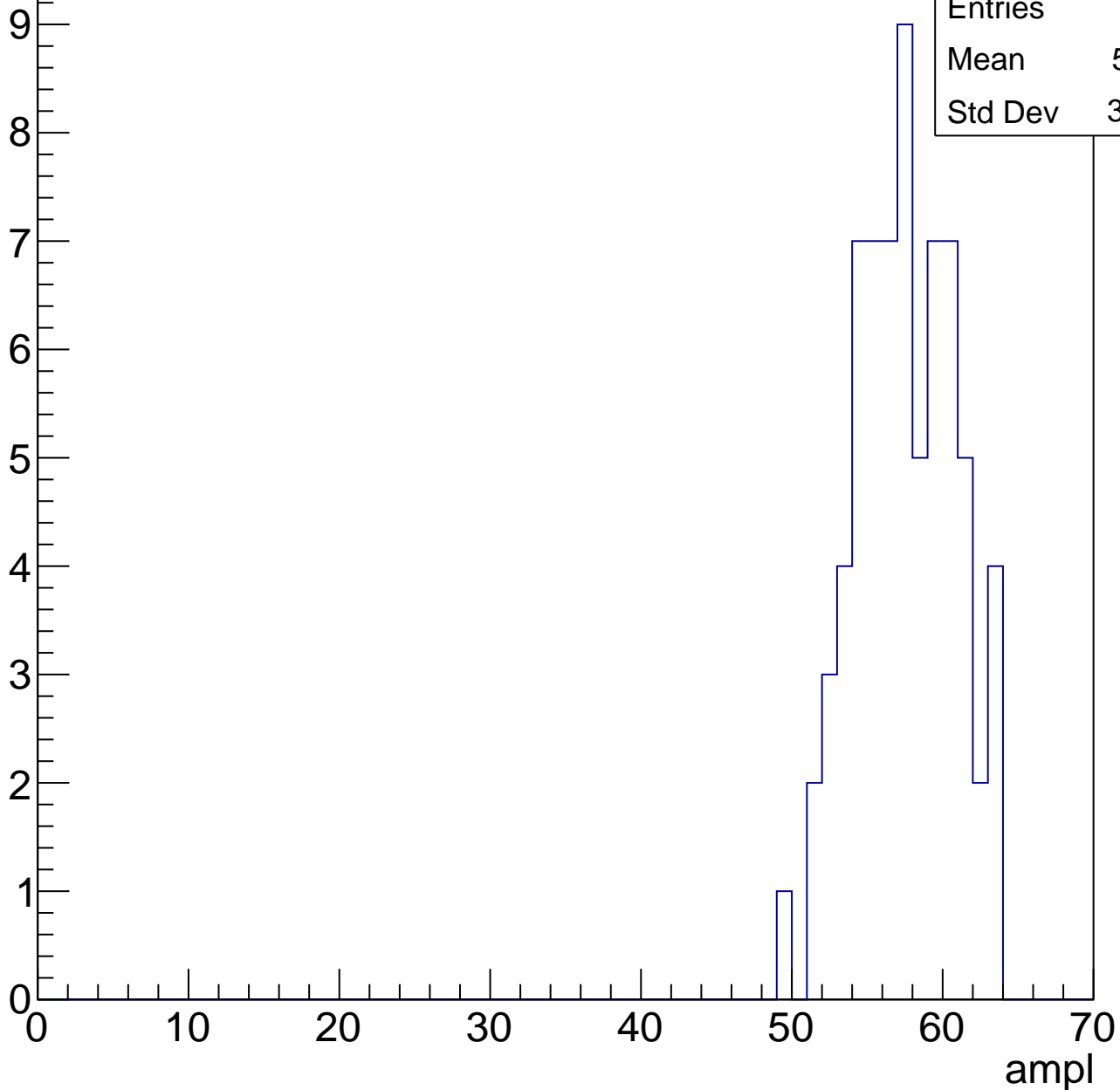


# B0L000S, U13-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	57.01
Std Dev	3.258

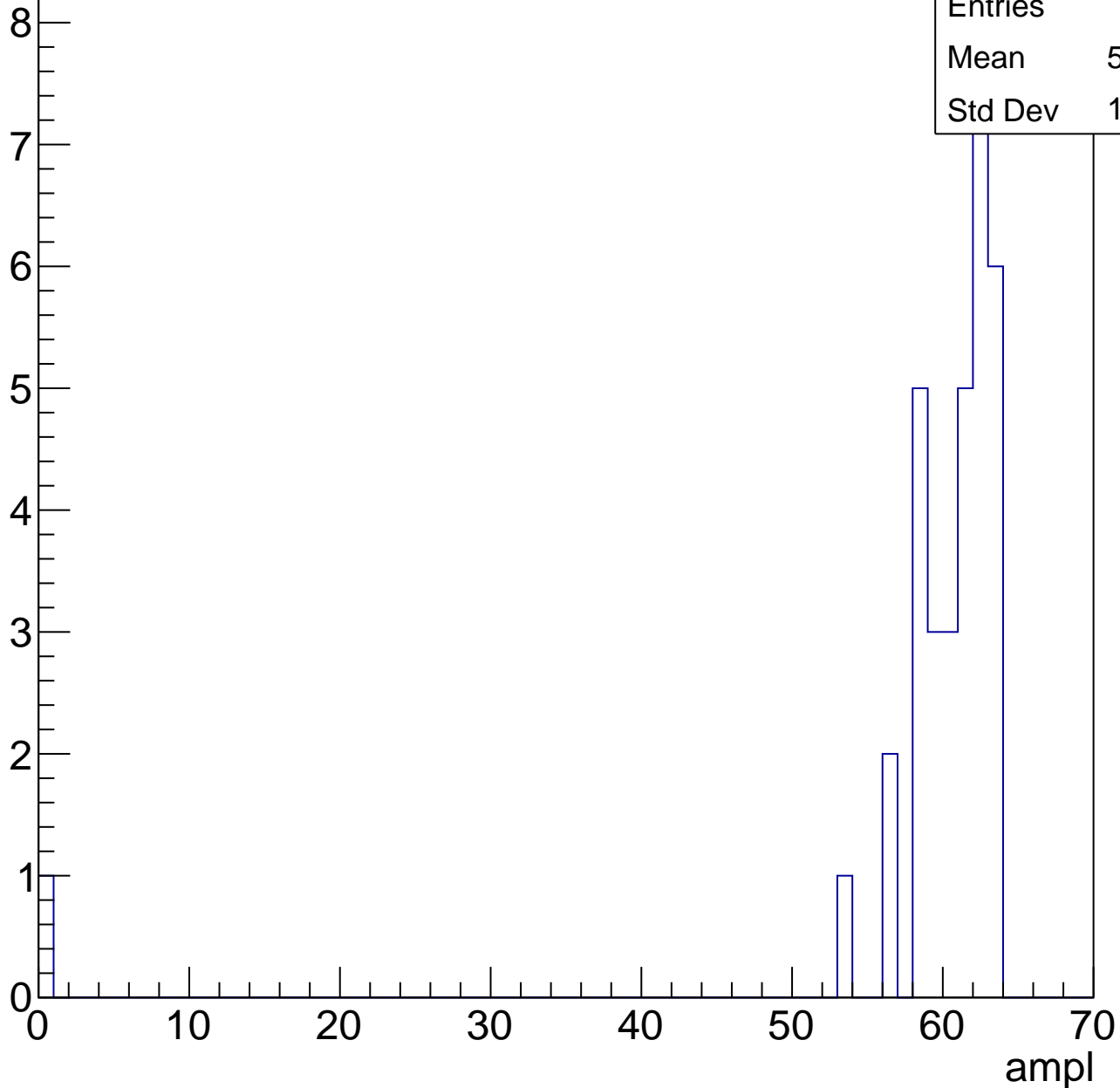


# B0L000S, U13-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.56
Std Dev	10.47



# B0L000S, U13-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch31, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.52
Std Dev	3.026

**Gaus mean : 30.1329**

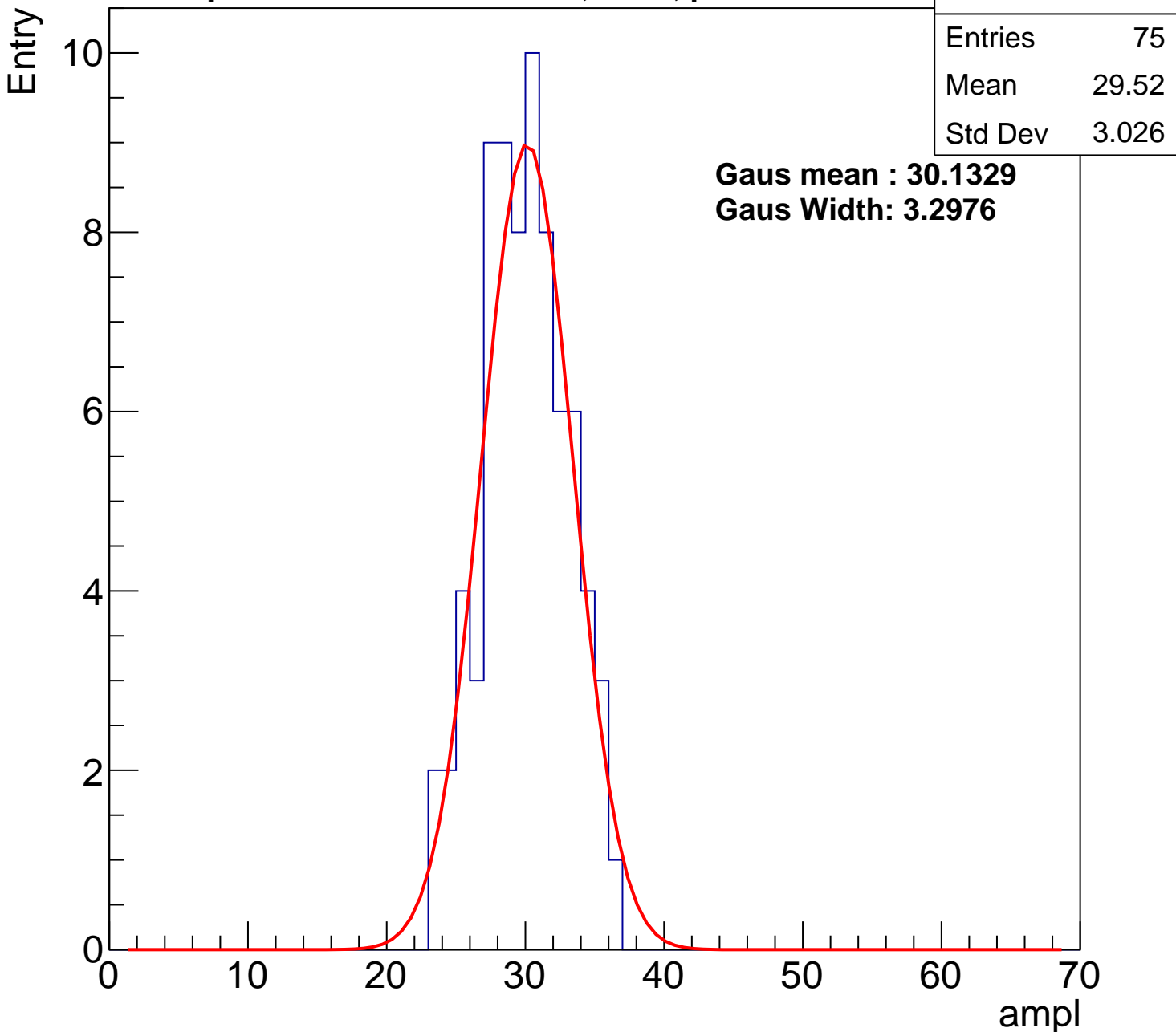
**Gaus Width: 3.2976**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch31, adc1

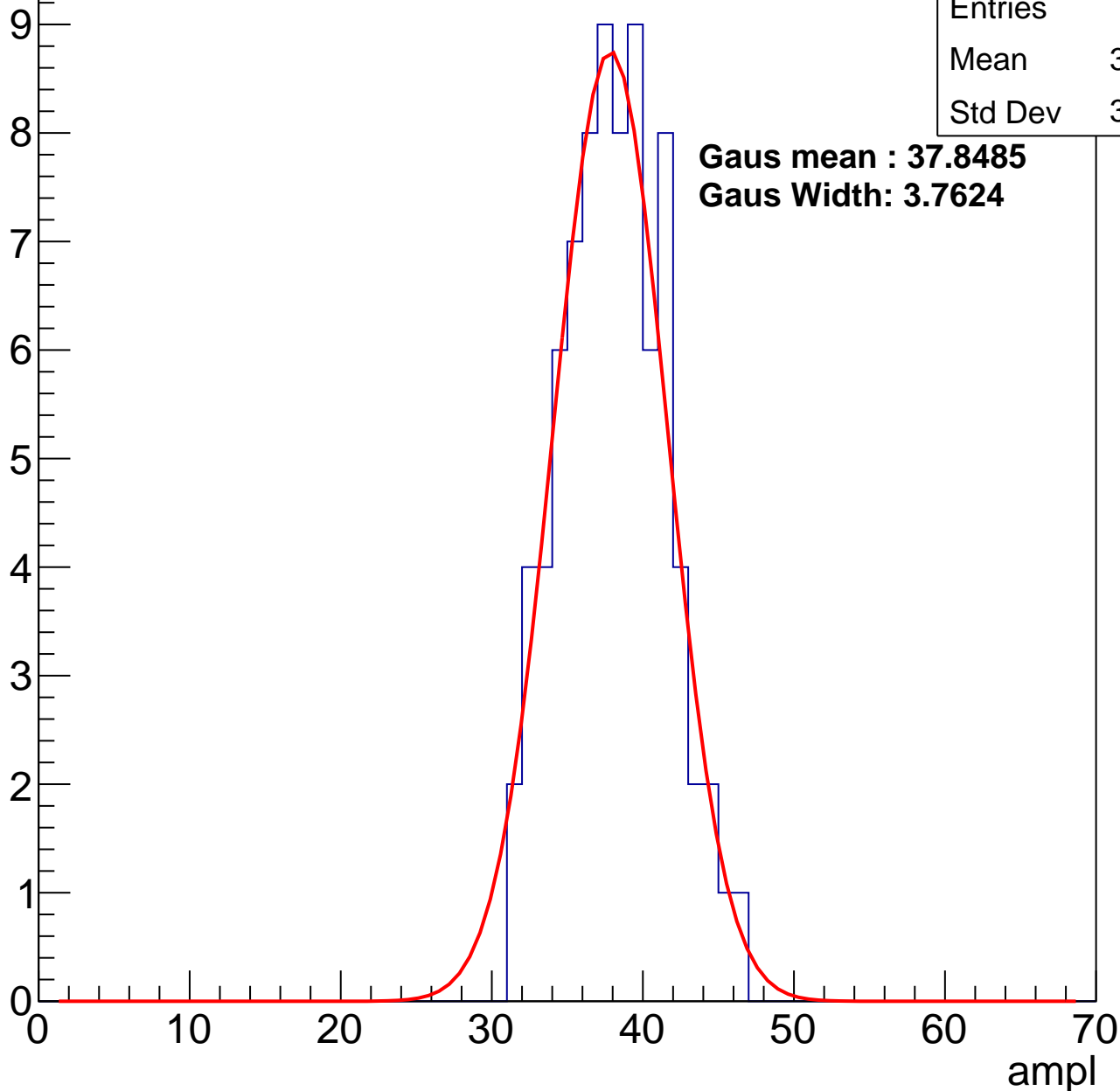
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	37.63
Std Dev	3.397

**Gaus mean : 37.8485**

**Gaus Width: 3.7624**



# B0L000S, U13-ch31, adc2

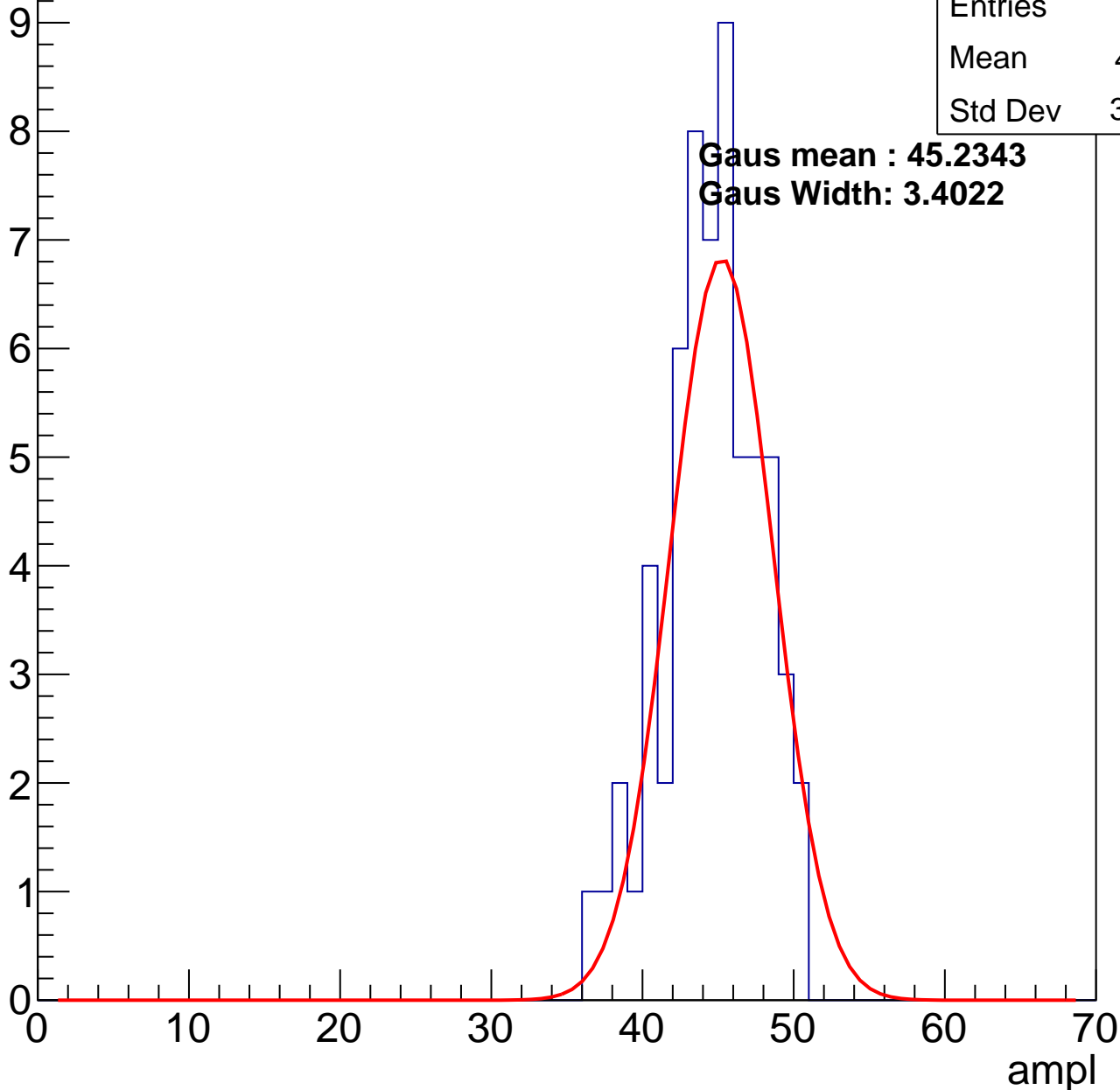
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.11
Std Dev	3.199

**Gaus mean : 45.2343**

**Gaus Width: 3.4022**

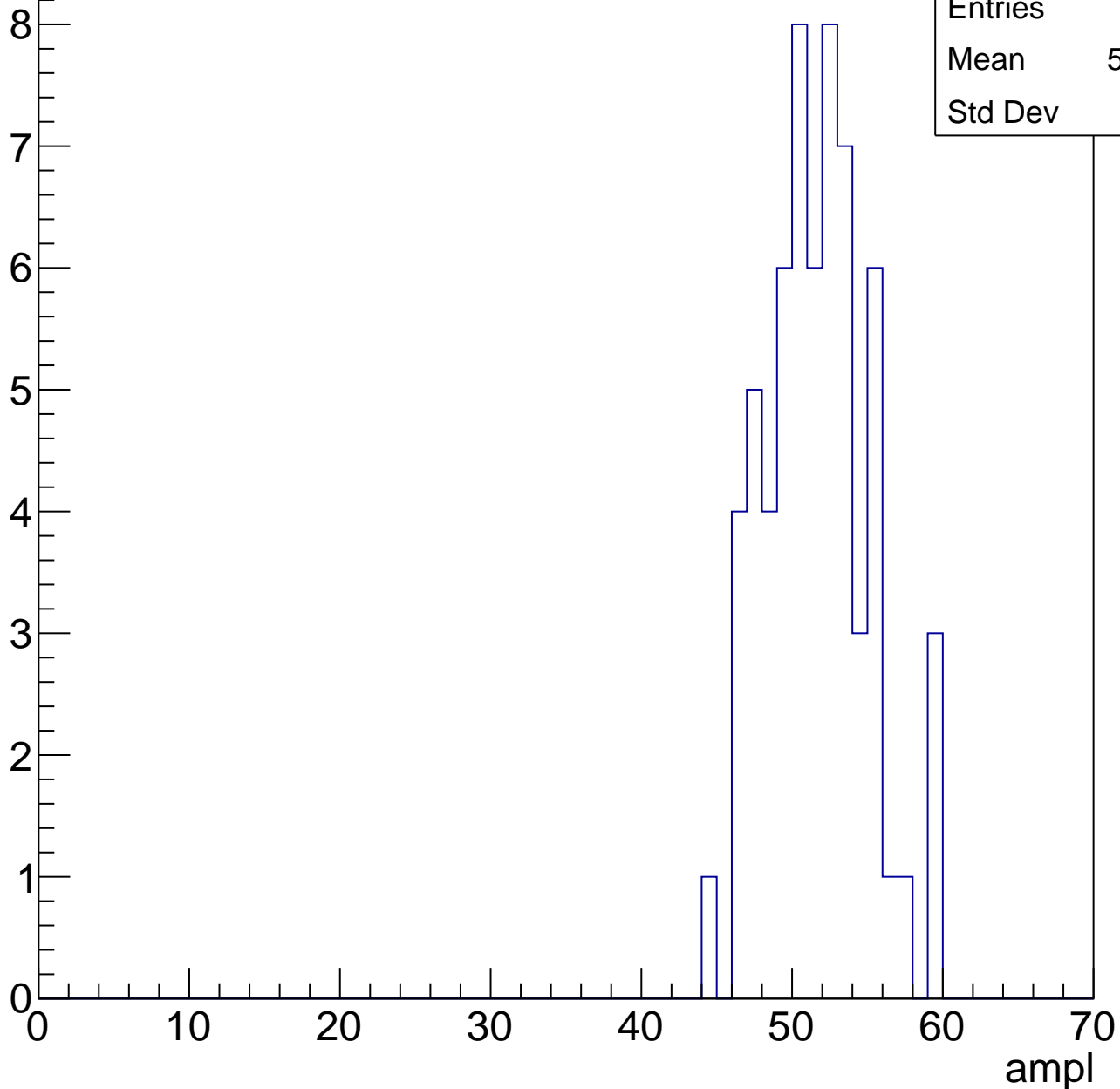


# B0L000S, U13-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	51.17
Std Dev	3.35

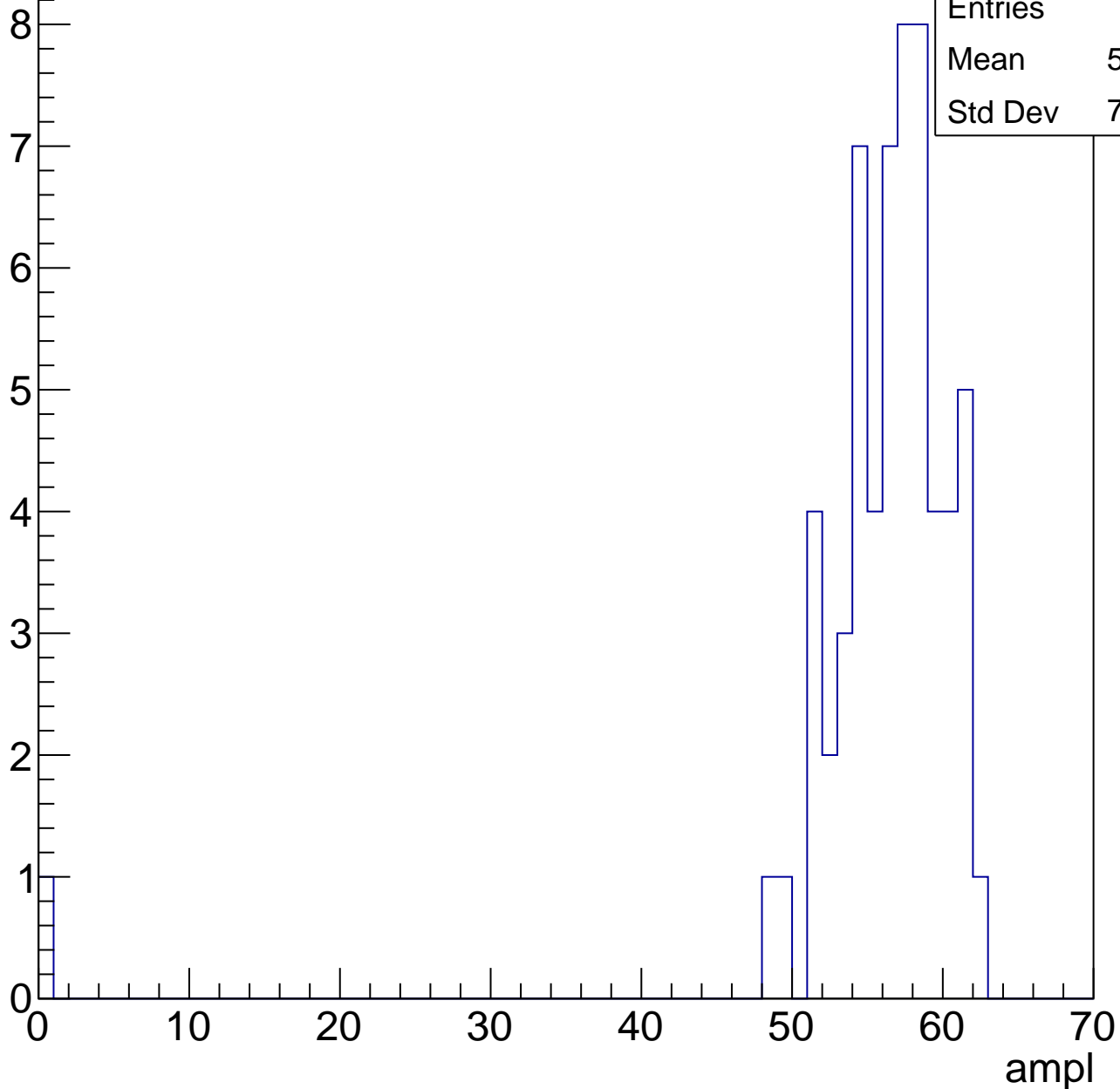


# B0L000S, U13-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.28
Std Dev	7.864

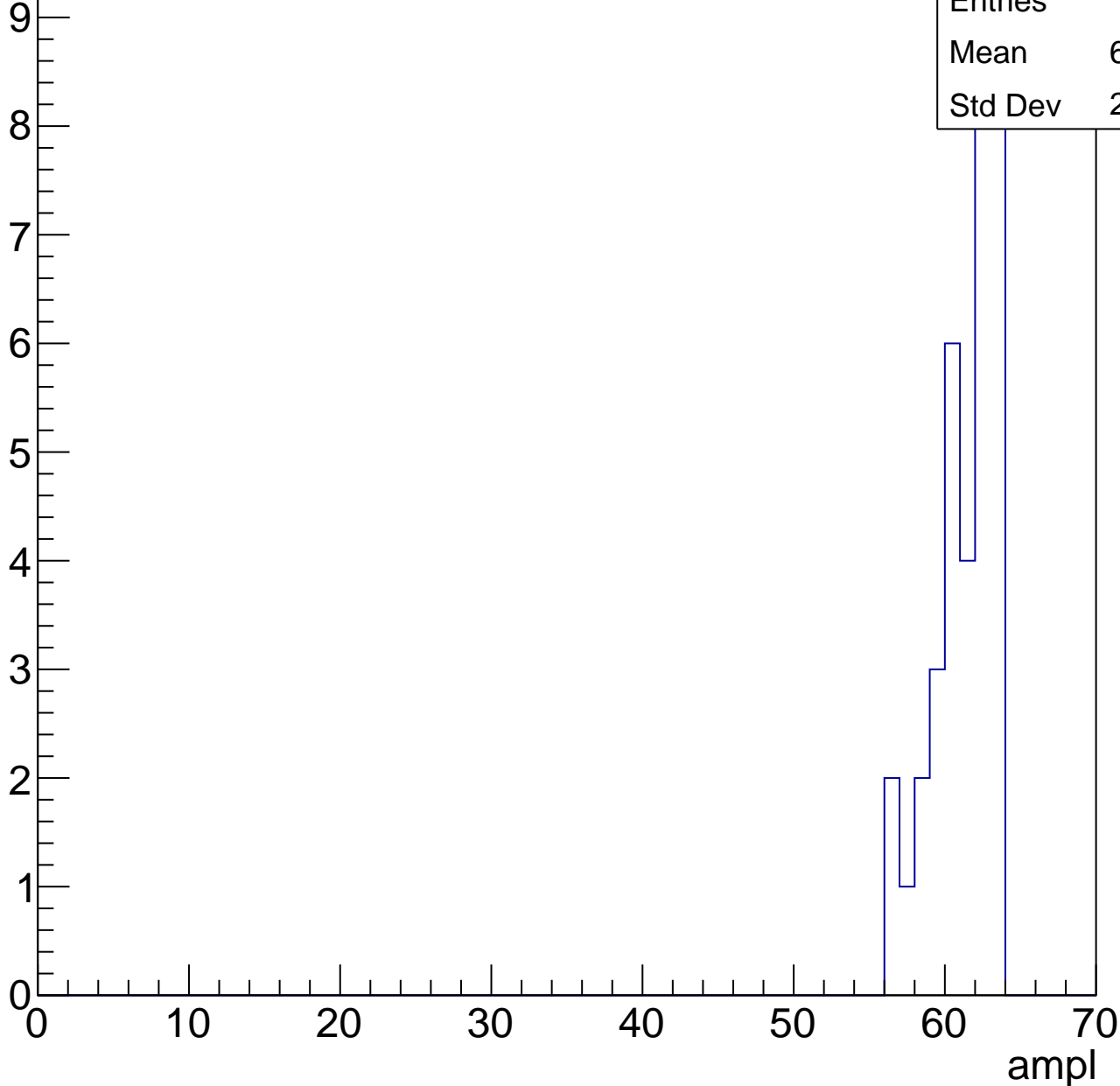


# B0L000S, U13-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	60.83
Std Dev	2.035



# B0L000S, U13-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

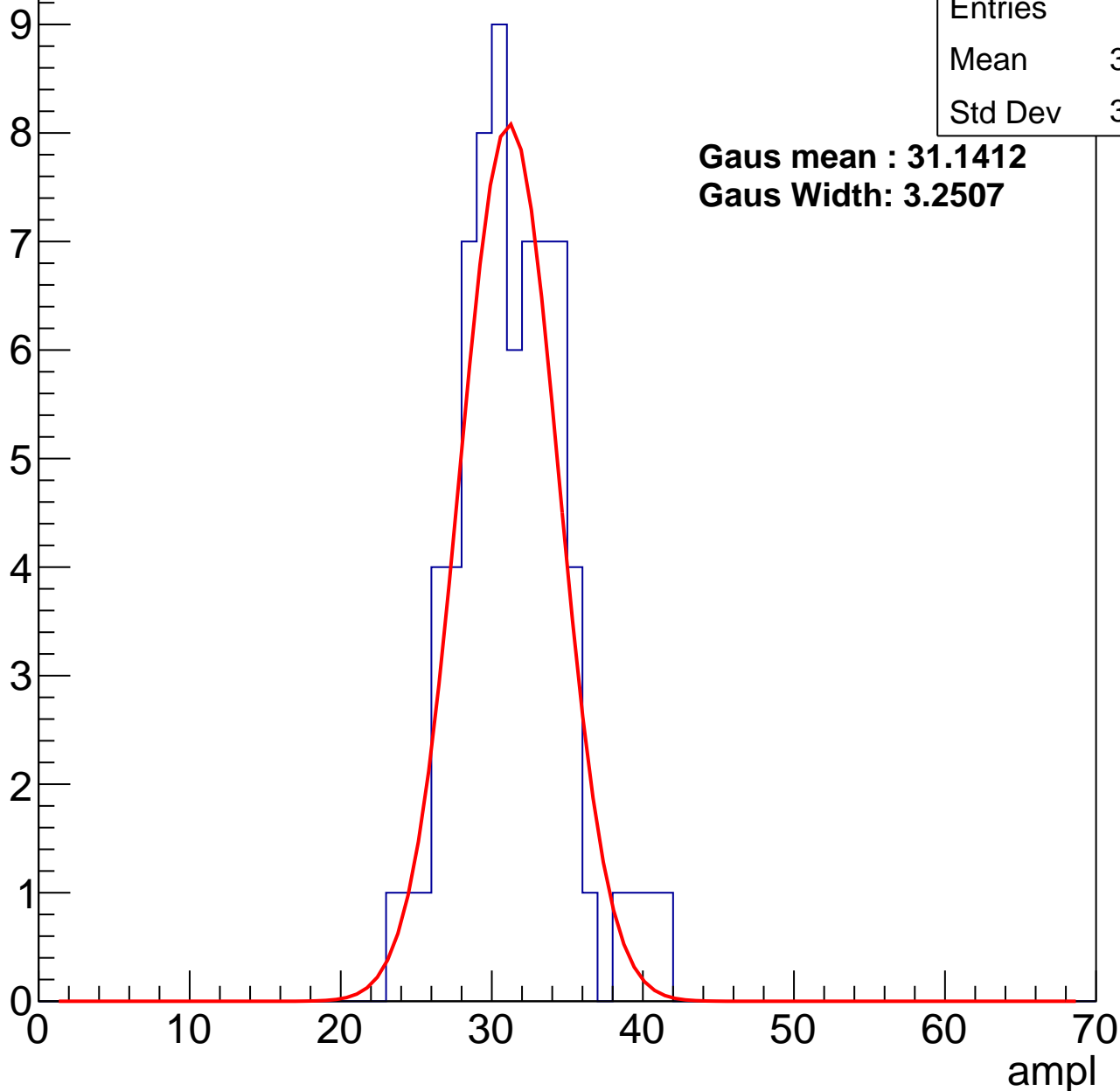


Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U13-ch32, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch32, adc1

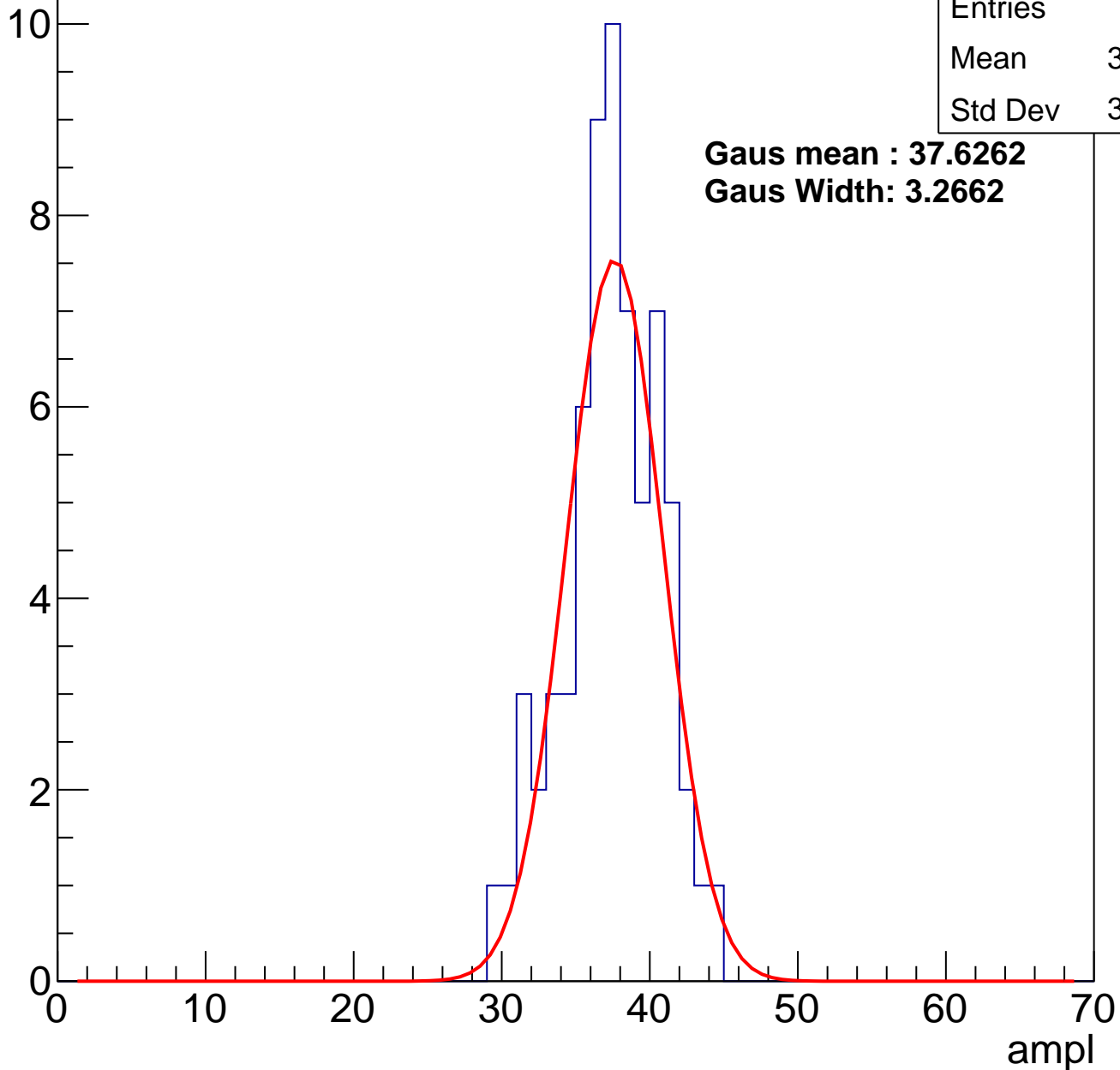
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	36.94
Std Dev	3.219

**Gaus mean : 37.6262**

**Gaus Width: 3.2662**

Entry



# B0L000S, U13-ch32, adc2

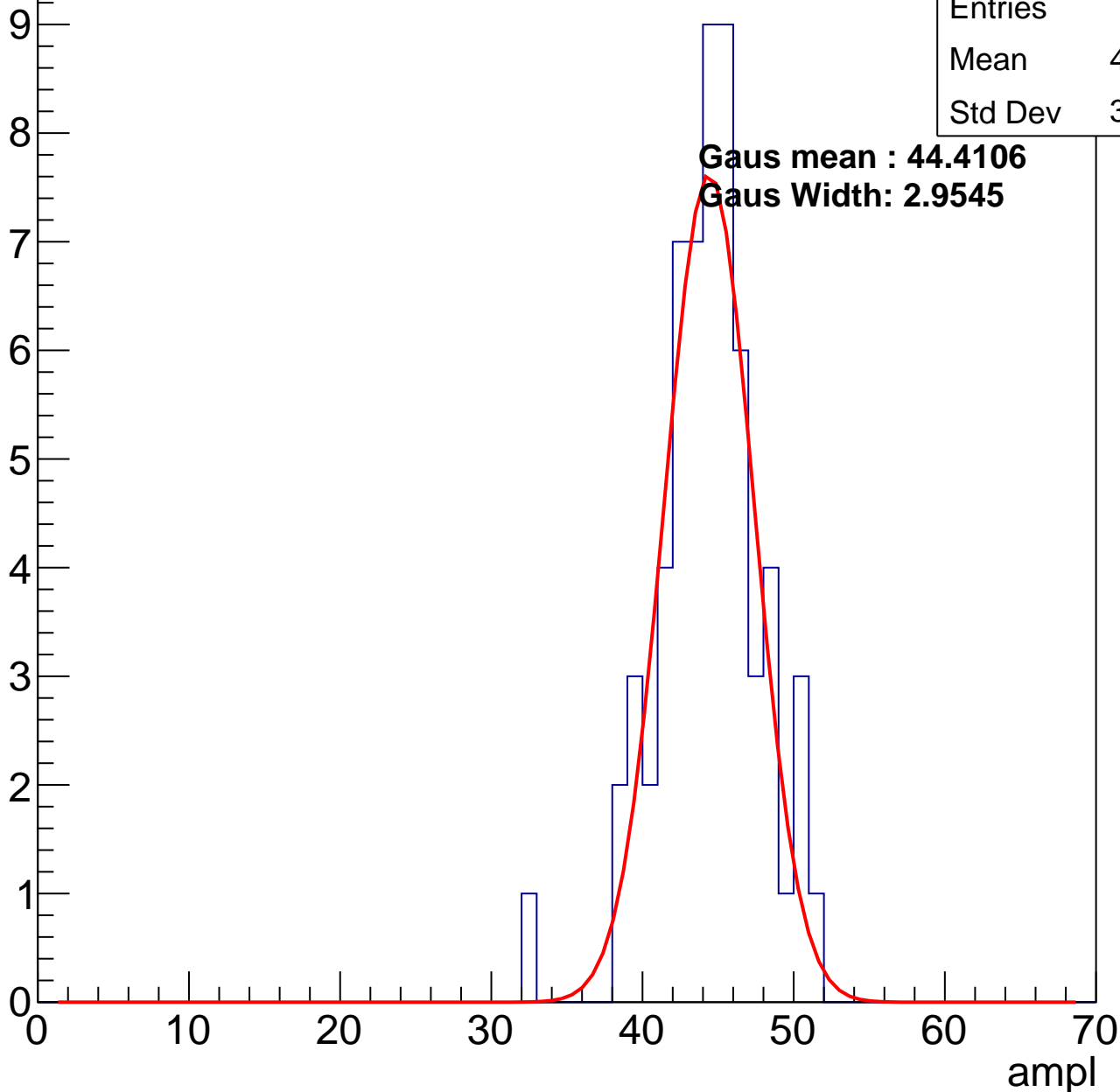
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.94
Std Dev	3.359

**Gaus mean : 44.4106**

**Gaus Width: 2.9545**

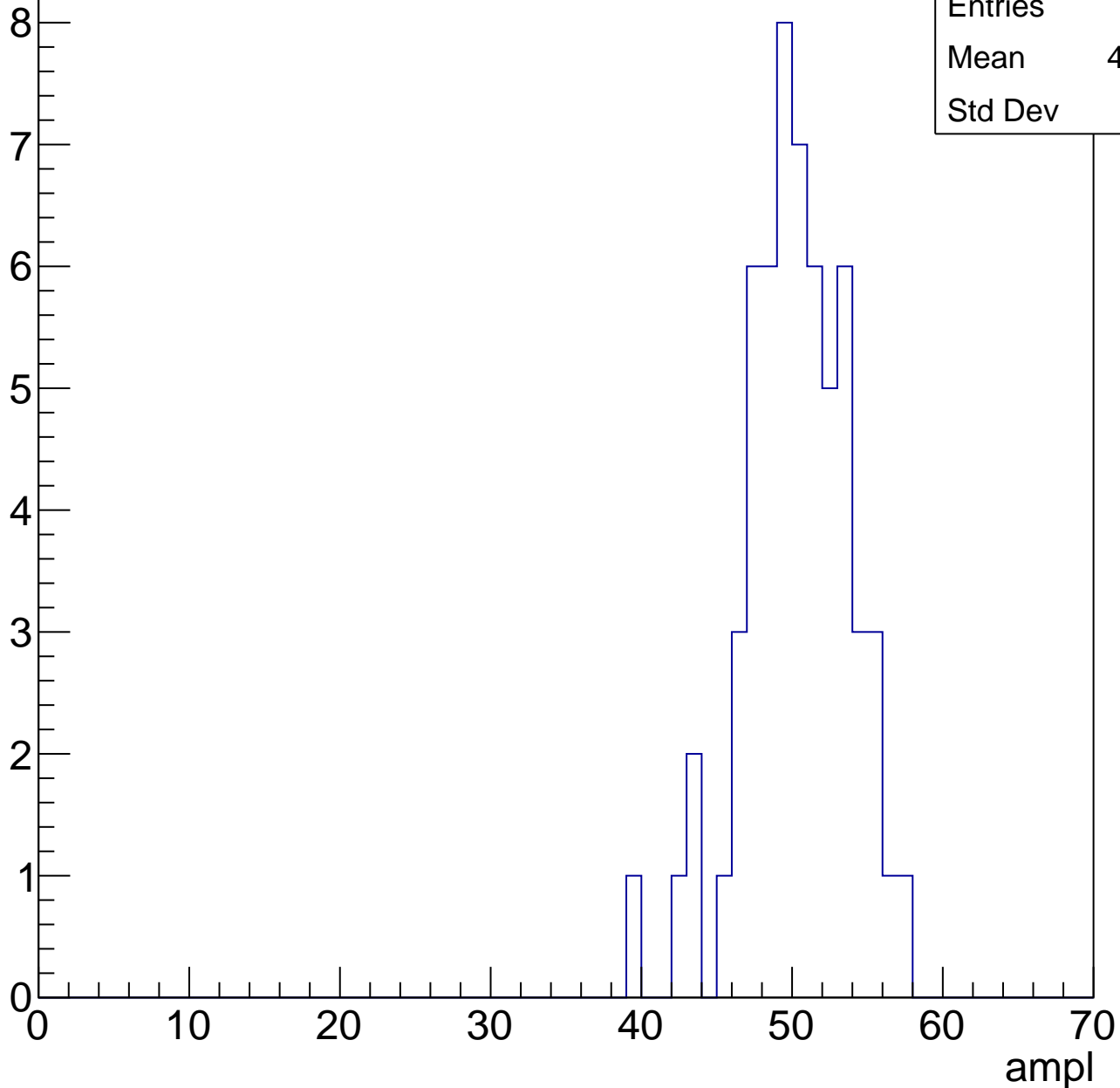


# B0L000S, U13-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

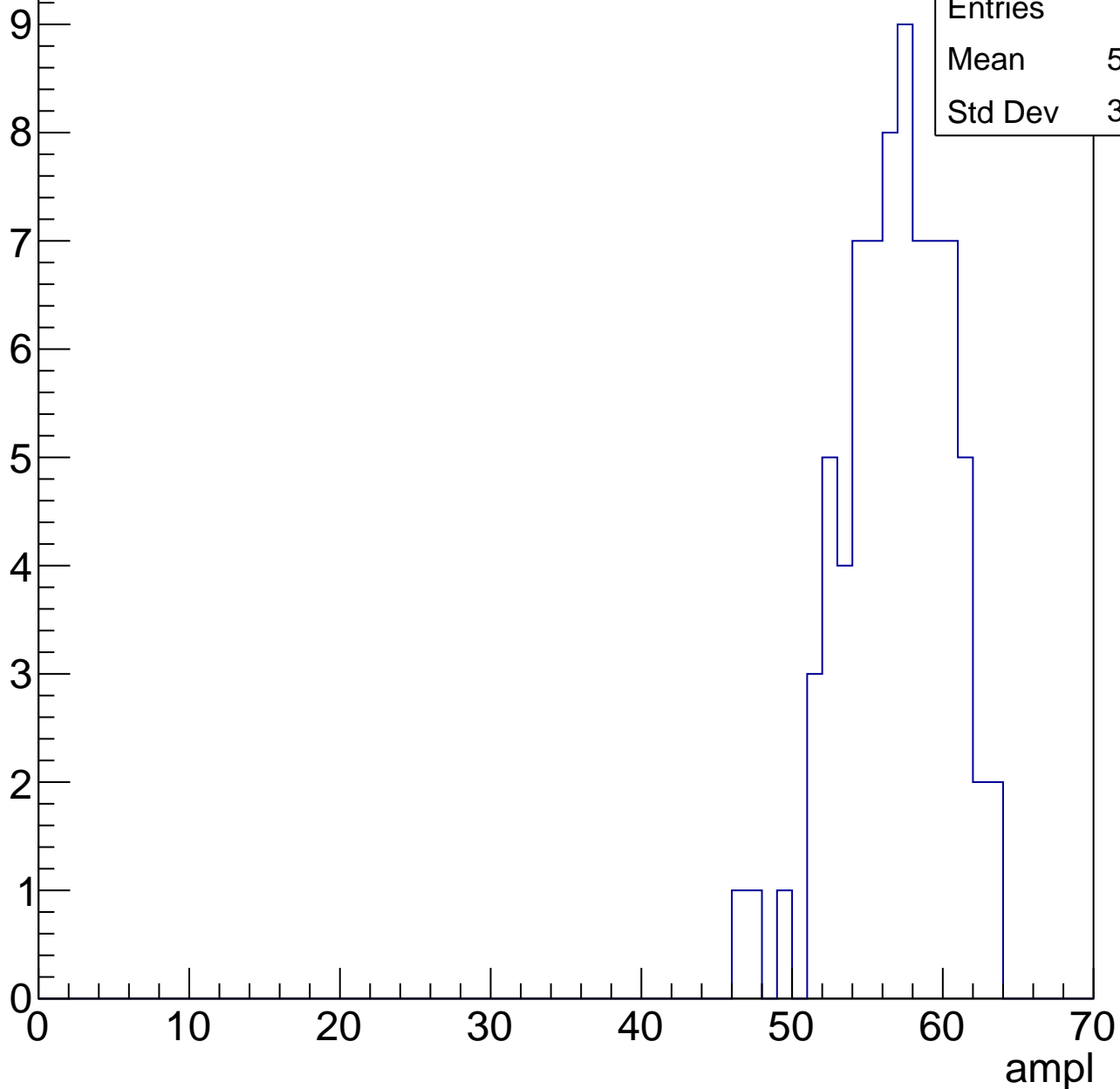
Entries	60
Mean	49.77
Std Dev	3.48



# B0L000S, U13-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

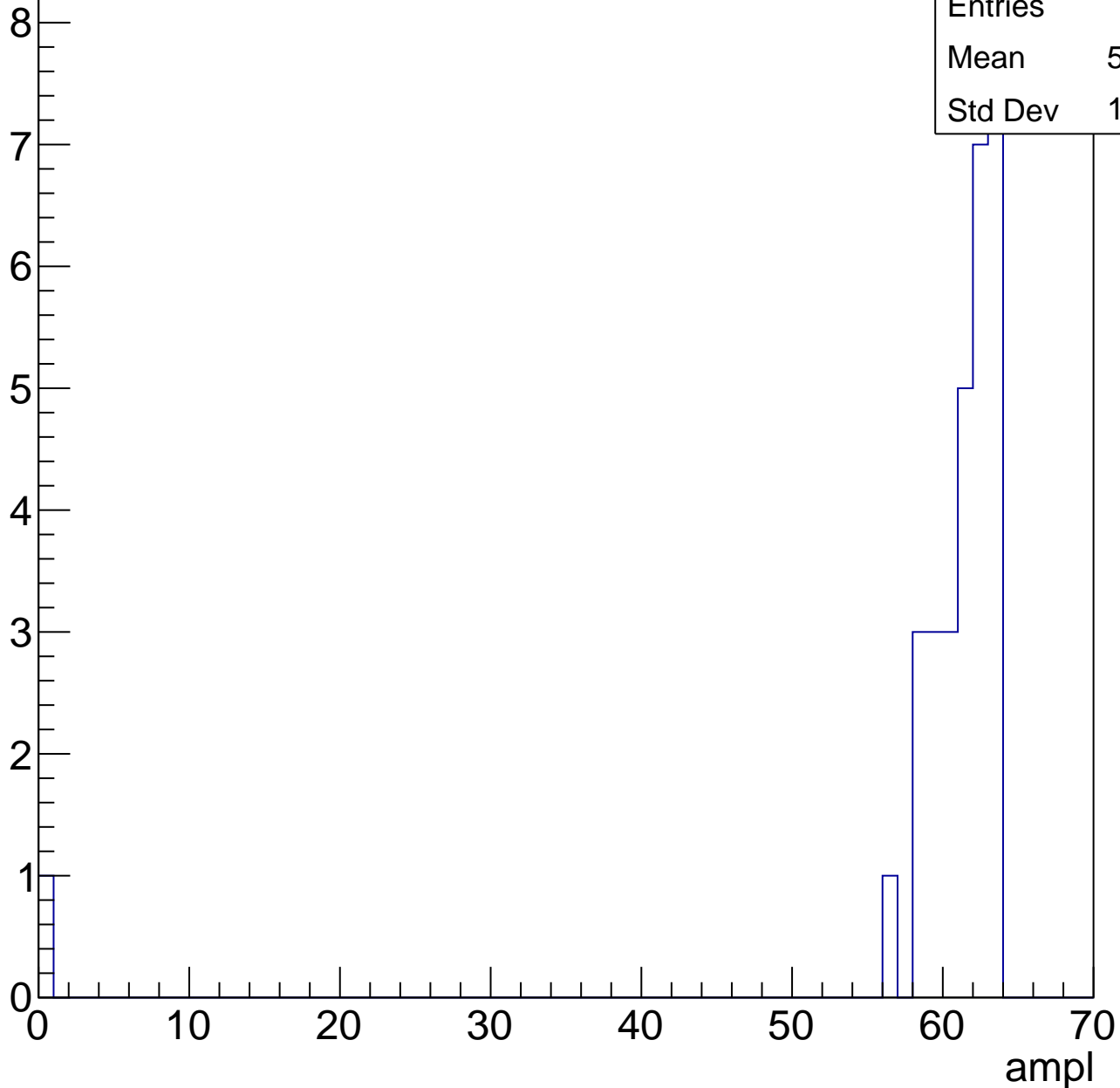


# B0L000S, U13-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	59.03
Std Dev	10.94



# B0L000S, U13-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U13-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	30.72
Std Dev	4.991

**Gaus mean : 31.6278**

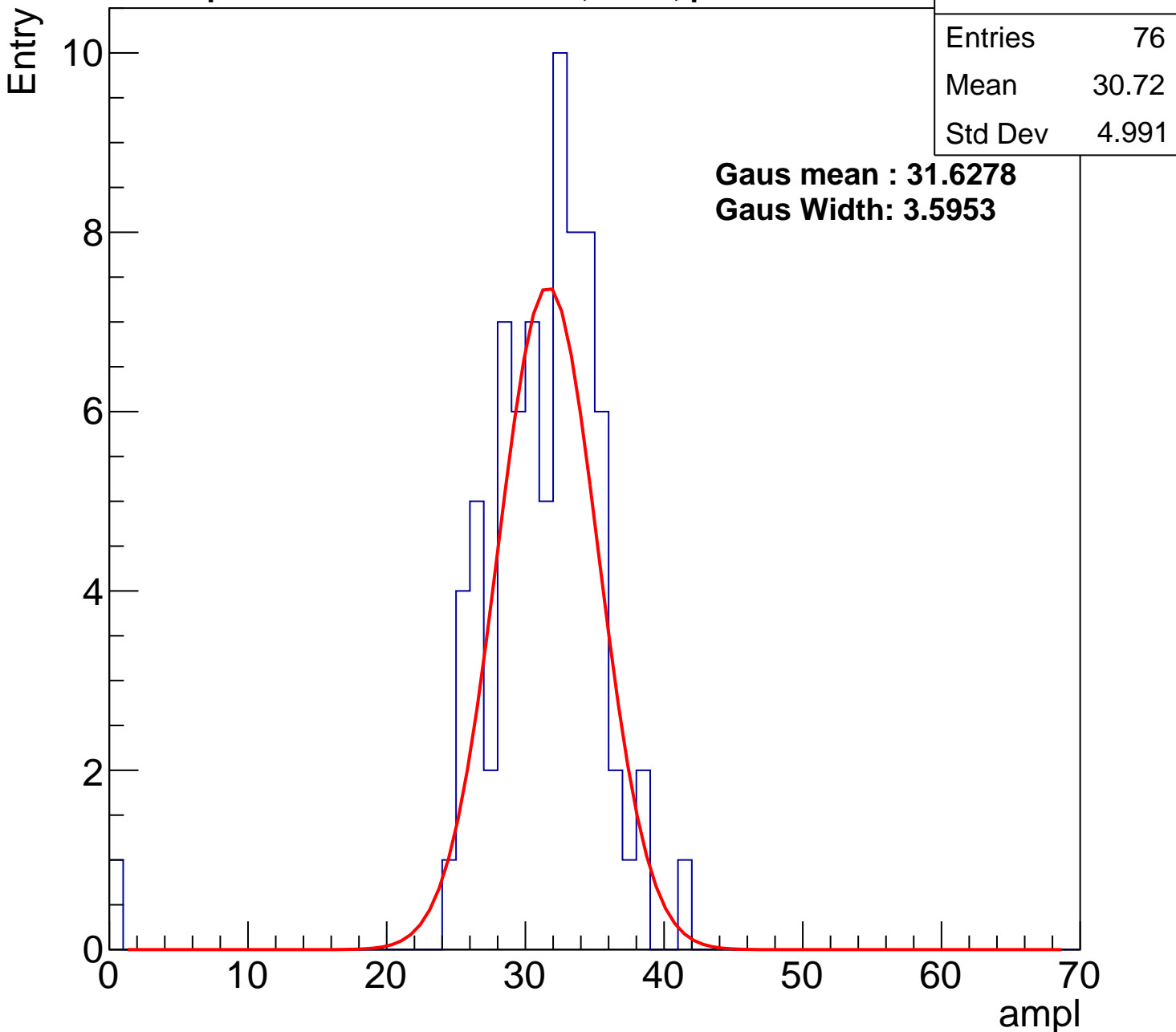
**Gaus Width: 3.5953**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



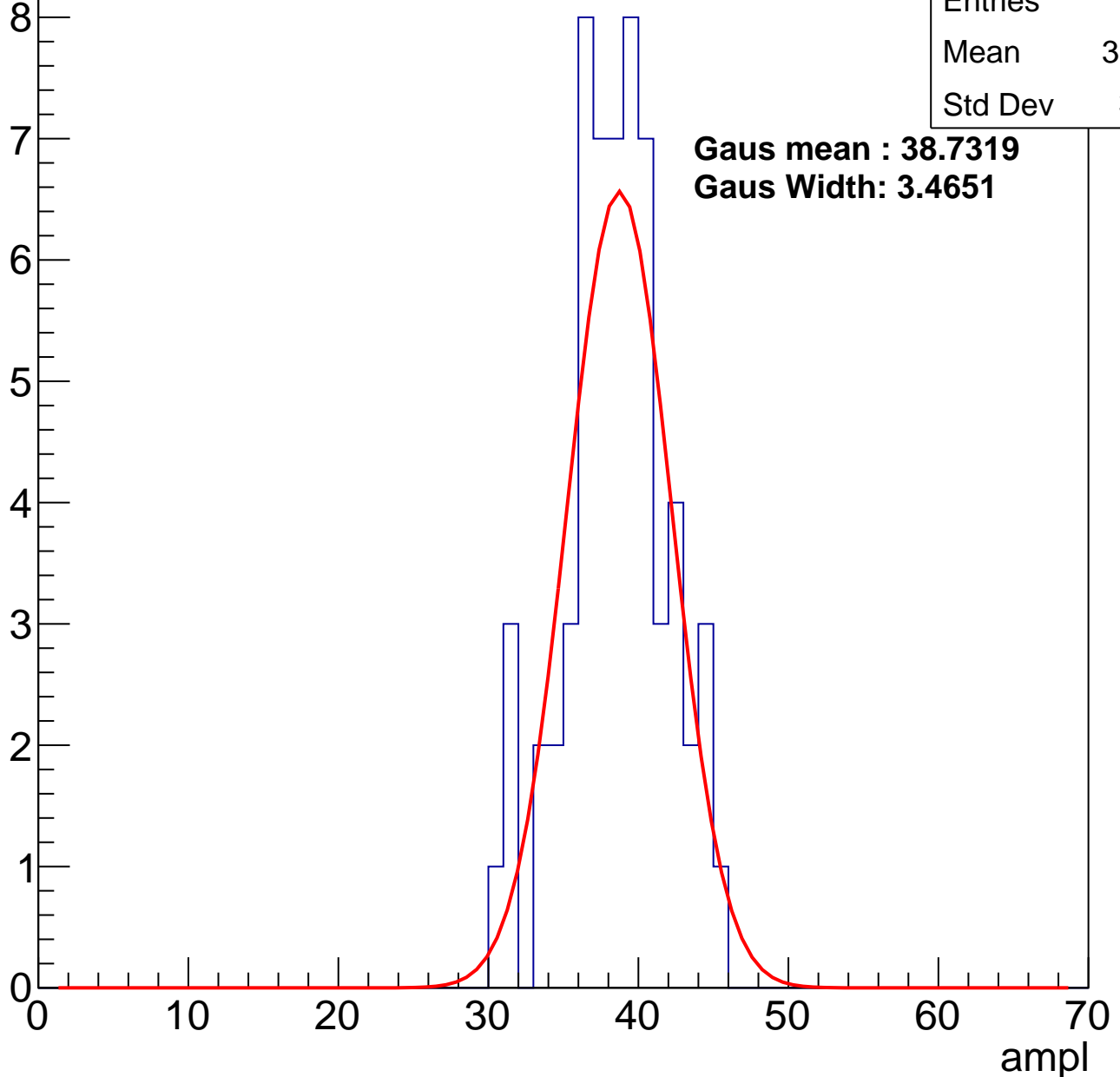
# B0L000S, U13-ch33, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	38.05
Std Dev	3.37

**Gaus mean : 38.7319**  
**Gaus Width: 3.4651**



# B0L000S, U13-ch33, adc2

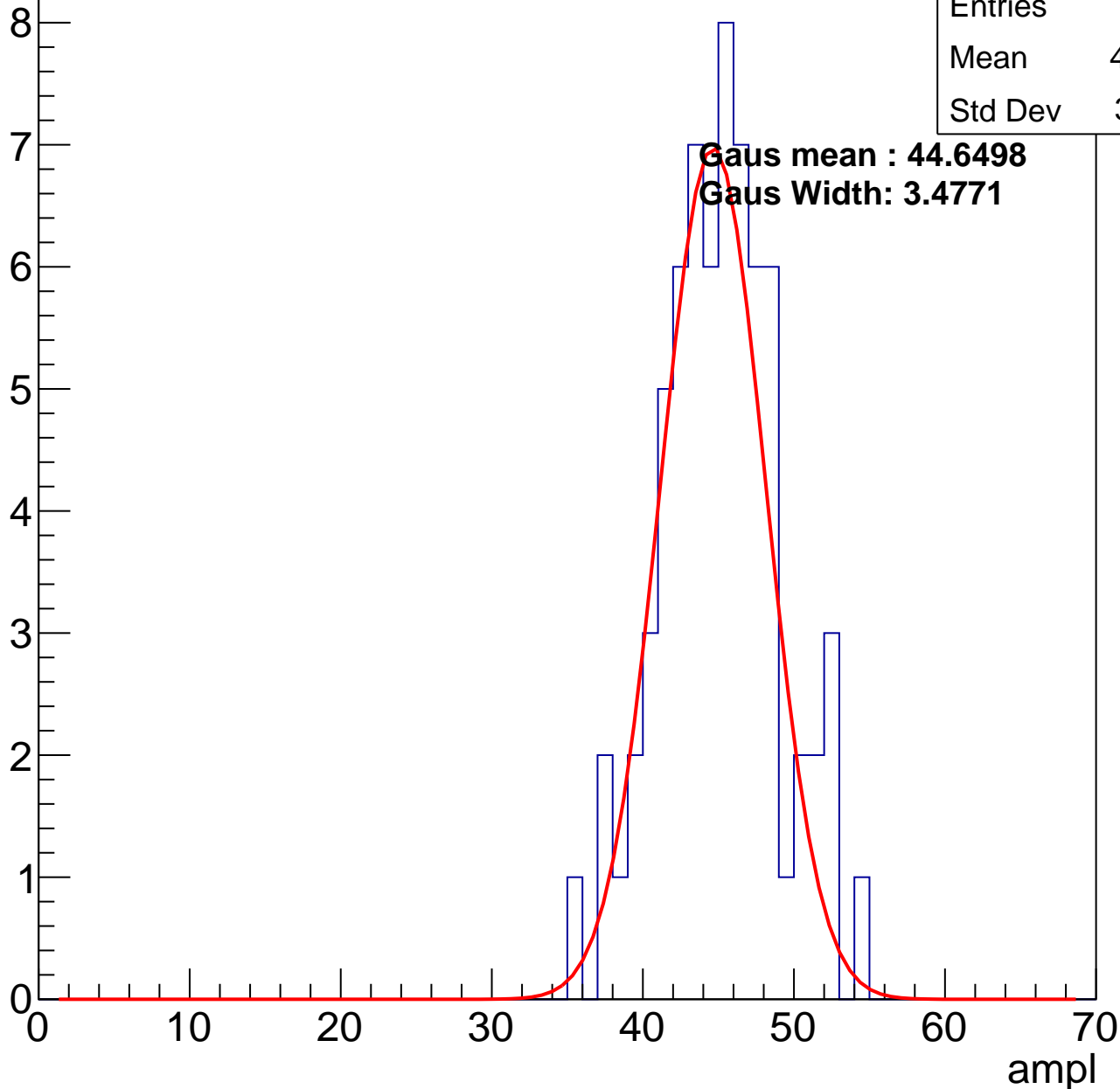
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	44.64
Std Dev	3.871

**Gaus mean : 44.6498**

**Gaus Width: 3.4771**

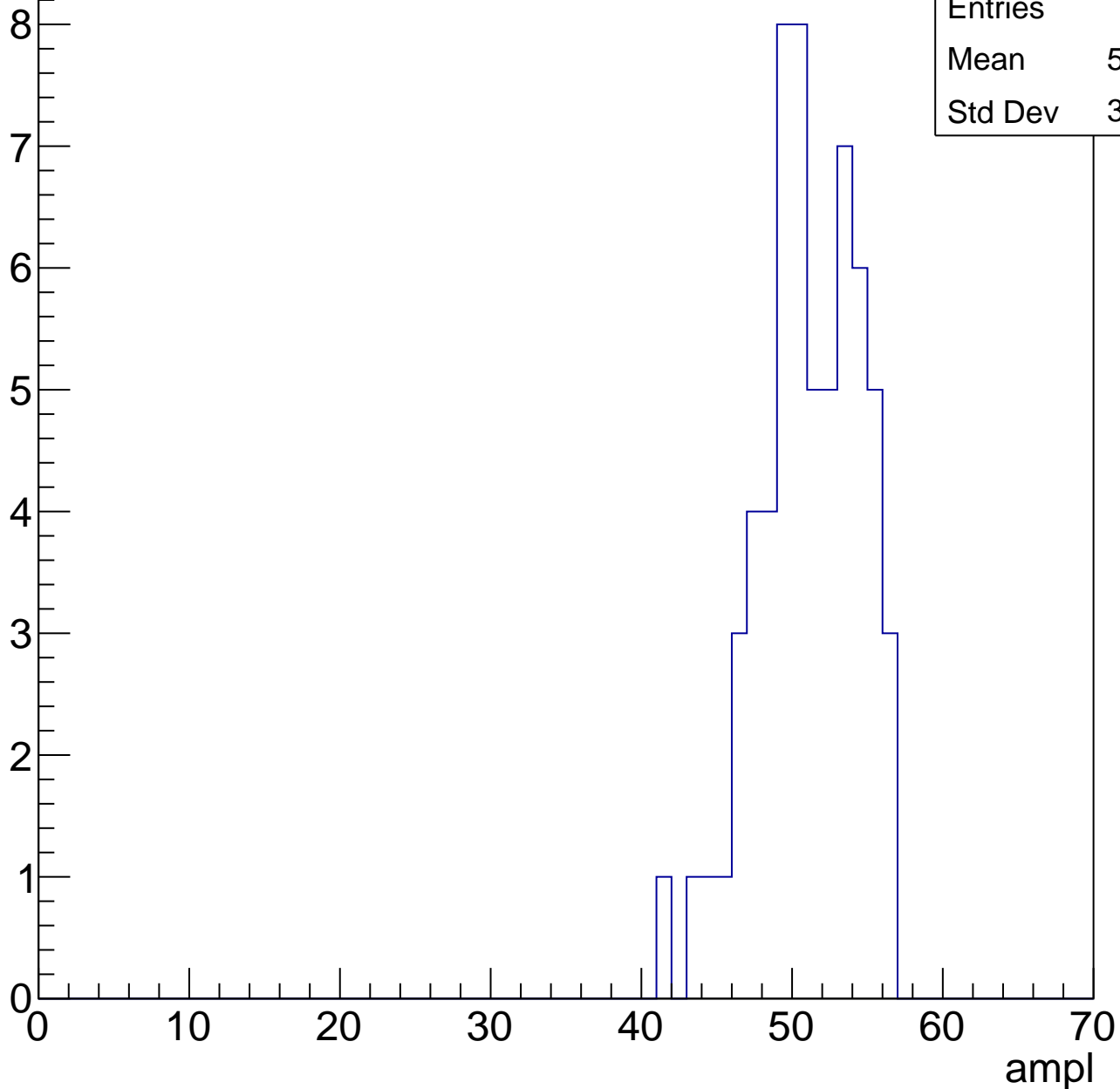


# B0L000S, U13-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.58
Std Dev	3.348

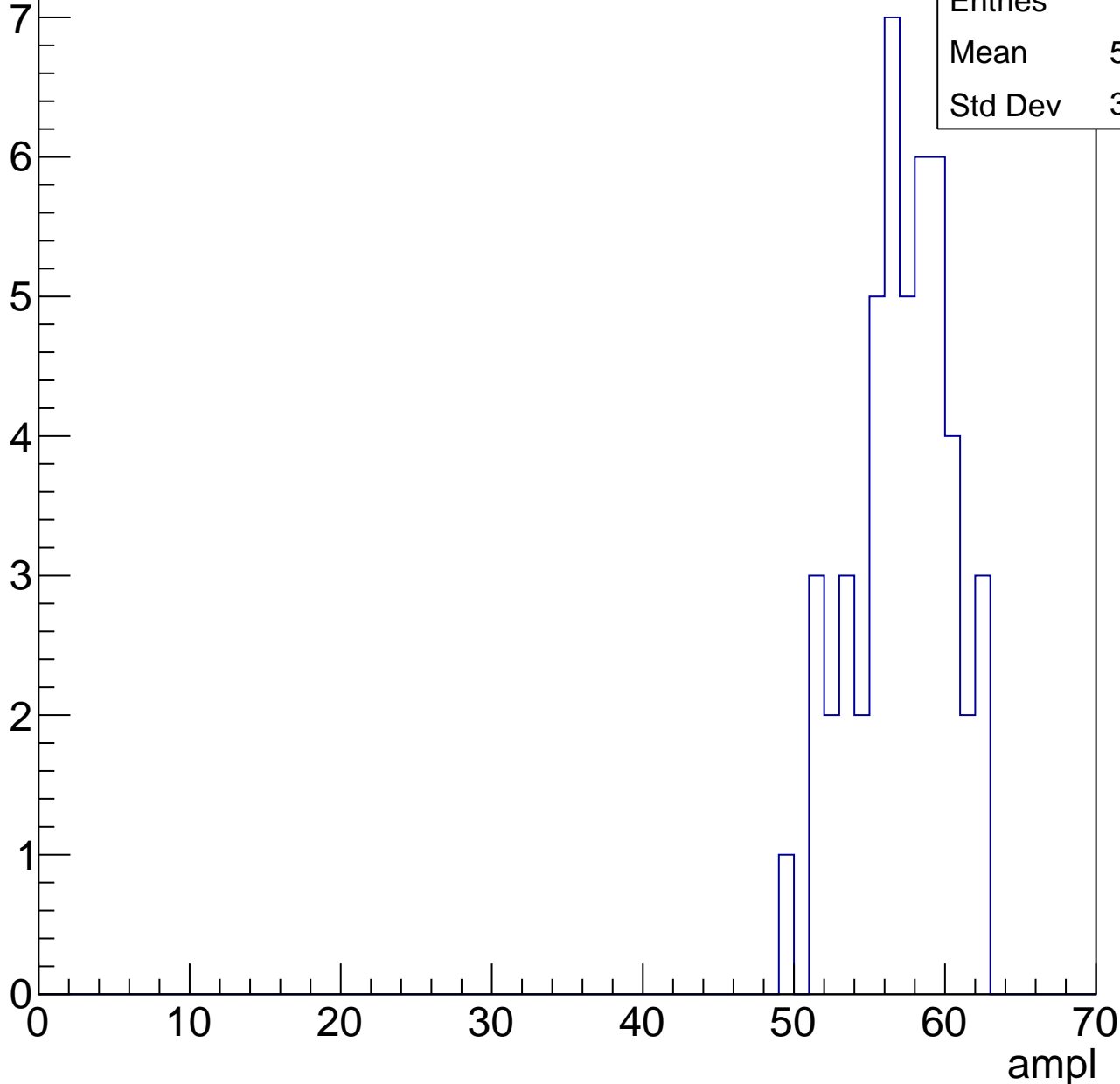


# B0L000S, U13-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	56.63
Std Dev	3.134

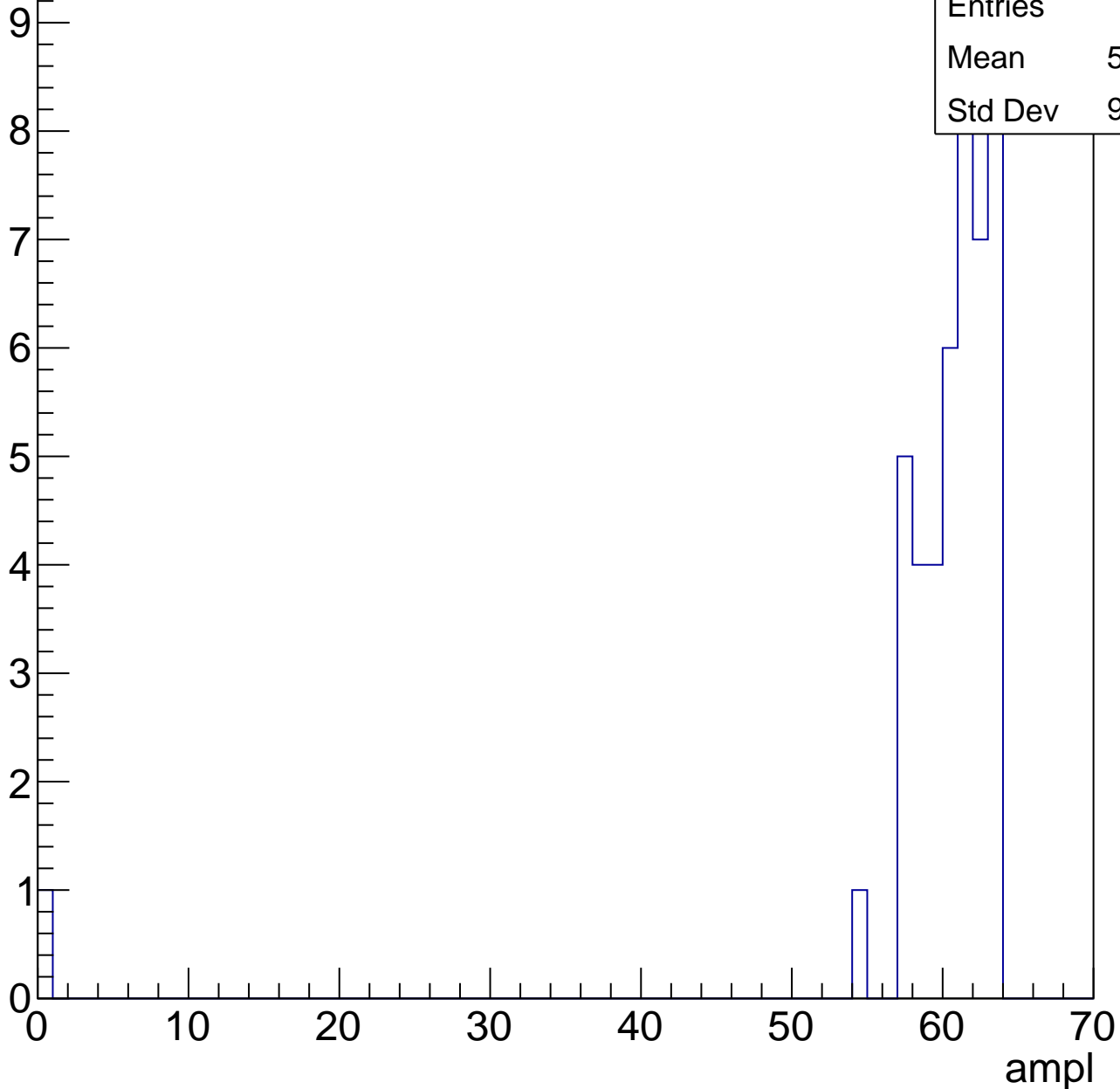


# B0L000S, U13-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	59.02
Std Dev	9.159



# B0L000S, U13-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U13-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch34, adc0

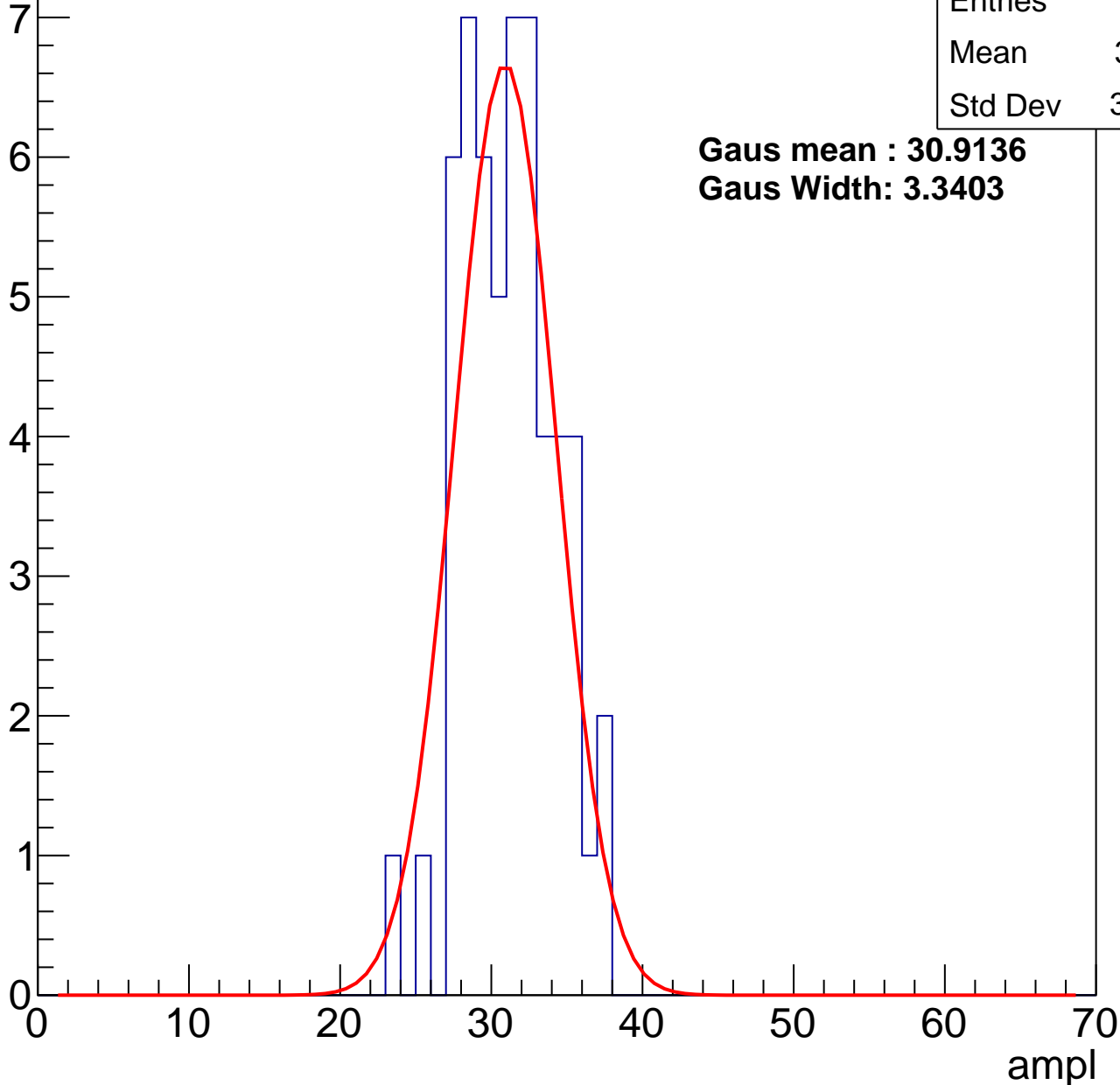
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	30.71
Std Dev	3.025

**Gaus mean : 30.9136**

**Gaus Width: 3.3403**



# B0L000S, U13-ch34, adc1

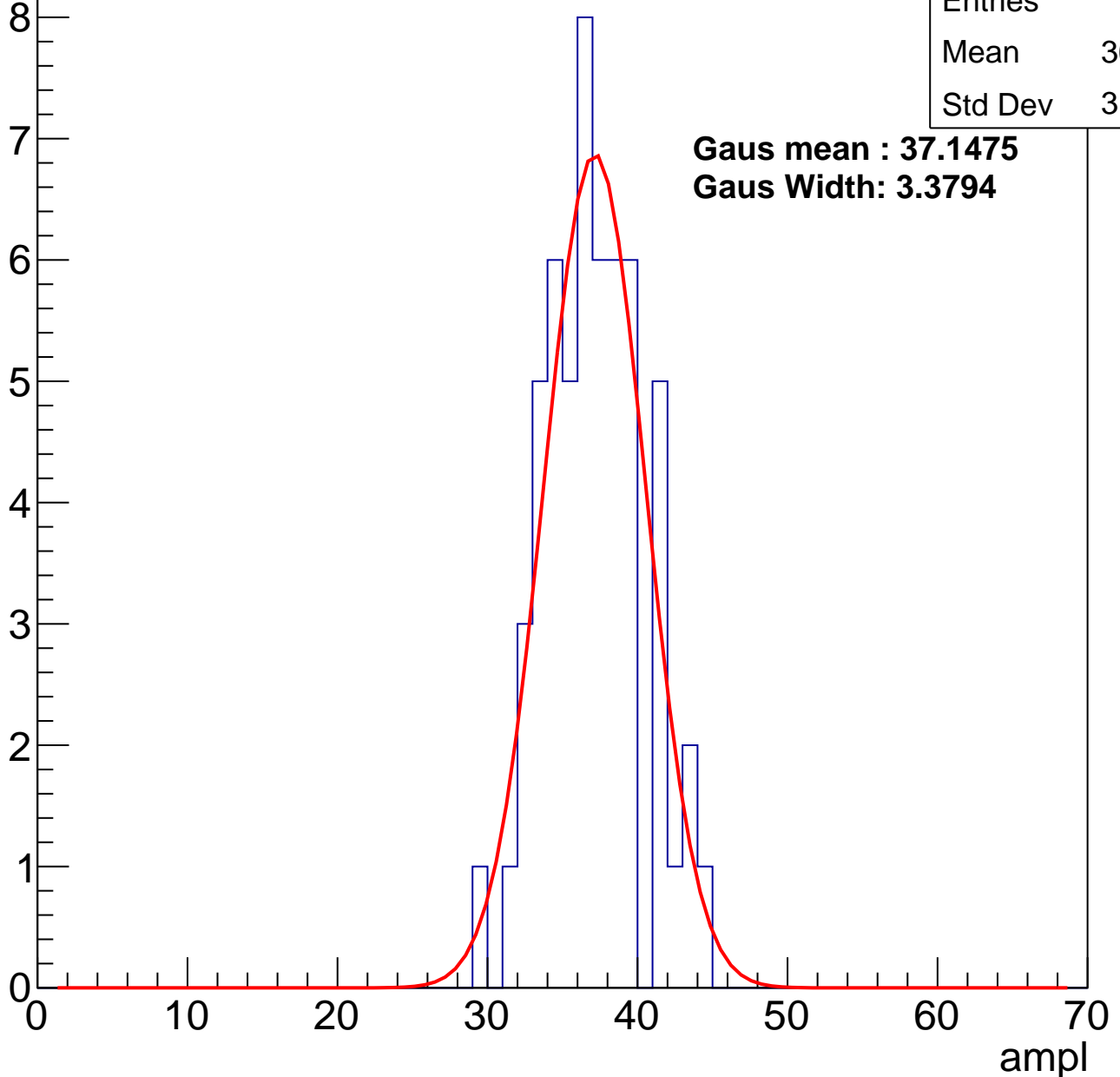
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.59
Std Dev	3.228

**Gaus mean : 37.1475**

**Gaus Width: 3.3794**



# B0L000S, U13-ch34, adc2

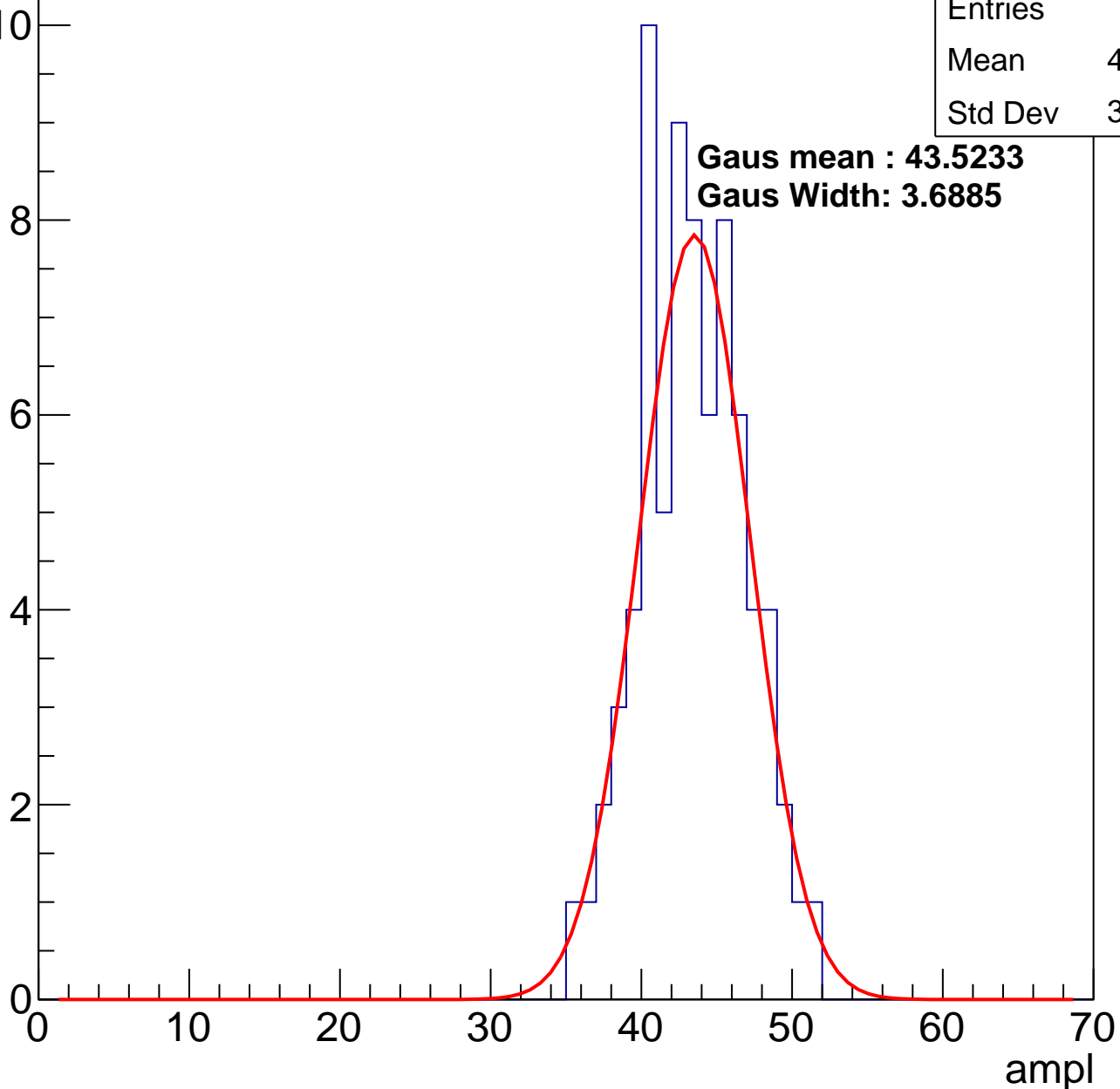
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	42.95
Std Dev	3.433

**Gaus mean : 43.5233**

**Gaus Width: 3.6885**

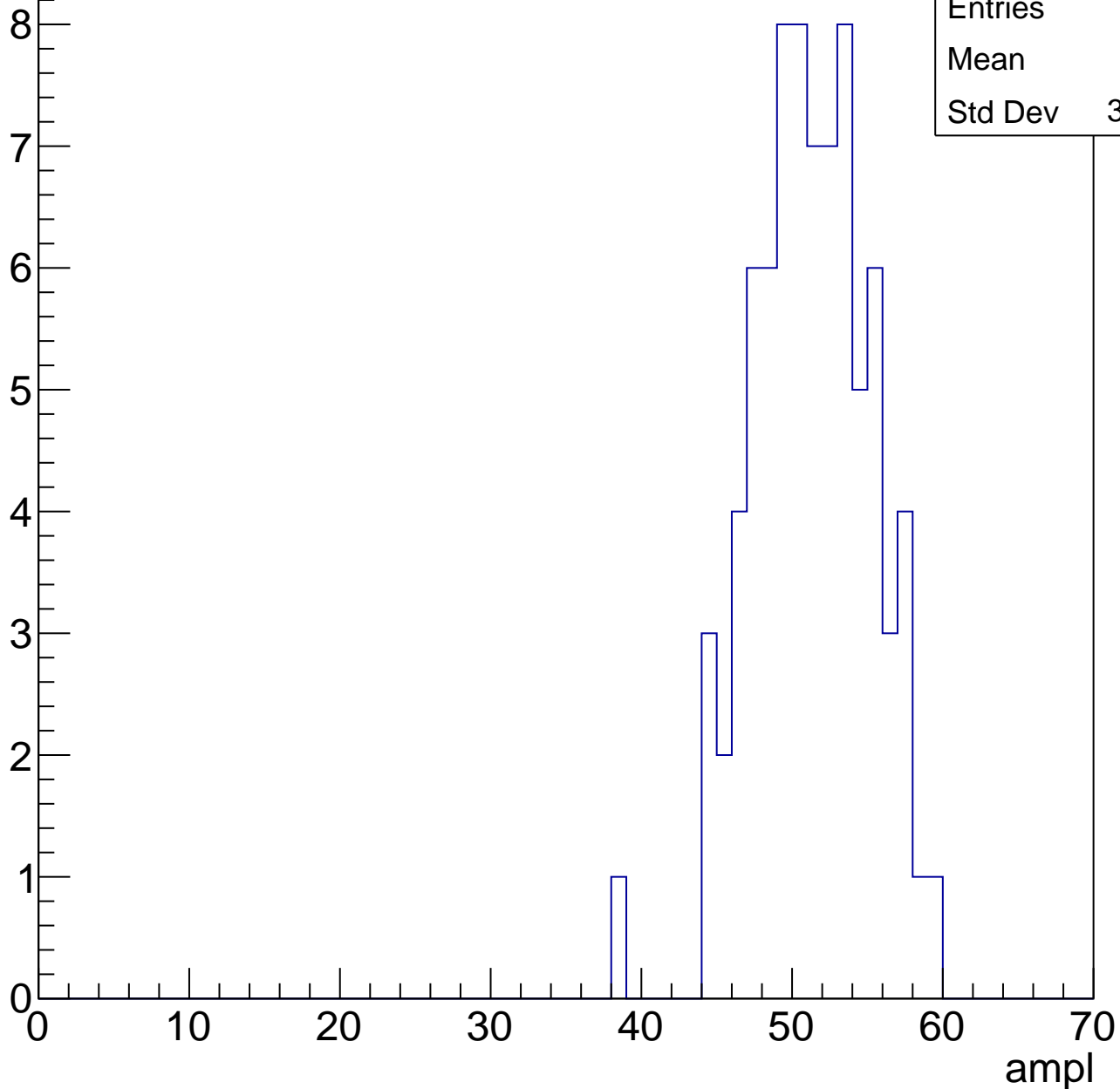


# B0L000S, U13-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	50.8
Std Dev	3.852

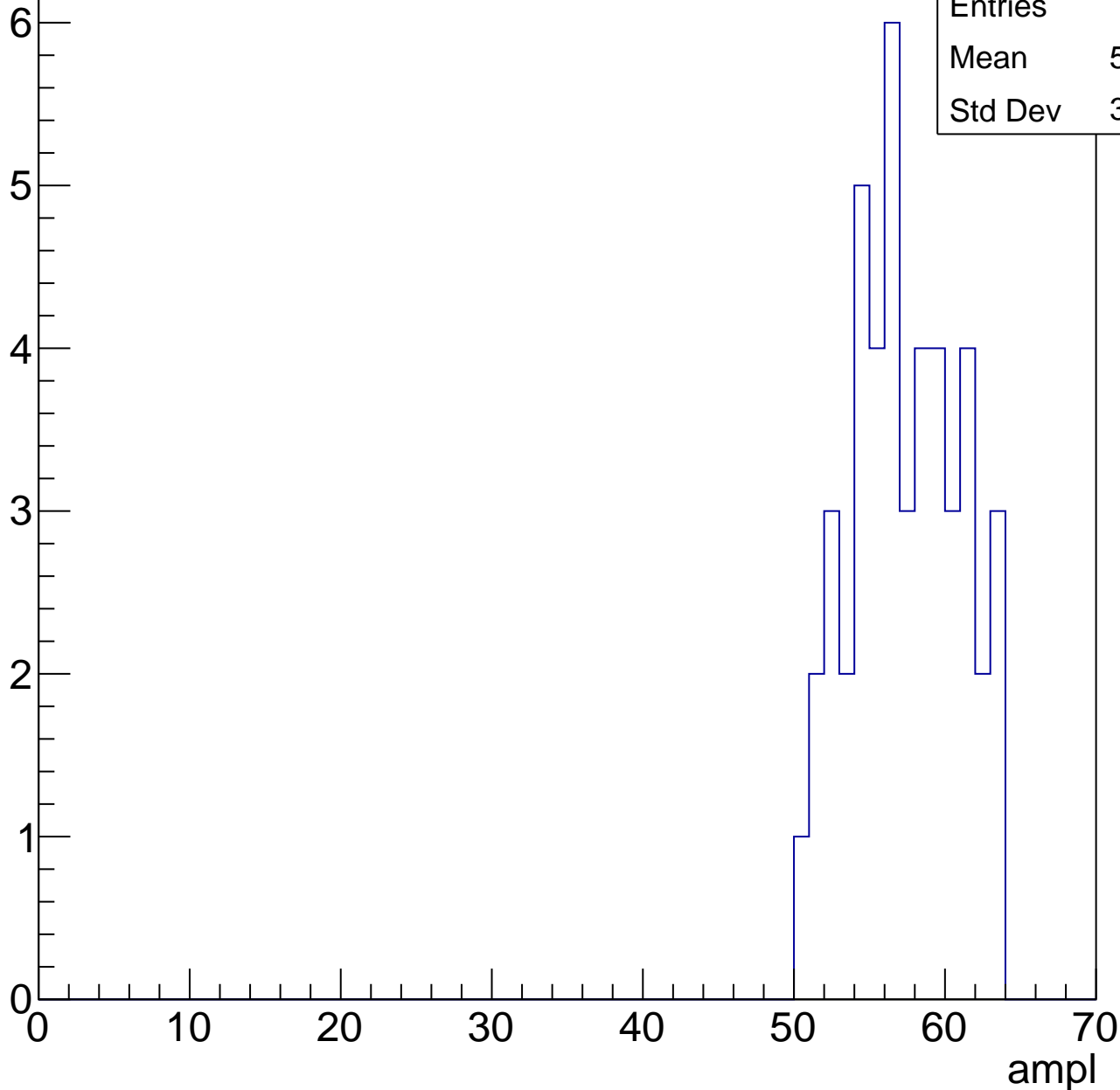


# B0L000S, U13-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	56.87
Std Dev	3.499

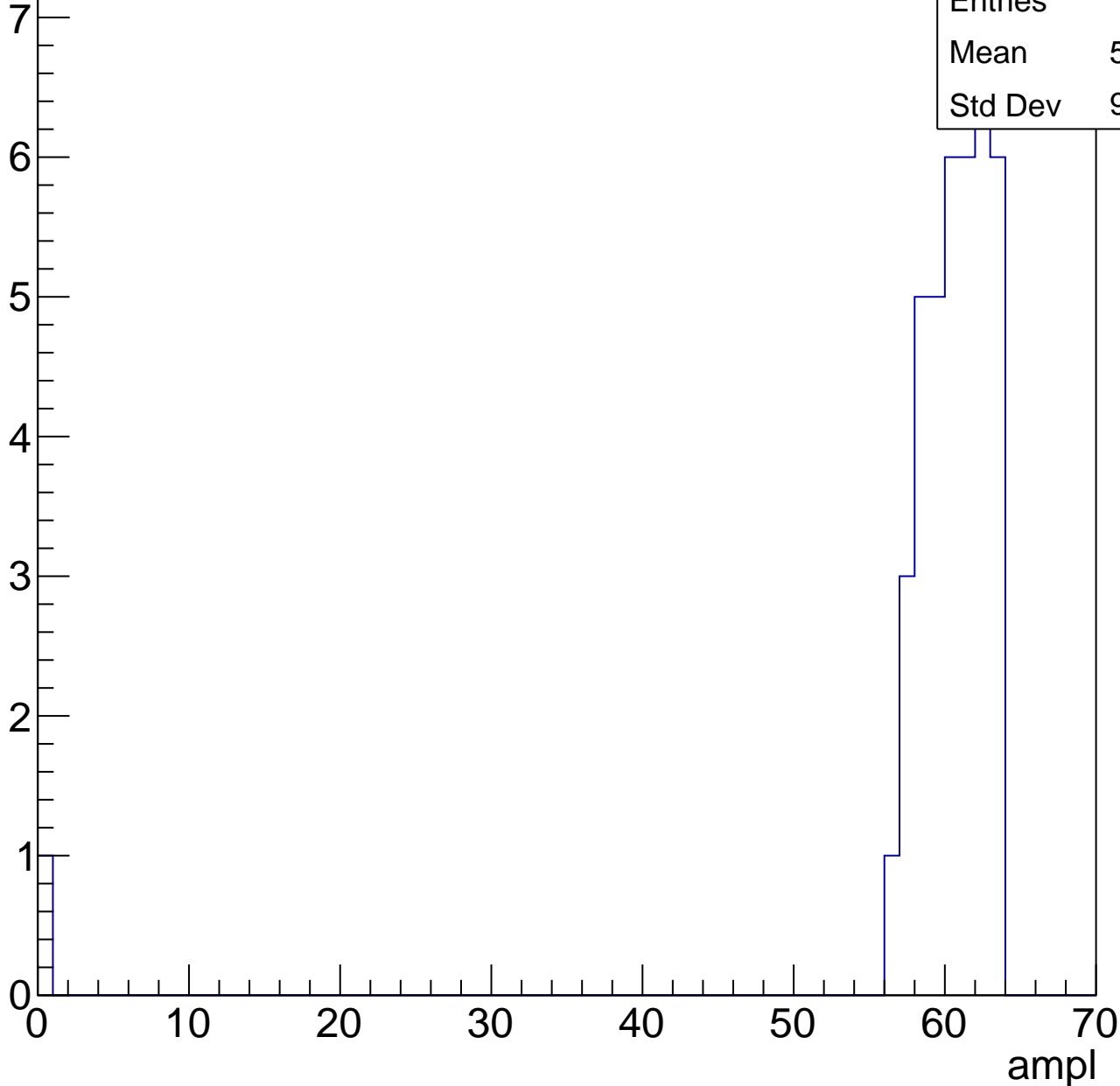


# B0L000S, U13-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

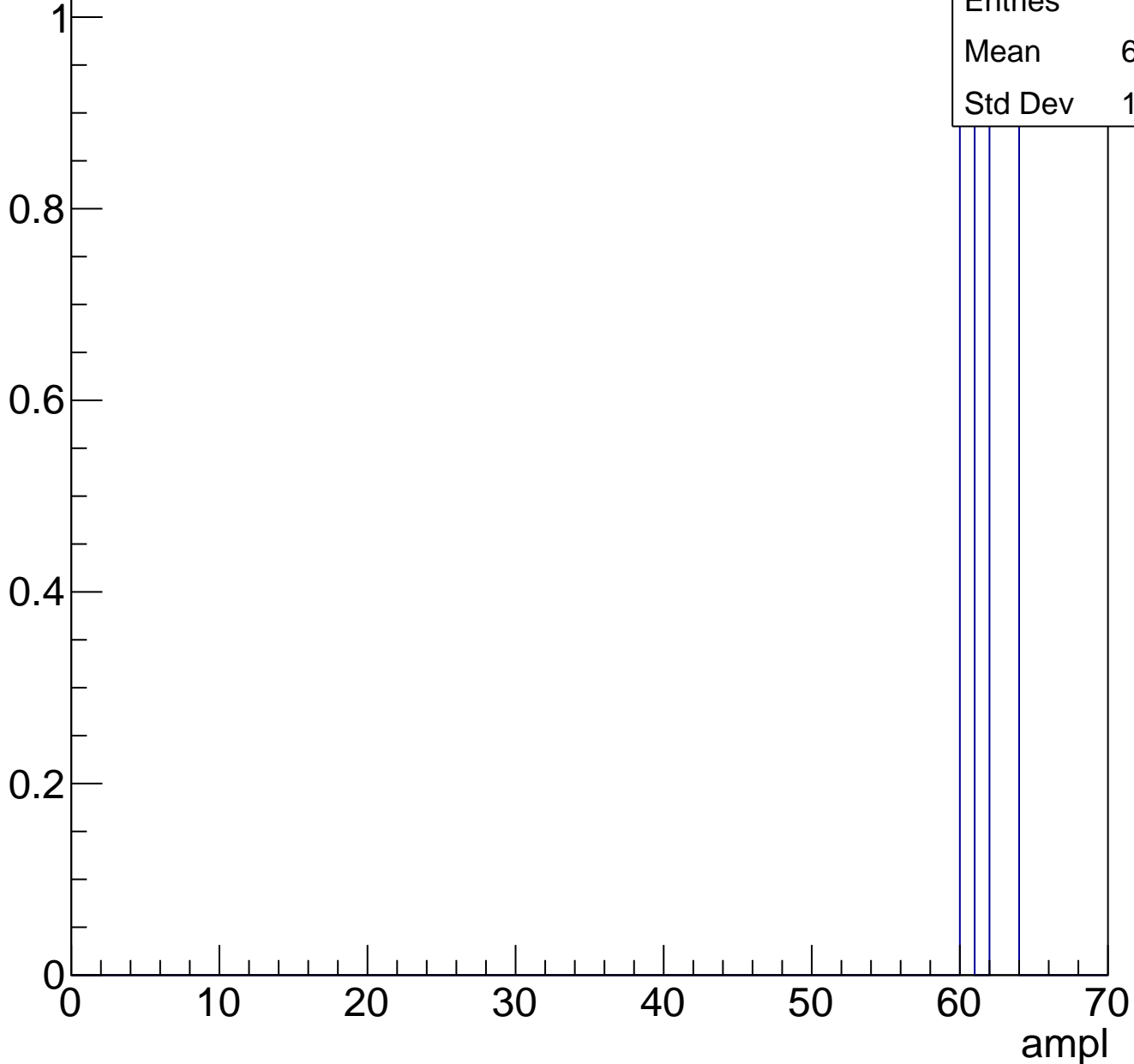
Entries	40
Mean	58.75
Std Dev	9.609



# B0L000S, U13-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch35, adc0

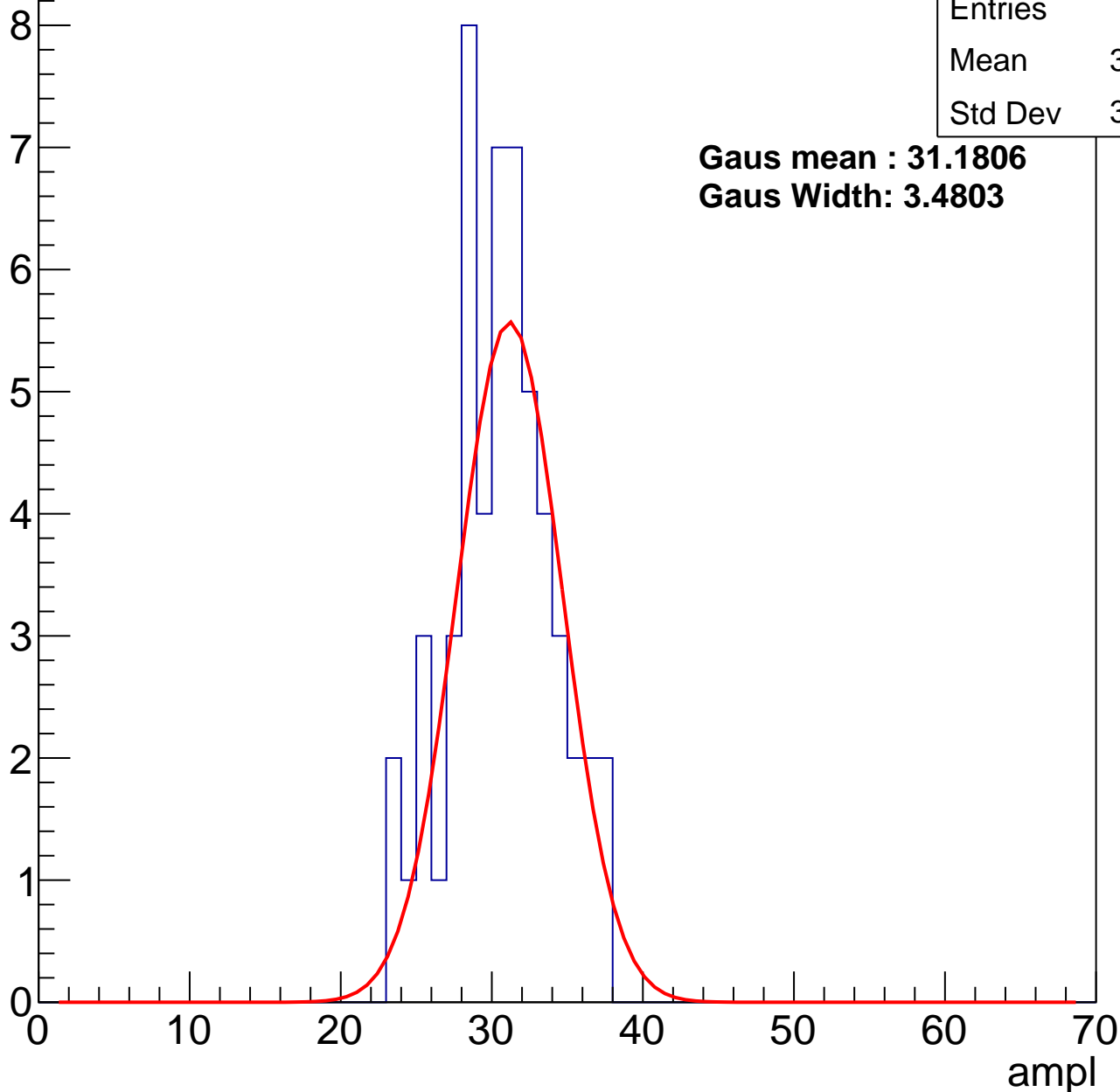
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	30.17
Std Dev	3.382

**Gaus mean : 31.1806**

**Gaus Width: 3.4803**



# B0L000S, U13-ch35, adc1

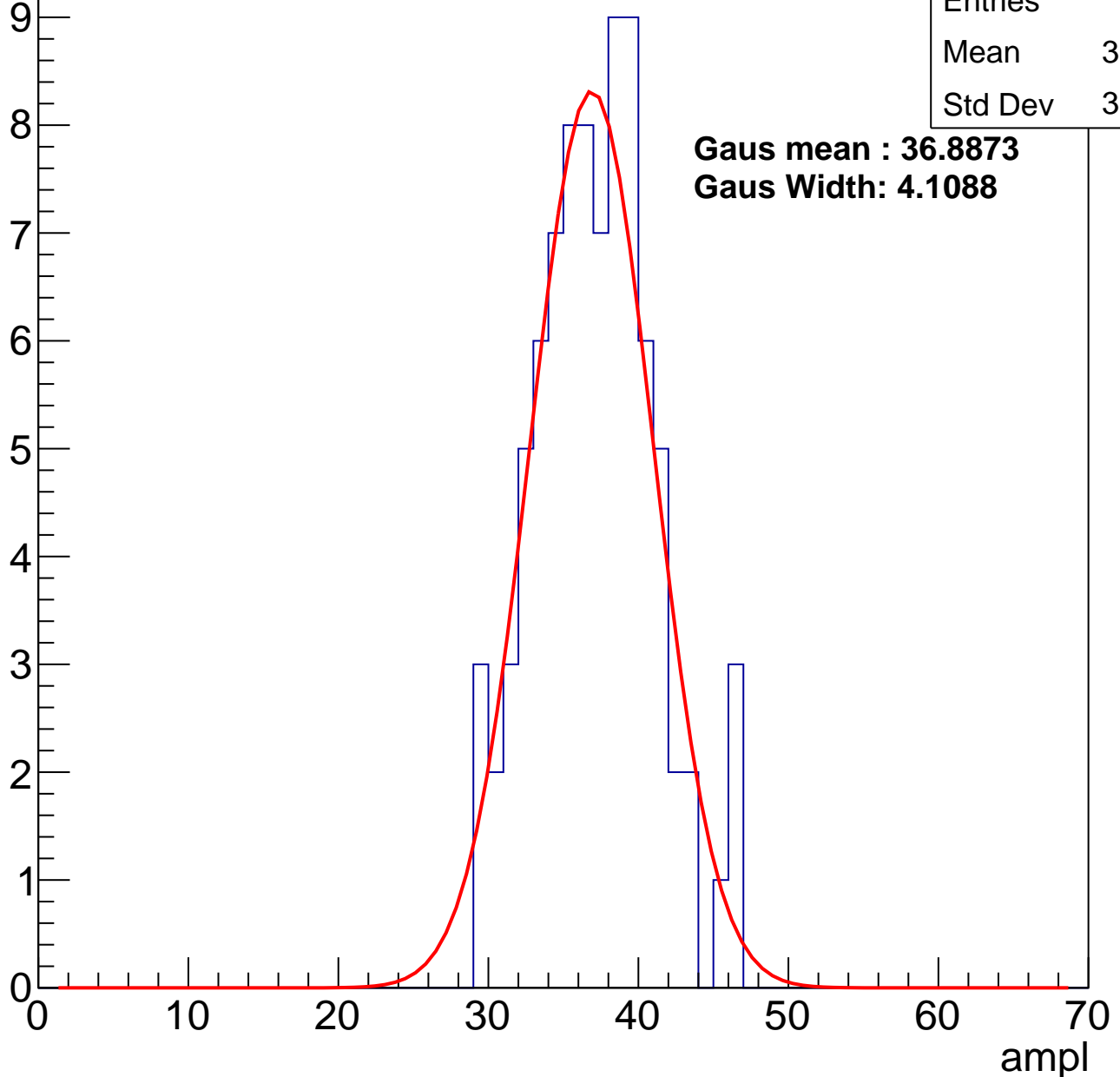
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	36.67
Std Dev	3.919

**Gaus mean : 36.8873**

**Gaus Width: 4.1088**



# B0L000S, U13-ch35, adc2

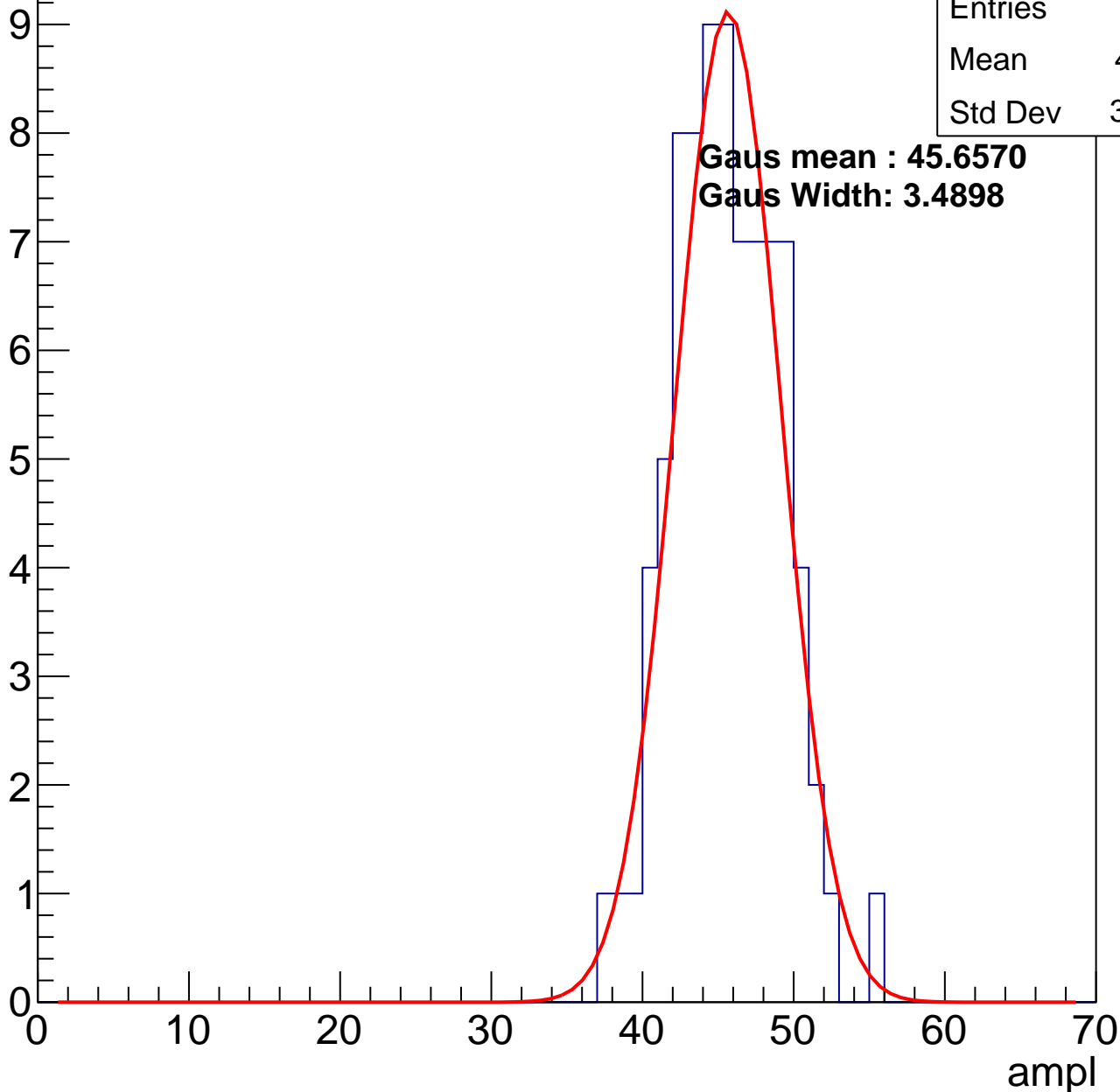
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	45.11
Std Dev	3.443

**Gaus mean : 45.6570**

**Gaus Width: 3.4898**

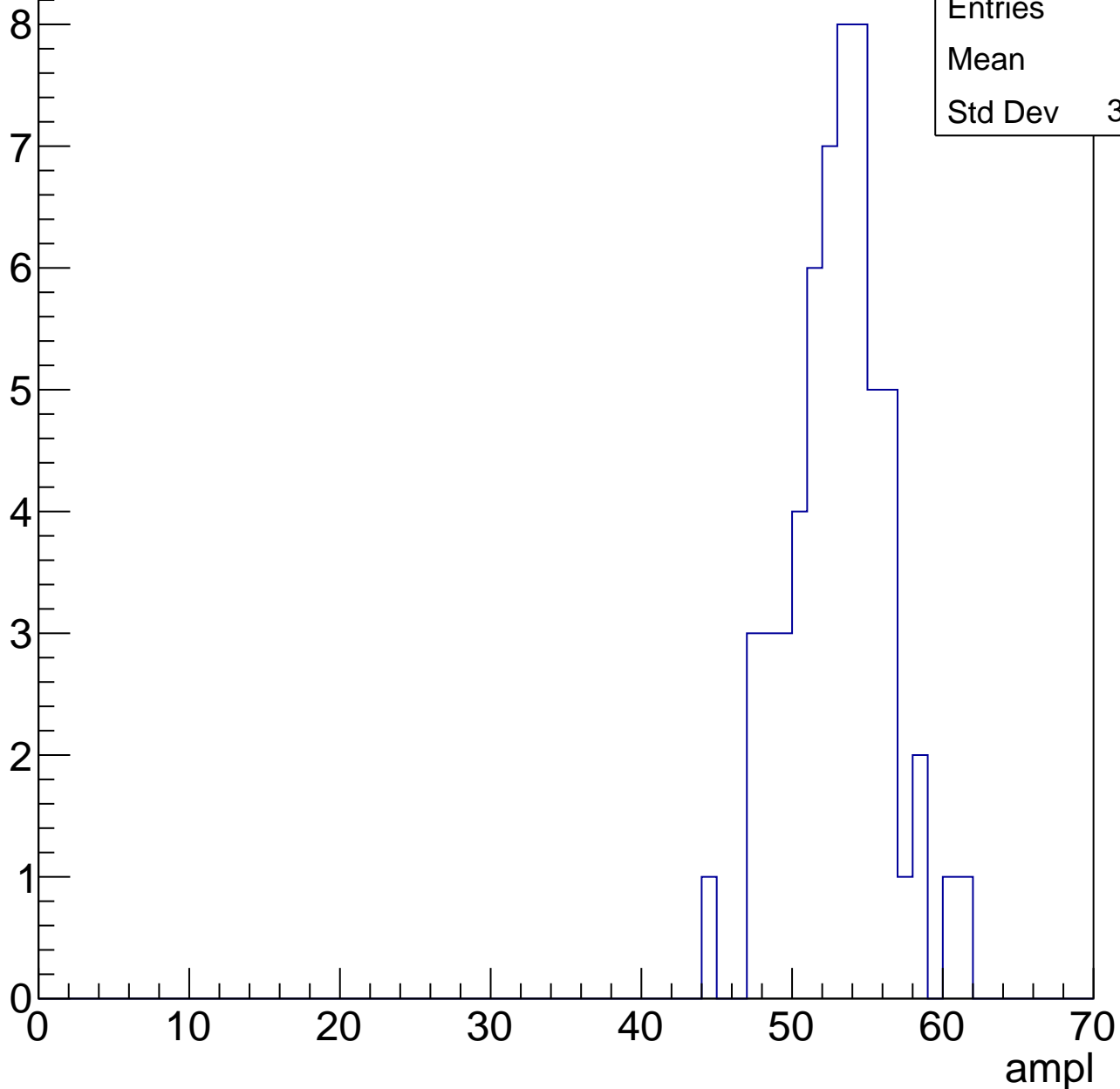


# B0L000S, U13-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

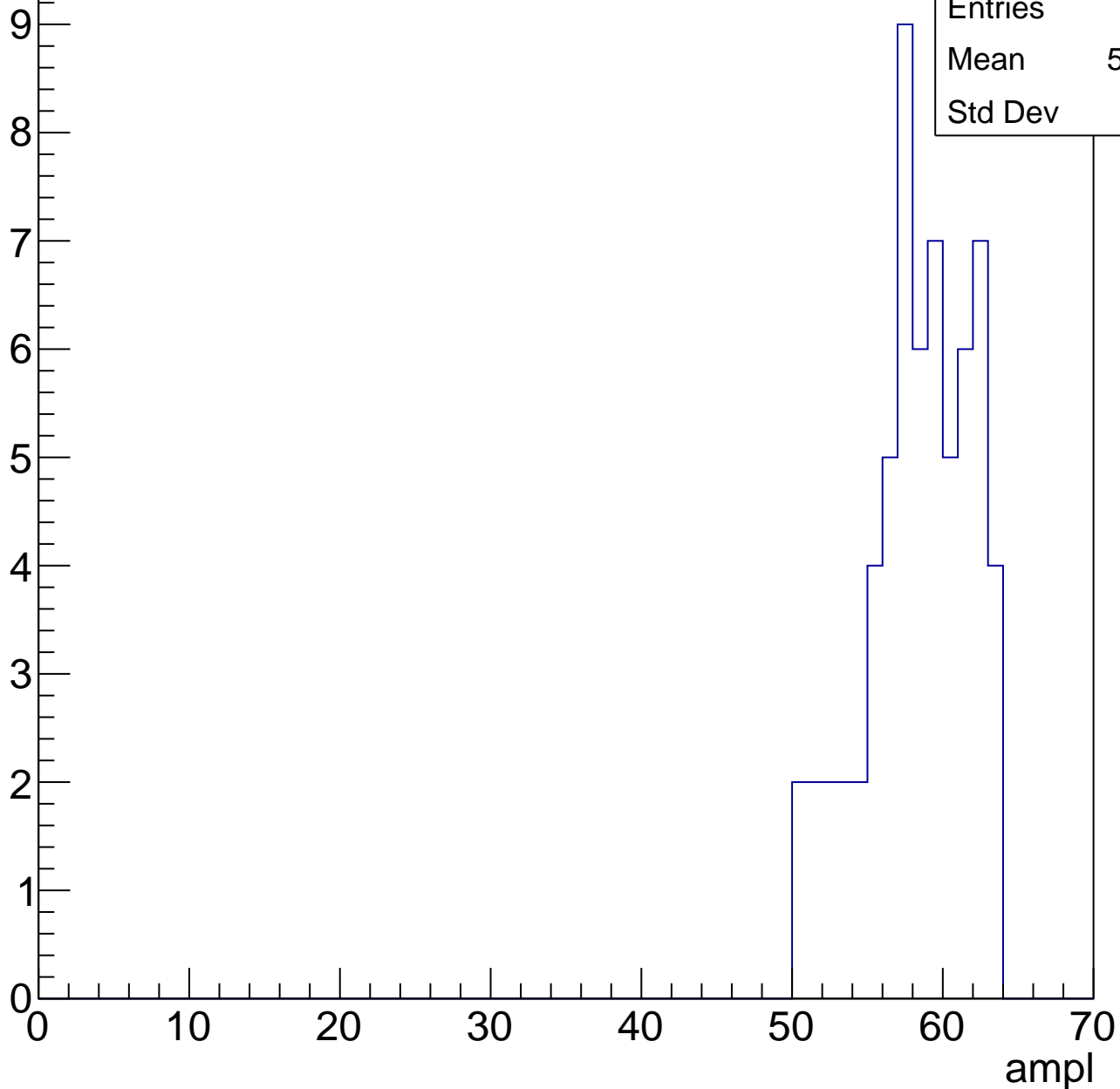
Entries	58
Mean	52.6
Std Dev	3.274



# B0L000S, U13-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

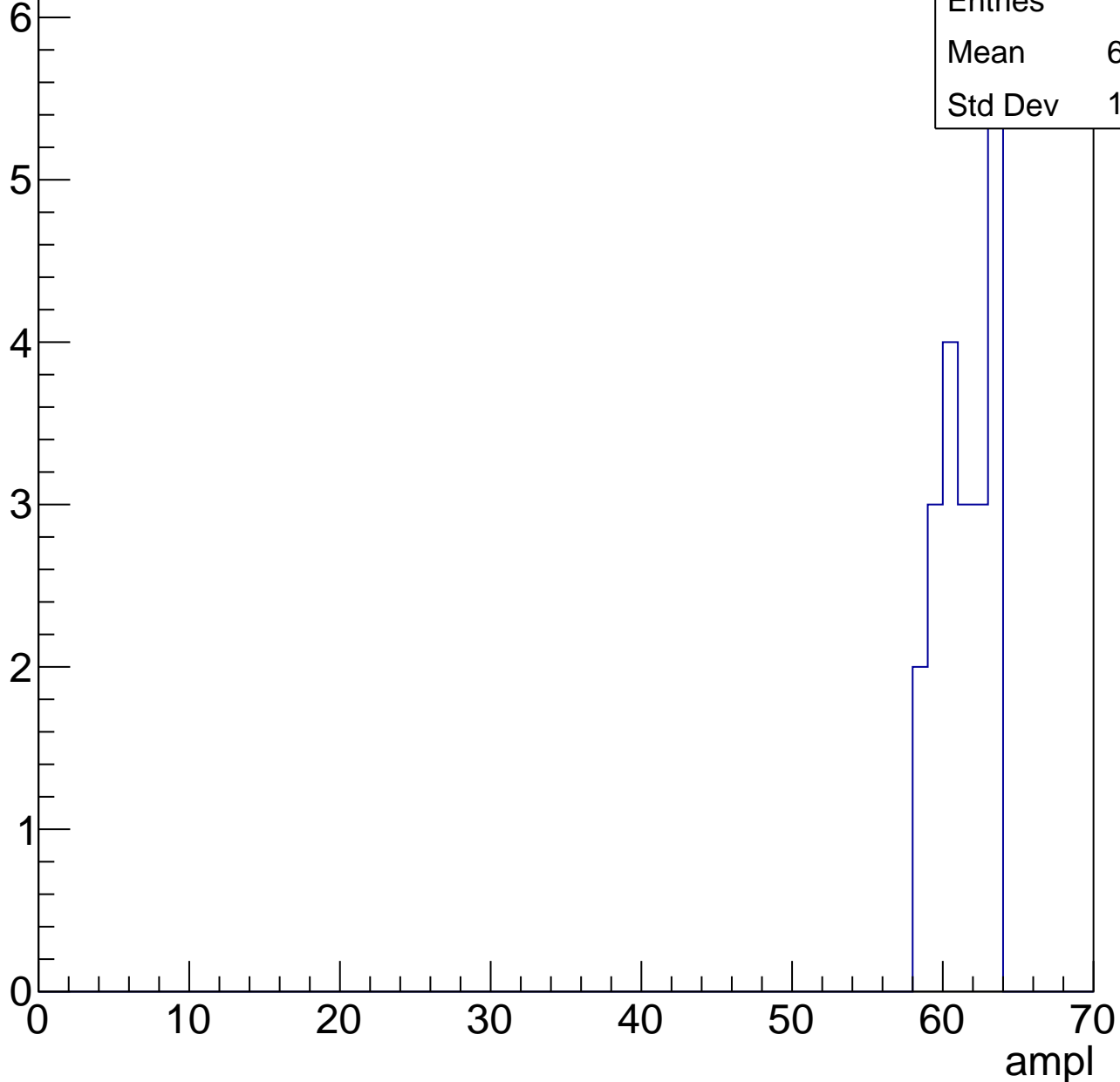
Entry



# B0L000S, U13-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



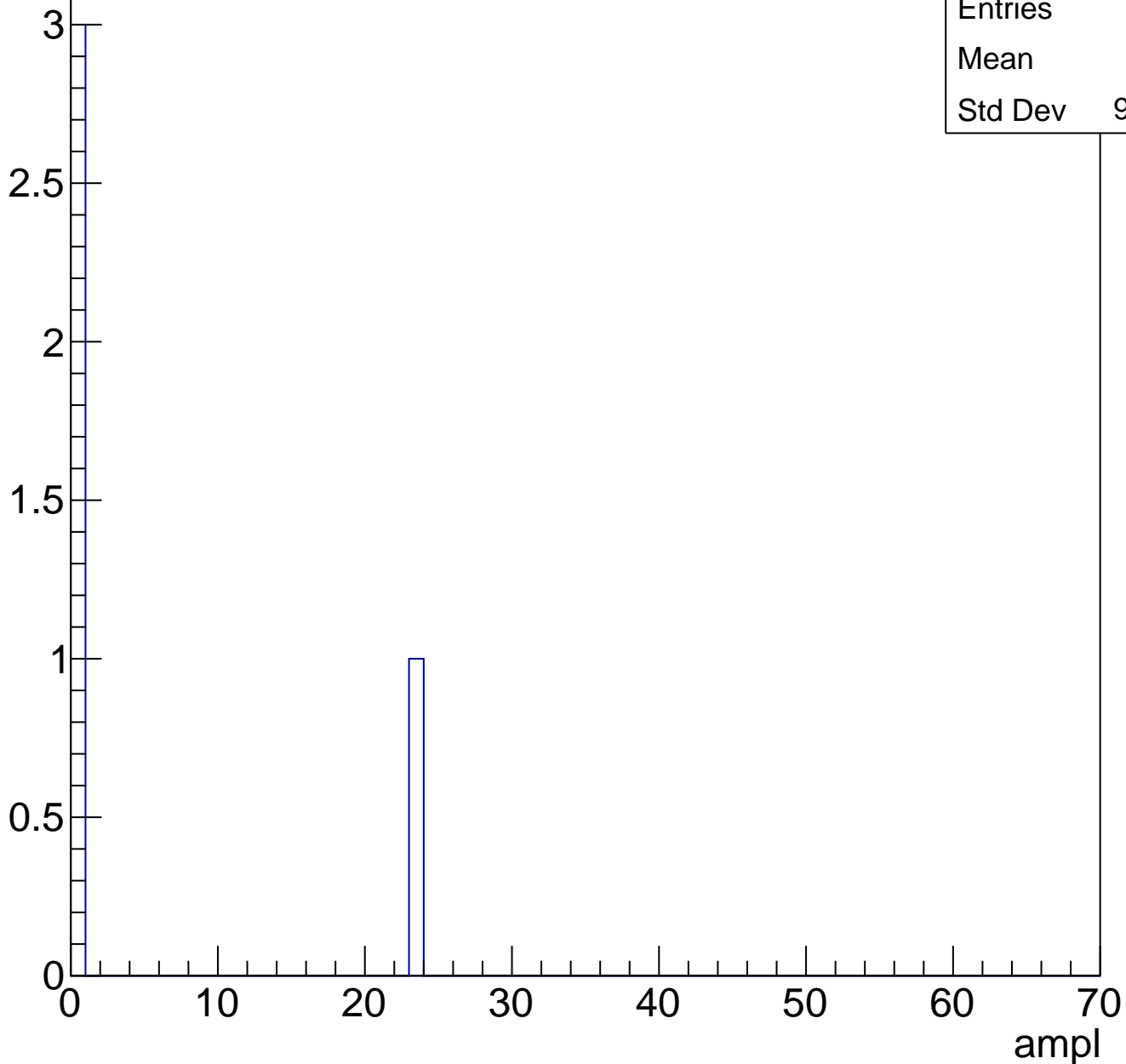
Entries	1
Mean	0
Std Dev	0



# B0L000S, U13-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch36, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	30.49
Std Dev	3.589

**Gaus mean : 30.9288**

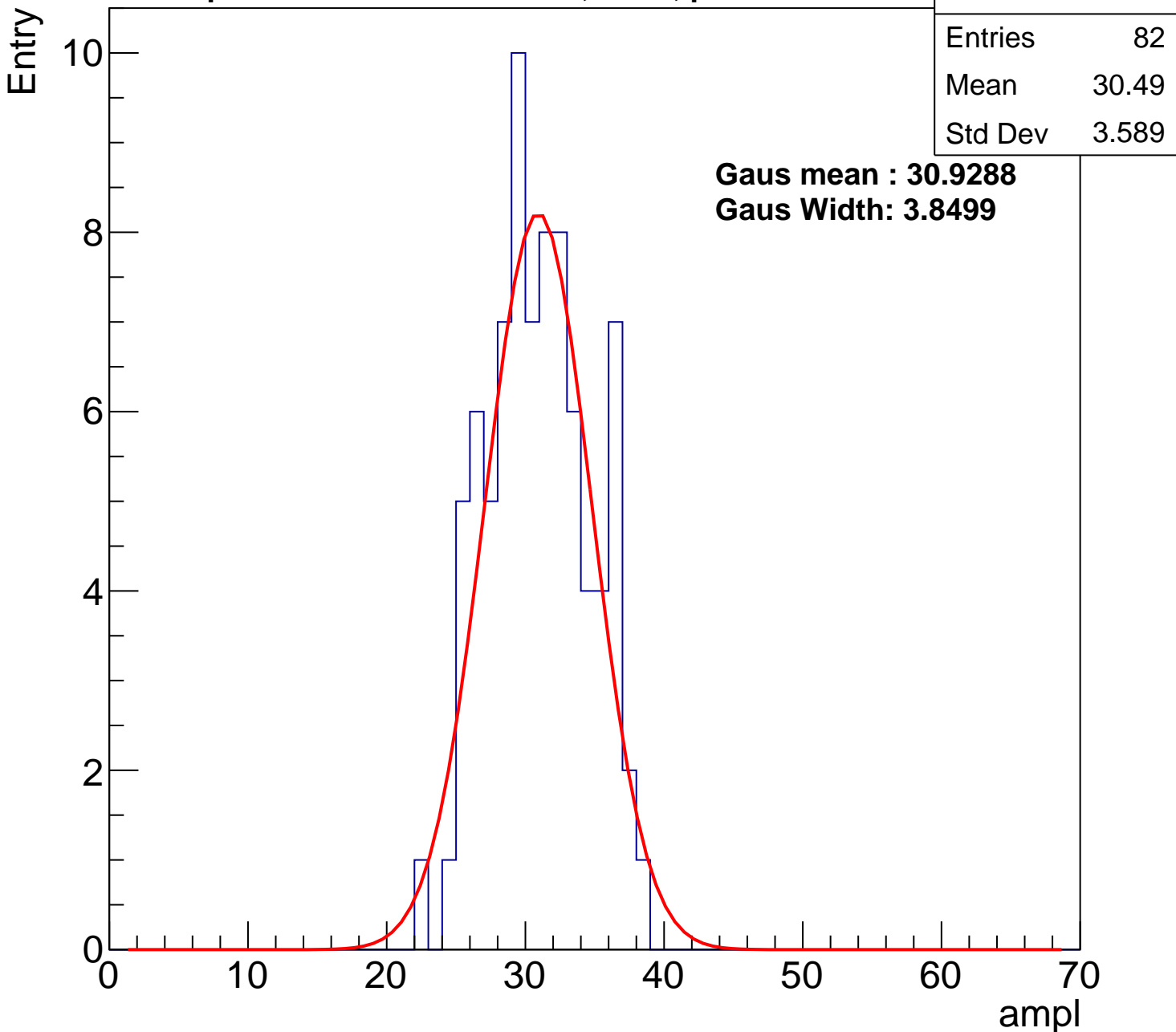
**Gaus Width: 3.8499**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch36, adc1

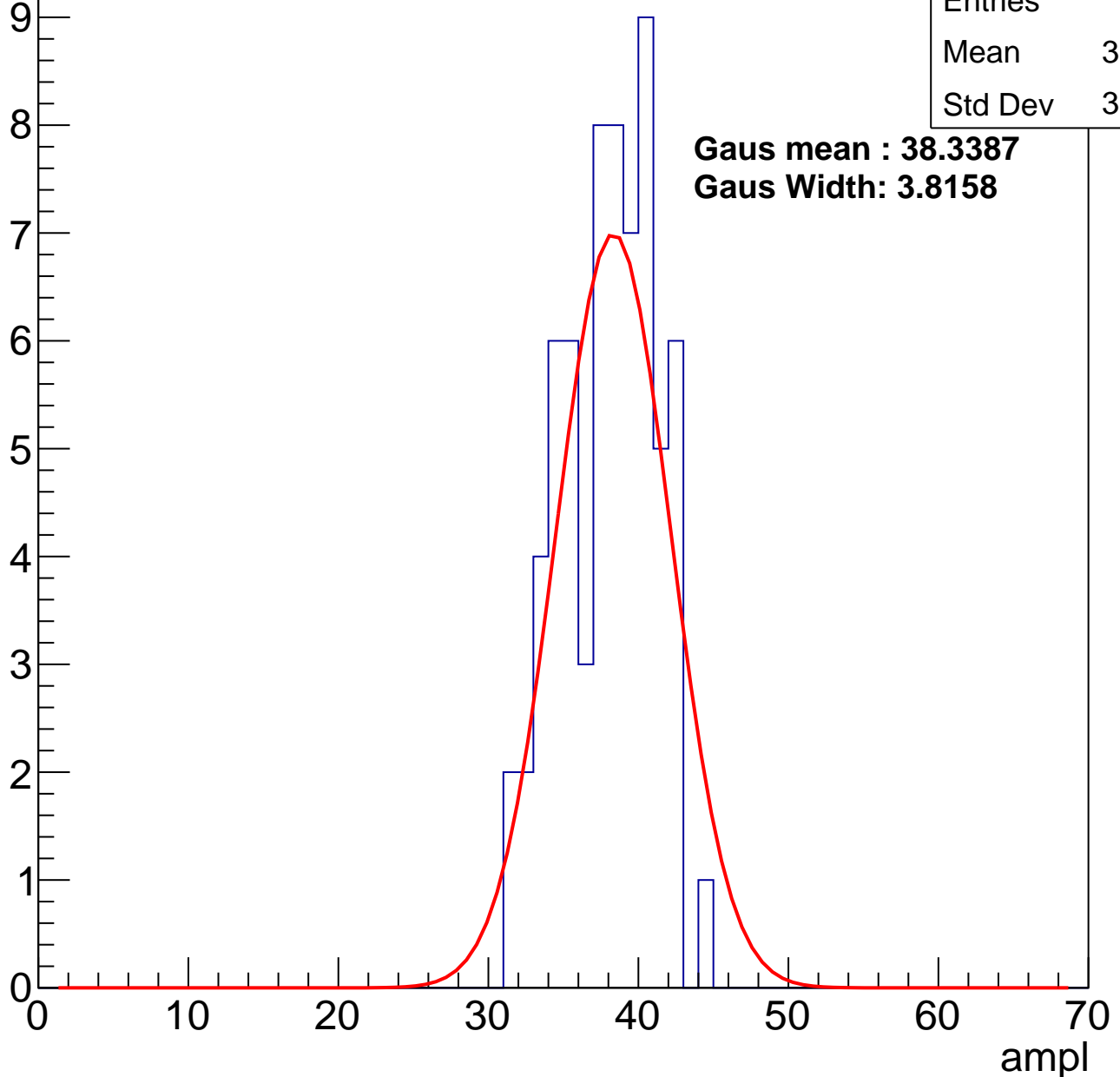
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.52
Std Dev	3.102

**Gaus mean : 38.3387**

**Gaus Width: 3.8158**



# B0L000S, U13-ch36, adc2

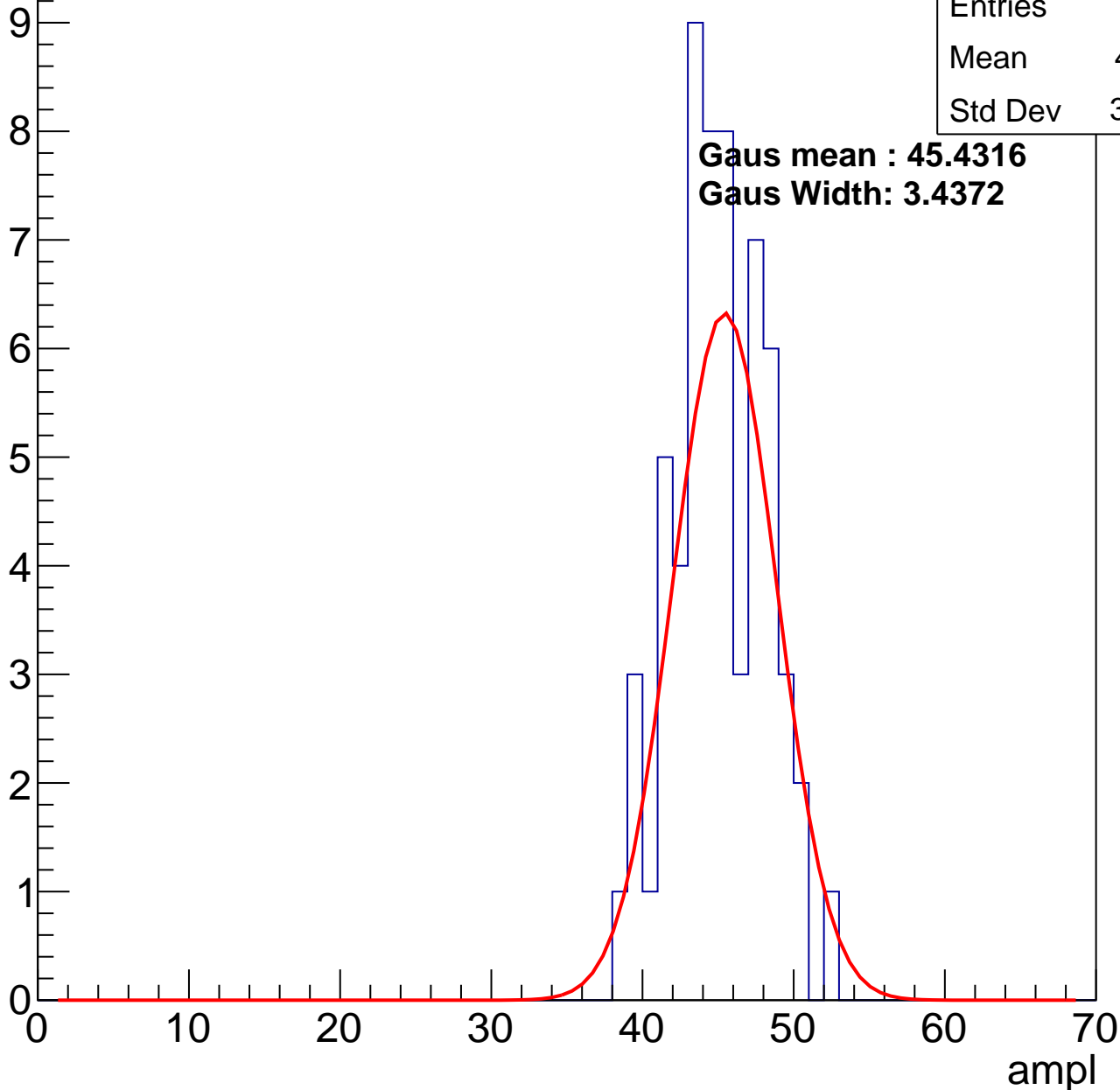
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.61
Std Dev	3.042

**Gaus mean : 45.4316**

**Gaus Width: 3.4372**

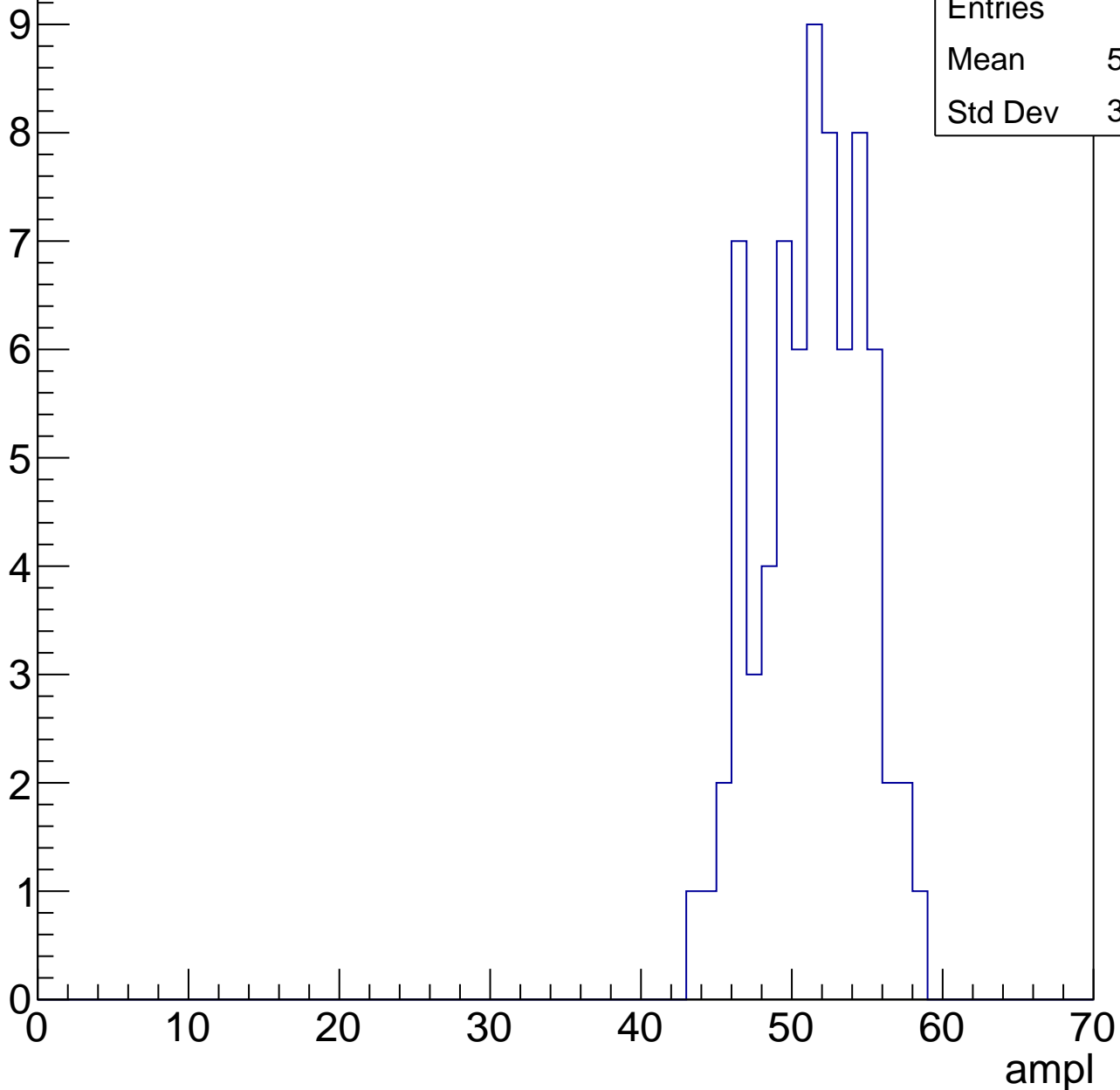


# B0L000S, U13-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

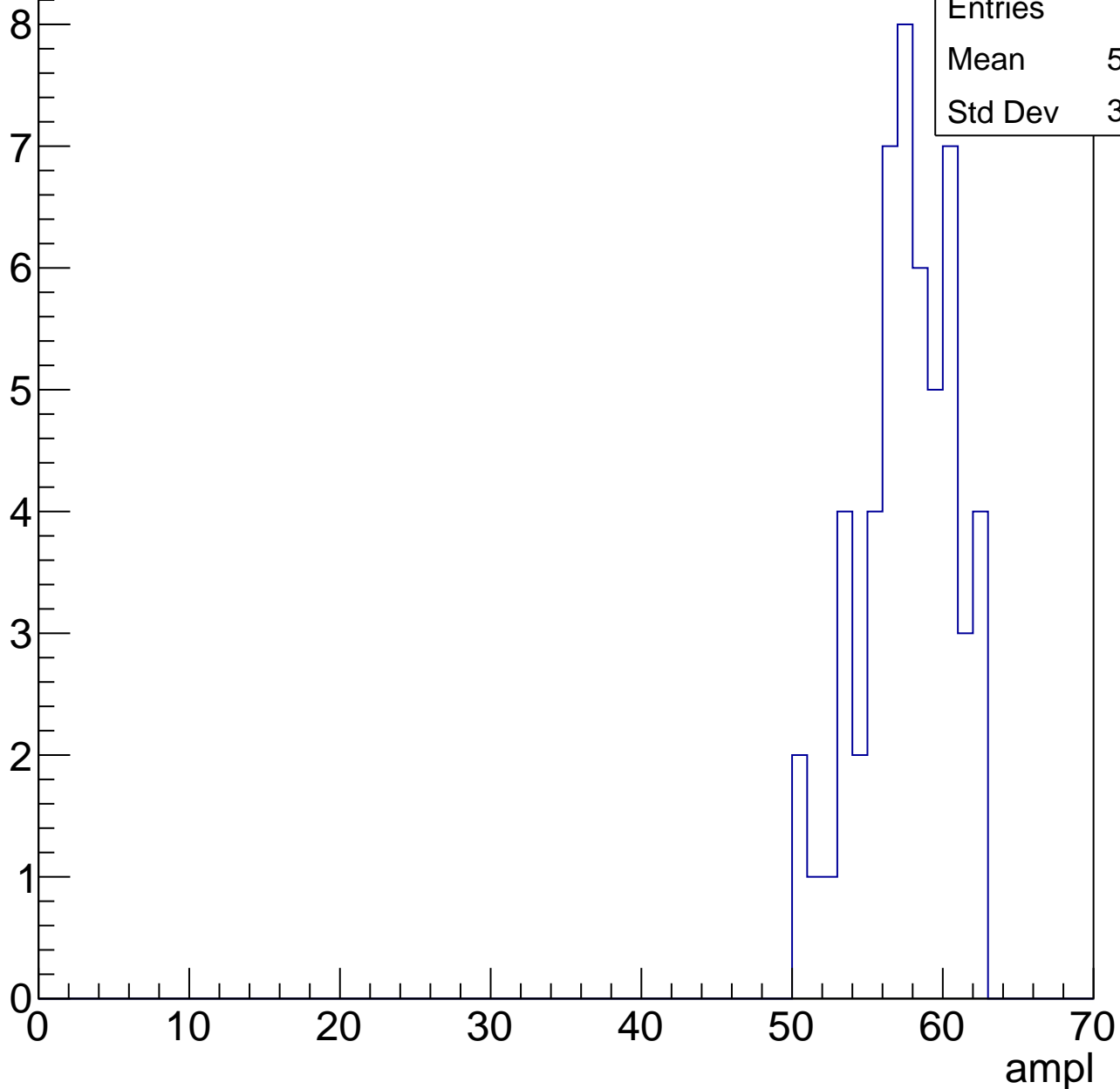
Entries	73
Mean	50.88
Std Dev	3.408



# B0L000S, U13-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



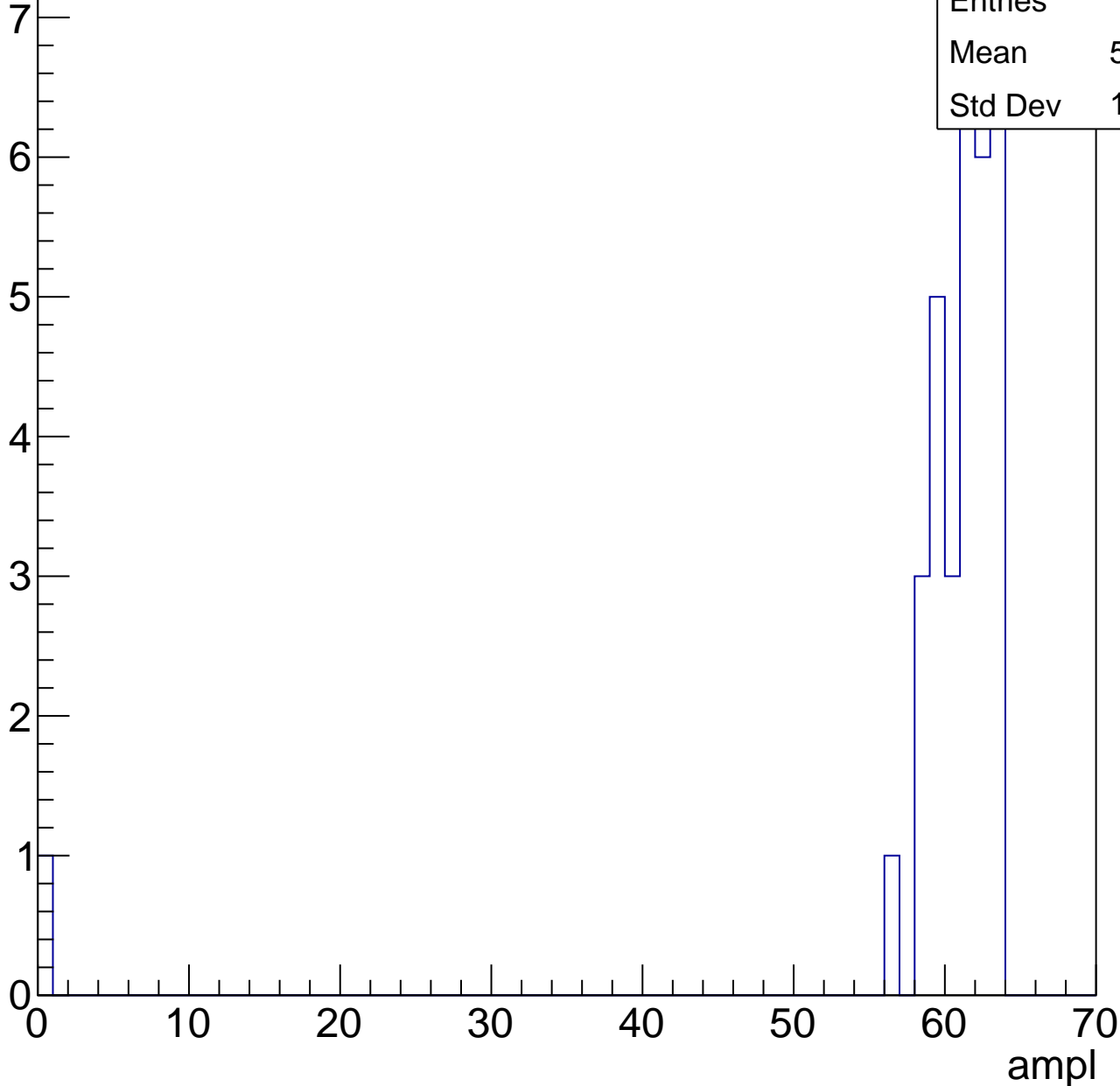
Entries	54
Mean	57.13
Std Dev	3.043

# B0L000S, U13-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	58.94
Std Dev	10.57



# B0L000S, U13-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch37, adc0

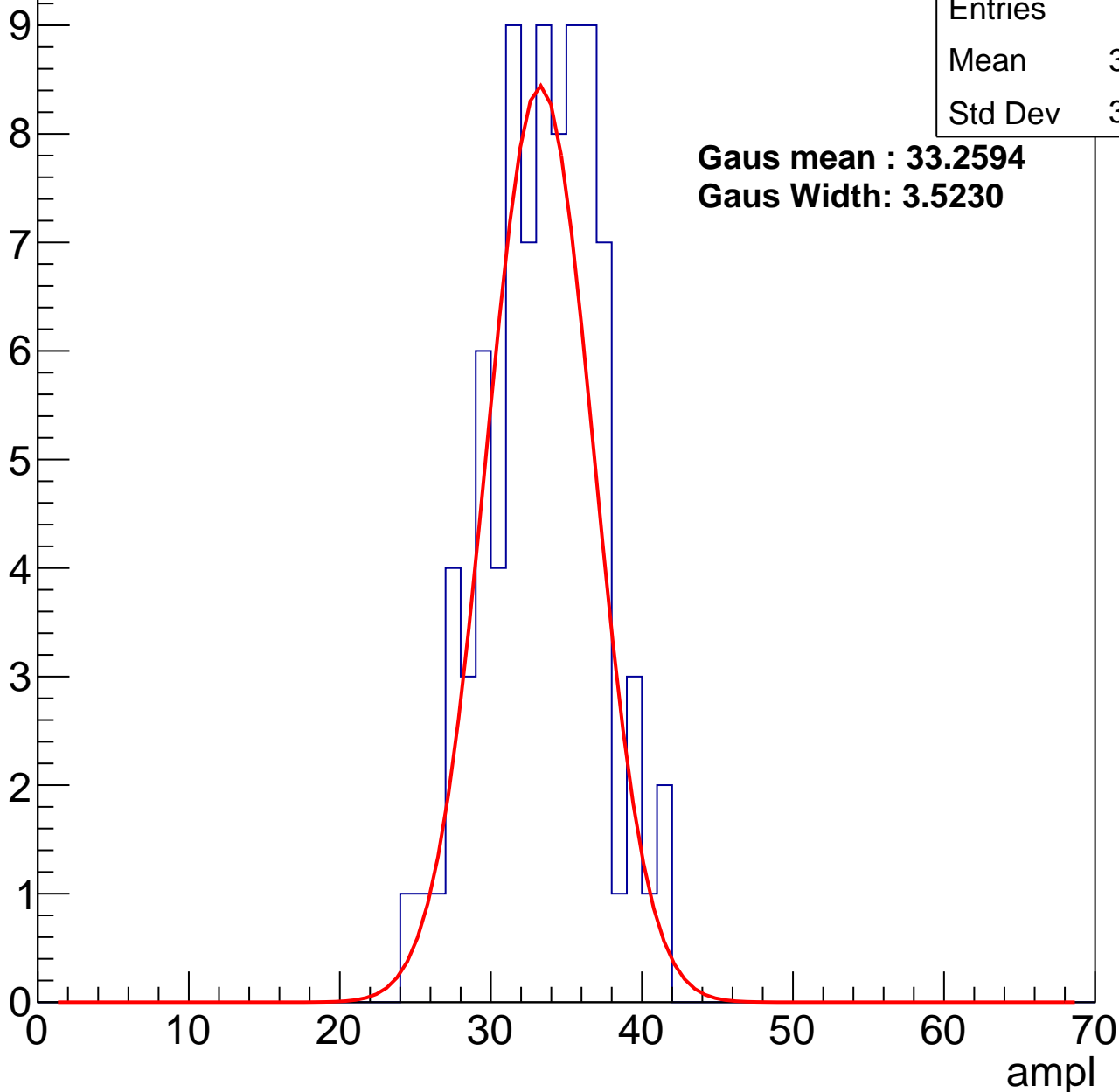
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	33.04
Std Dev	3.654

**Gaus mean : 33.2594**

**Gaus Width: 3.5230**



# B0L000S, U13-ch37, adc1

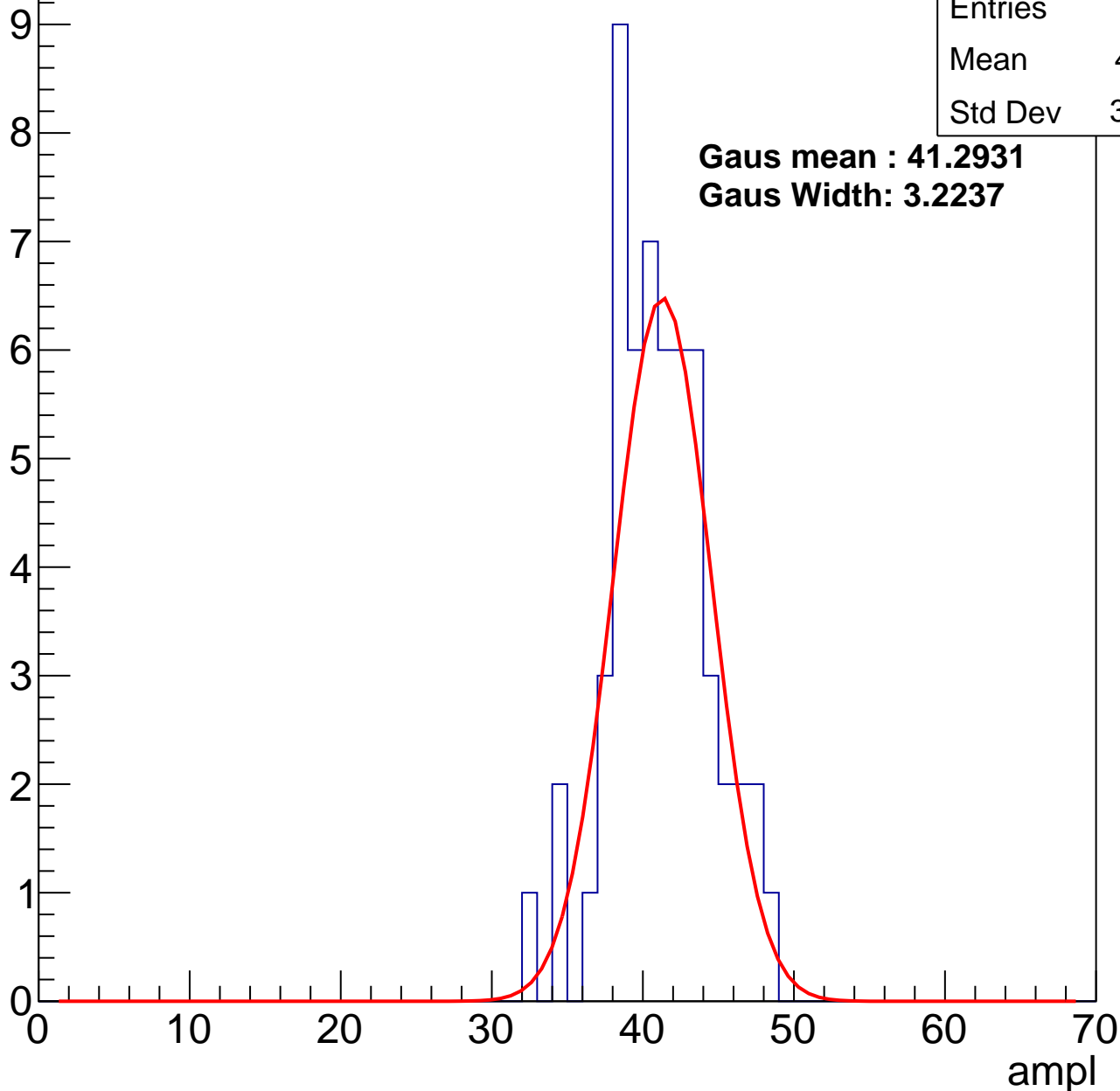
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	40.61
Std Dev	3.286

**Gaus mean : 41.2931**

**Gaus Width: 3.2237**



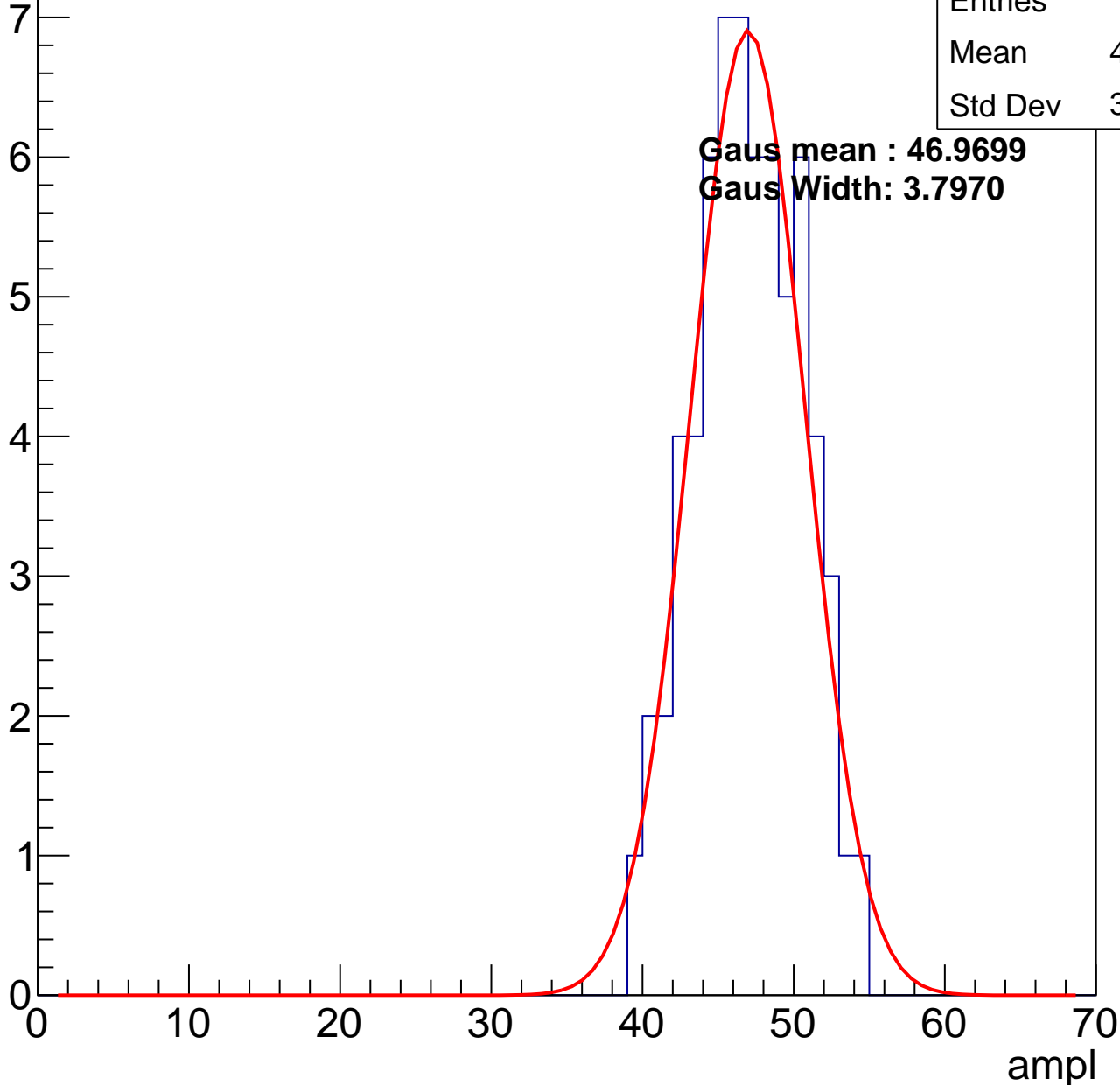
# B0L000S, U13-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	46.52
Std Dev	3.456

**Gaus mean : 46.9699**  
**Gaus Width: 3.7970**

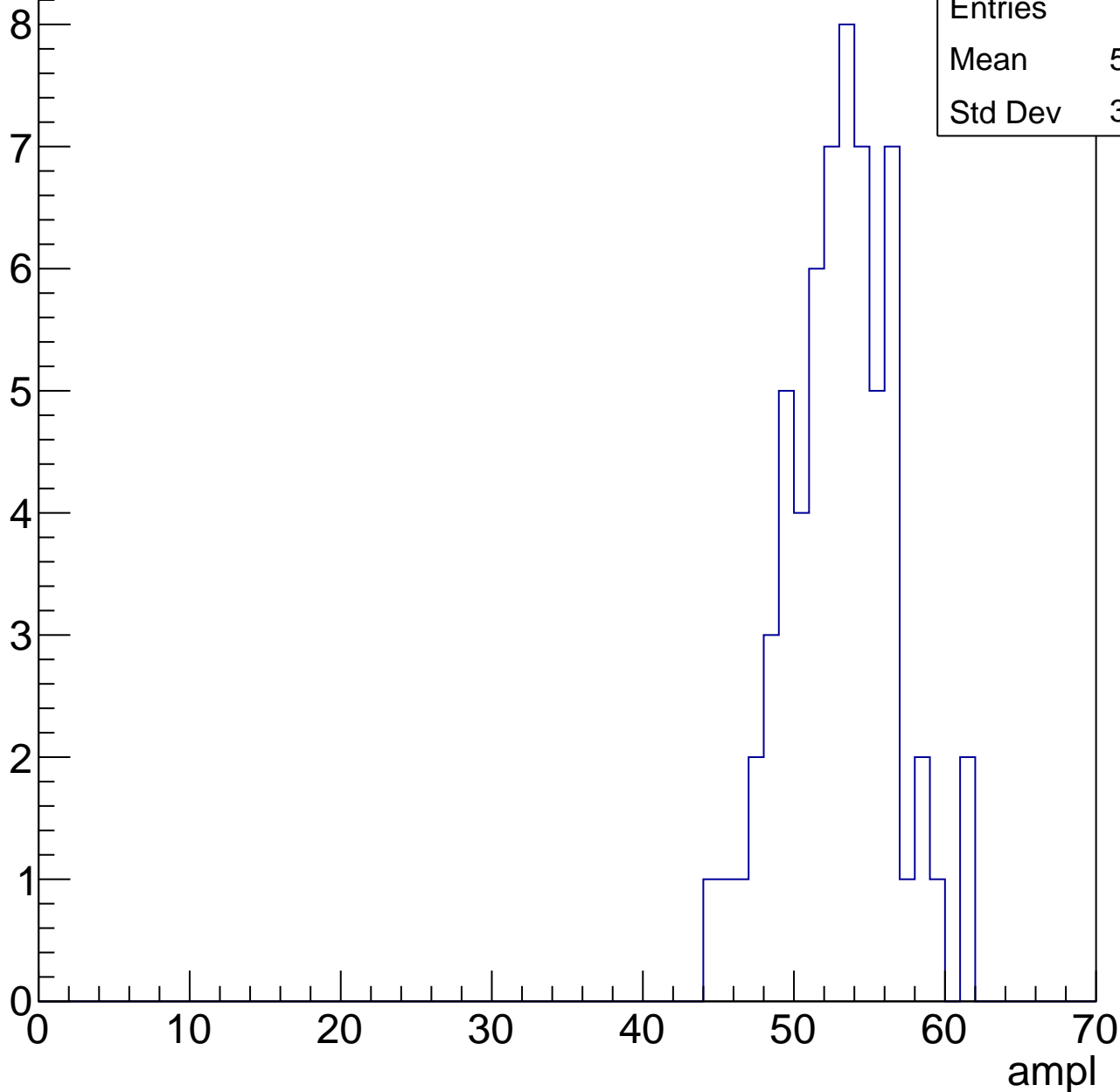


# B0L000S, U13-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

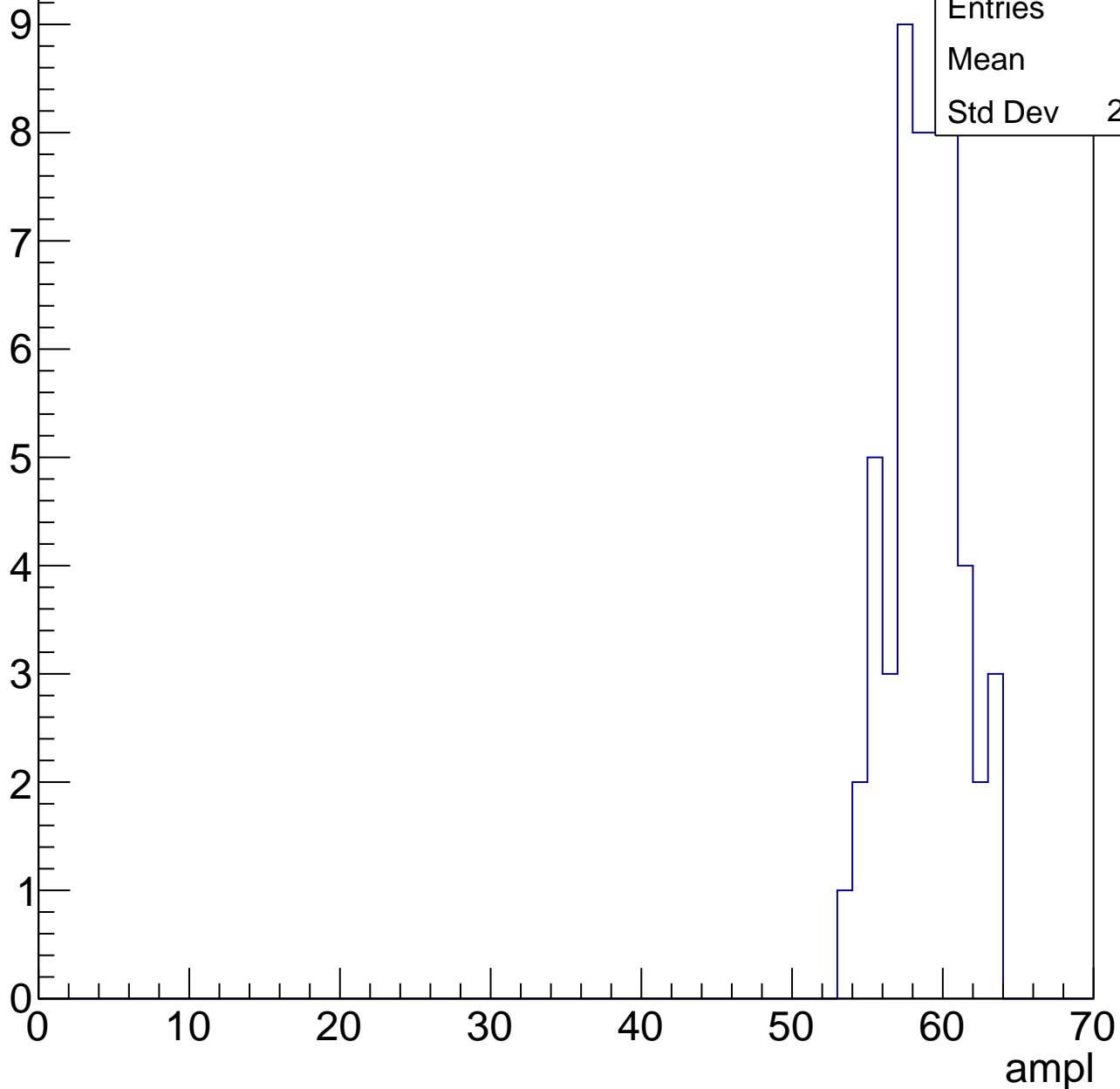
Entries	63
Mean	52.56
Std Dev	3.554



# B0L000S, U13-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



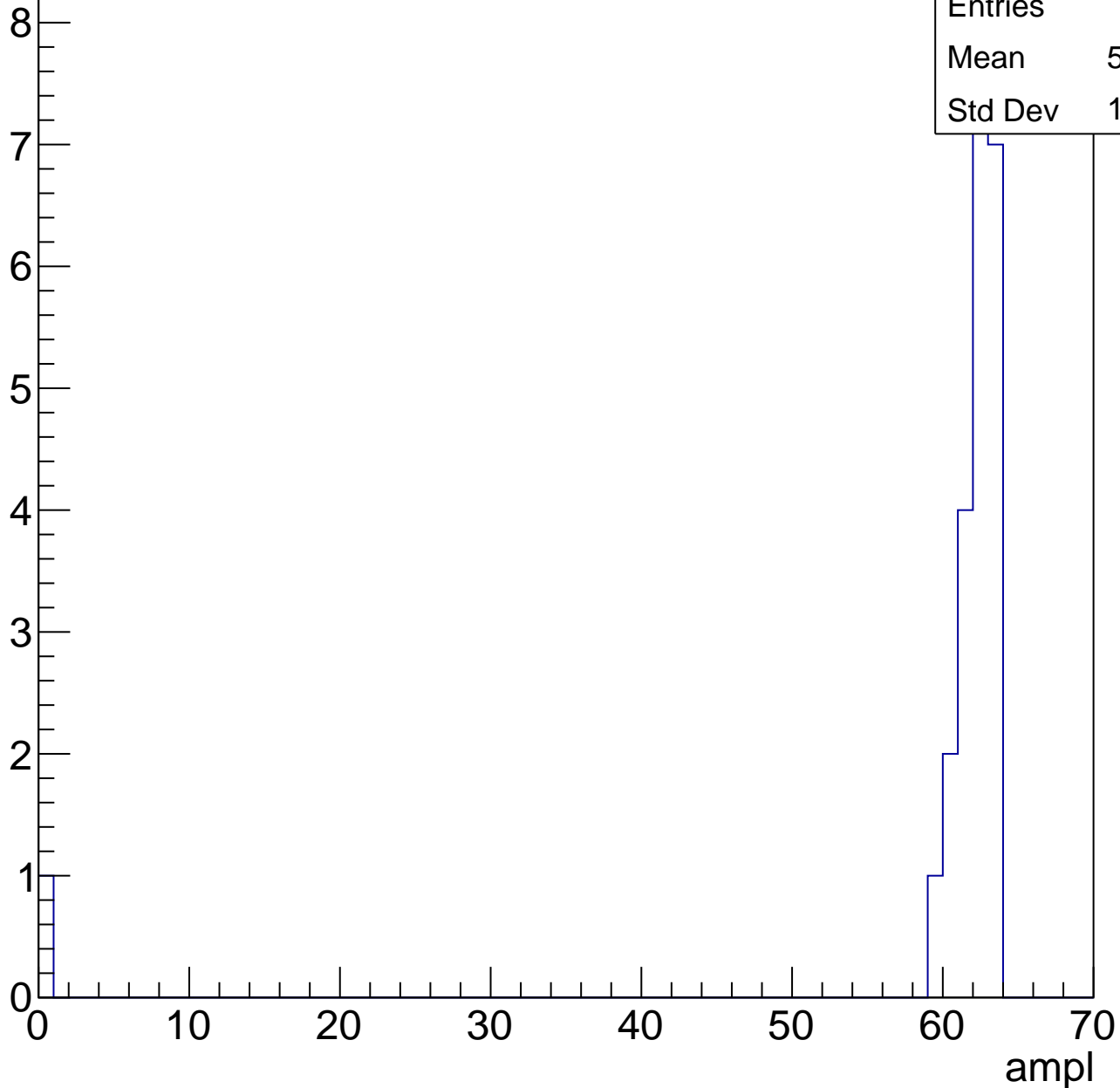
Entries	53
Mean	58.3
Std Dev	2.384

# B0L000S, U13-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	59.13
Std Dev	12.65



# B0L000S, U13-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch38, adc0

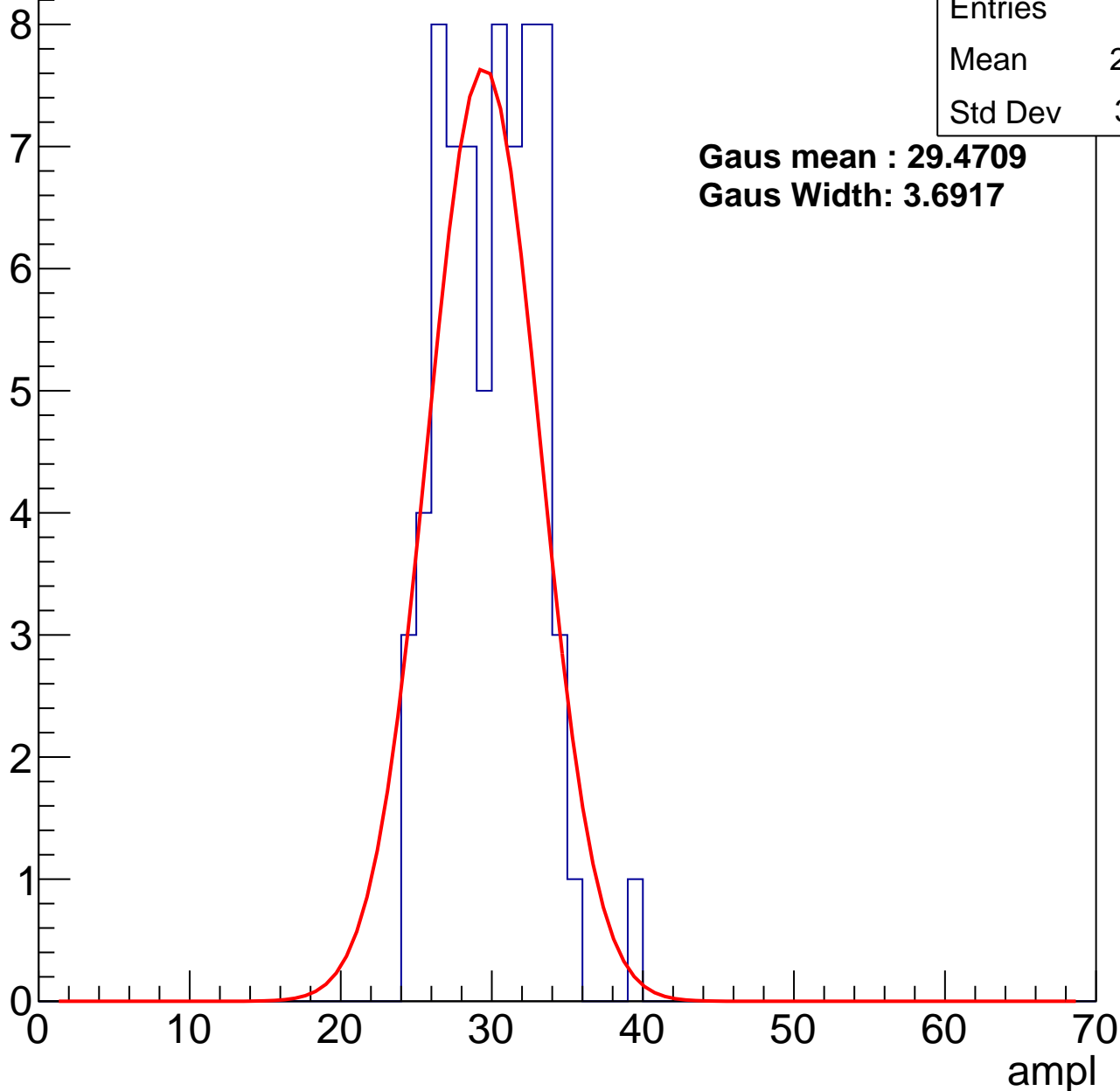
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.47
Std Dev	3.111

**Gaus mean : 29.4709**

**Gaus Width: 3.6917**



# B0L000S, U13-ch38, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	36.67
Std Dev	3.47

**Gaus mean : 37.5409**

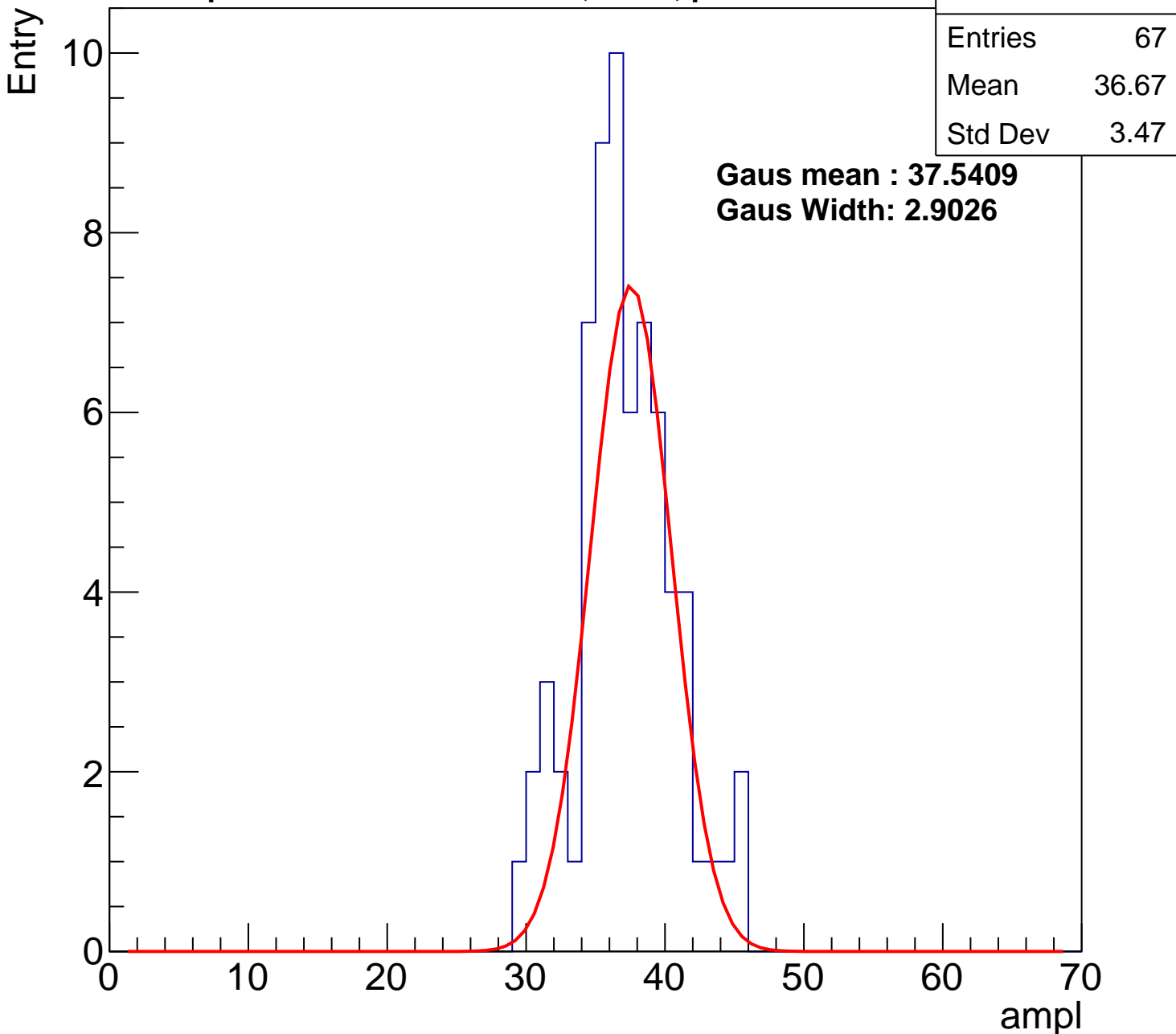
**Gaus Width: 2.9026**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch38, adc2

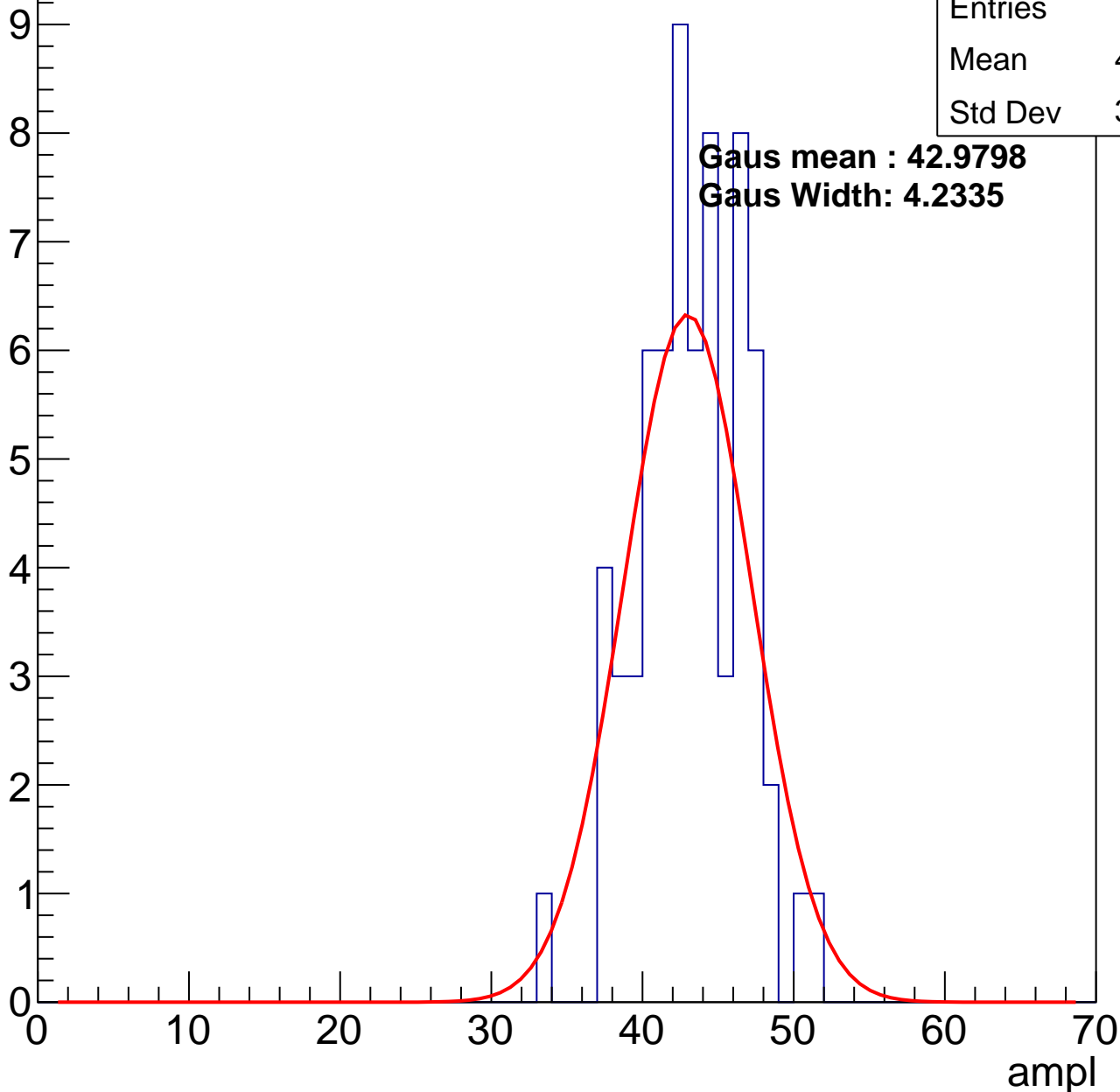
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.81
Std Dev	3.461

**Gaus mean : 42.9798**

**Gaus Width: 4.2335**



# B0L000S, U13-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

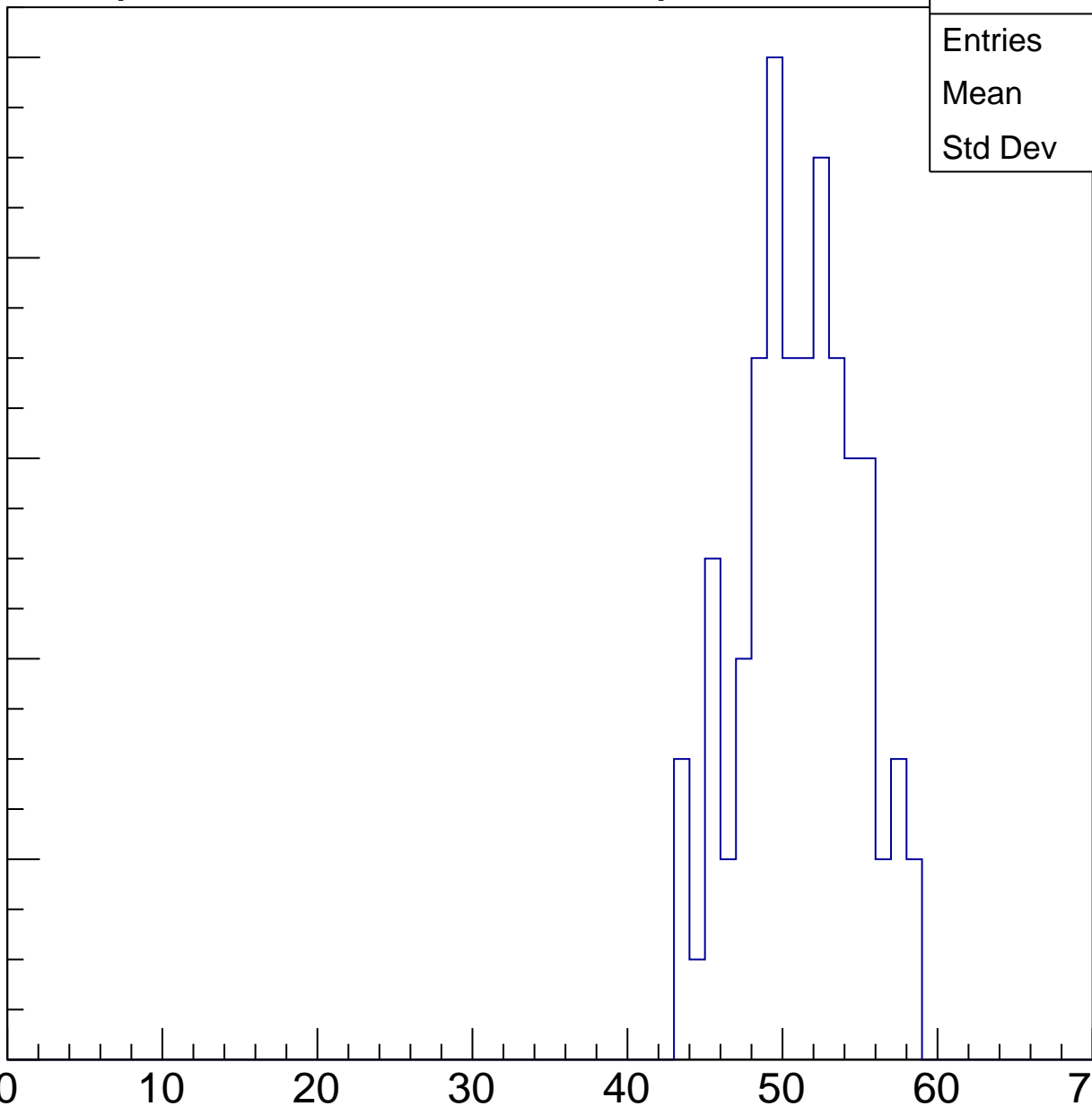
Entries	81
Mean	50.65
Std Dev	3.649

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

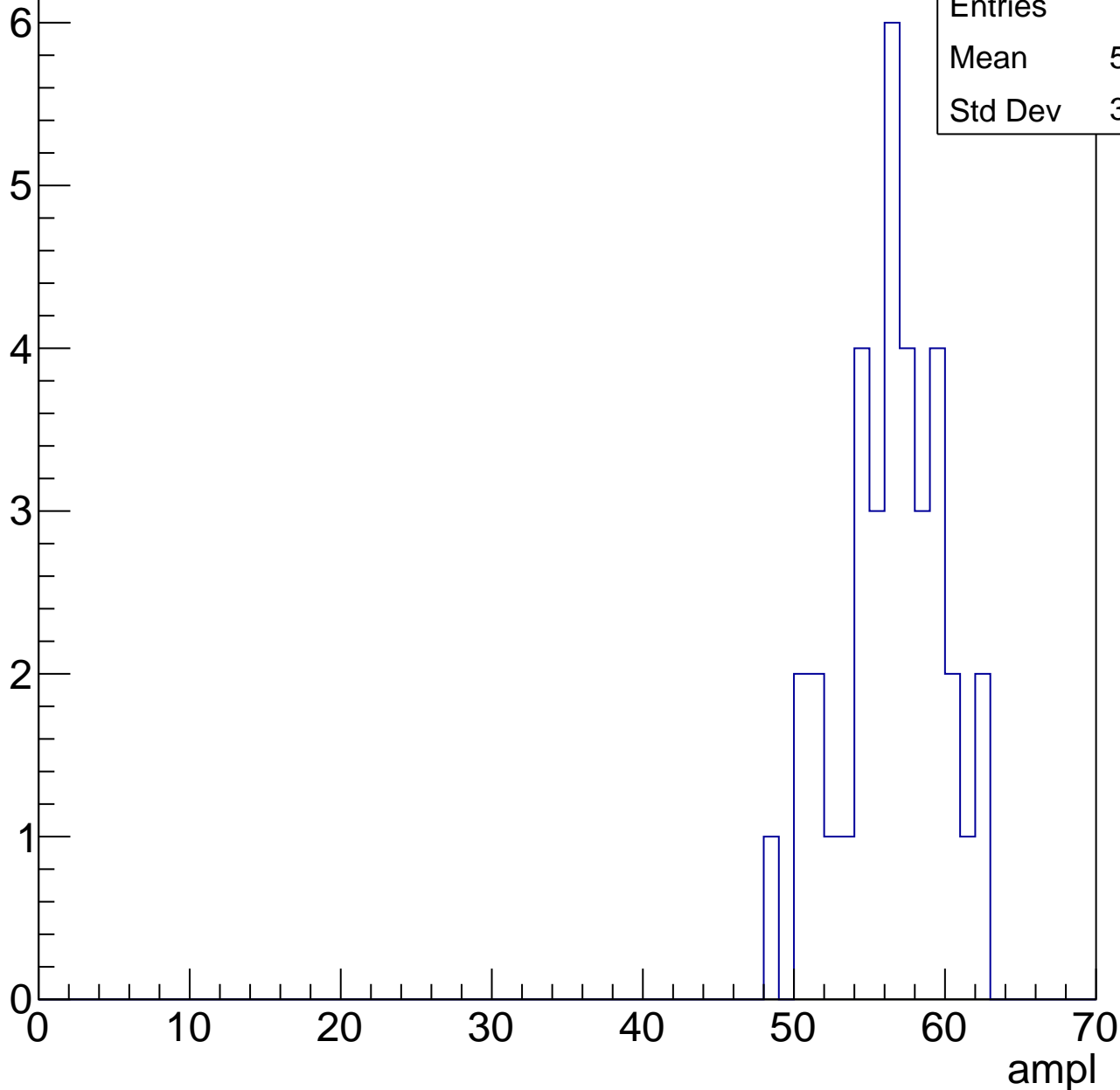


# B0L000S, U13-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

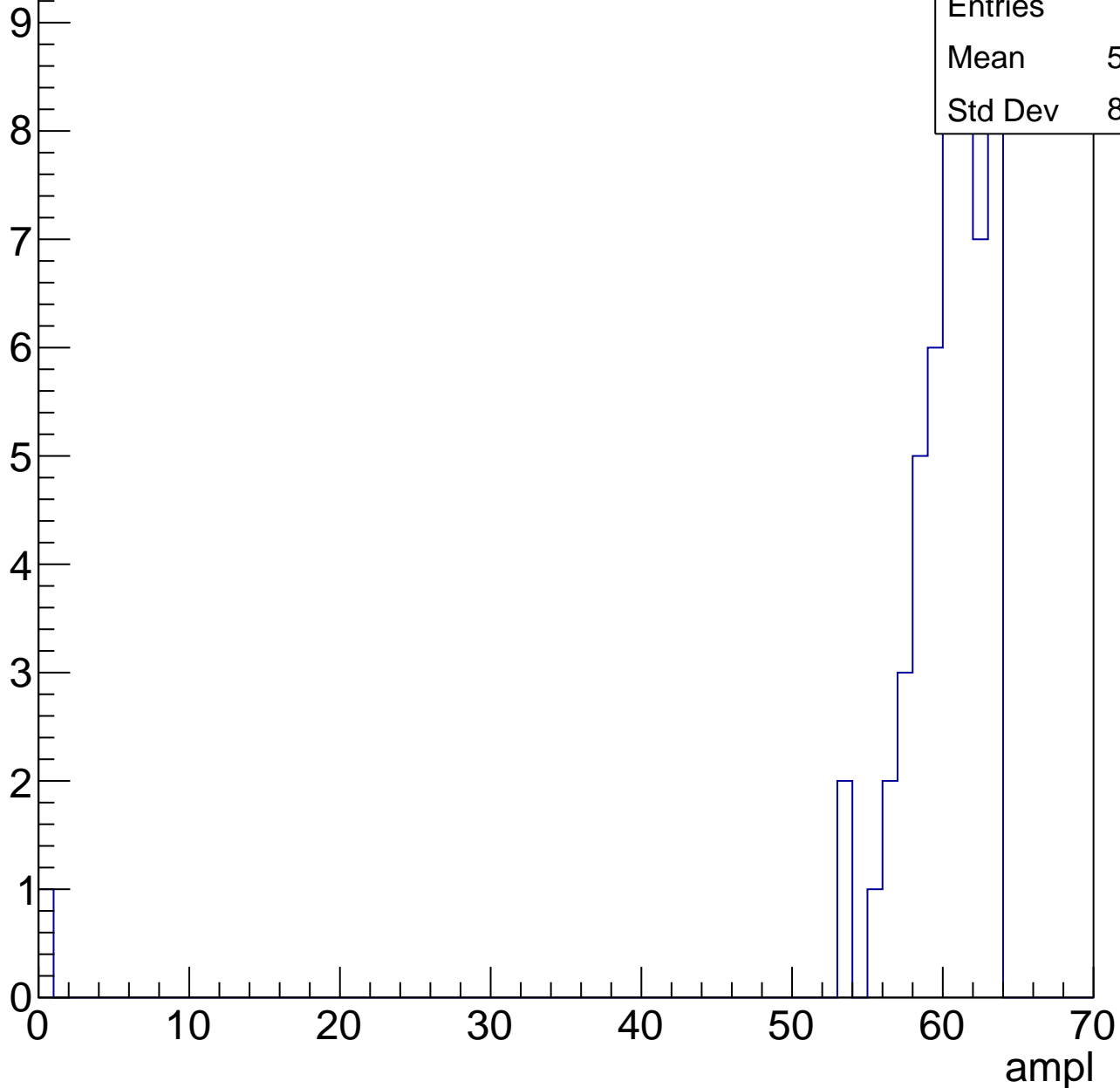
Entries	36
Mean	55.97
Std Dev	3.379



# B0L000S, U13-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U13-ch39, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.27
Std Dev	3.135

**Gaus mean : 30.0273**

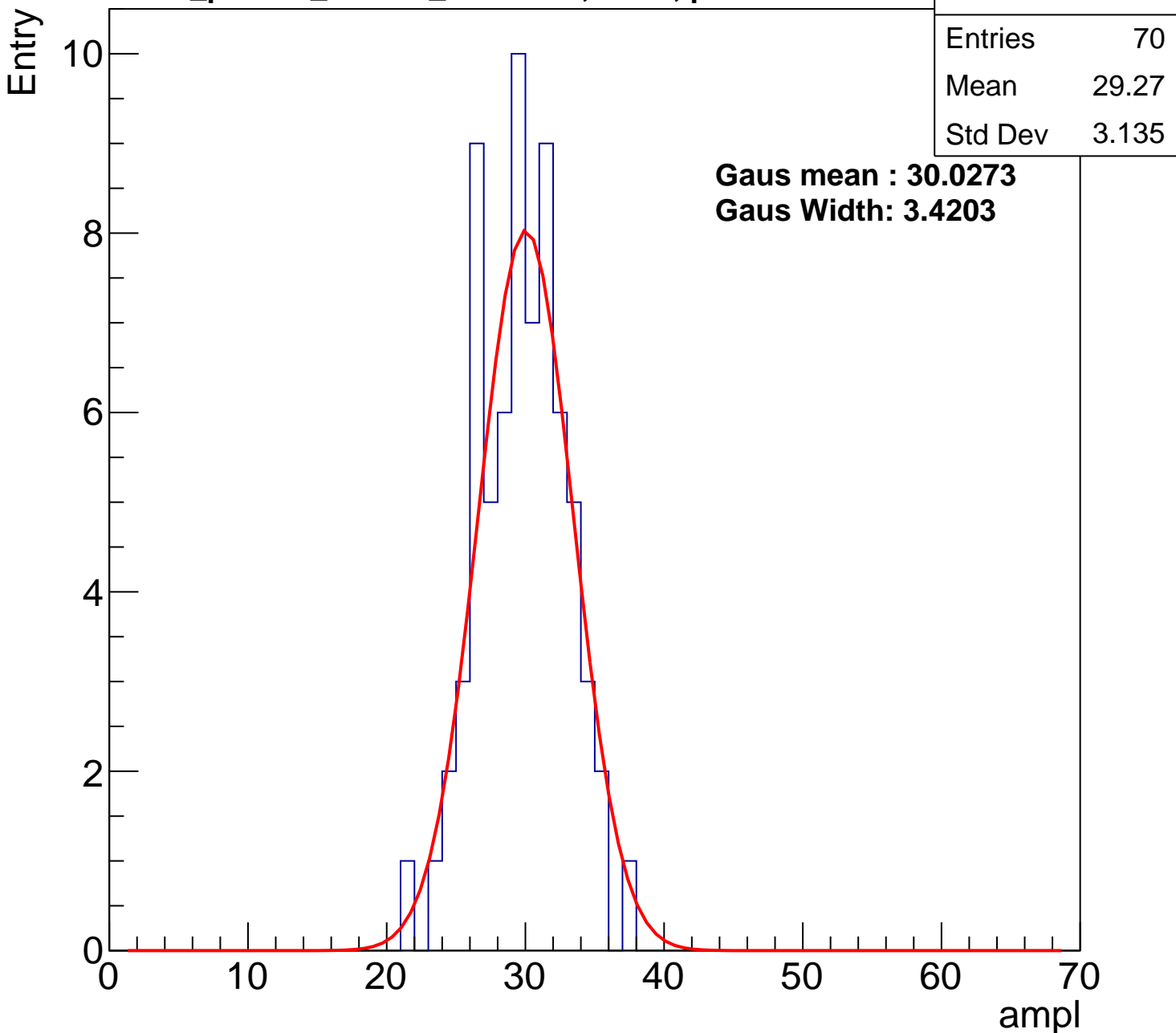
**Gaus Width: 3.4203**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch39, adc1

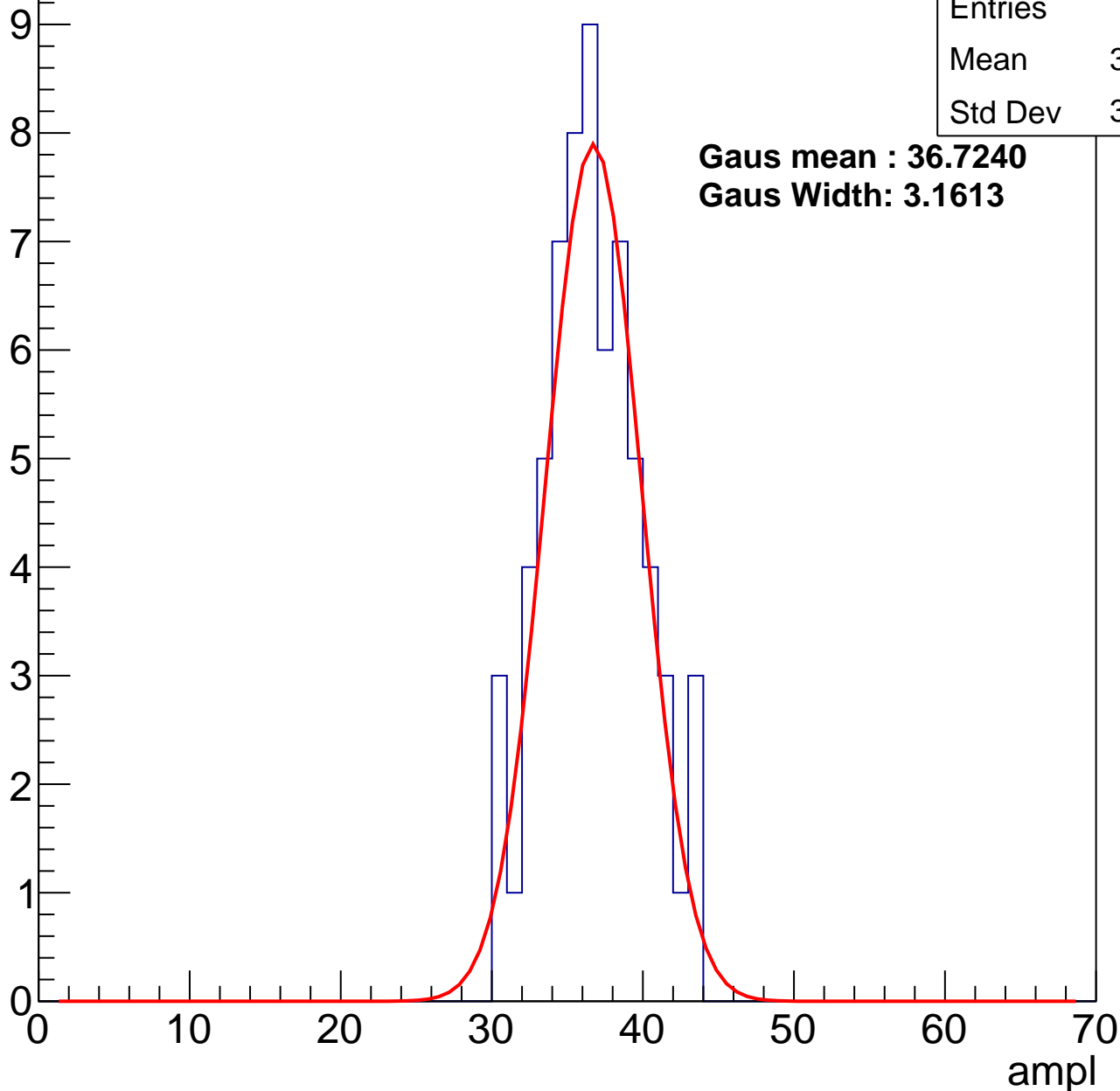
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.26
Std Dev	3.197

**Gaus mean : 36.7240**

**Gaus Width: 3.1613**



# B0L000S, U13-ch39, adc2

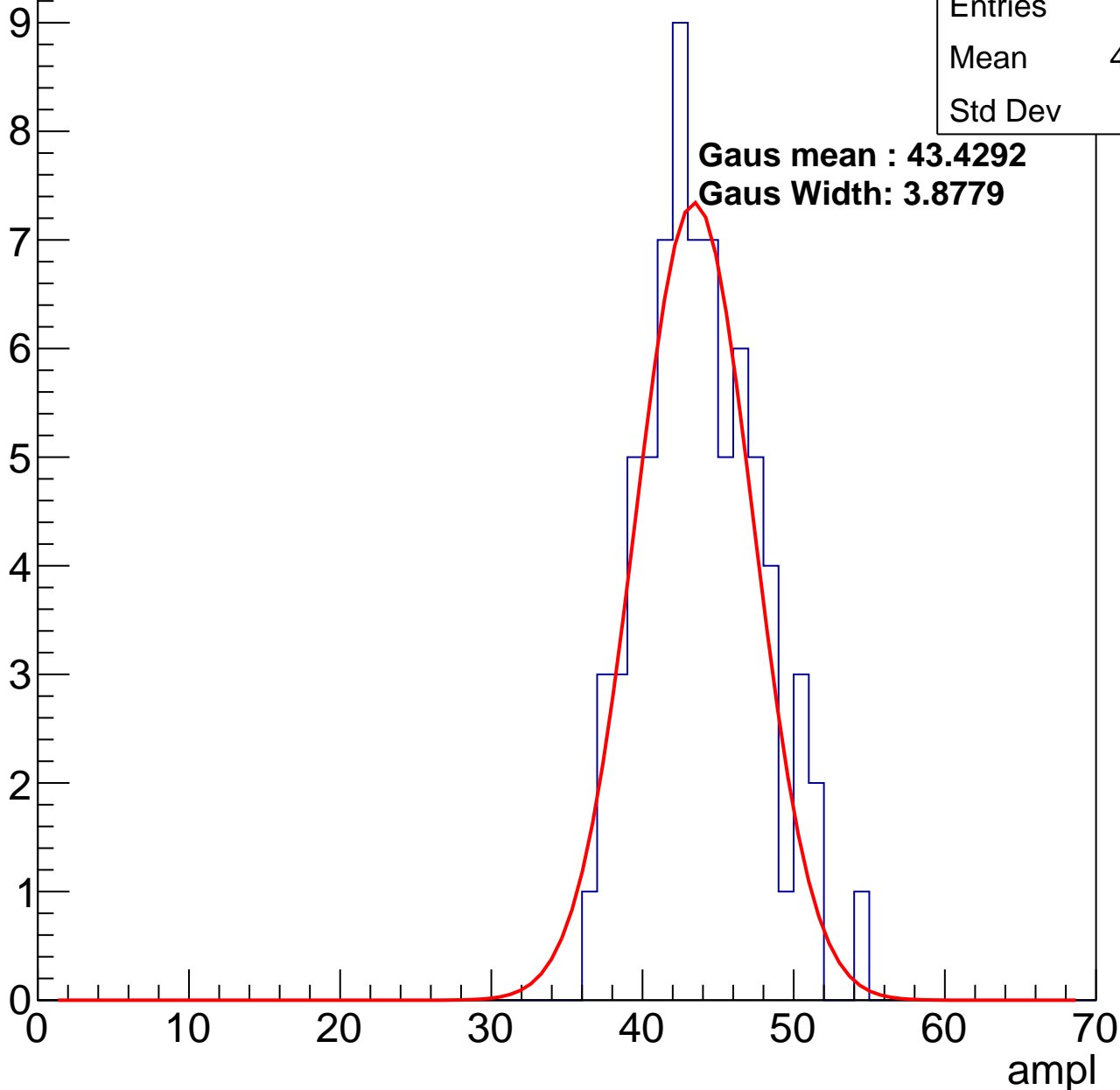
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.42
Std Dev	3.81

**Gaus mean : 43.4292**

**Gaus Width: 3.8779**

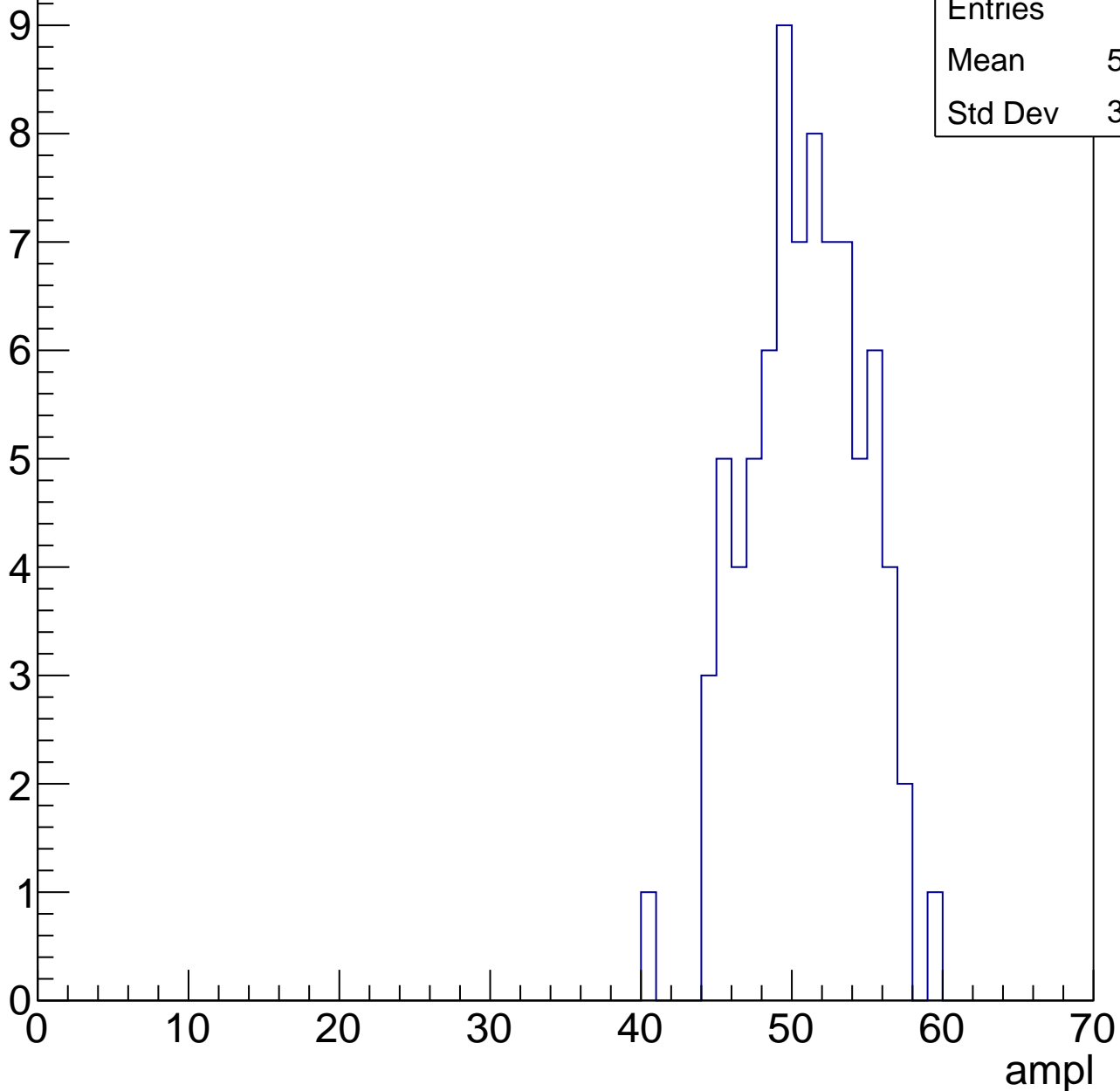


# B0L000S, U13-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

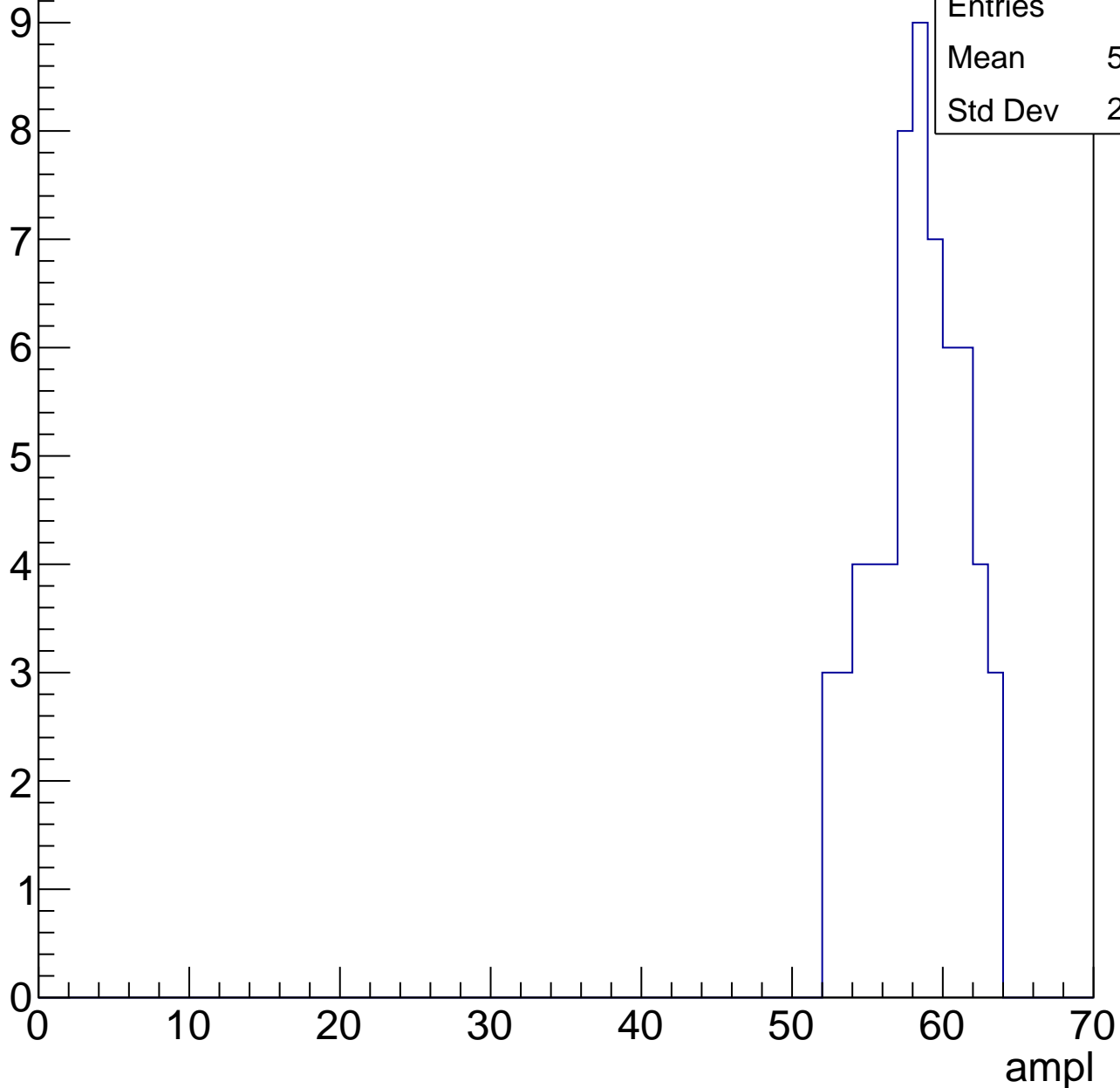
Entries	80
Mean	50.44
Std Dev	3.728



# B0L000S, U13-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



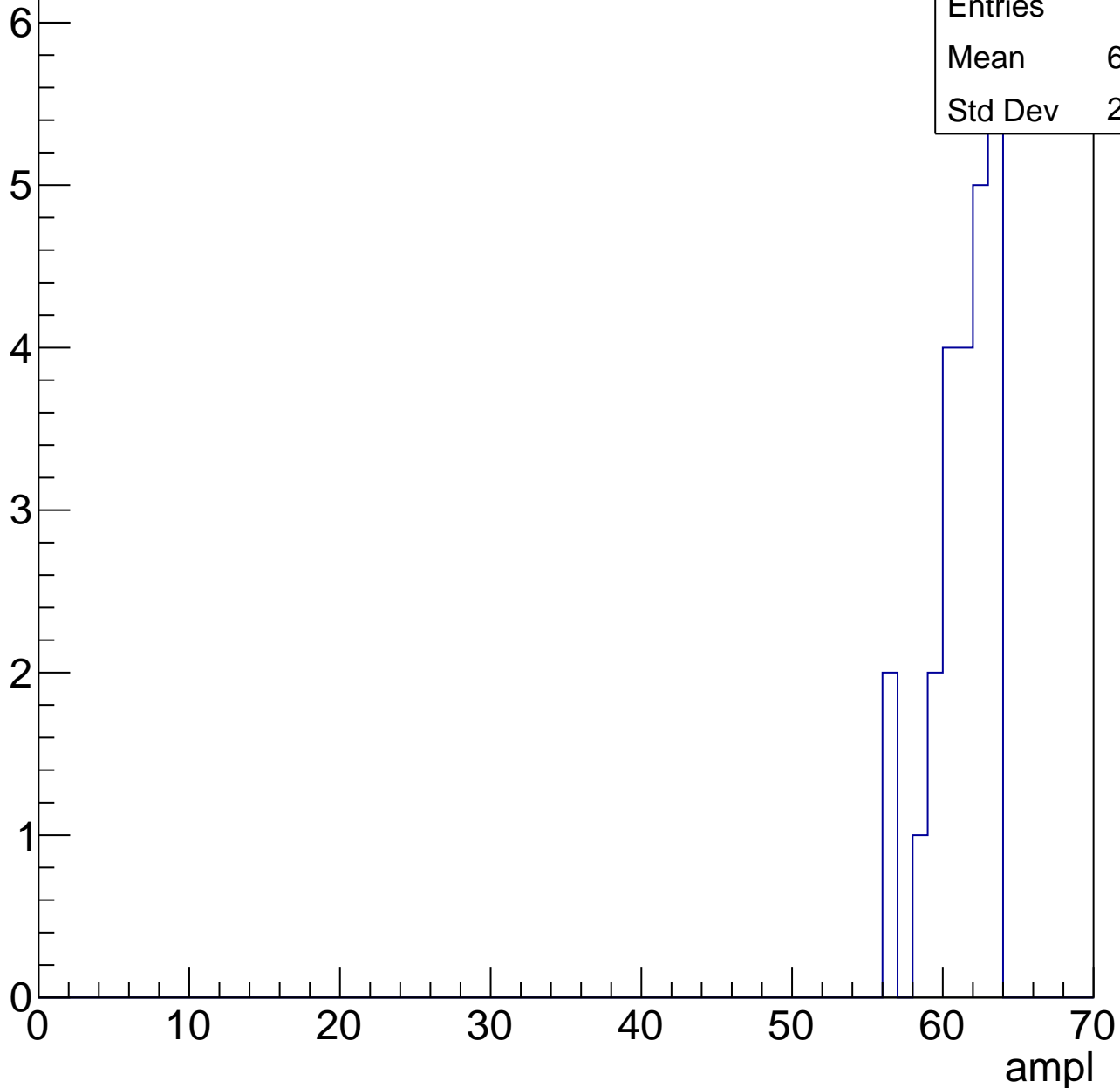
Entries	61
Mean	57.85
Std Dev	2.947

# B0L000S, U13-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

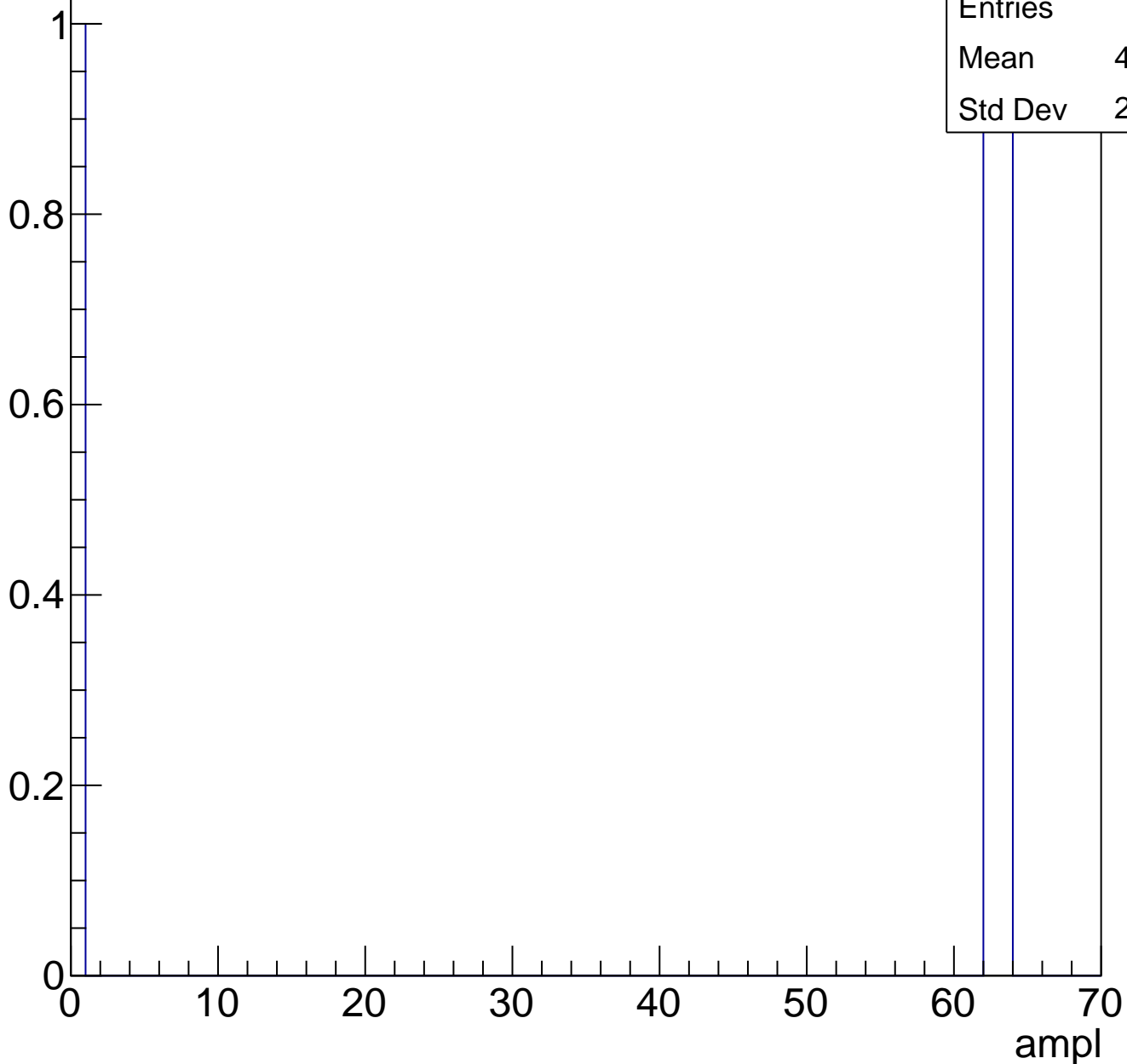
Entries	24
Mean	60.83
Std Dev	2.034



# B0L000S, U13-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch40, adc0

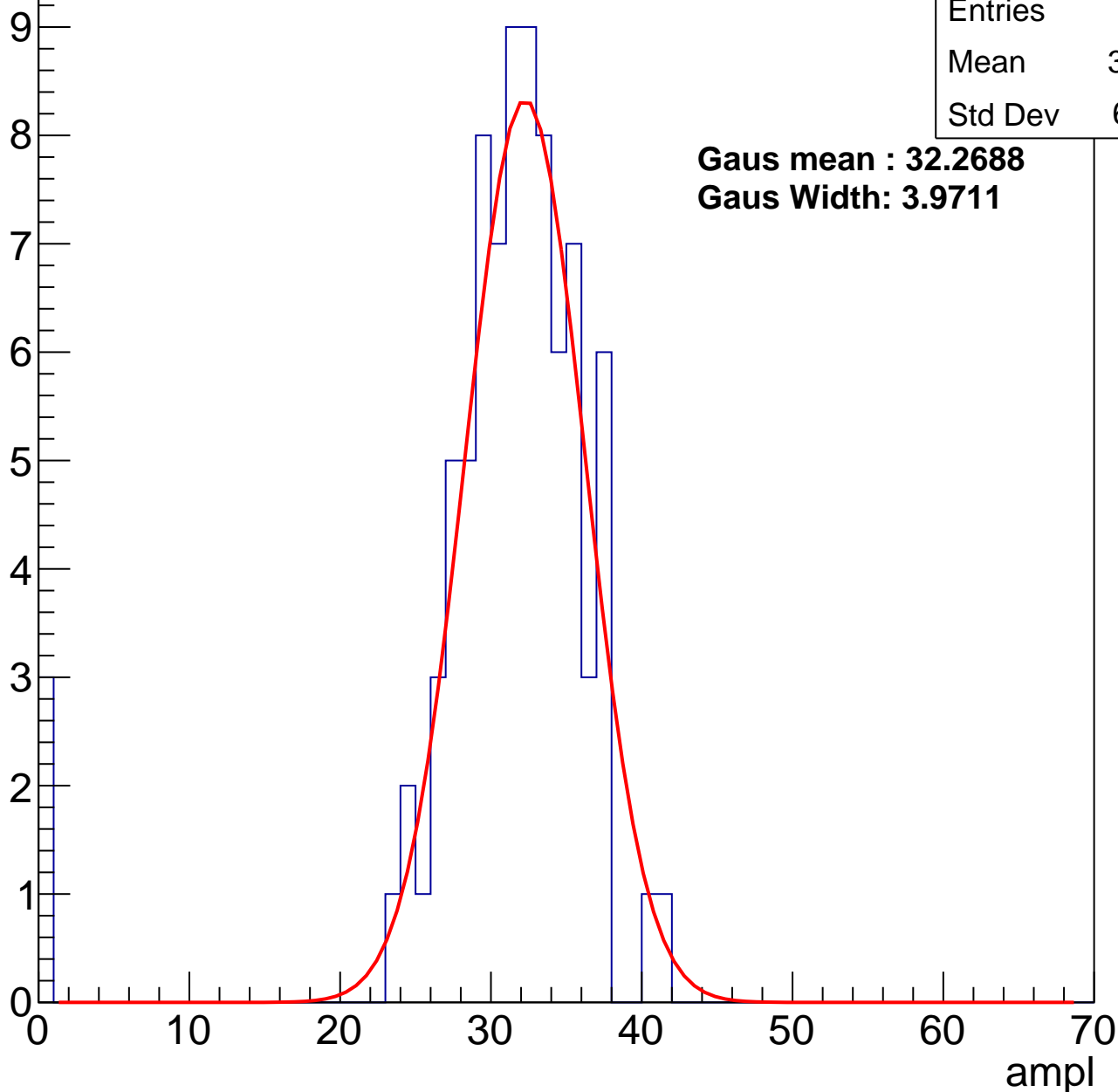
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	30.38
Std Dev	6.831

**Gaus mean : 32.2688**

**Gaus Width: 3.9711**



# B0L000S, U13-ch40, adc1

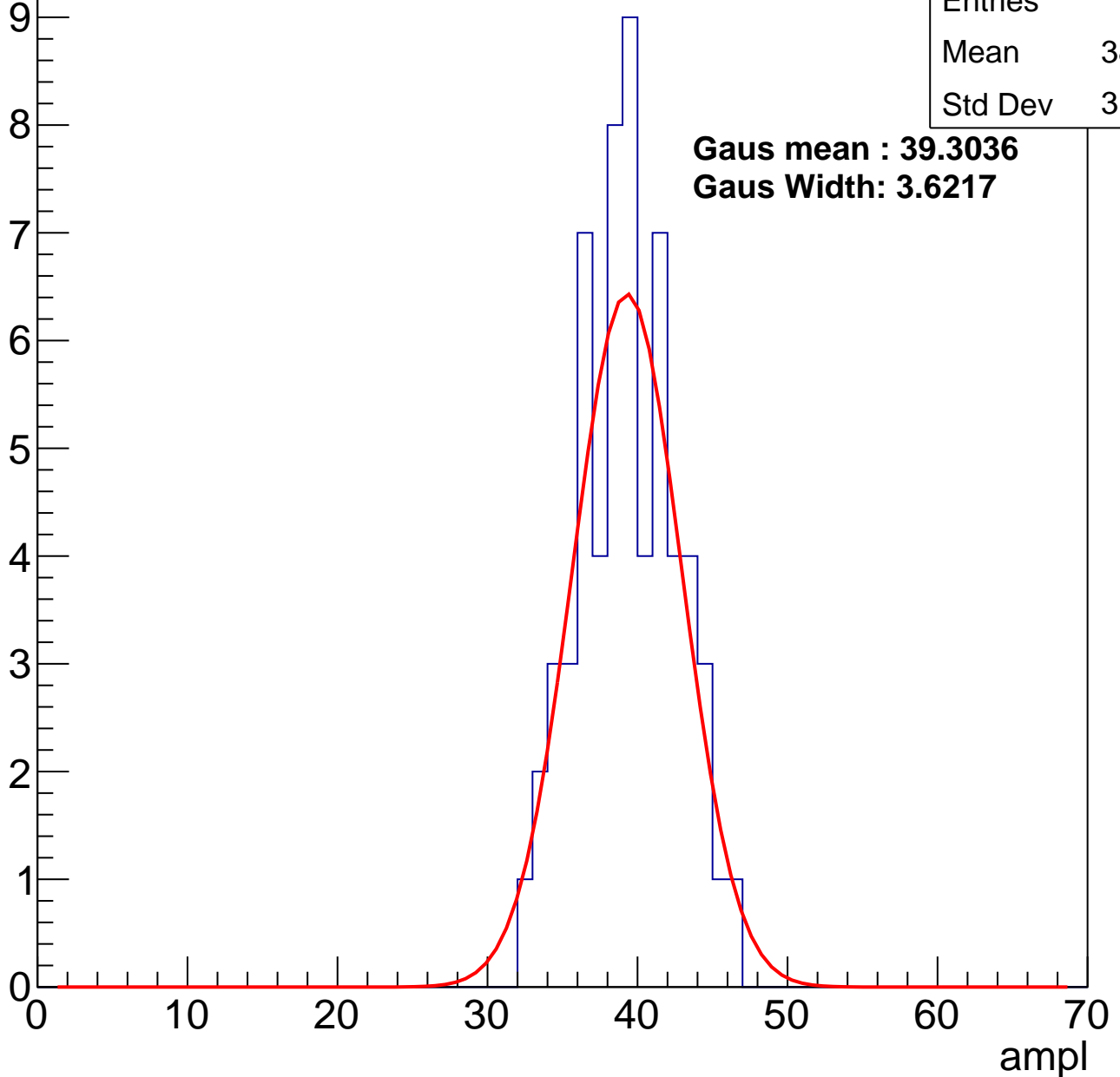
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	38.85
Std Dev	3.192

**Gaus mean : 39.3036**

**Gaus Width: 3.6217**



# B0L000S, U13-ch40, adc2

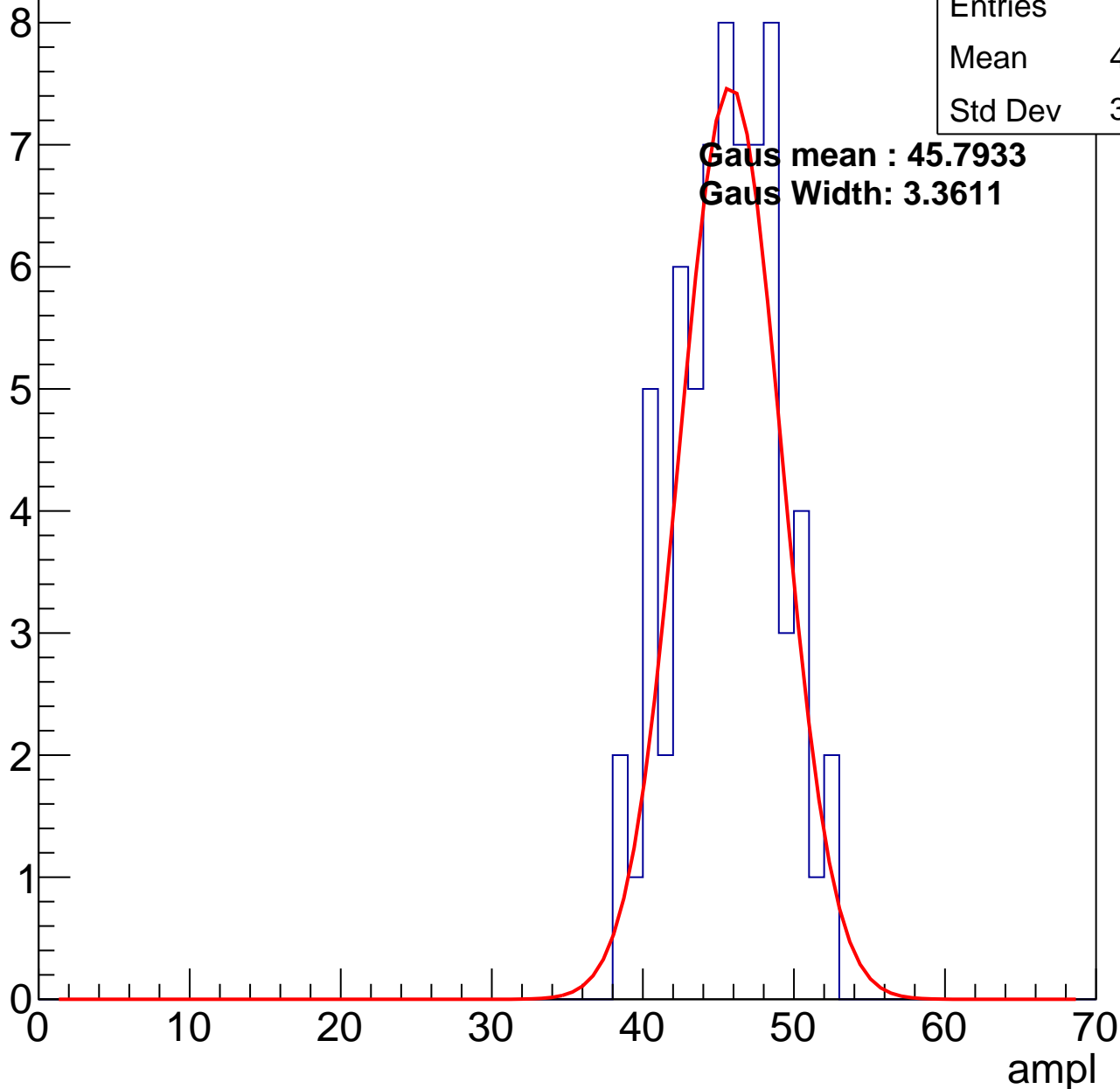
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	45.13
Std Dev	3.343

**Gaus mean : 45.7933**

**Gaus Width: 3.3611**

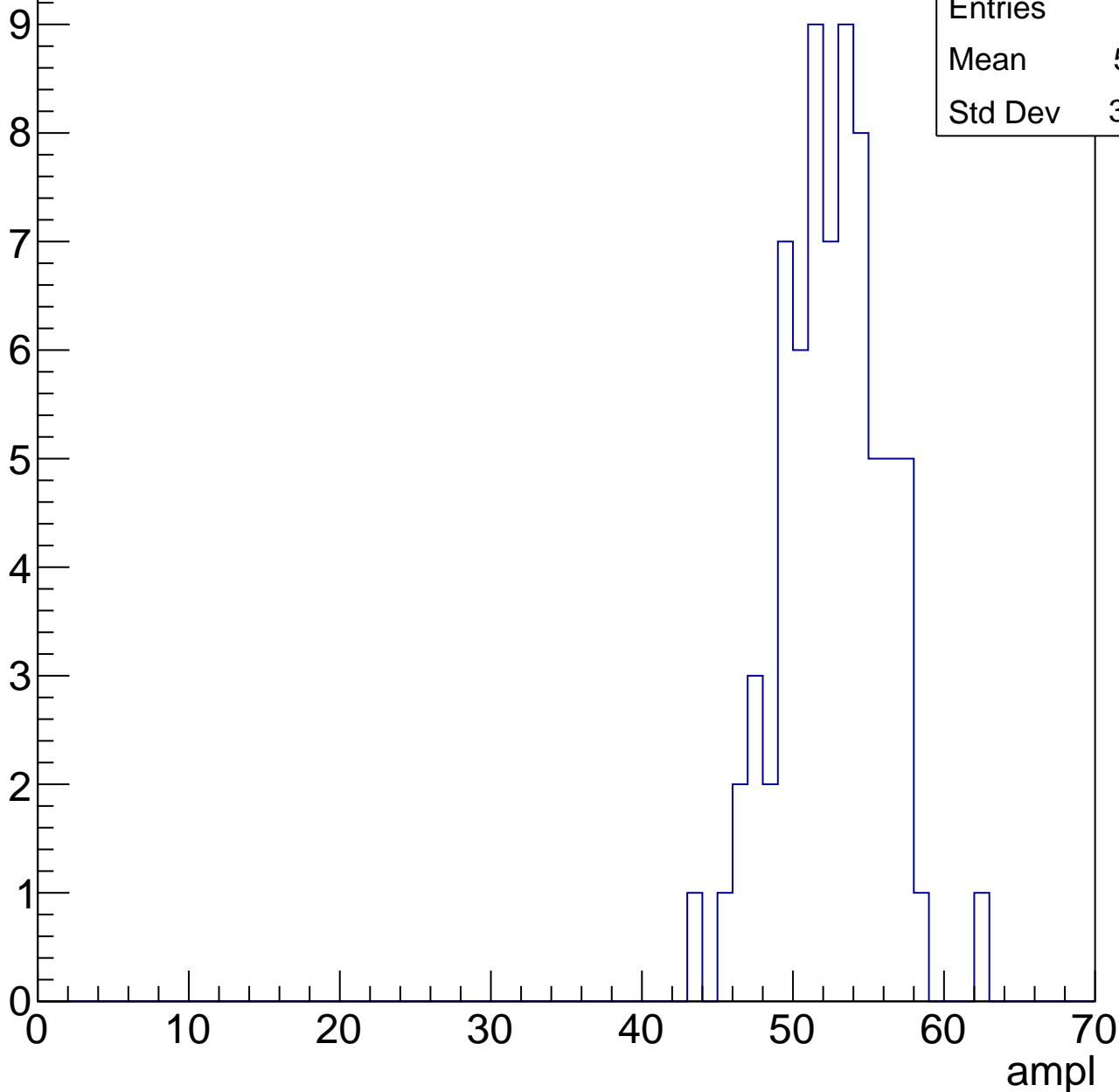


# B0L000S, U13-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	52.11
Std Dev	3.398

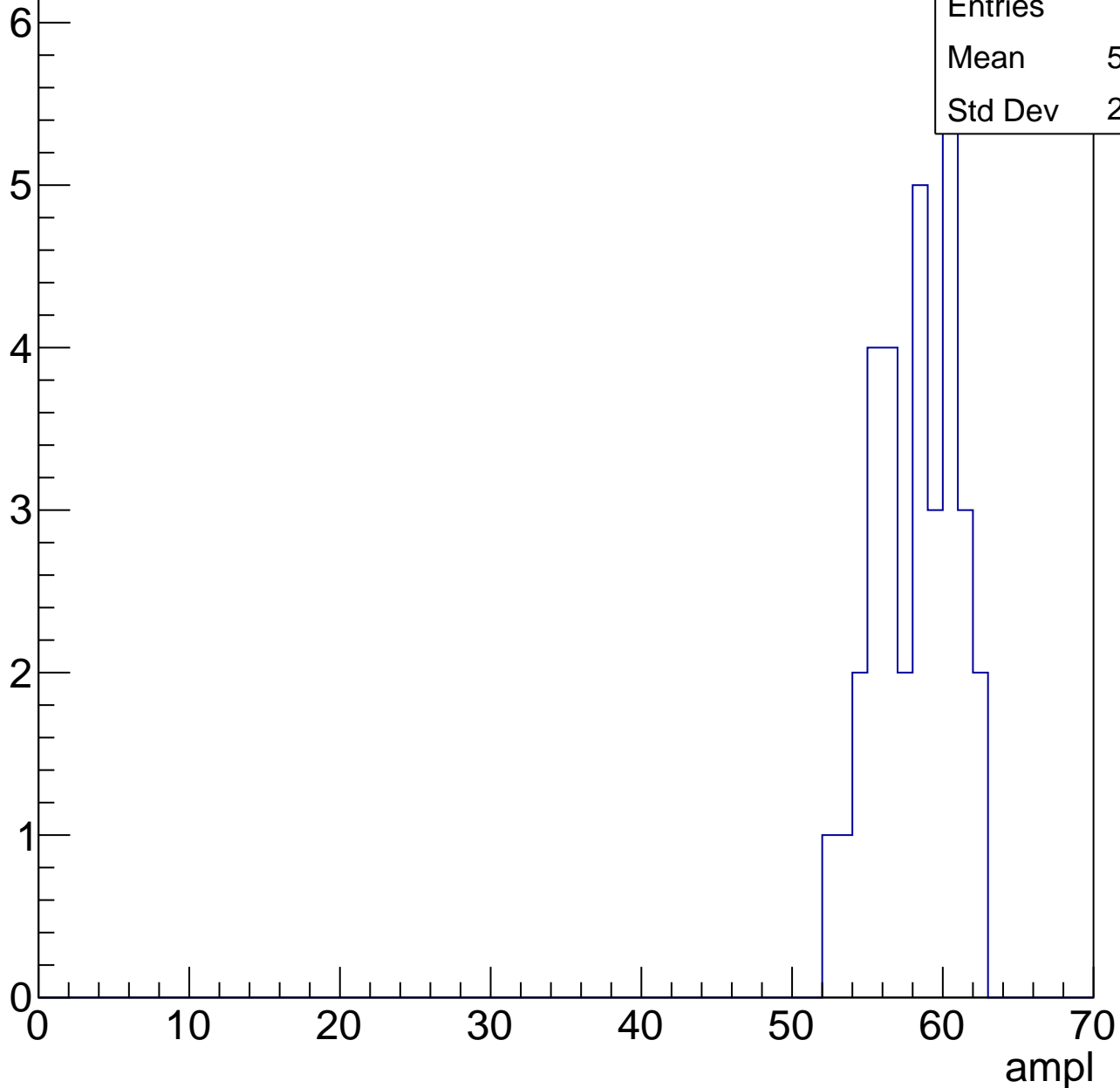


# B0L000S, U13-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	57.73
Std Dev	2.643

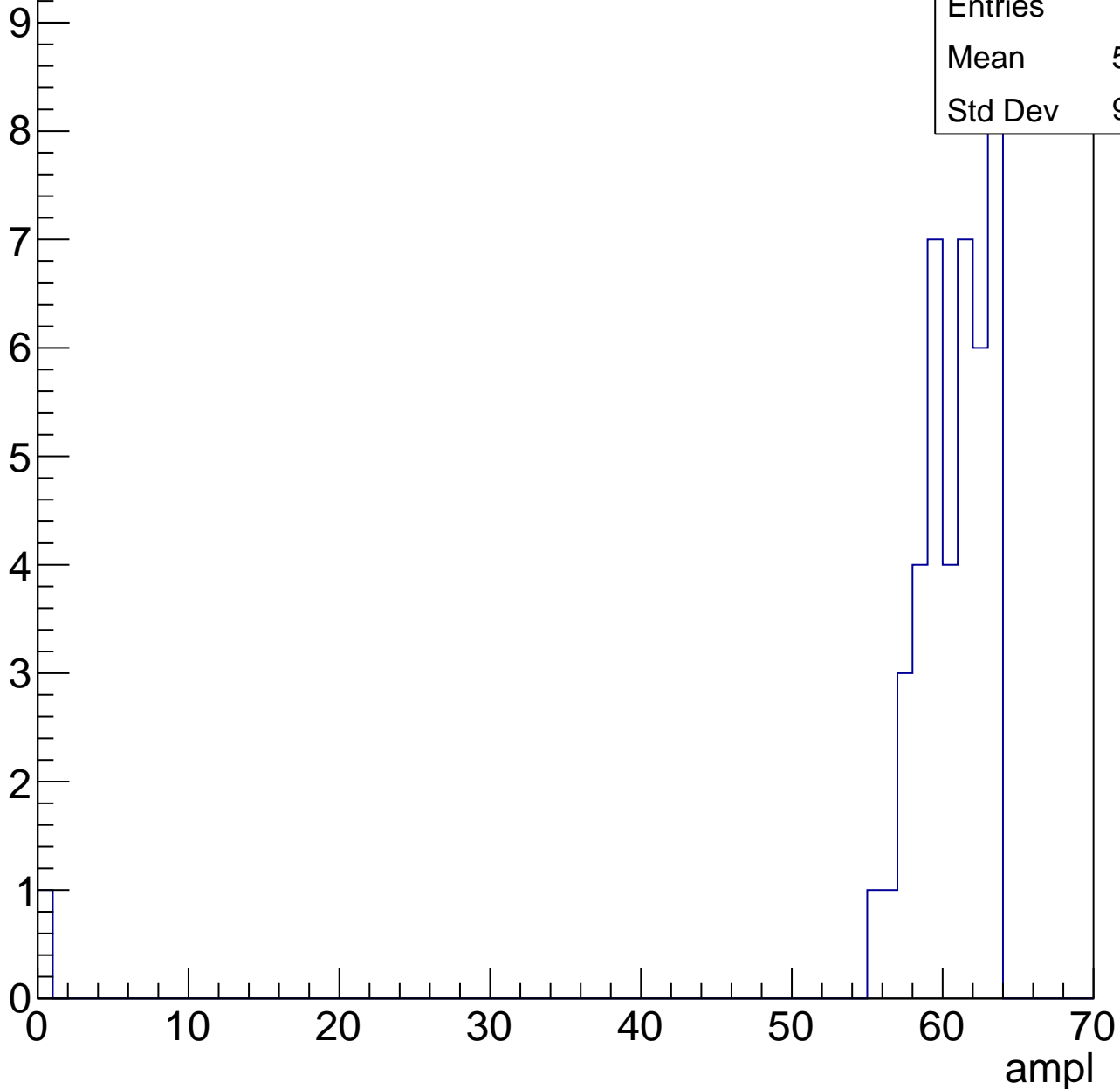


# B0L000S, U13-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.91
Std Dev	9.341



# B0L000S, U13-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch41, adc0

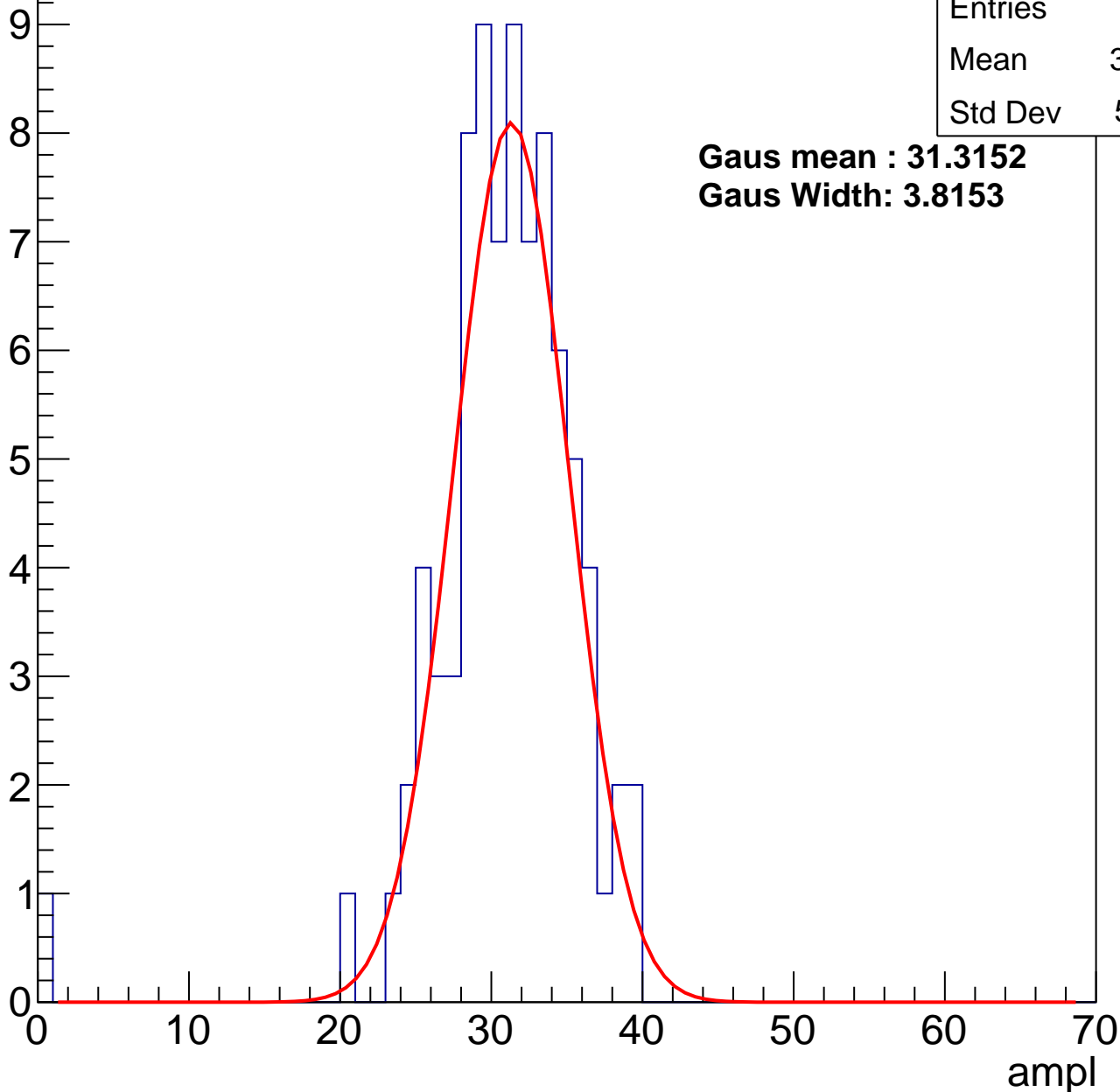
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	30.43
Std Dev	5.071

**Gaus mean : 31.3152**

**Gaus Width: 3.8153**



# B0L000S, U13-ch41, adc1

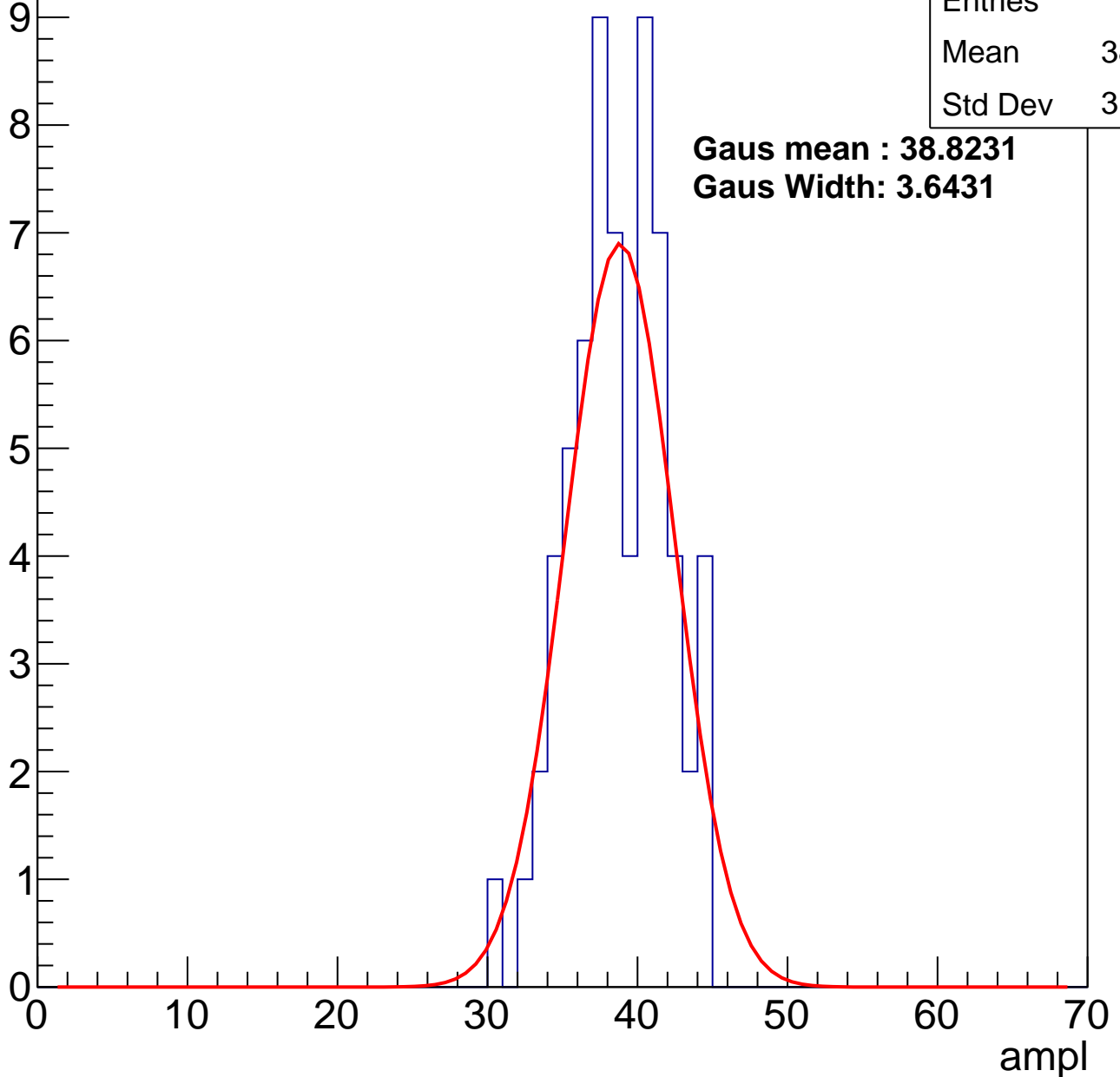
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	38.26
Std Dev	3.159

**Gaus mean : 38.8231**

**Gaus Width: 3.6431**



# B0L000S, U13-ch41, adc2

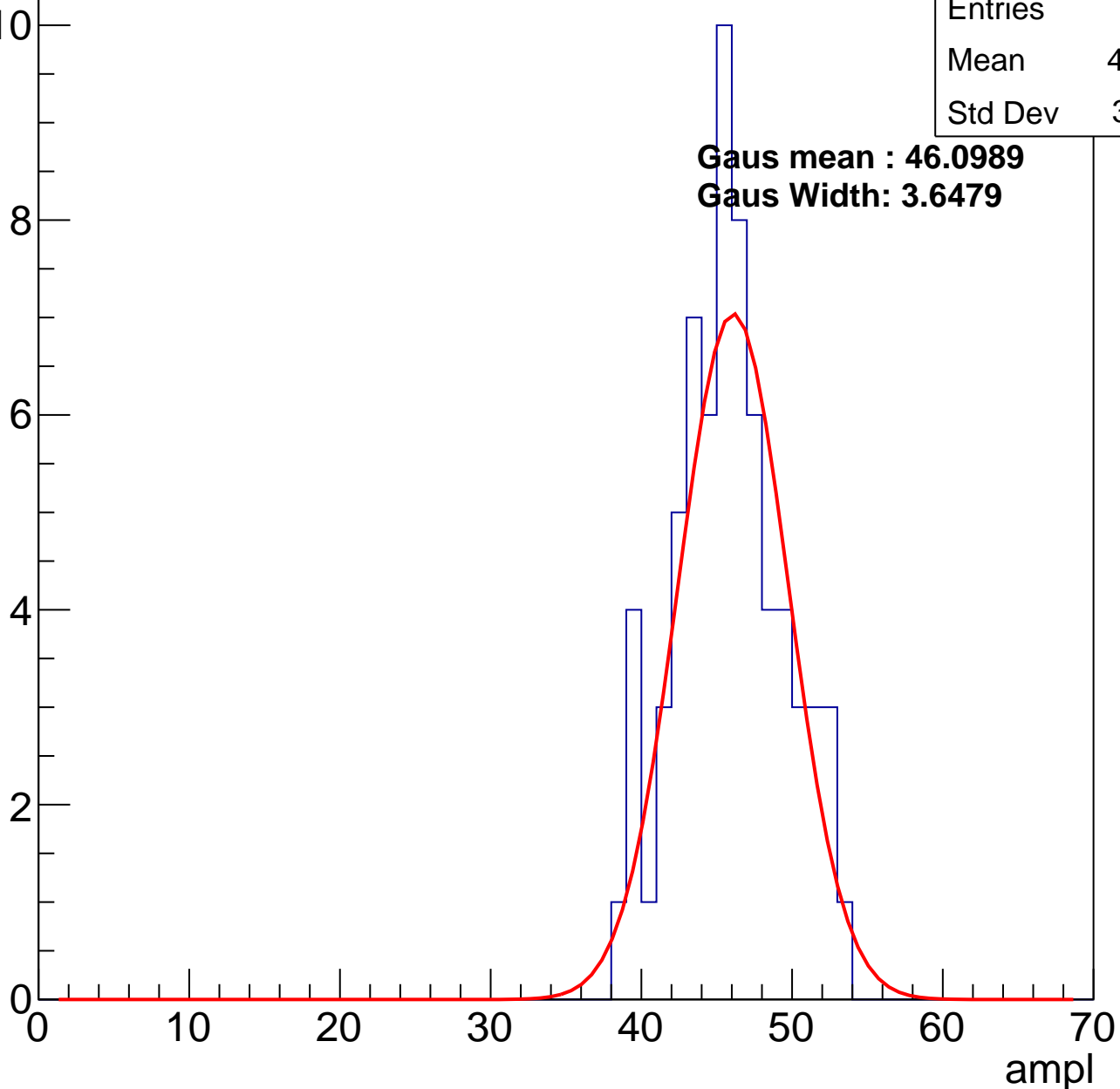
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	45.39
Std Dev	3.531

**Gaus mean : 46.0989**

**Gaus Width: 3.6479**

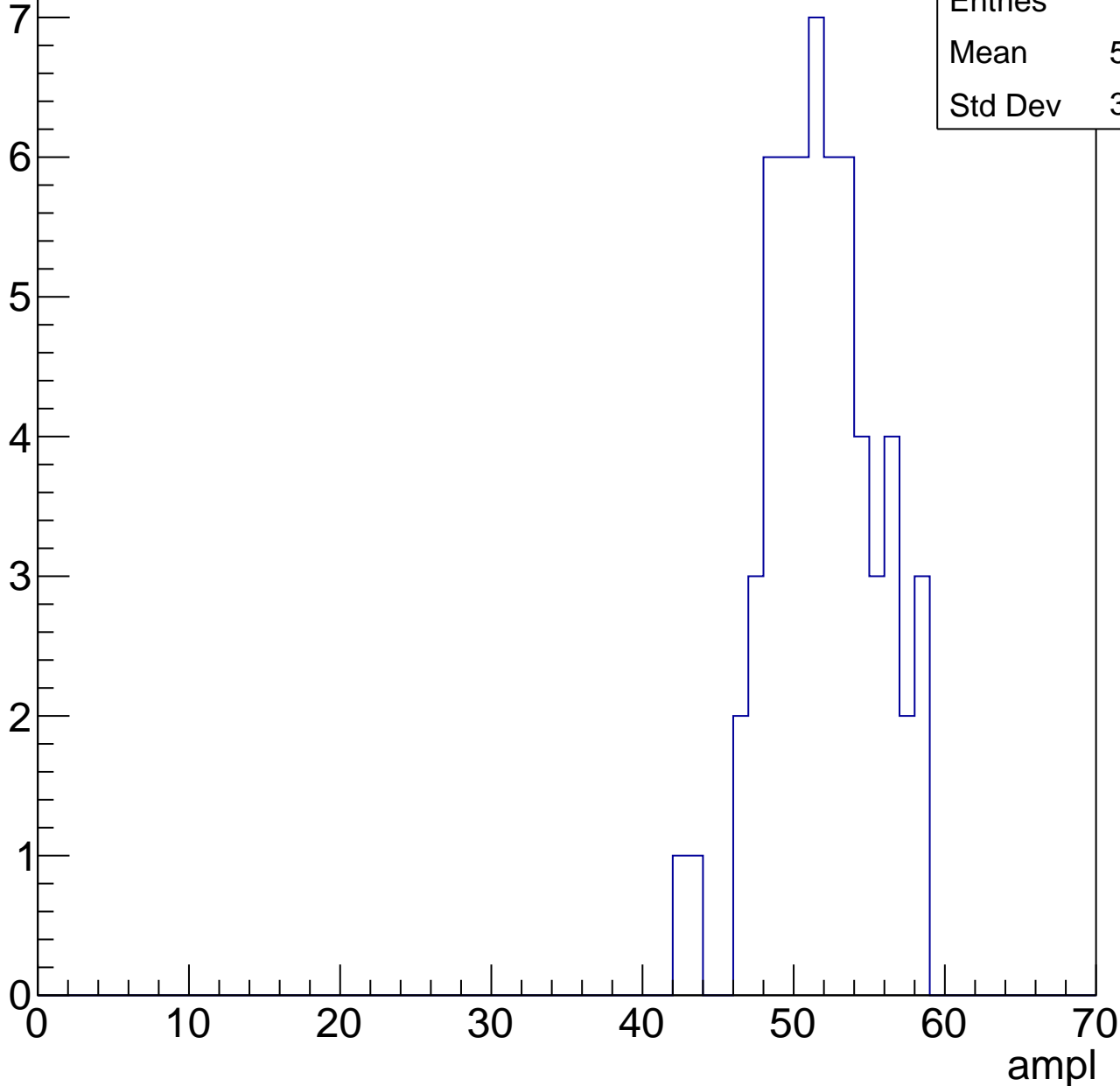


# B0L000S, U13-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.33
Std Dev	3.543

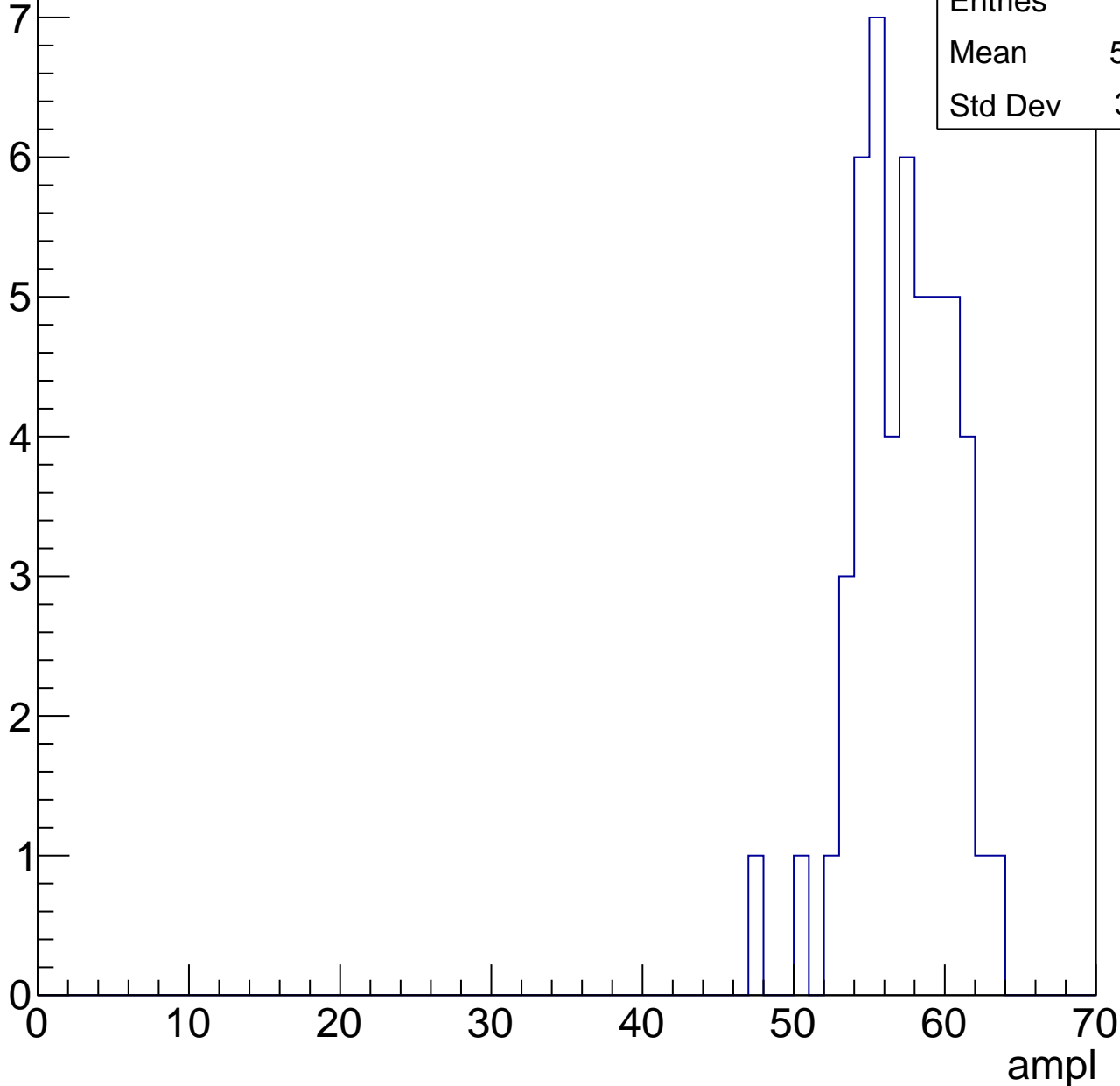


# B0L000S, U13-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.74
Std Dev	3.161

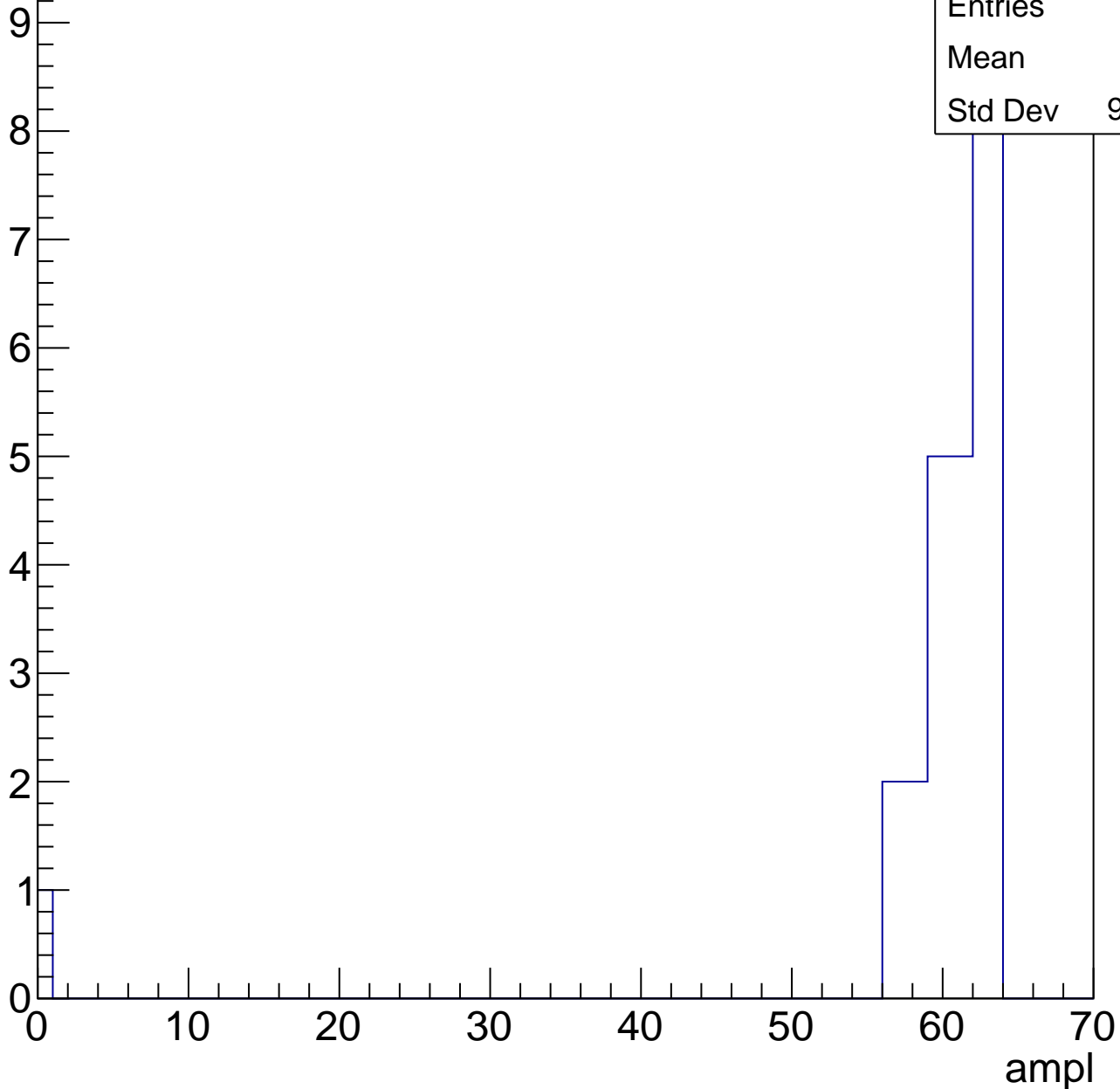


# B0L000S, U13-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	59.1
Std Dev	9.805



# B0L000S, U13-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch42, adc0

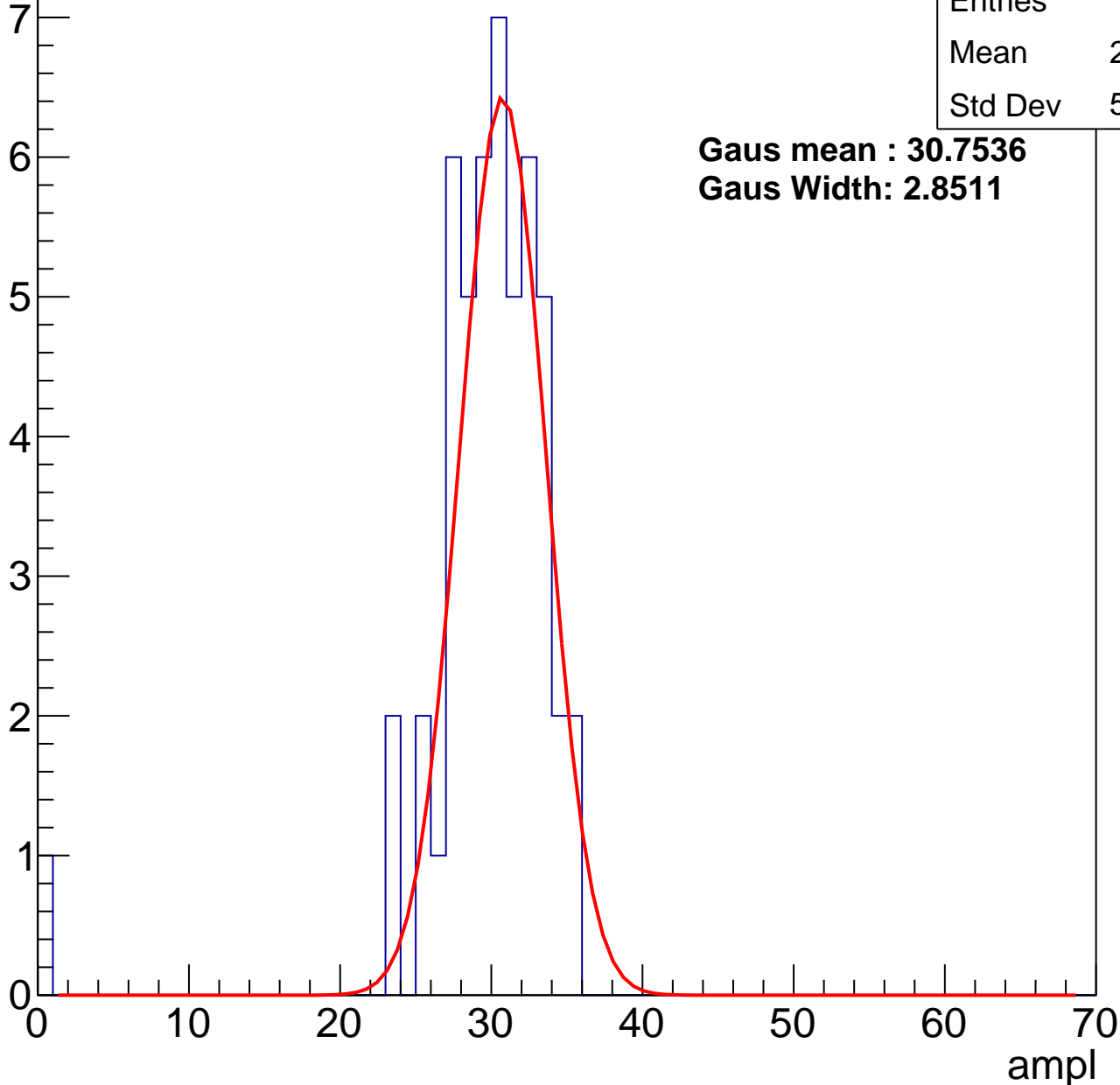
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	29.16
Std Dev	5.029

**Gaus mean : 30.7536**

**Gaus Width: 2.8511**



# B0L000S, U13-ch42, adc1

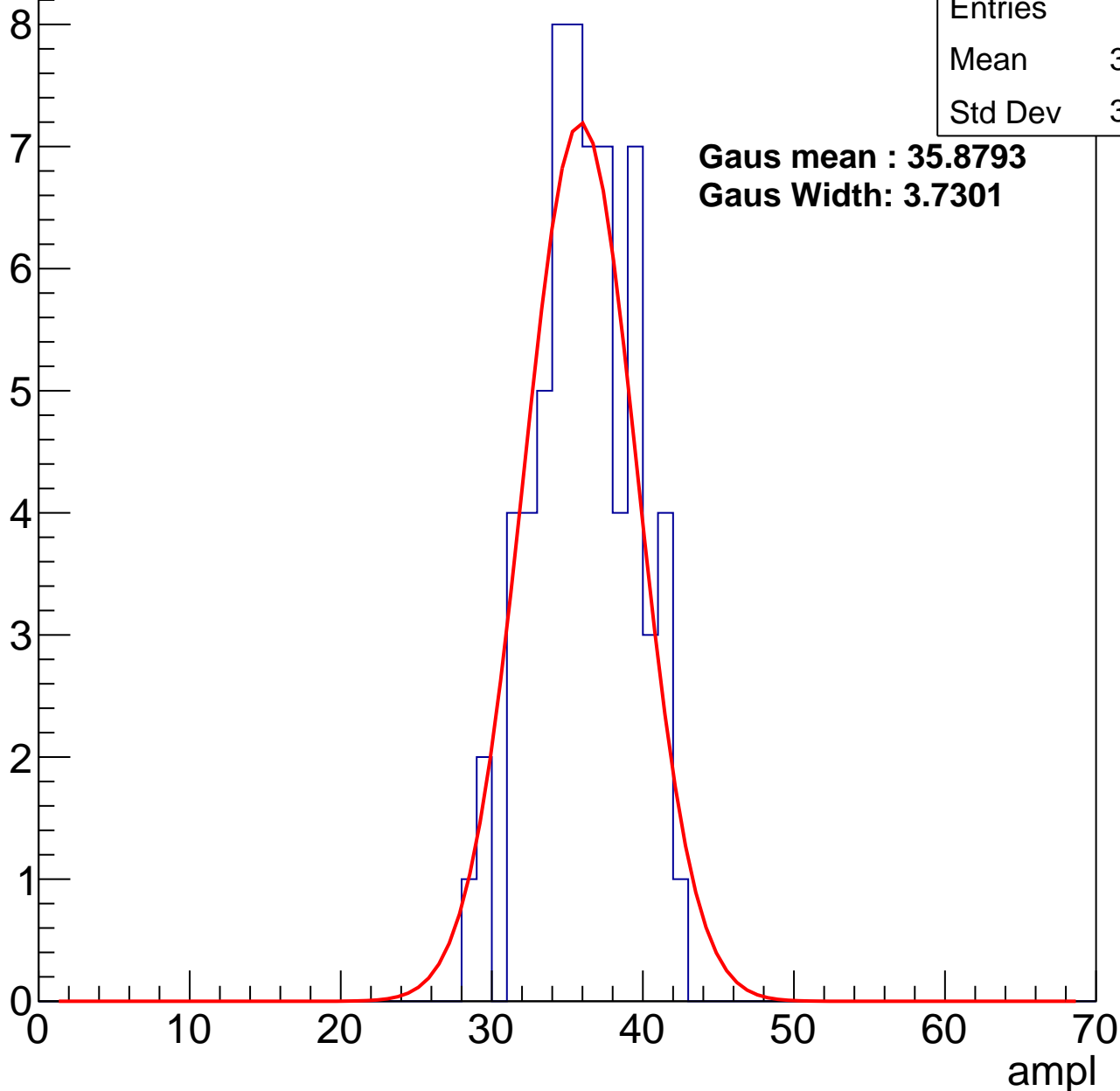
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.65
Std Dev	3.217

**Gaus mean : 35.8793**

**Gaus Width: 3.7301**



# B0L000S, U13-ch42, adc2

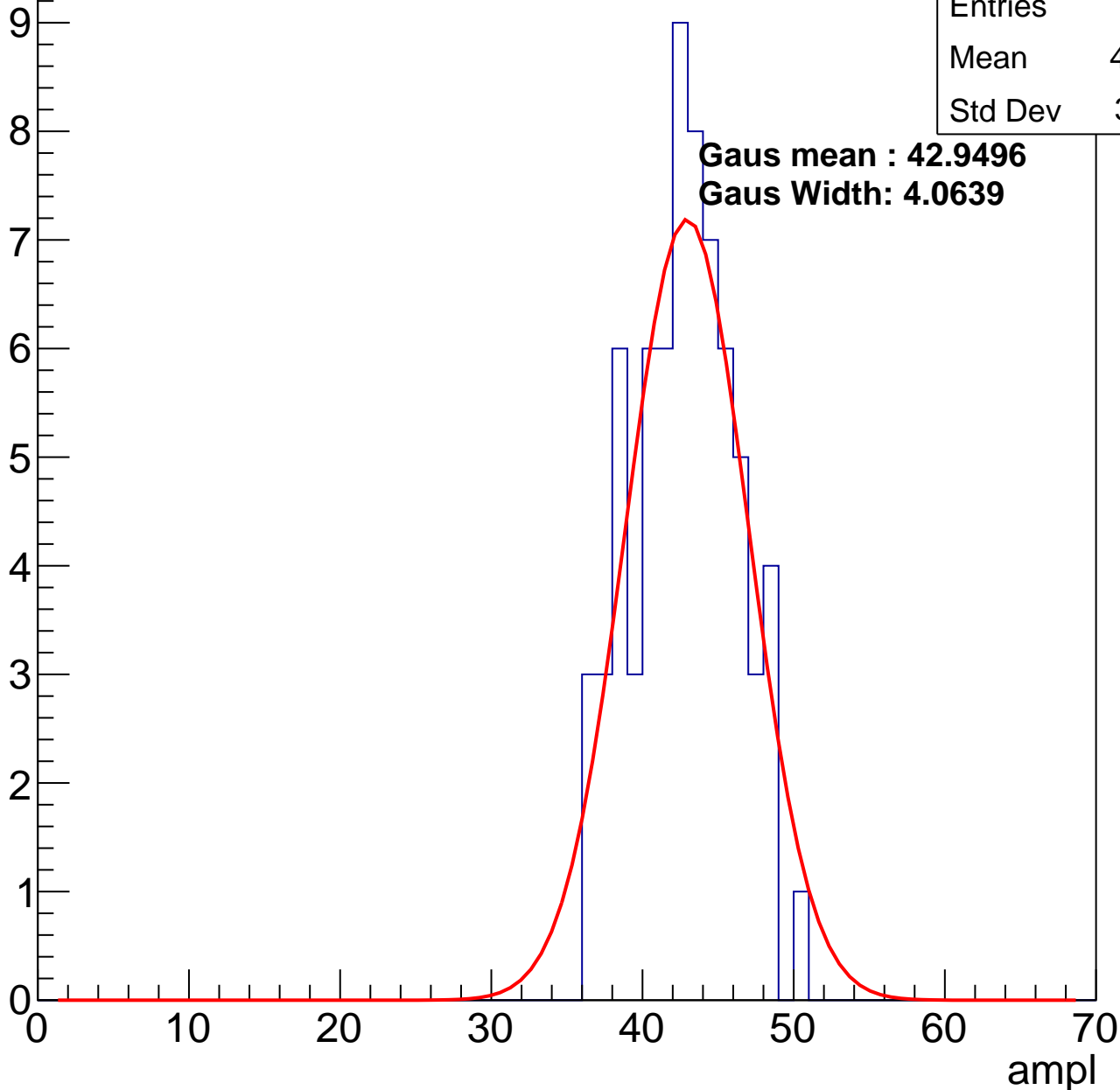
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.33
Std Dev	3.341

**Gaus mean : 42.9496**

**Gaus Width: 4.0639**

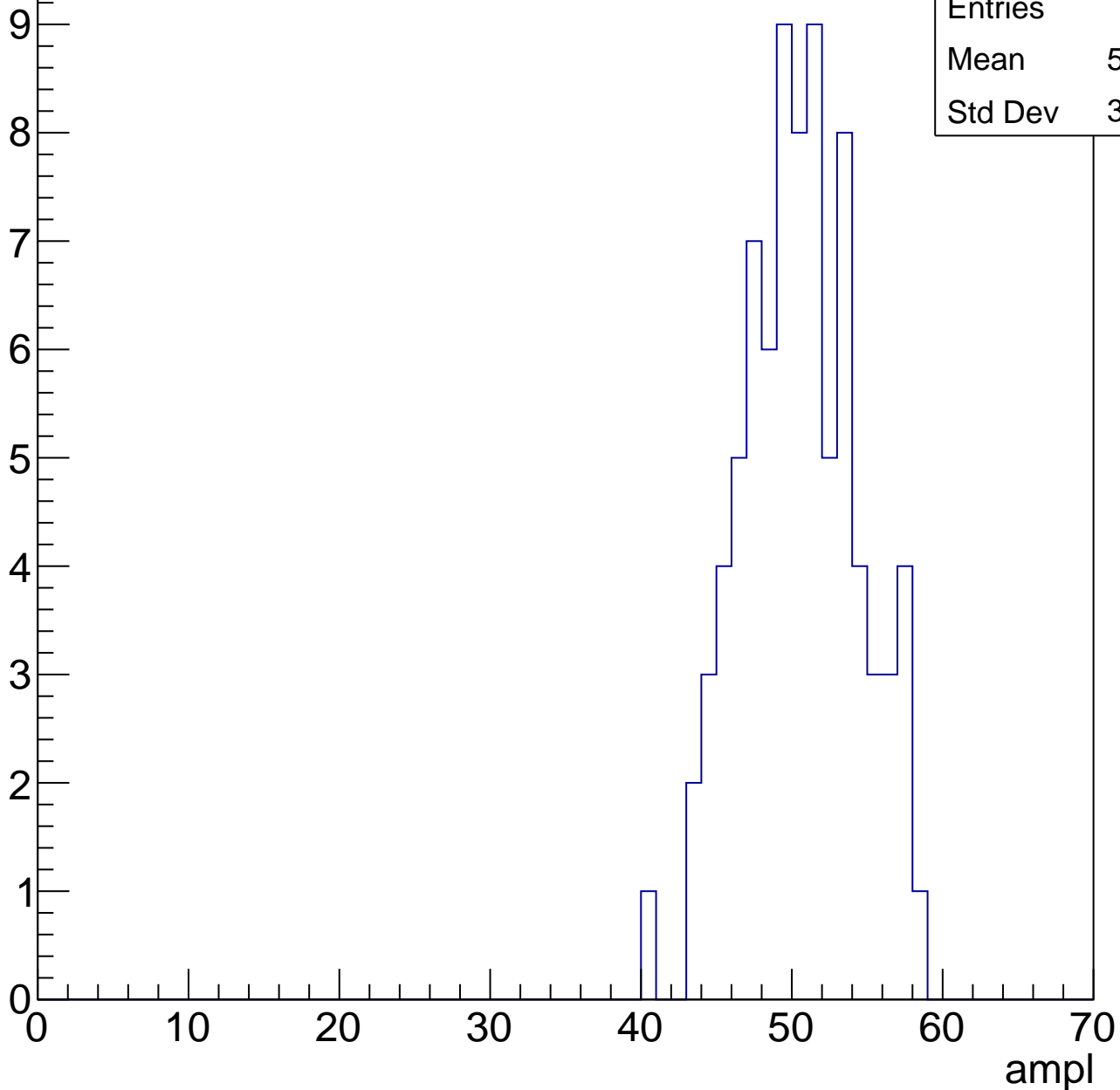


# B0L000S, U13-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	50.05
Std Dev	3.809

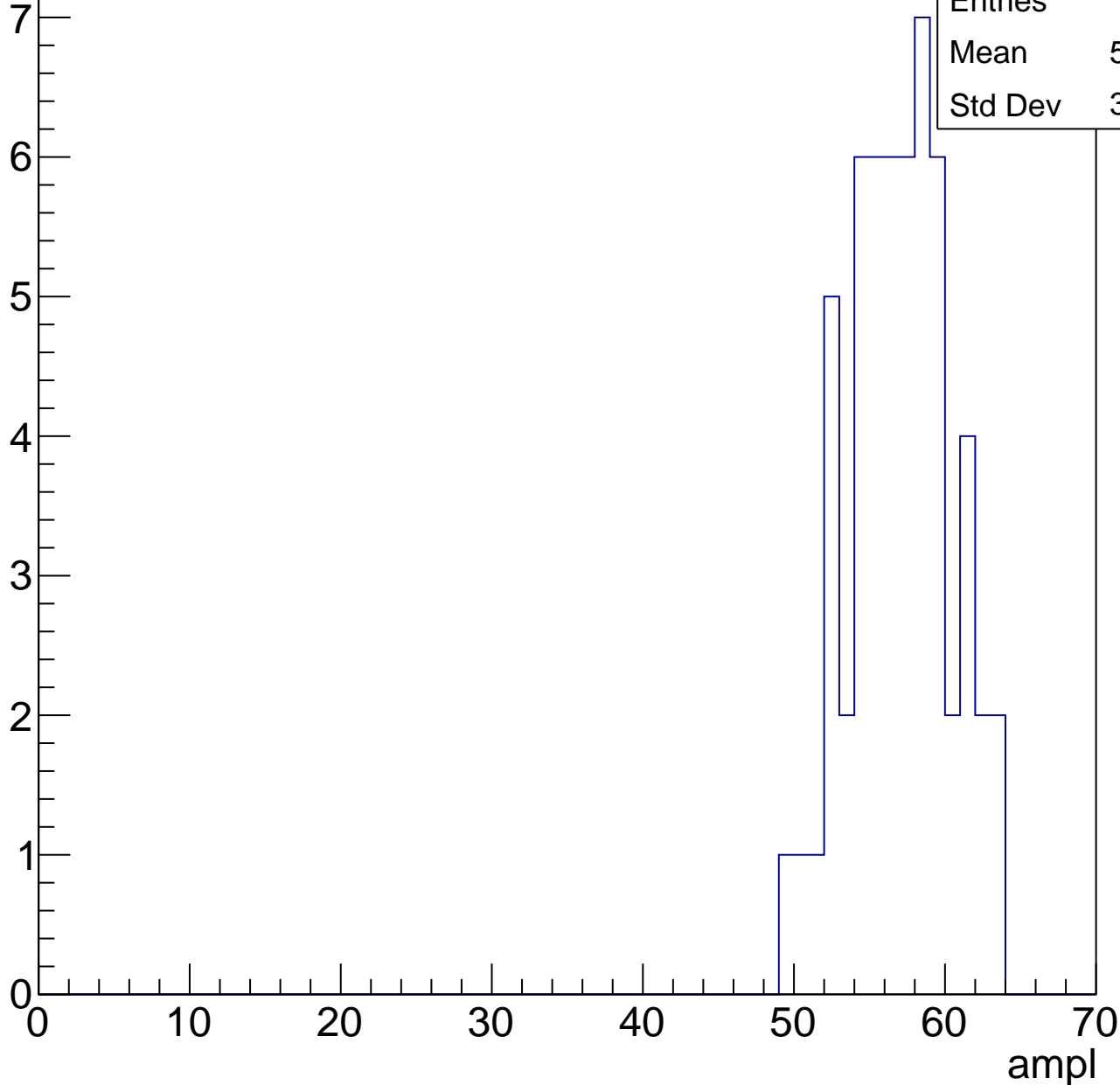


# B0L000S, U13-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.53
Std Dev	3.277

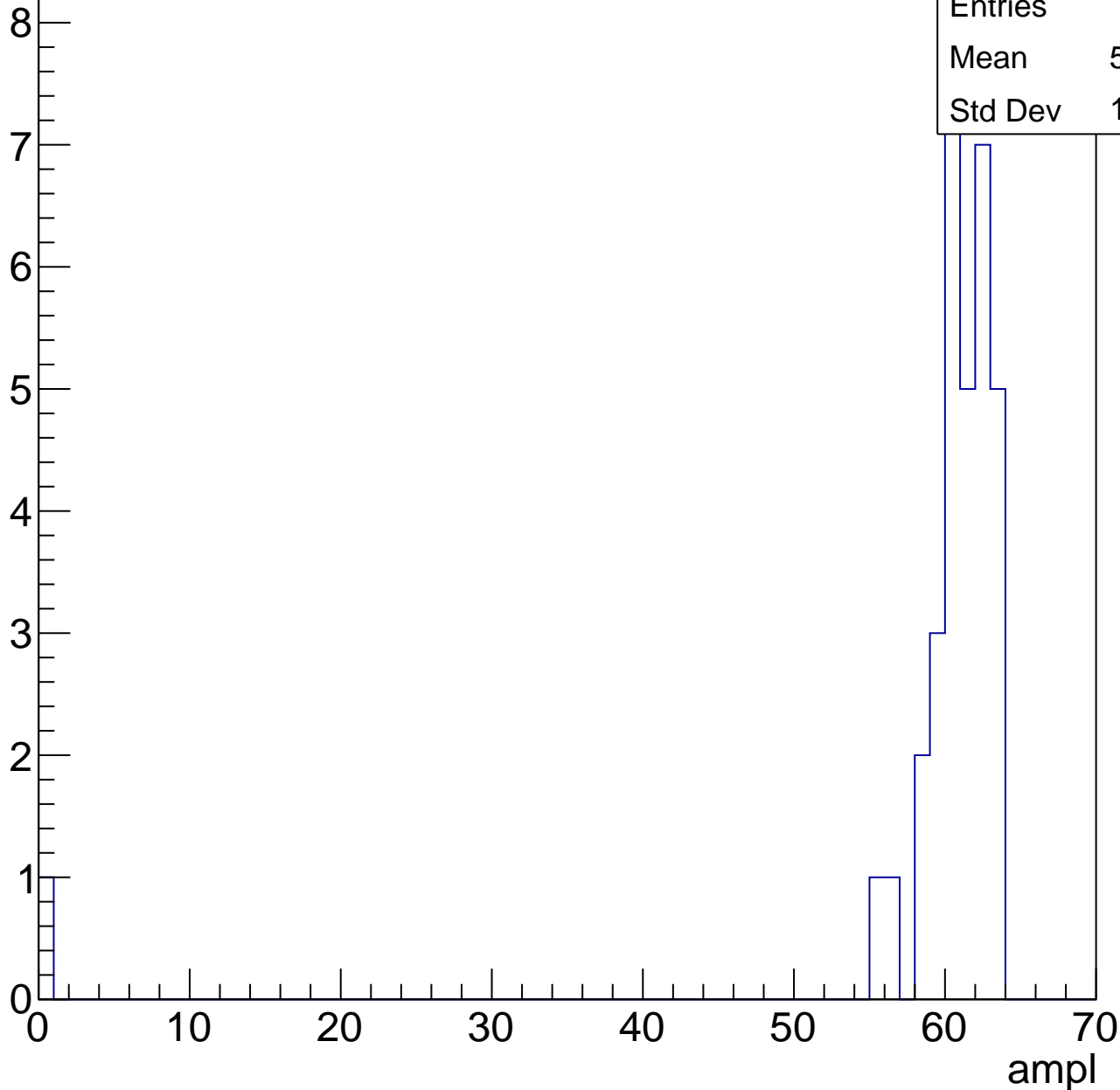


# B0L000S, U13-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	58.73
Std Dev	10.56



# B0L000S, U13-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U13-ch43, adc0

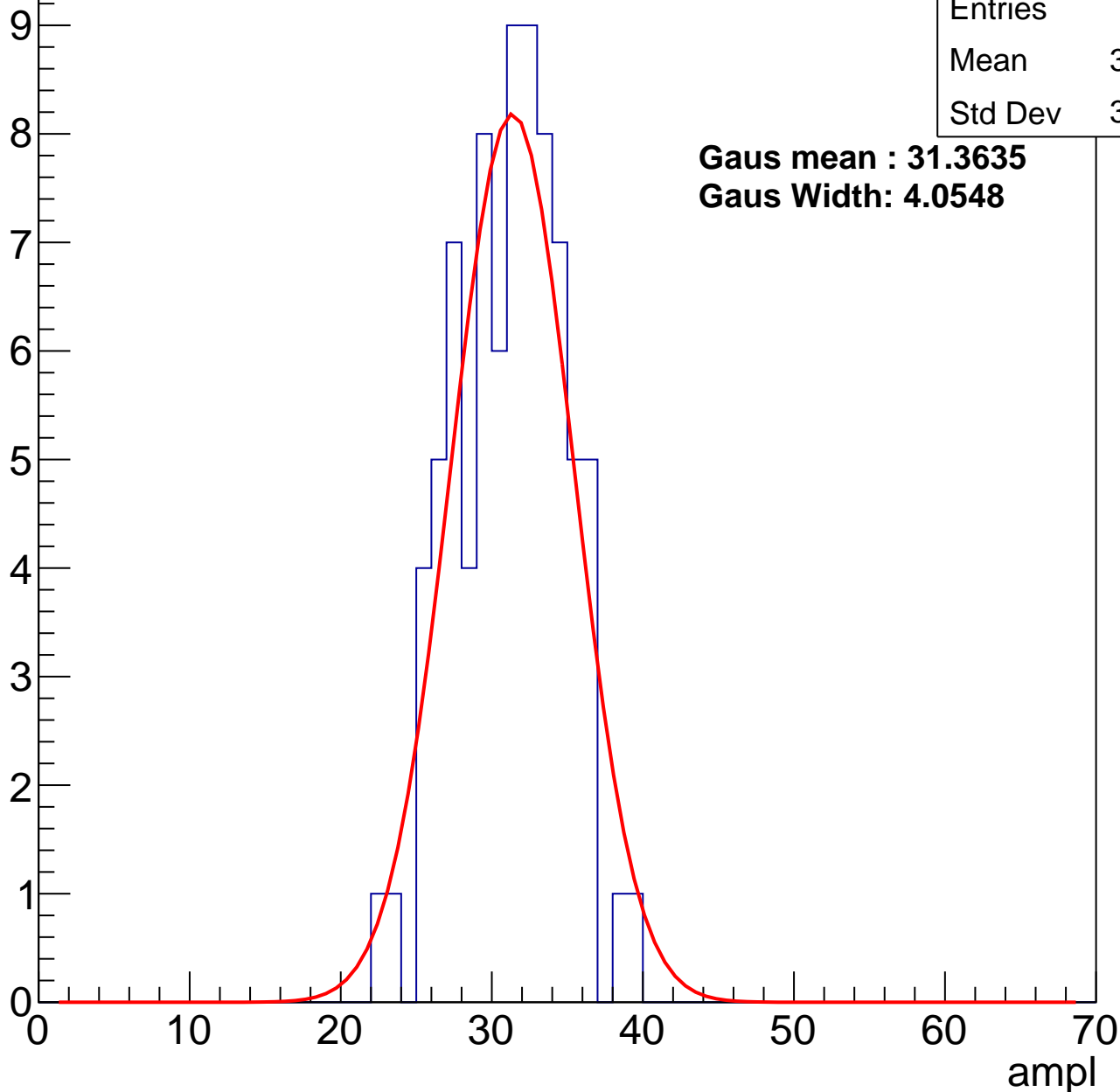
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	30.73
Std Dev	3.542

**Gaus mean : 31.3635**

**Gaus Width: 4.0548**

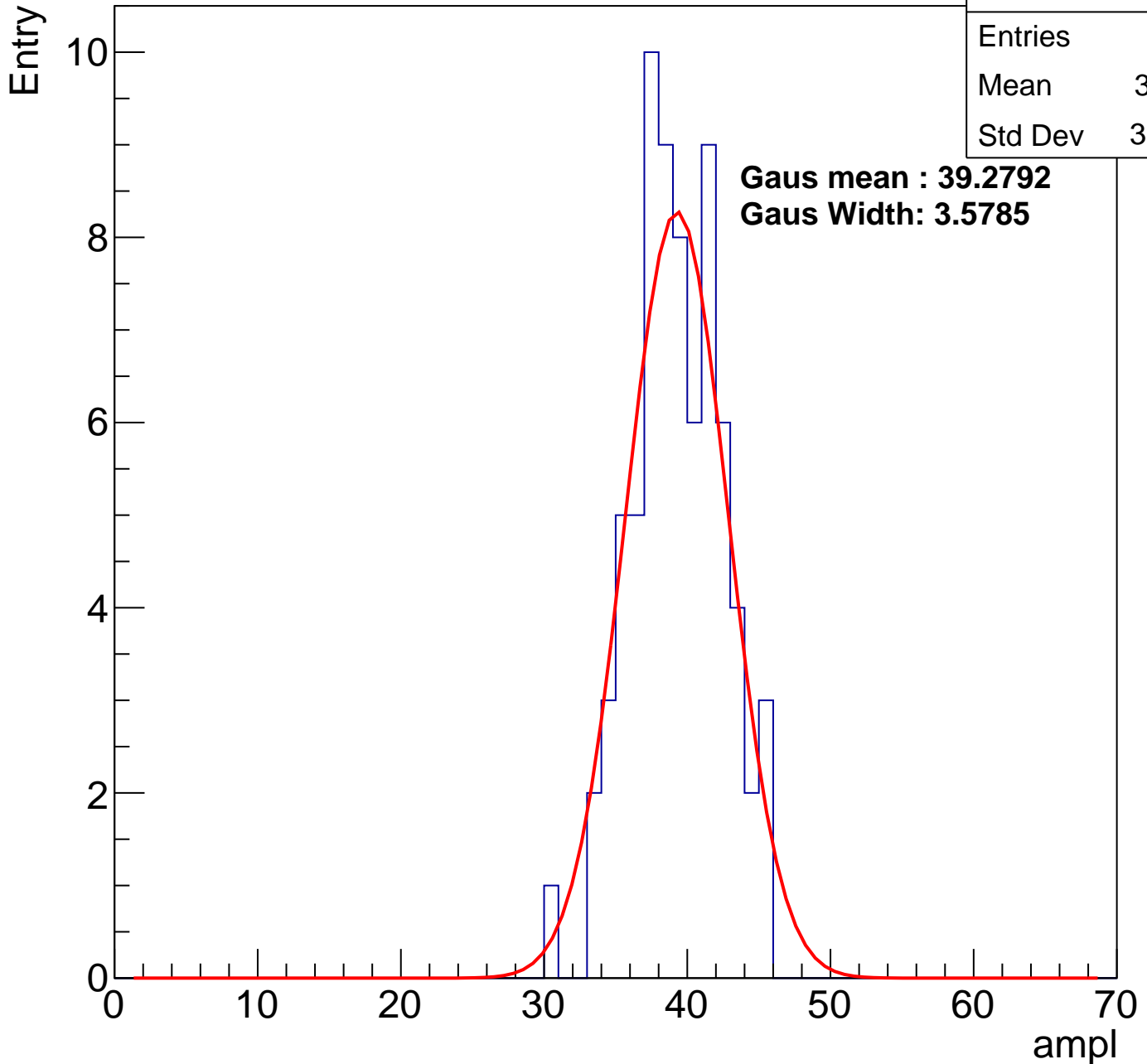


# B0L000S, U13-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	38.81
Std Dev	3.135

**Gaus mean : 39.2792**  
**Gaus Width: 3.5785**



# B0L000S, U13-ch43, adc2

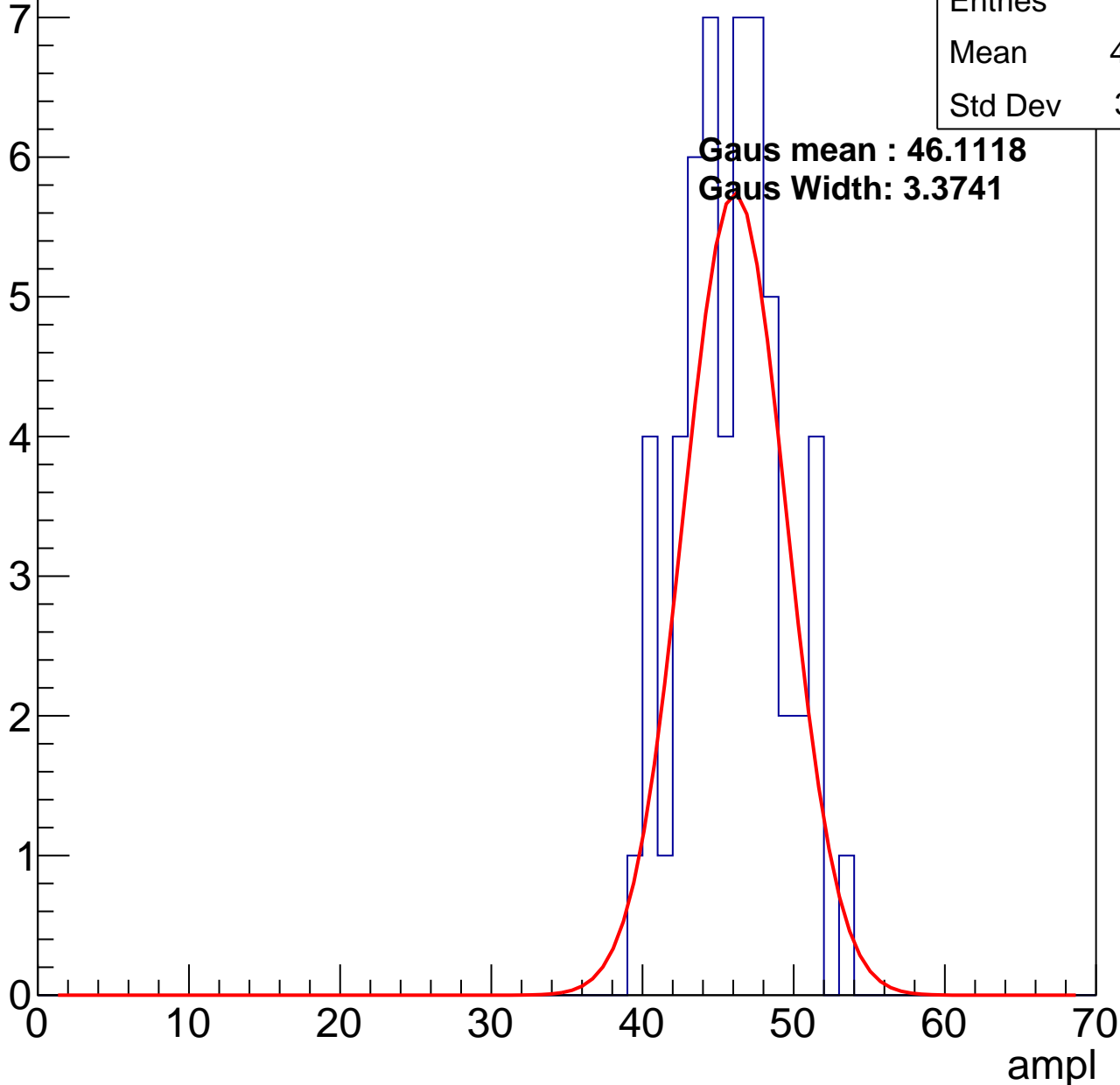
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	45.45
Std Dev	3.241

**Gaus mean : 46.1118**

**Gaus Width: 3.3741**



# B0L000S, U13-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

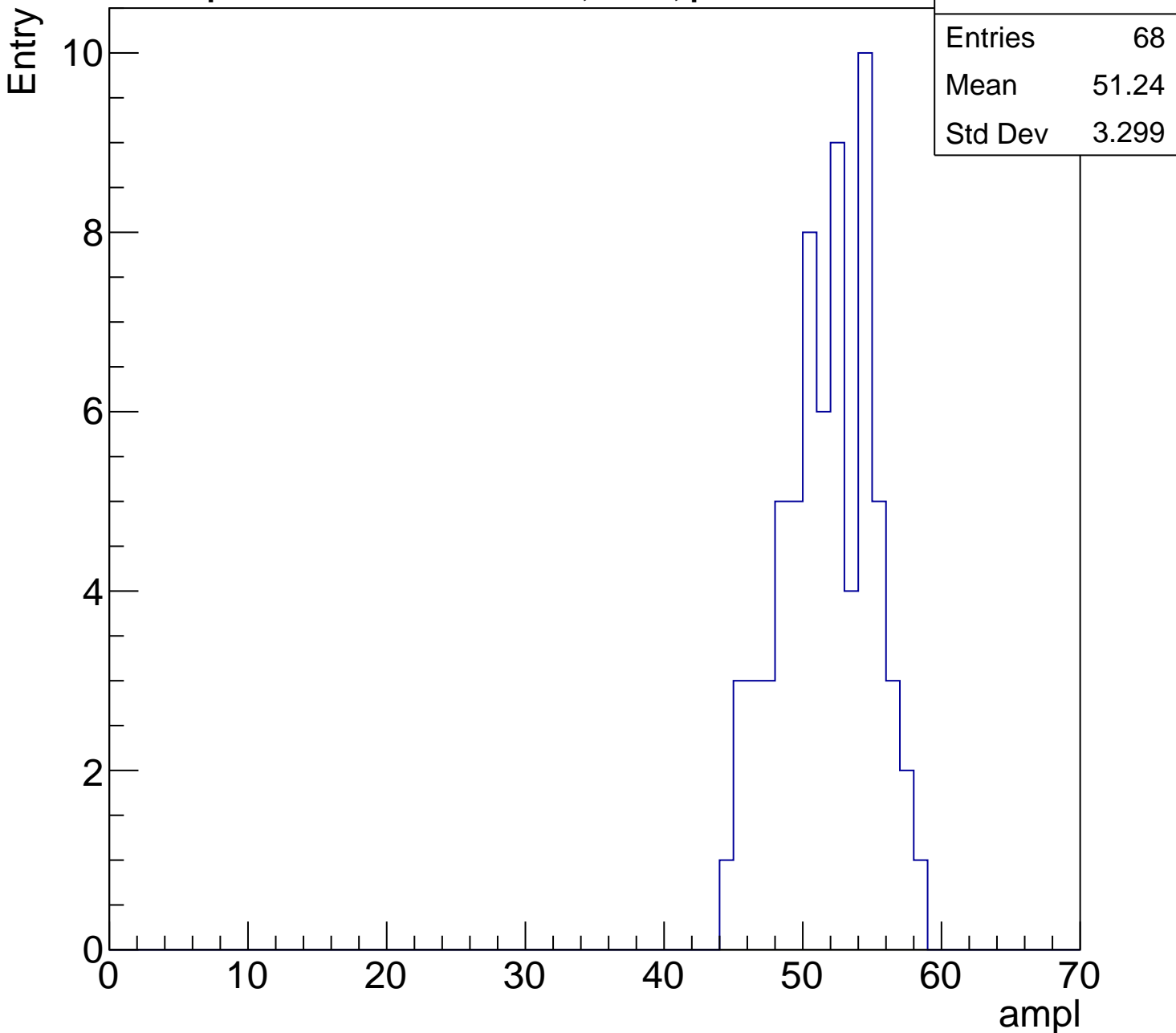
Entries	68
Mean	51.24
Std Dev	3.299

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

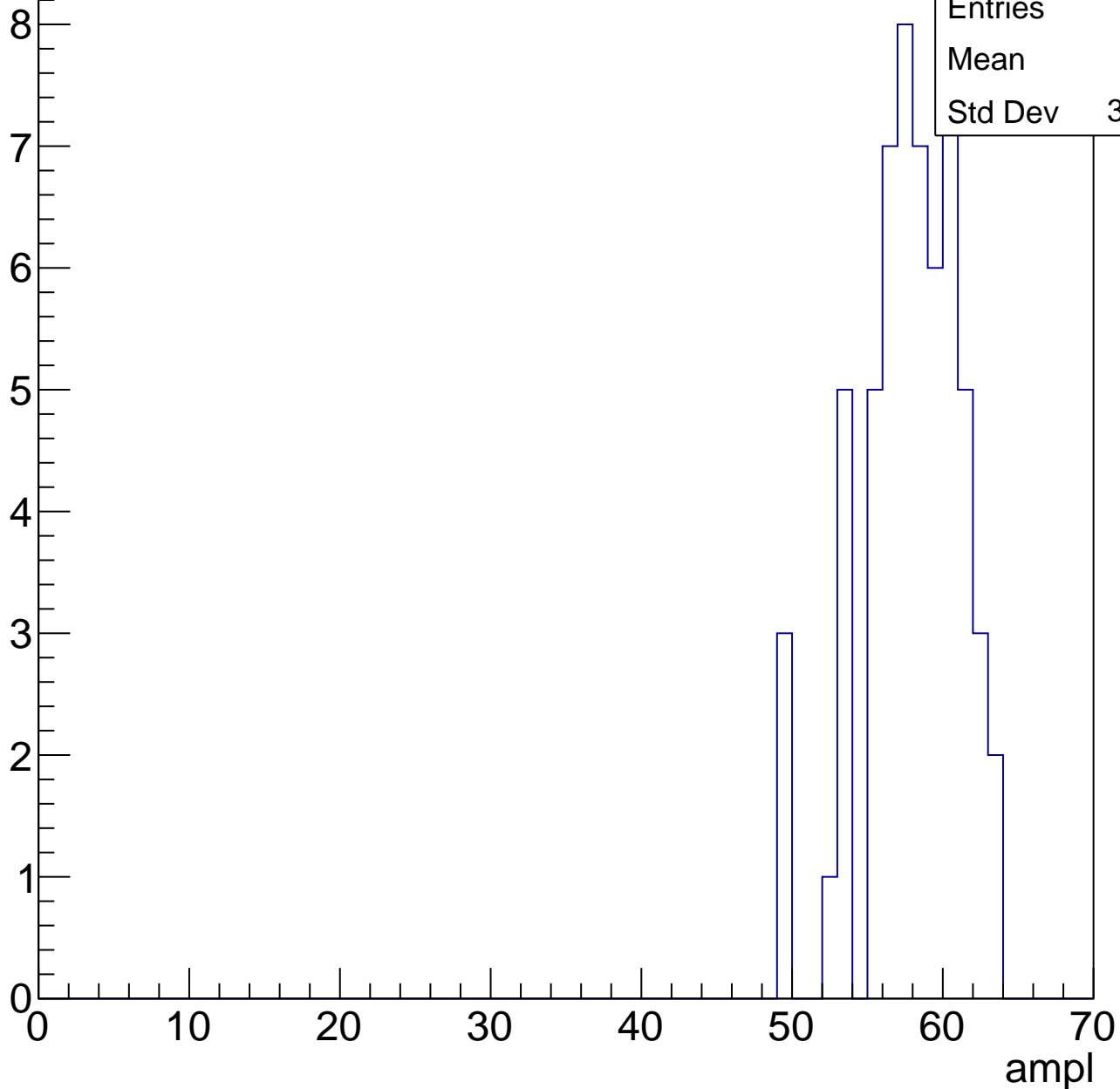


# B0L000S, U13-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.4
Std Dev	3.277

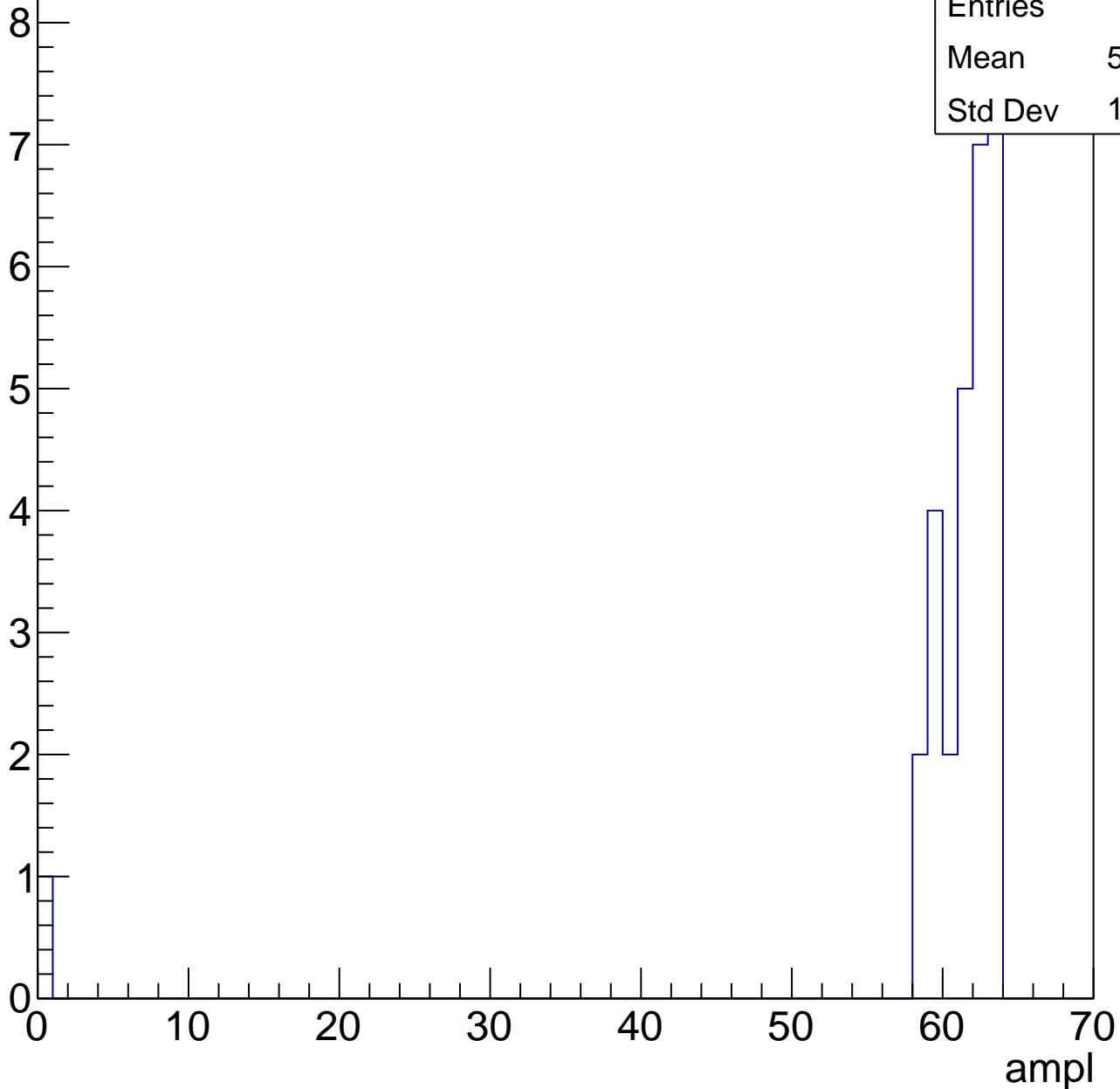


# B0L000S, U13-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	29
Mean	59.14
Std Dev	11.29



# B0L000S, U13-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch44, adc0

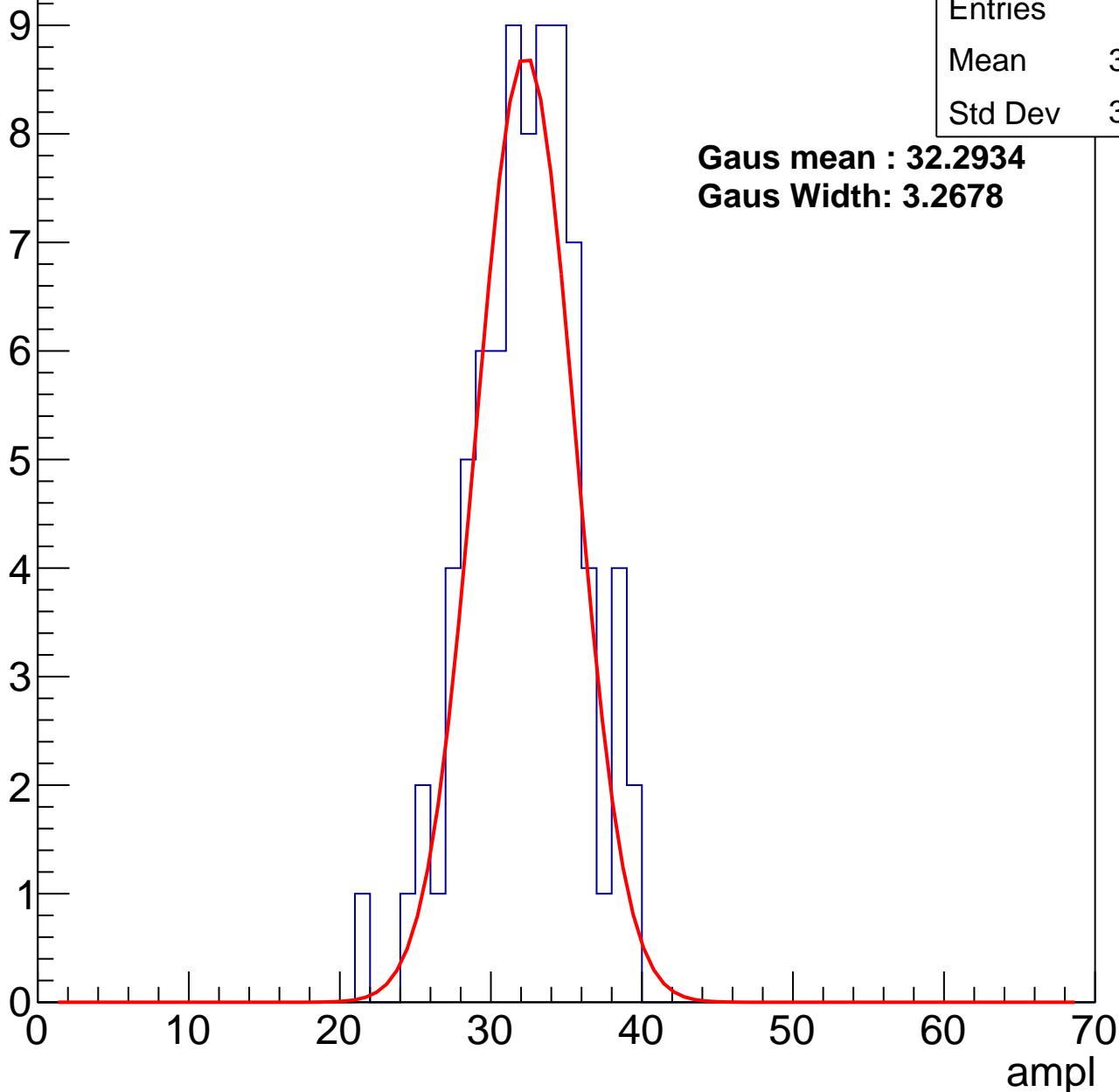
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	31.86
Std Dev	3.599

**Gaus mean : 32.2934**

**Gaus Width: 3.2678**



# B0L000S, U13-ch44, adc1

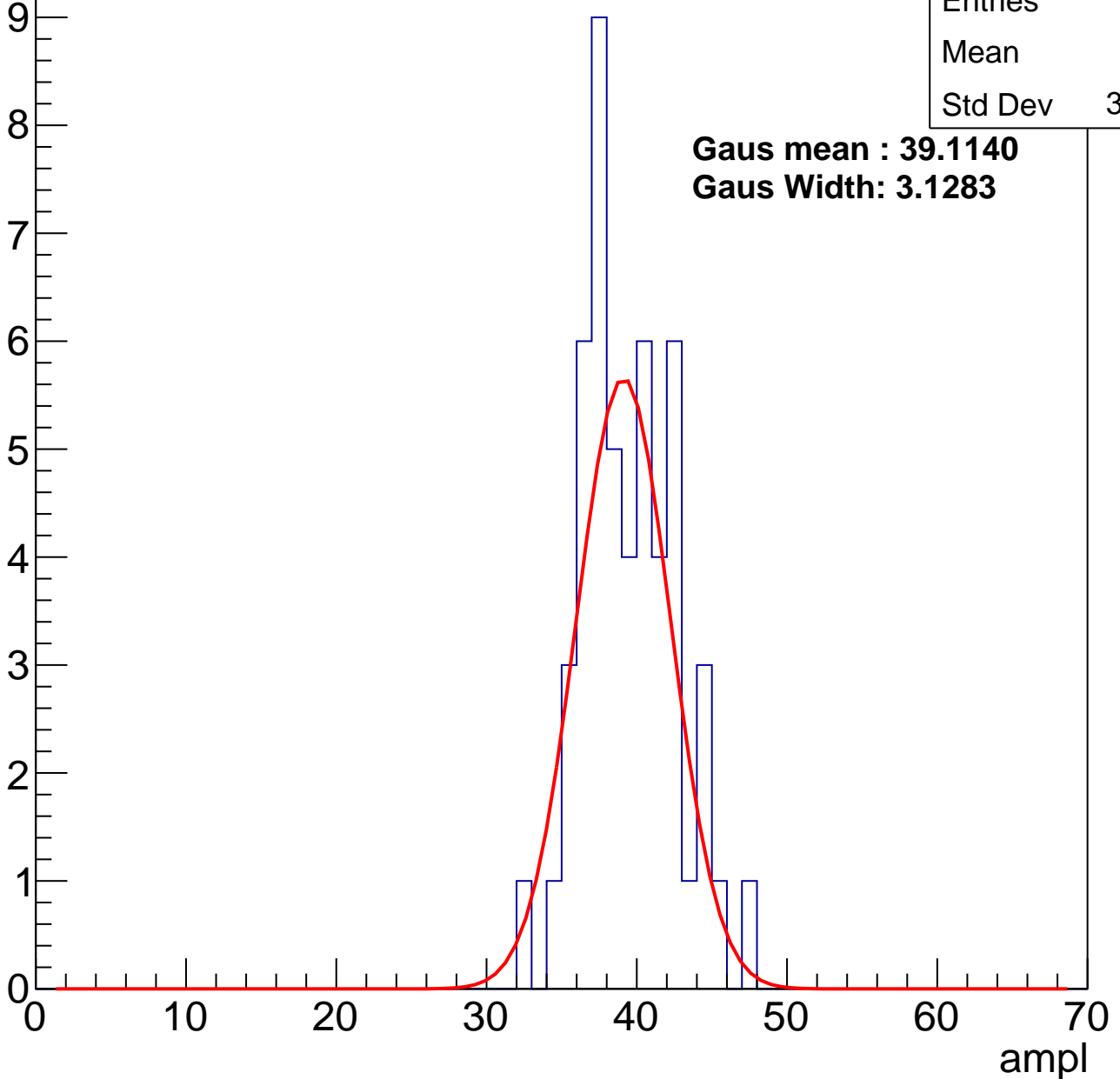
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	39
Std Dev	3.081

**Gaus mean : 39.1140**

**Gaus Width: 3.1283**



# B0L000S, U13-ch44, adc2

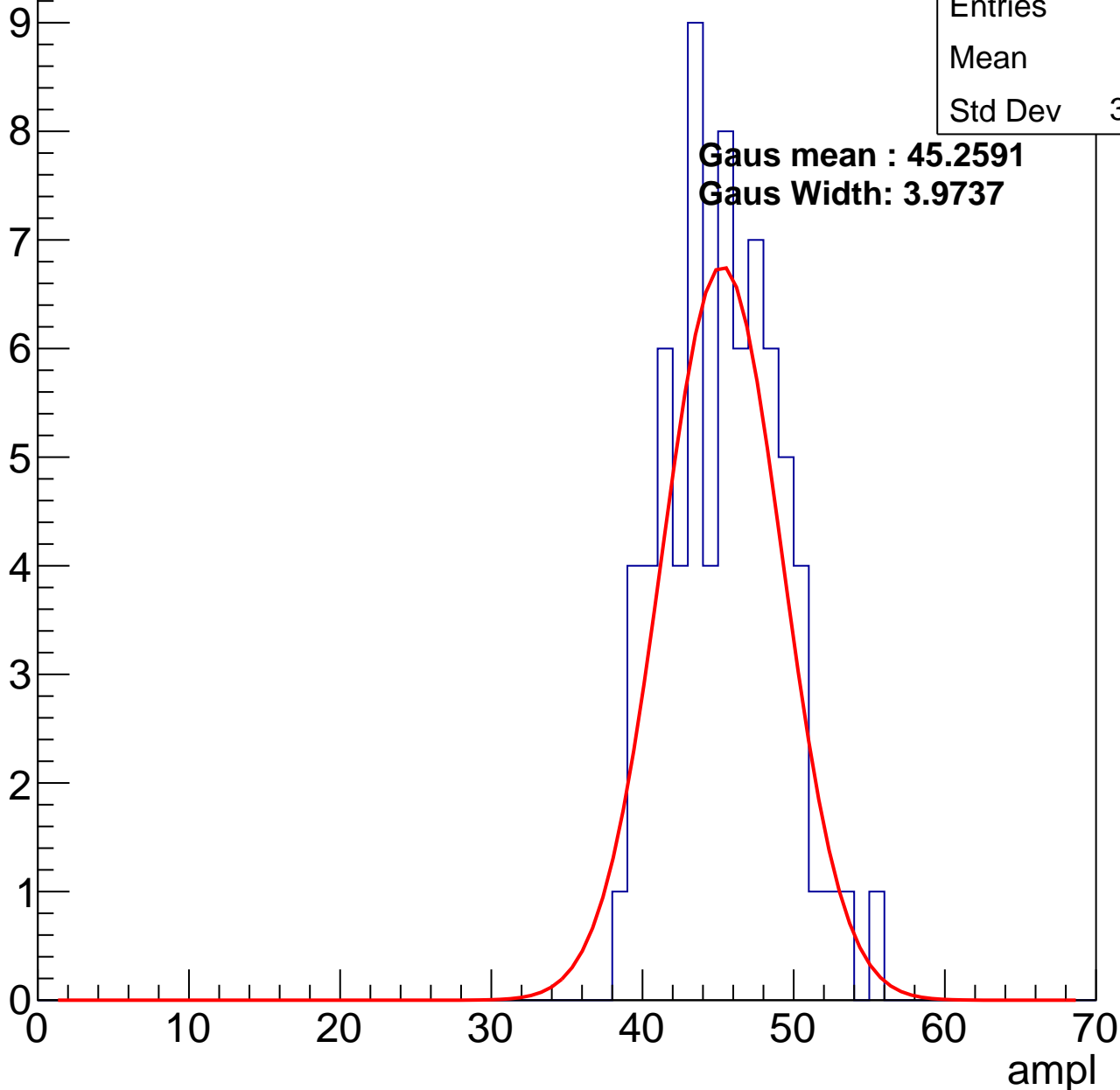
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	45
Std Dev	3.693

**Gaus mean : 45.2591**

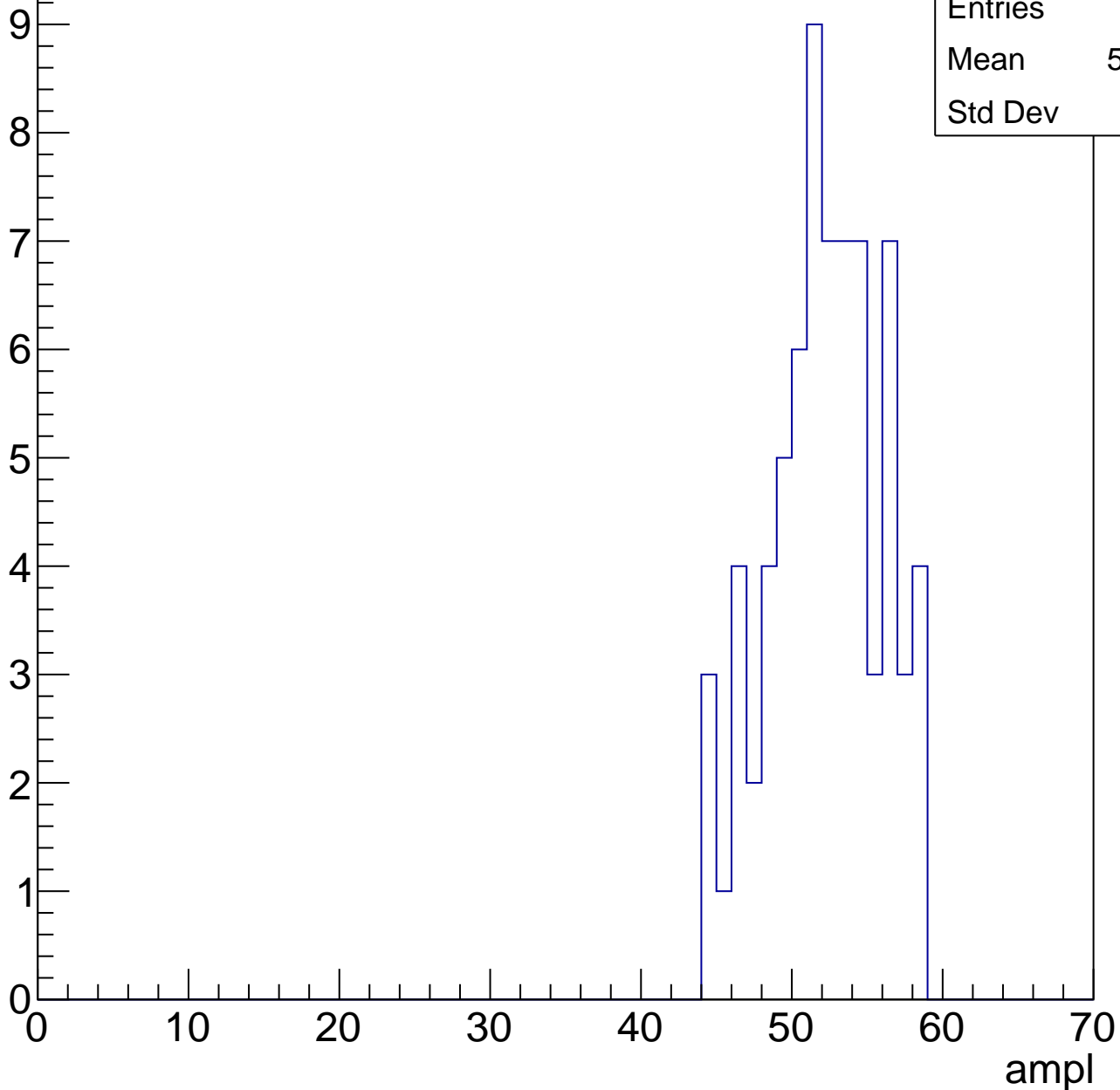
**Gaus Width: 3.9737**



# B0L000S, U13-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

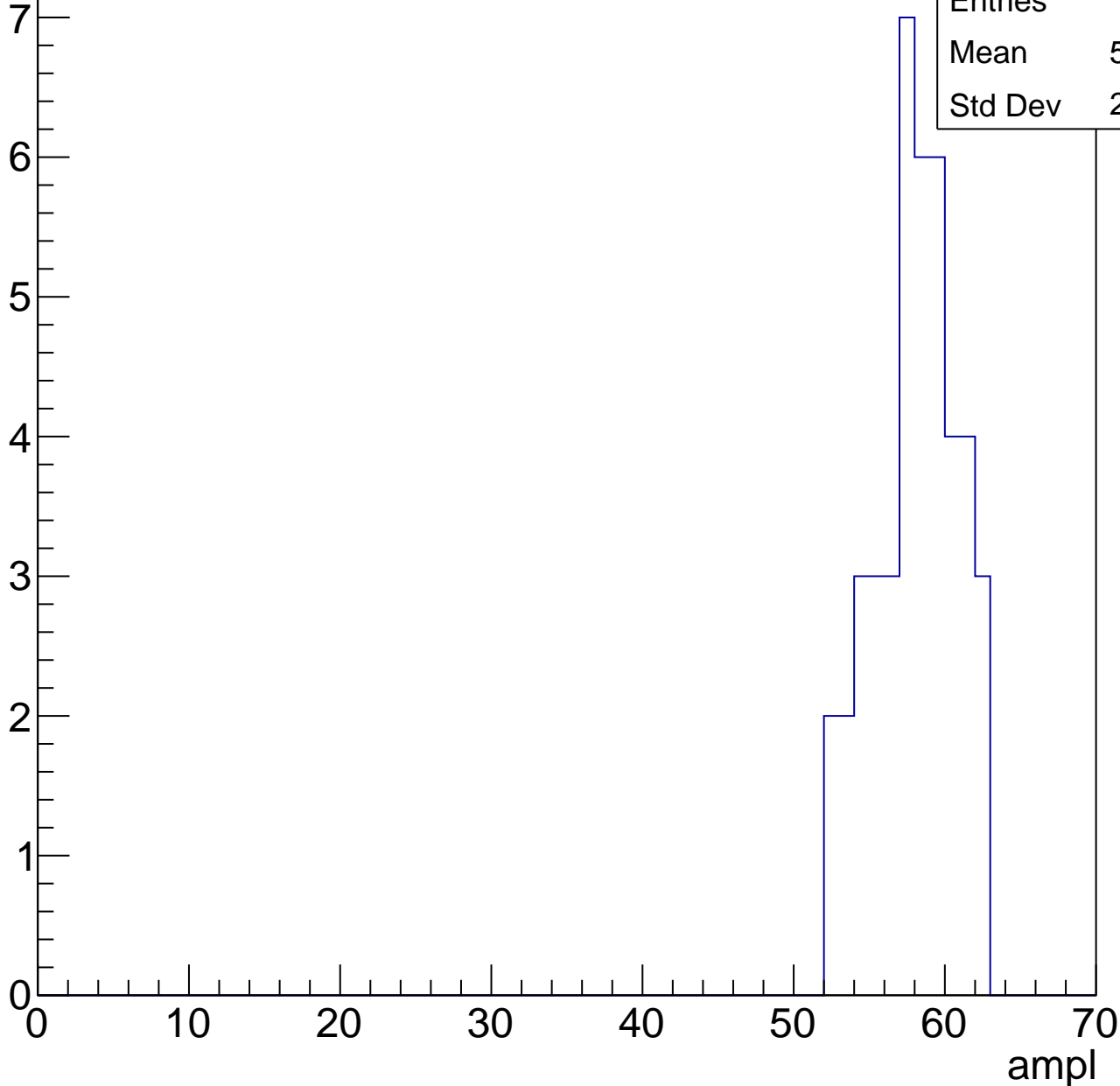


# B0L000S, U13-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	57.58
Std Dev	2.704

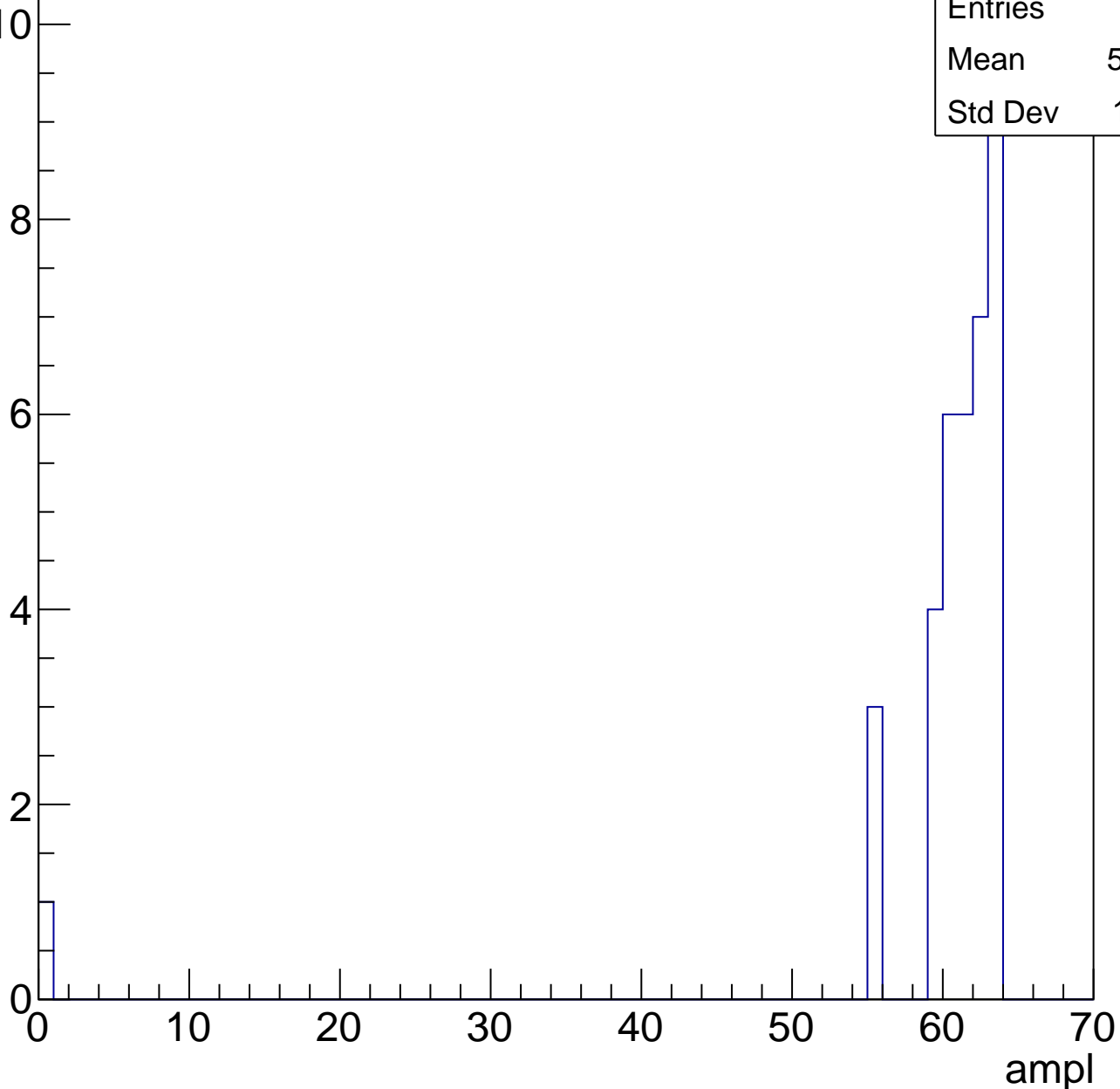


# B0L000S, U13-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	59.22
Std Dev	10.11



# B0L000S, U13-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch45, adc0

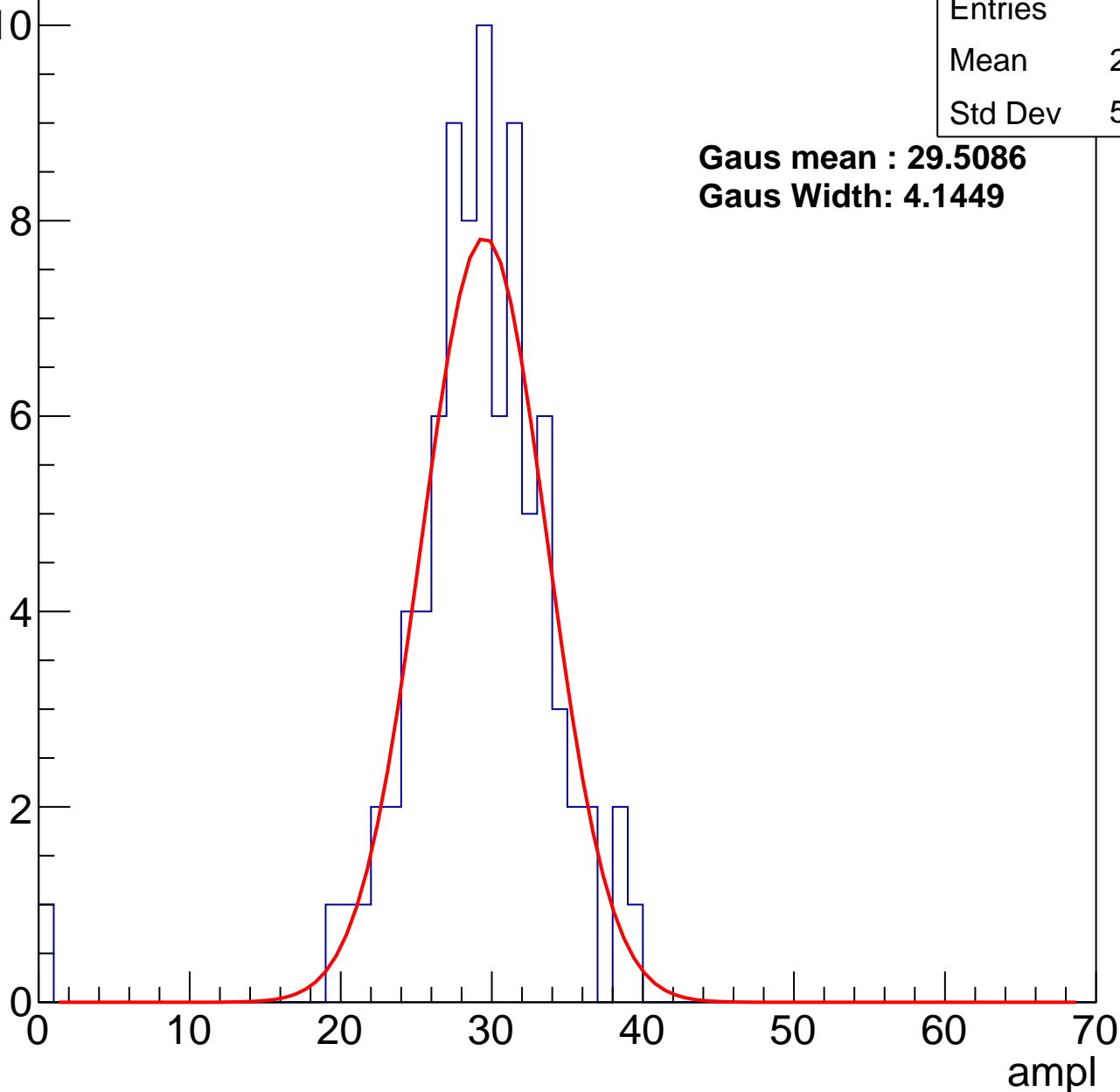
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	28.65
Std Dev	5.084

**Gaus mean : 29.5086**

**Gaus Width: 4.1449**



# B0L000S, U13-ch45, adc1

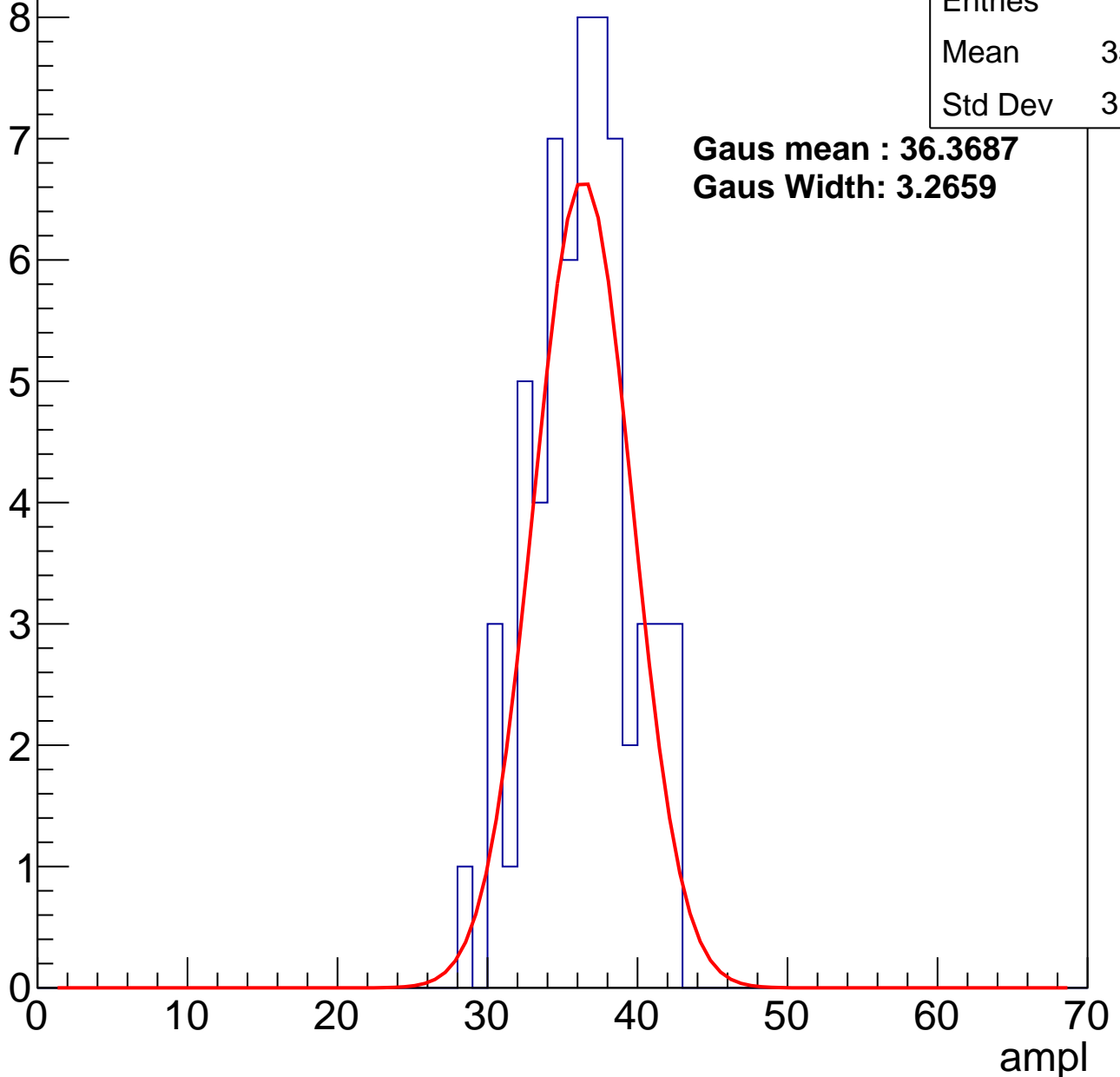
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.84
Std Dev	3.215

**Gaus mean : 36.3687**

**Gaus Width: 3.2659**



# B0L000S, U13-ch45, adc2

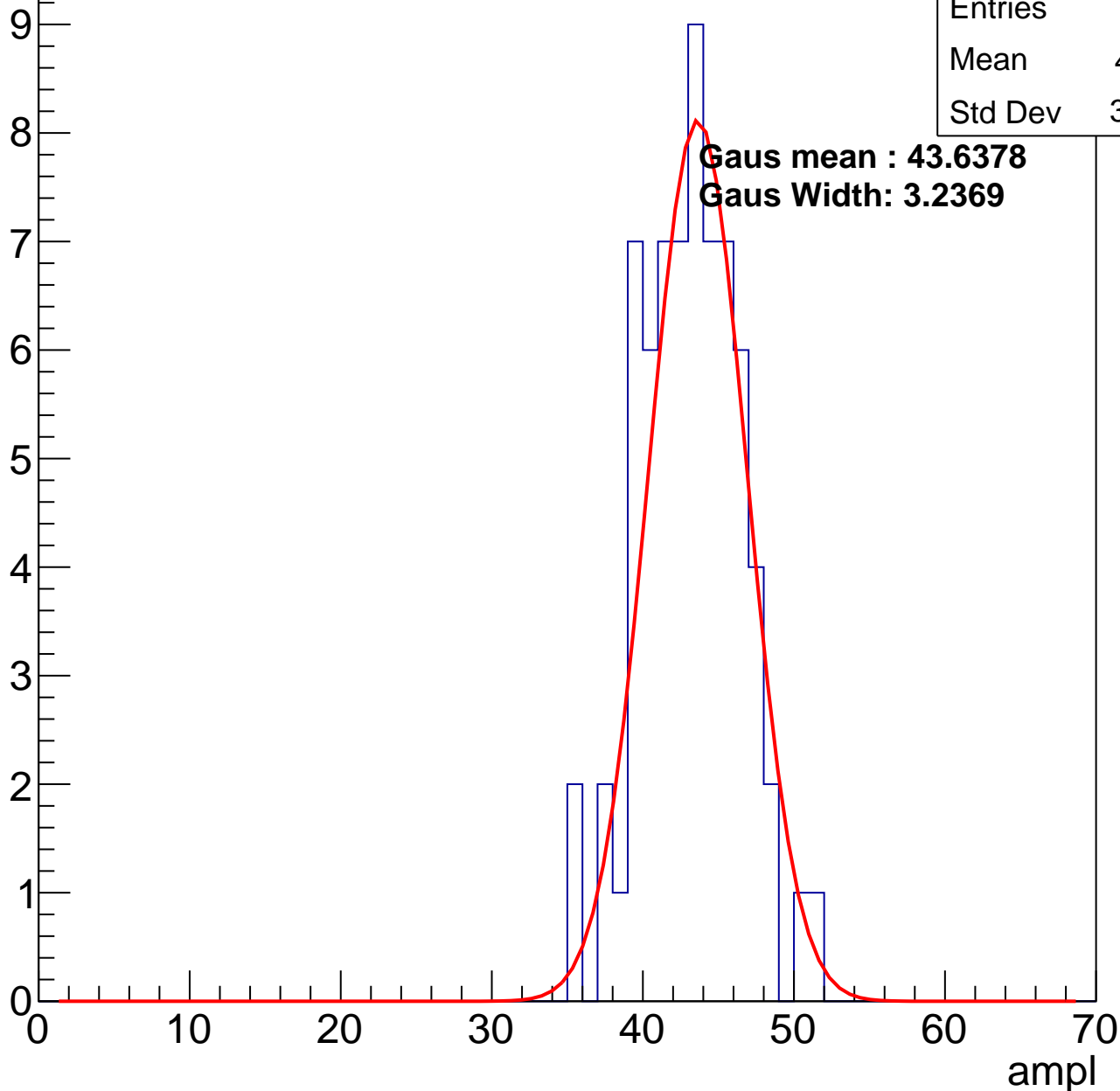
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.71
Std Dev	3.266

**Gaus mean : 43.6378**

**Gaus Width: 3.2369**



# B0L000S, U13-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	50.1
Std Dev	3.536

Entry

10

8

6

4

2

0

0

10

20

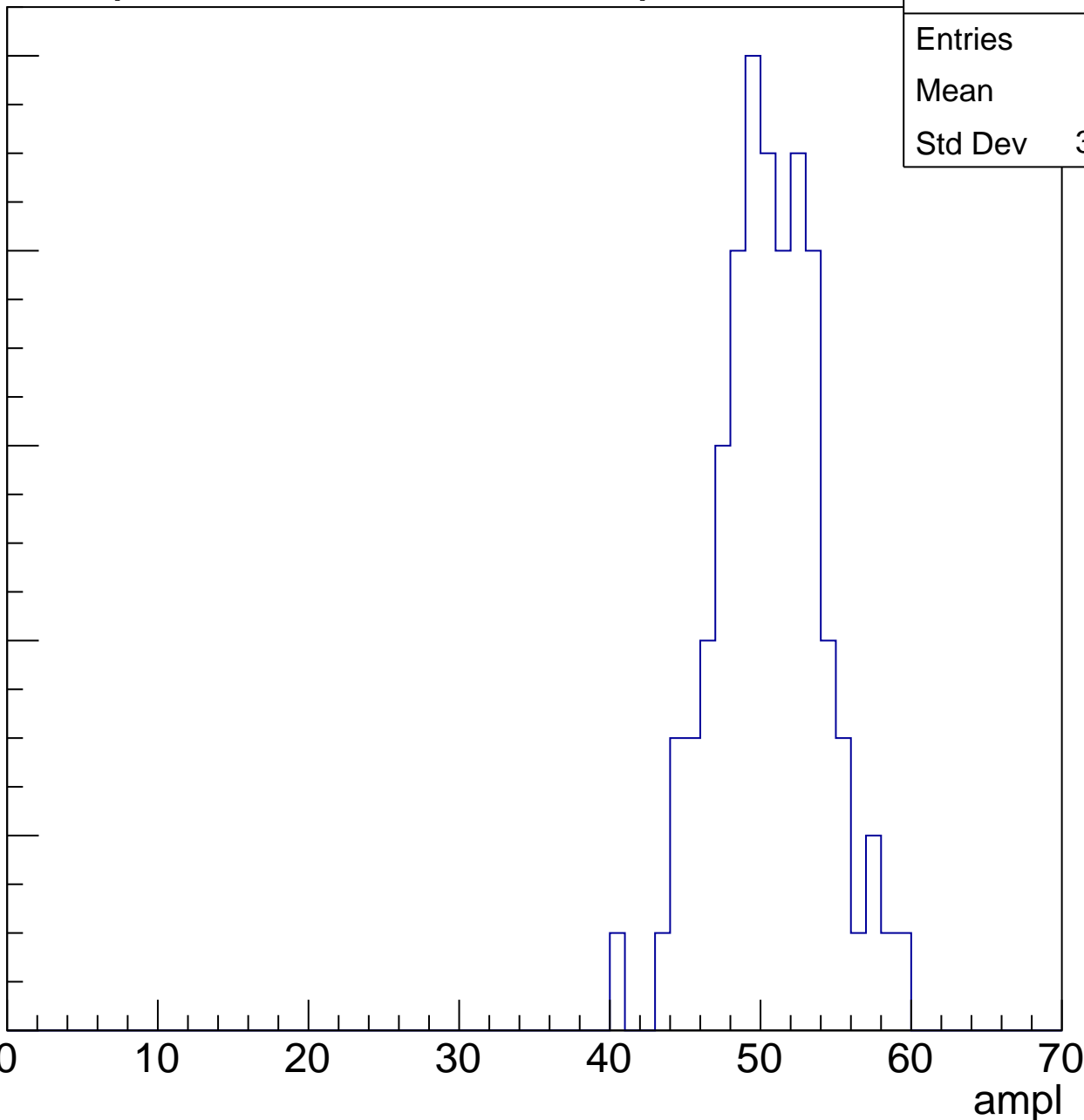
30

40

50

60

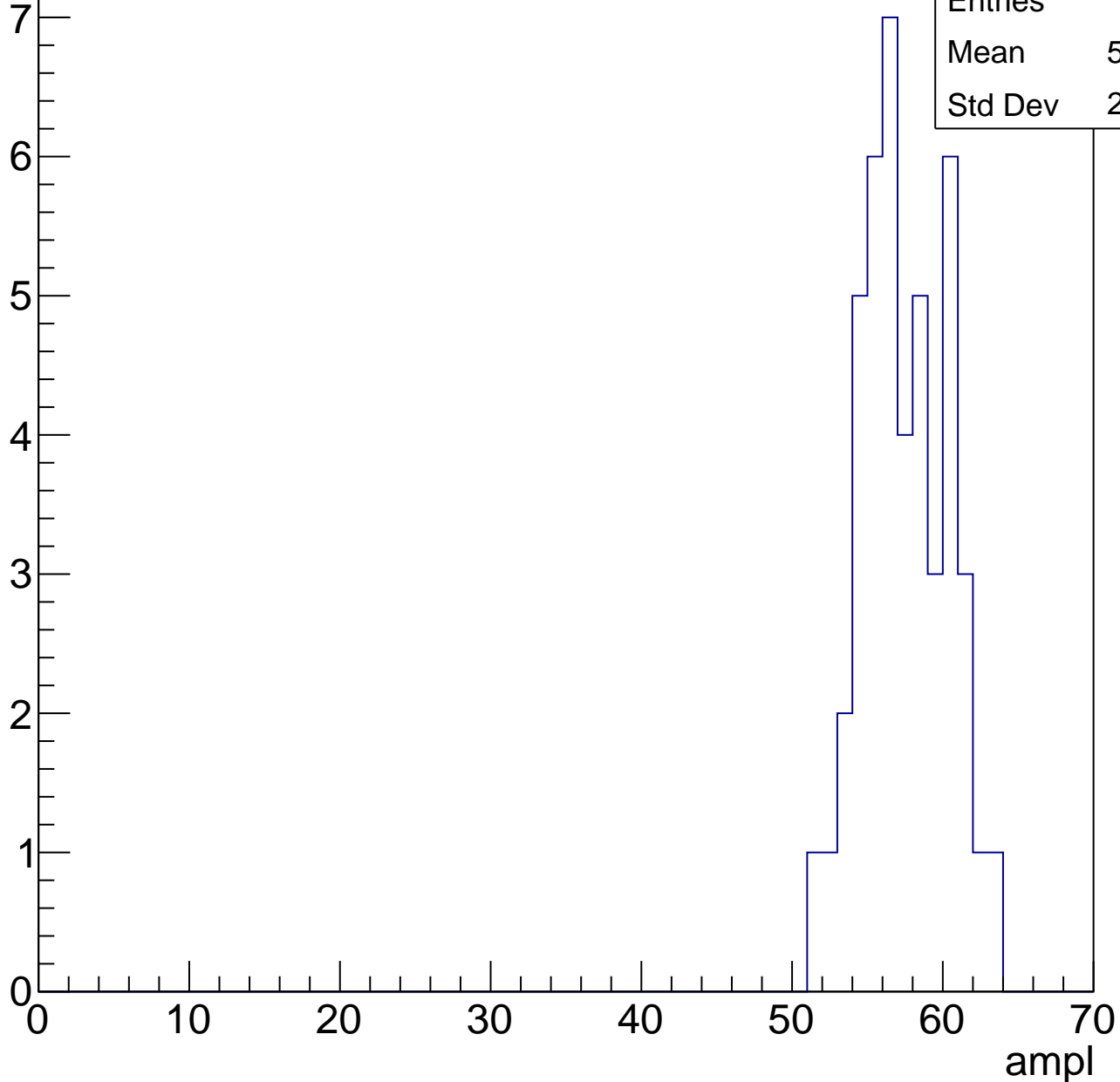
ampl



# B0L000S, U13-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



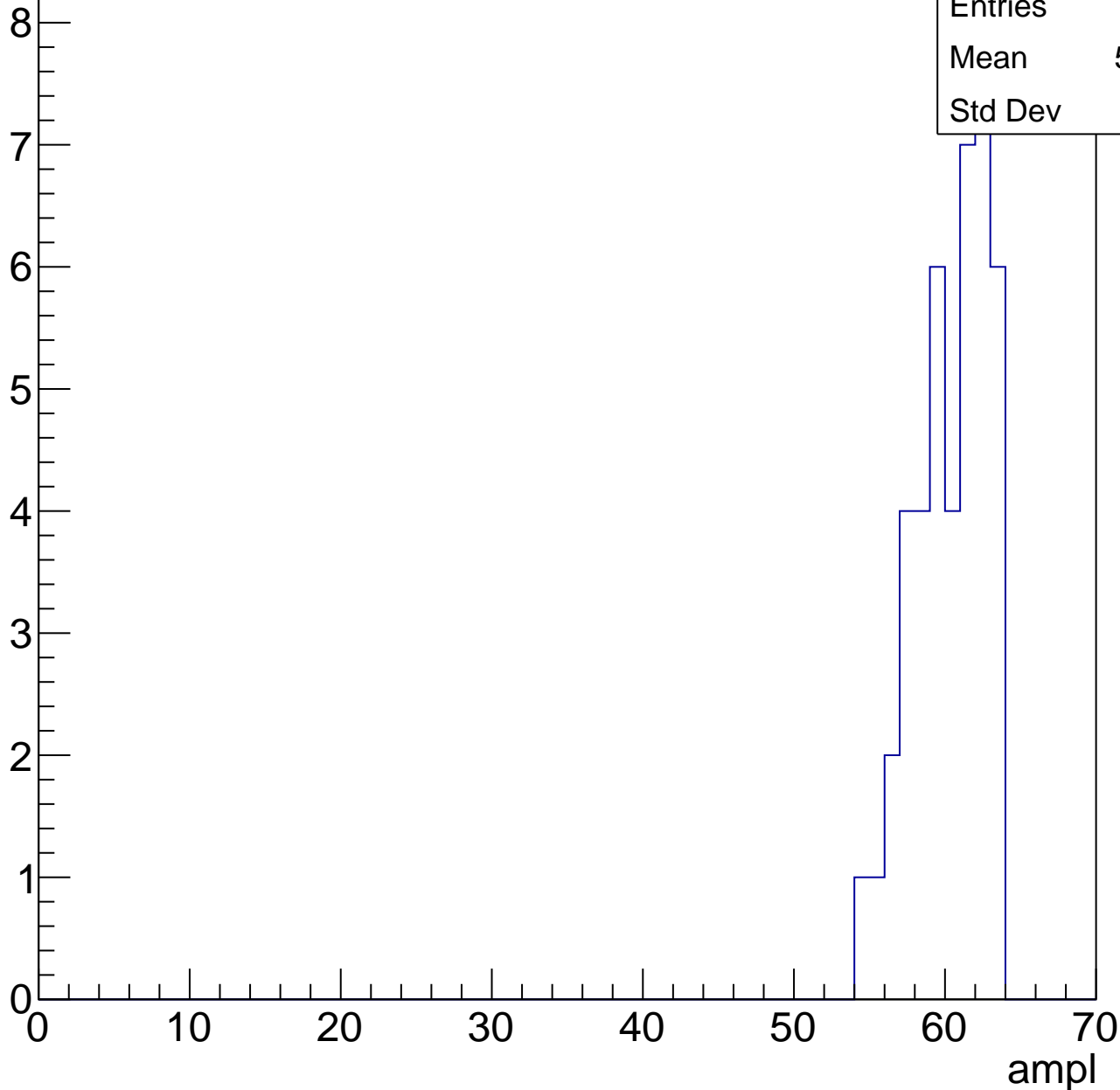
Entries	45
Mean	56.98
Std Dev	2.785

# B0L000S, U13-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	59.91
Std Dev	2.38



# B0L000S, U13-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	29.32
Std Dev	6.798

**Gaus mean : 31.1342**

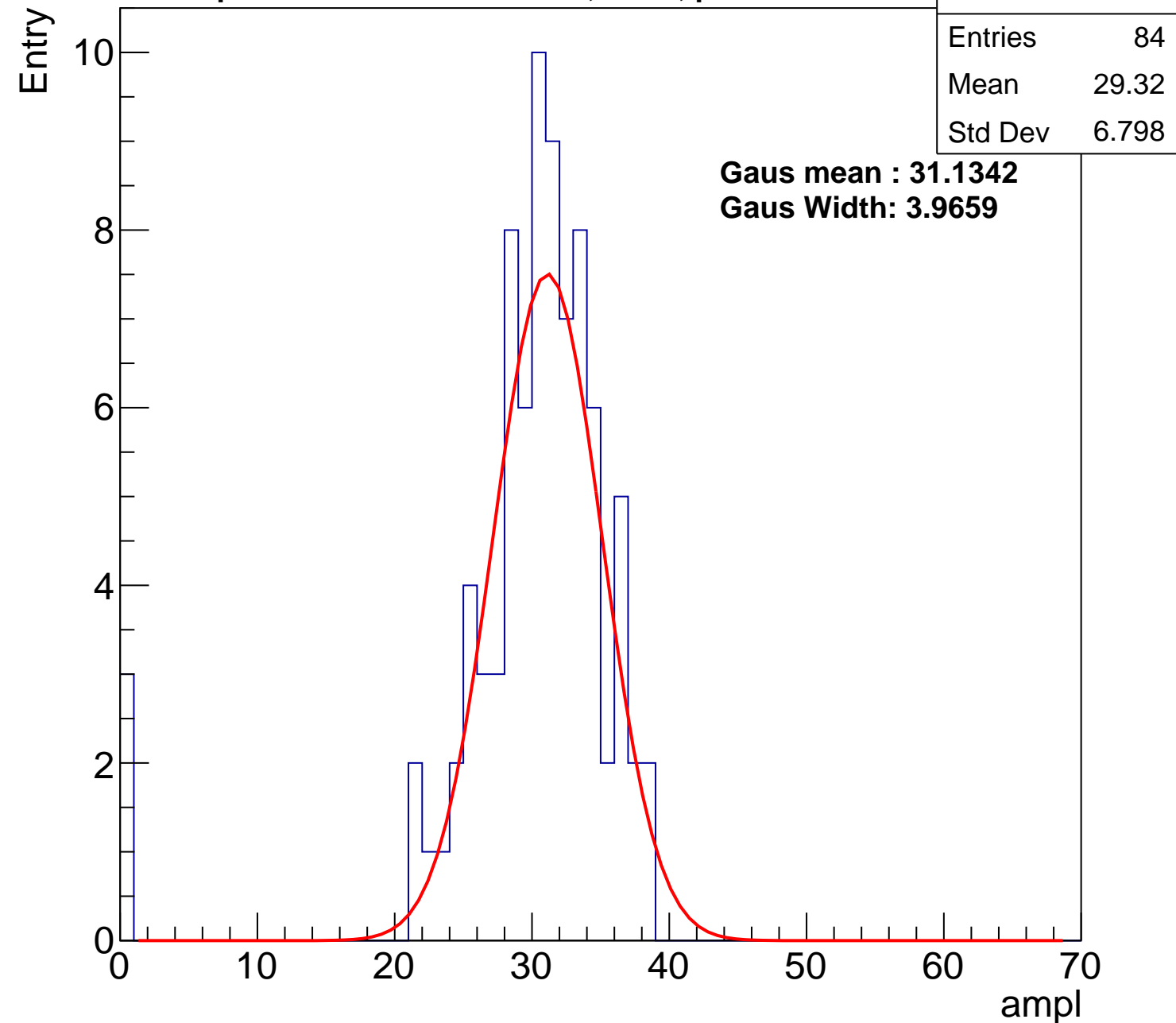
**Gaus Width: 3.9659**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch46, adc1

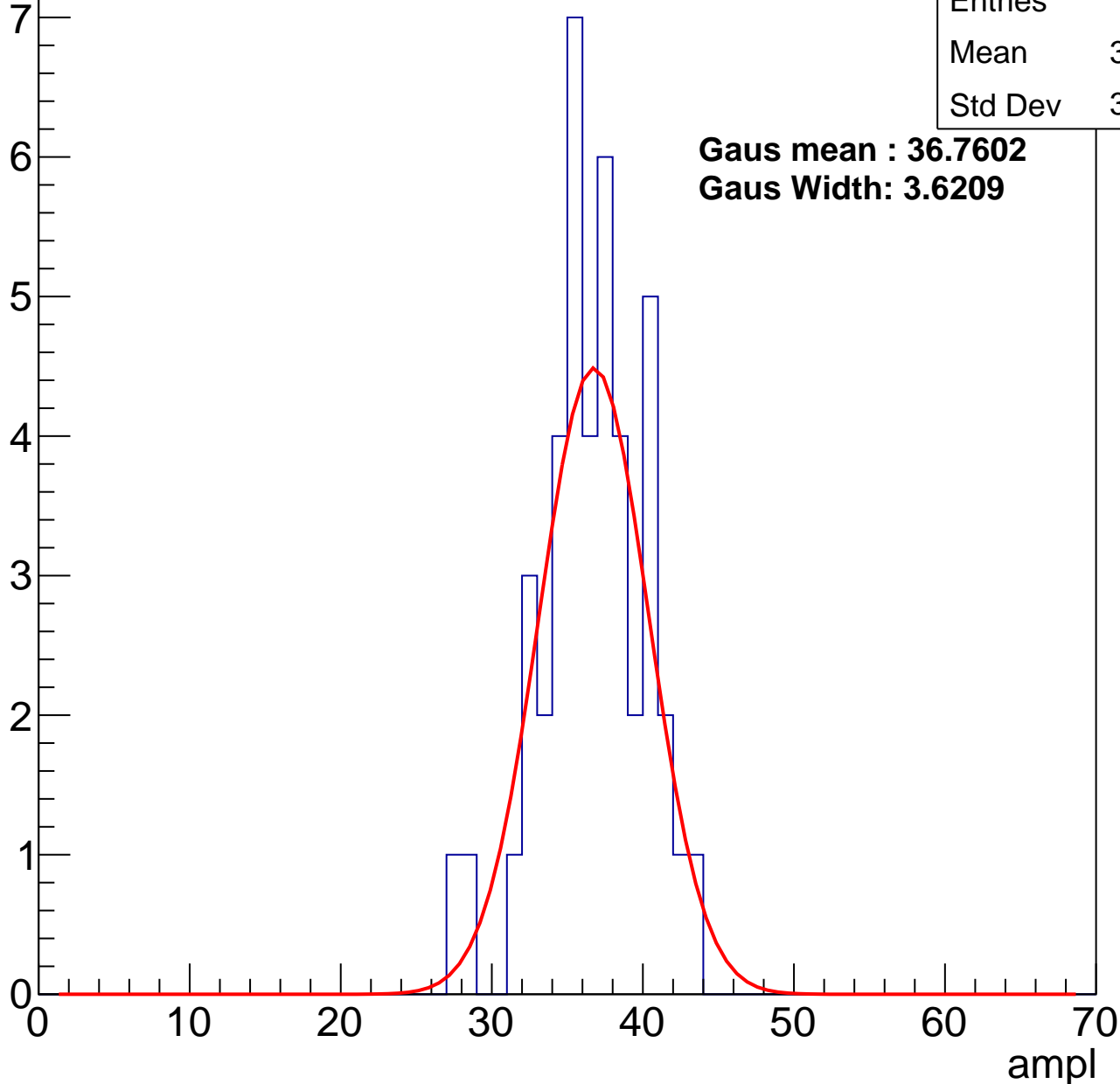
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	36.18
Std Dev	3.413

**Gaus mean : 36.7602**

**Gaus Width: 3.6209**



# B0L000S, U13-ch46, adc2

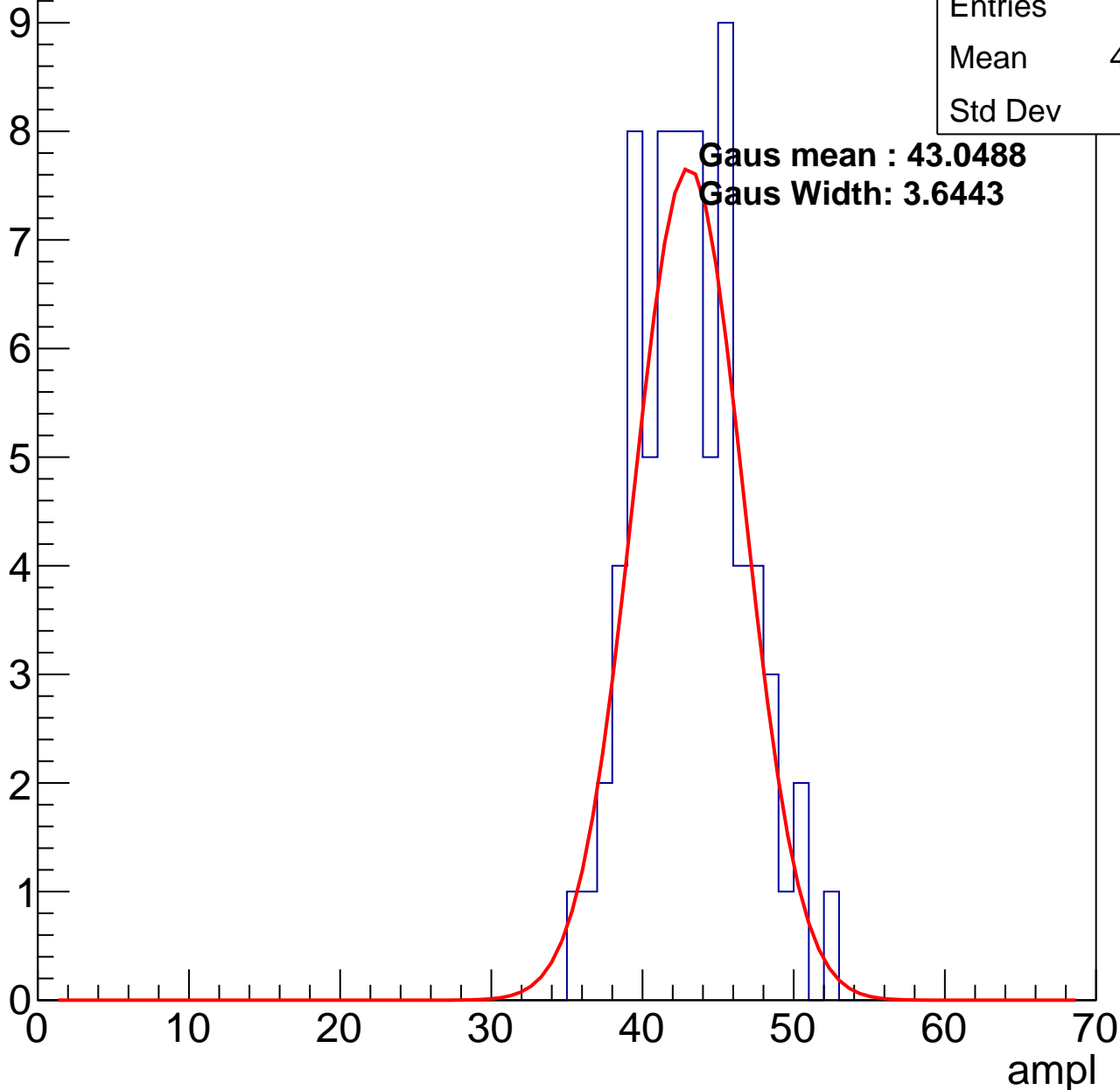
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.69
Std Dev	3.53

**Gaus mean : 43.0488**

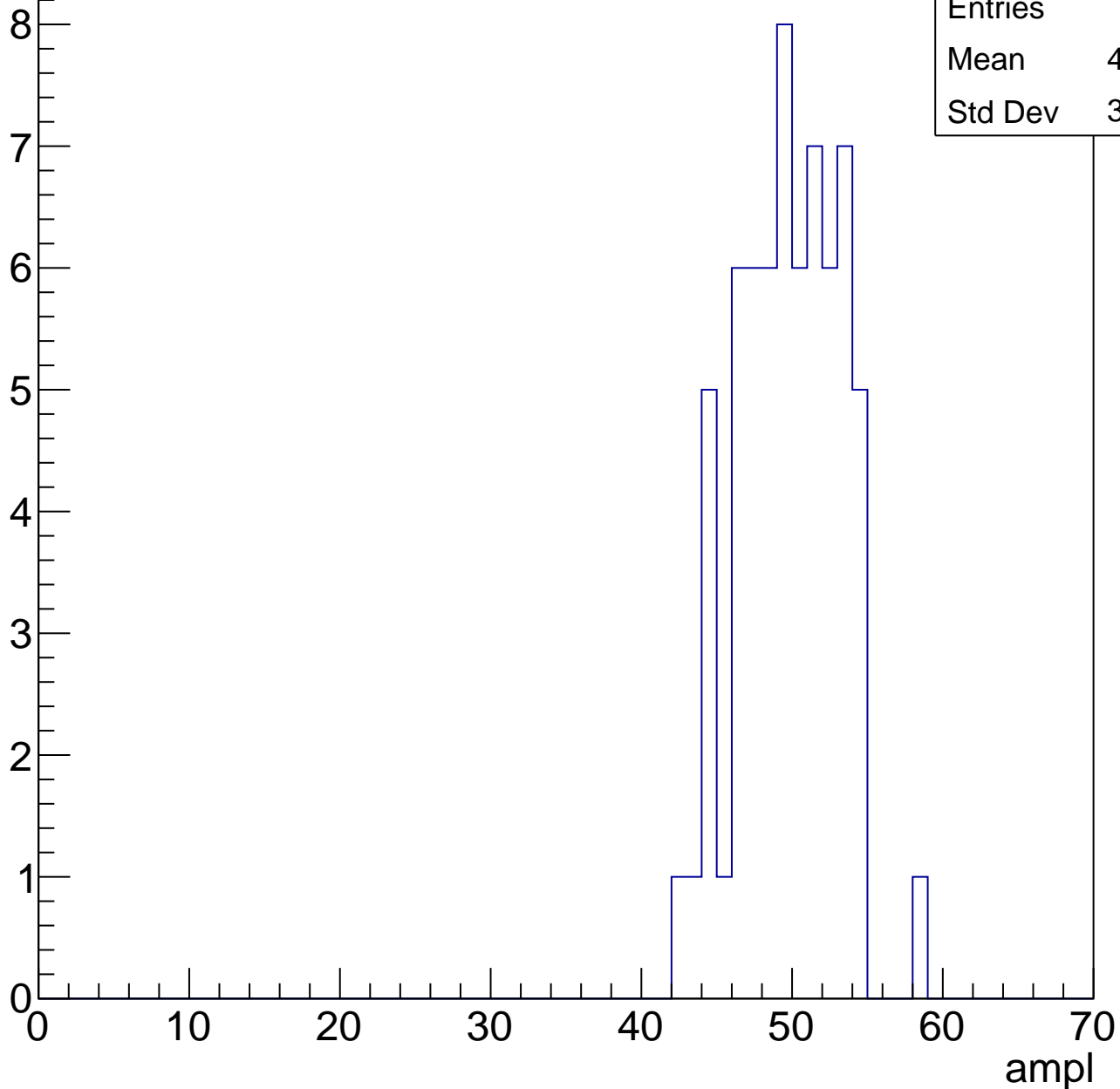
**Gaus Width: 3.6443**



# B0L000S, U13-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



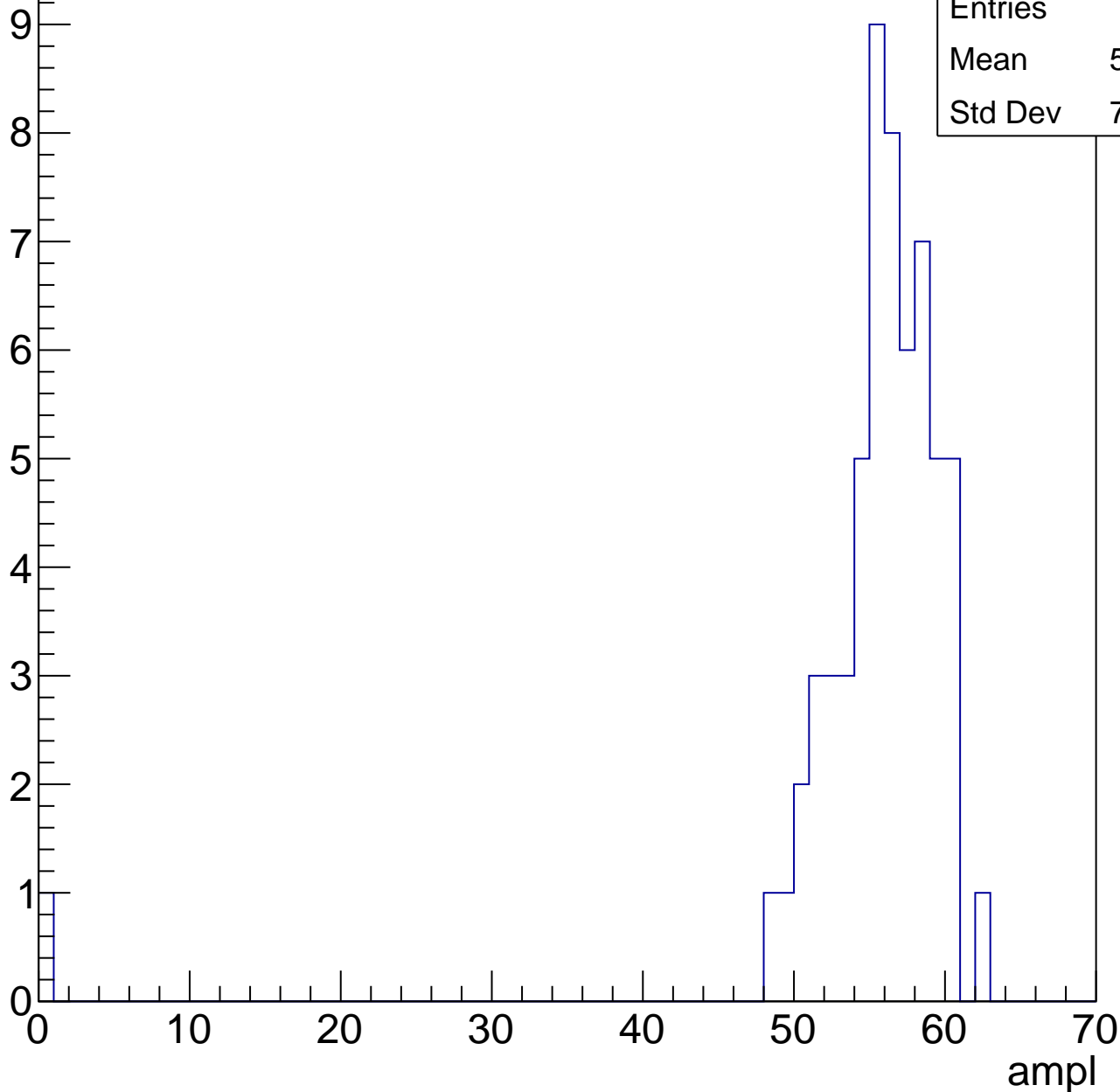
Entries	66
Mean	49.33
Std Dev	3.277

# B0L000S, U13-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.72
Std Dev	7.742



# B0L000S, U13-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	47
Mean	60.45
Std Dev	2.181

Entry

10

8

6

4

2

0

0

10

20

30

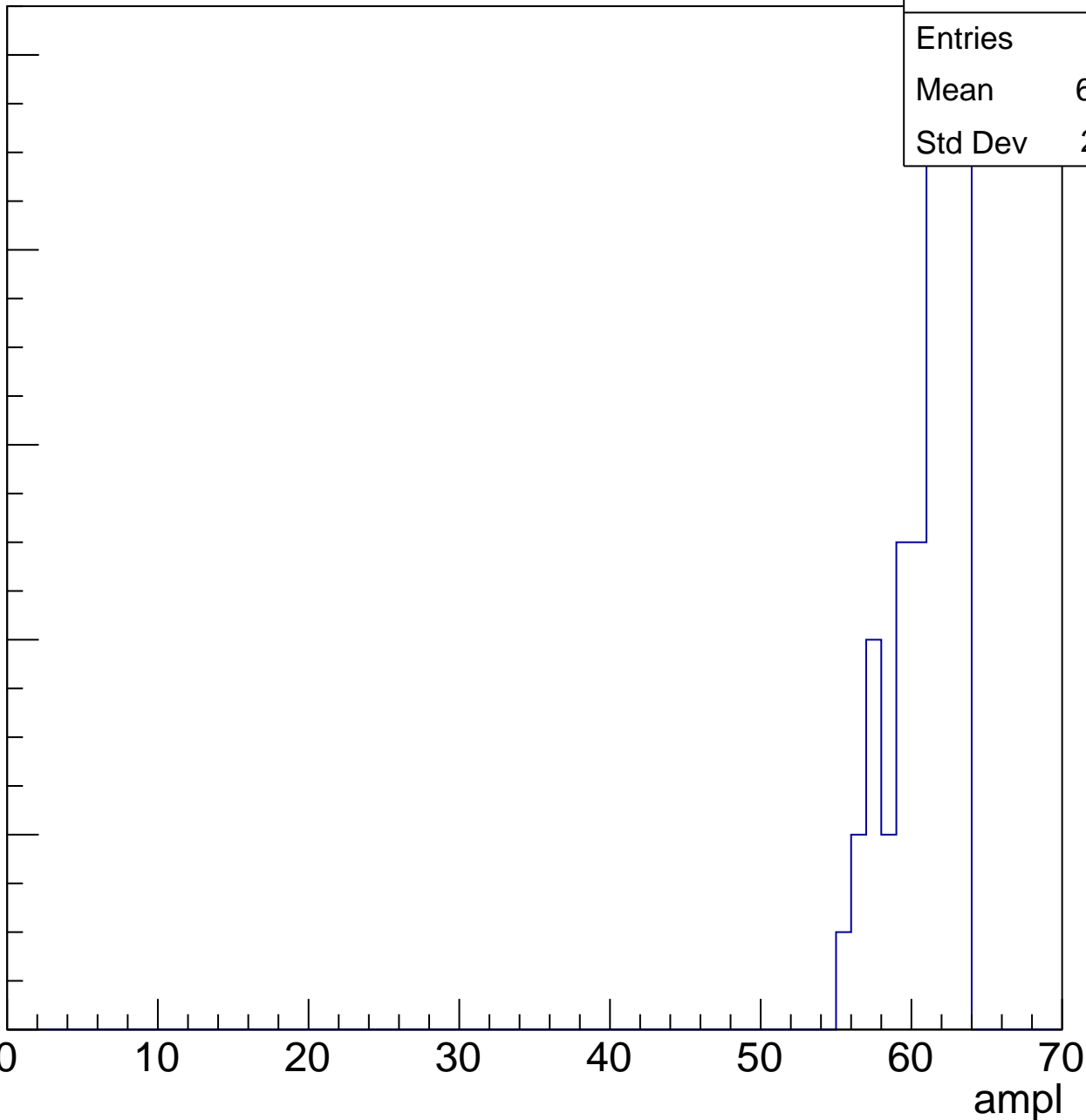
40

50

60

70

ampl



# B0L000S, U13-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	30.21
Std Dev	3.541

**Gaus mean : 30.5666**

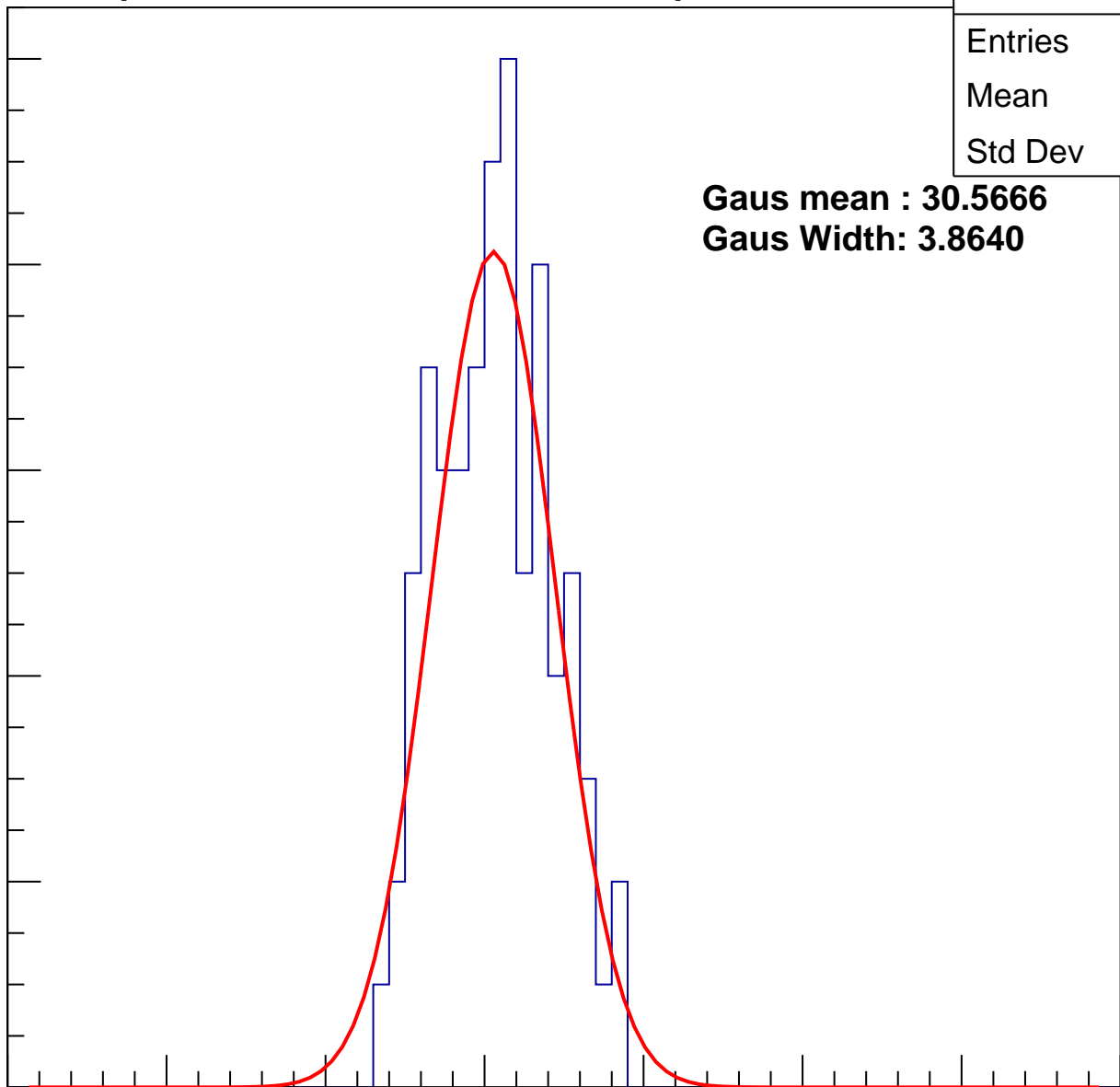
**Gaus Width: 3.8640**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch47, adc1

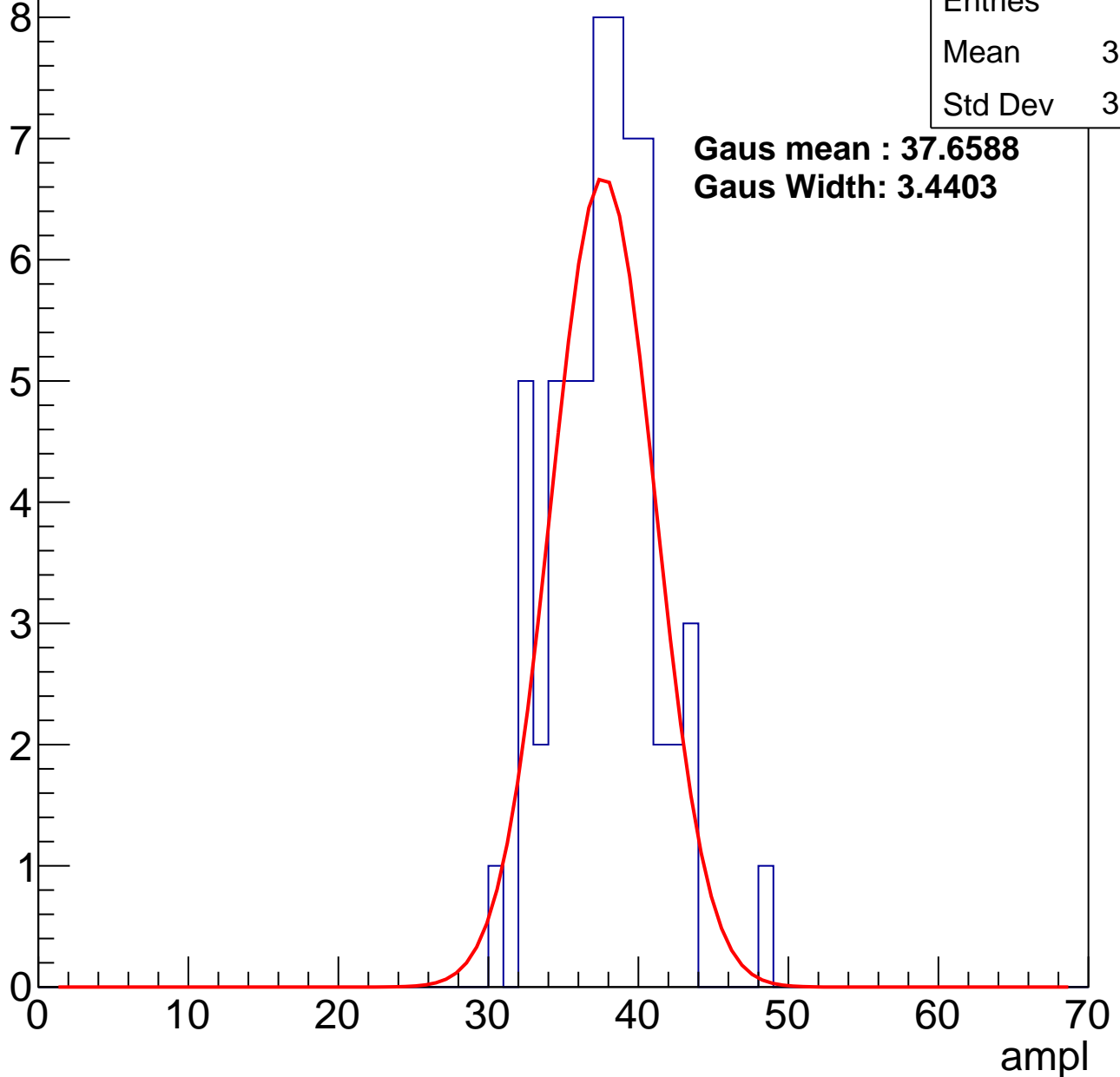
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.33
Std Dev	3.337

**Gaus mean : 37.6588**

**Gaus Width: 3.4403**



# B0L000S, U13-ch47, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	44.16
Std Dev	3.721

**Gaus mean : 44.2339**

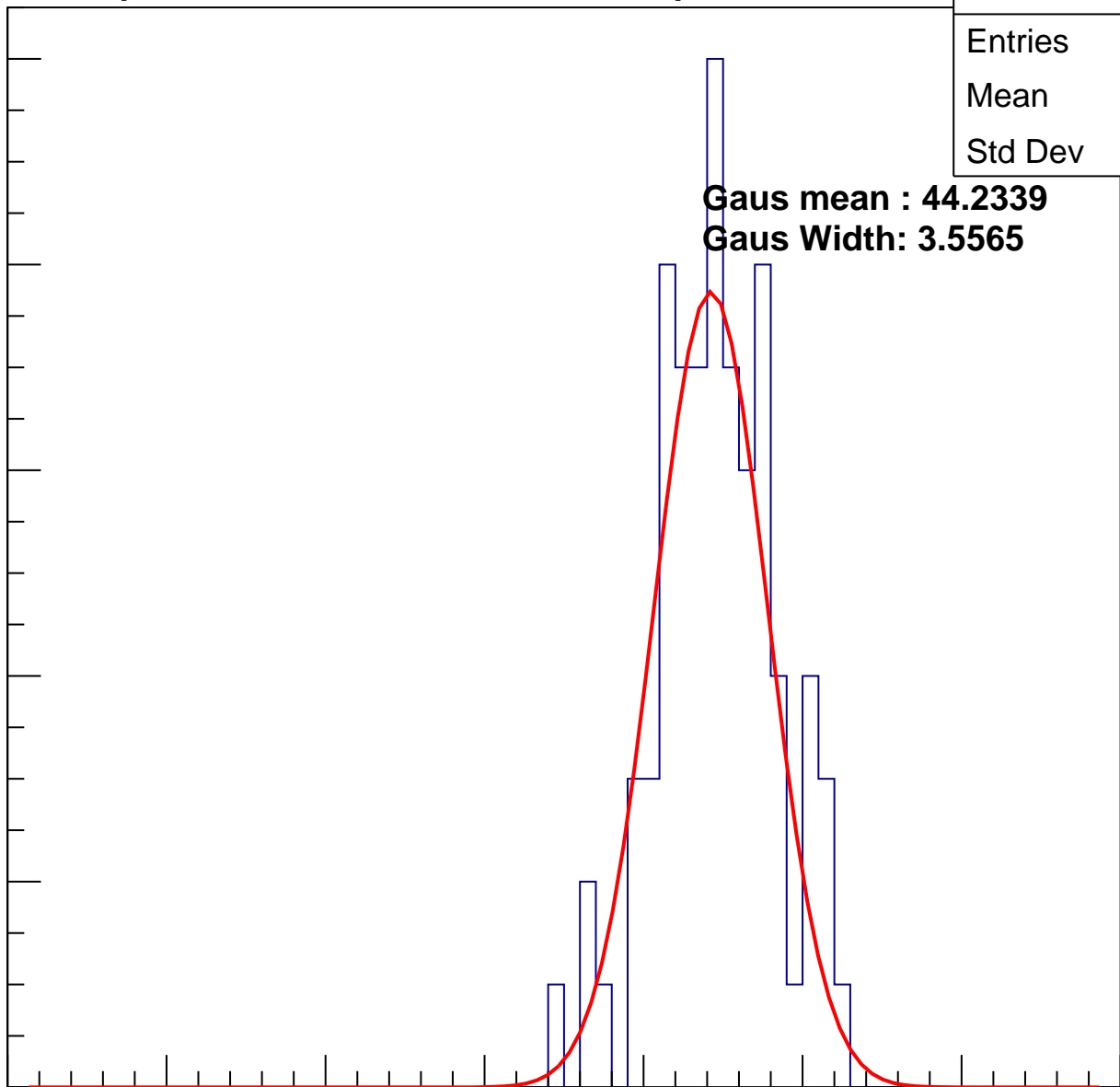
**Gaus Width: 3.5565**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

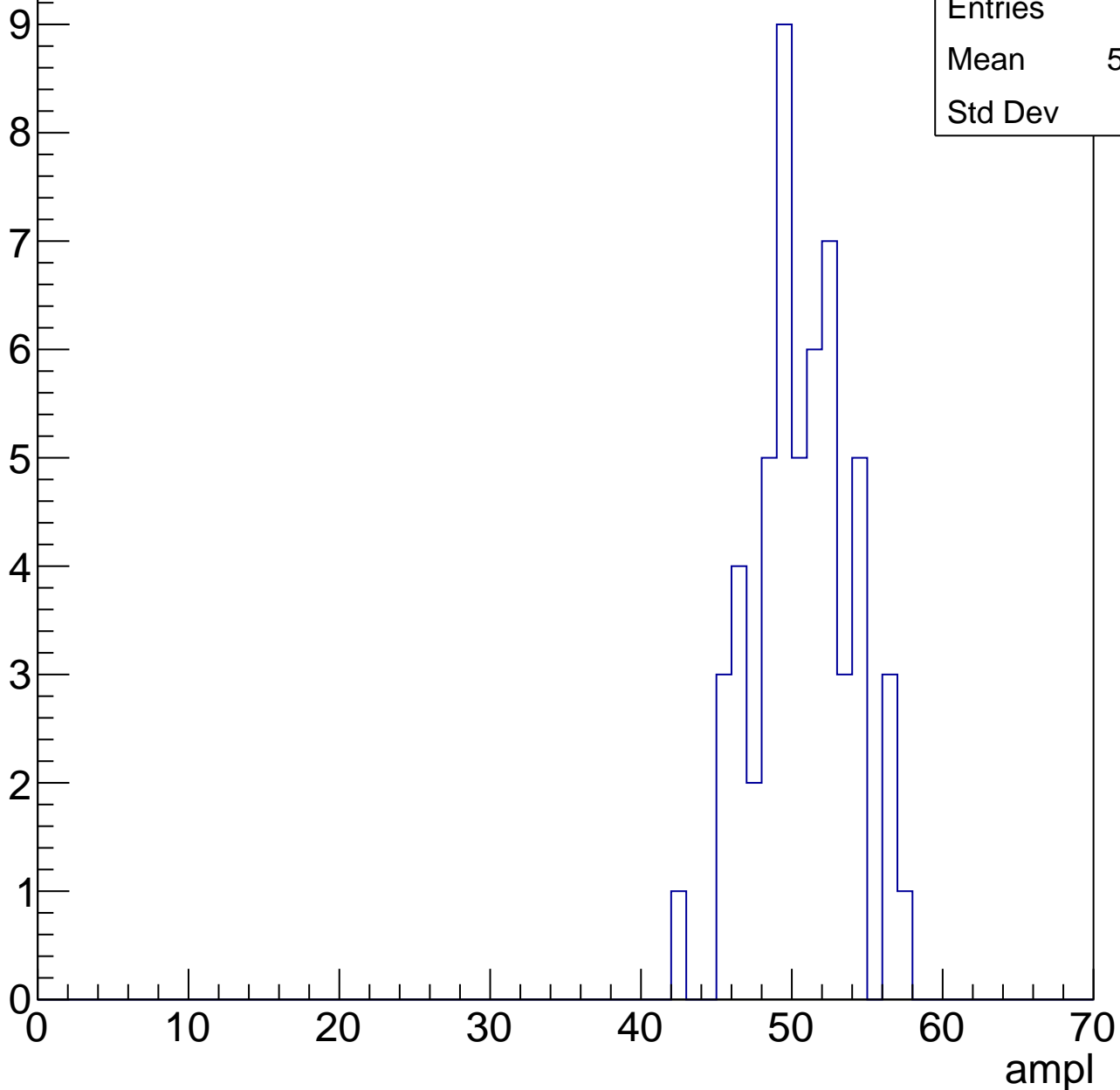


# B0L000S, U13-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

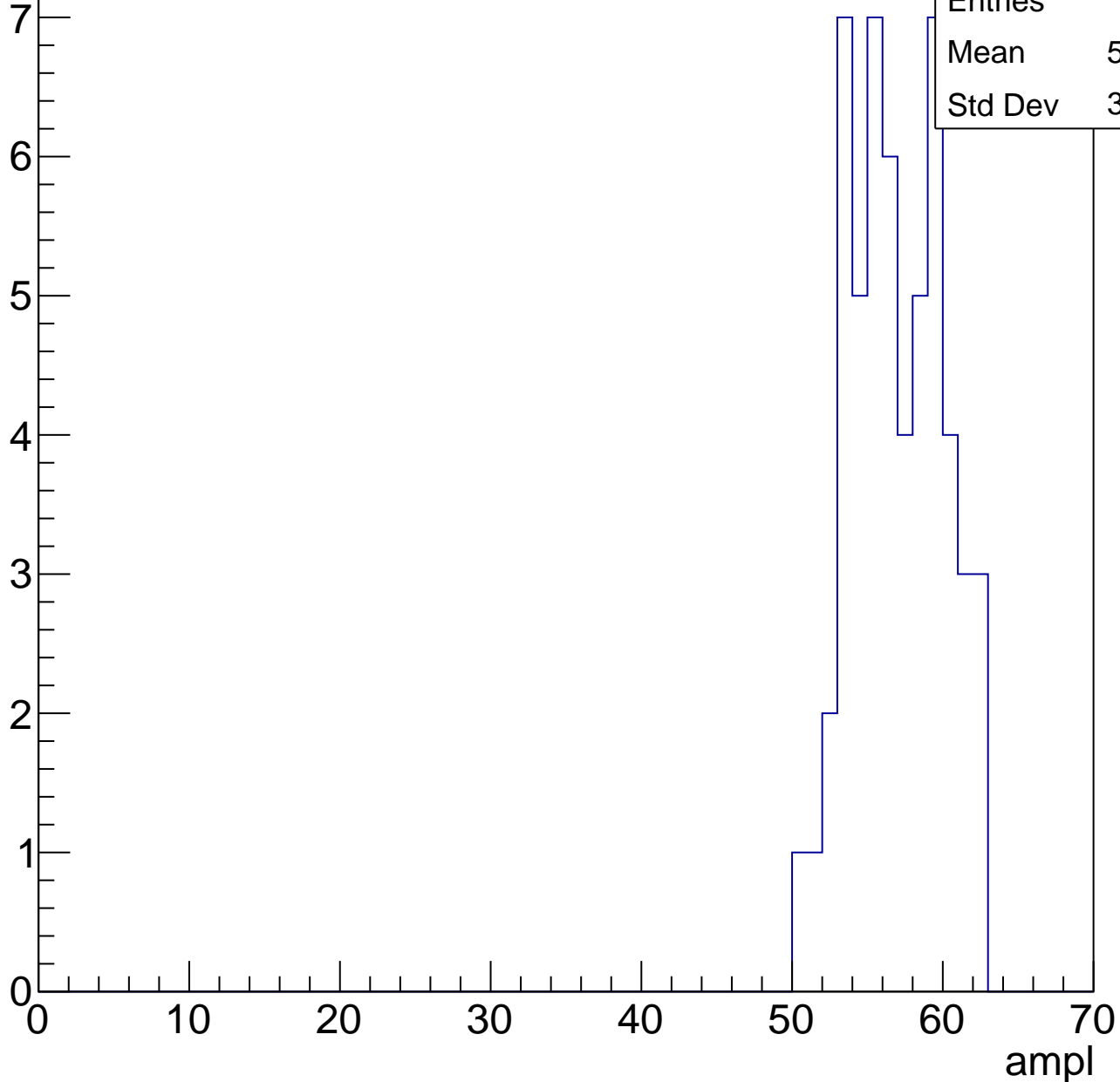
Entries	54
Mean	50.19
Std Dev	3.18



# B0L000S, U13-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



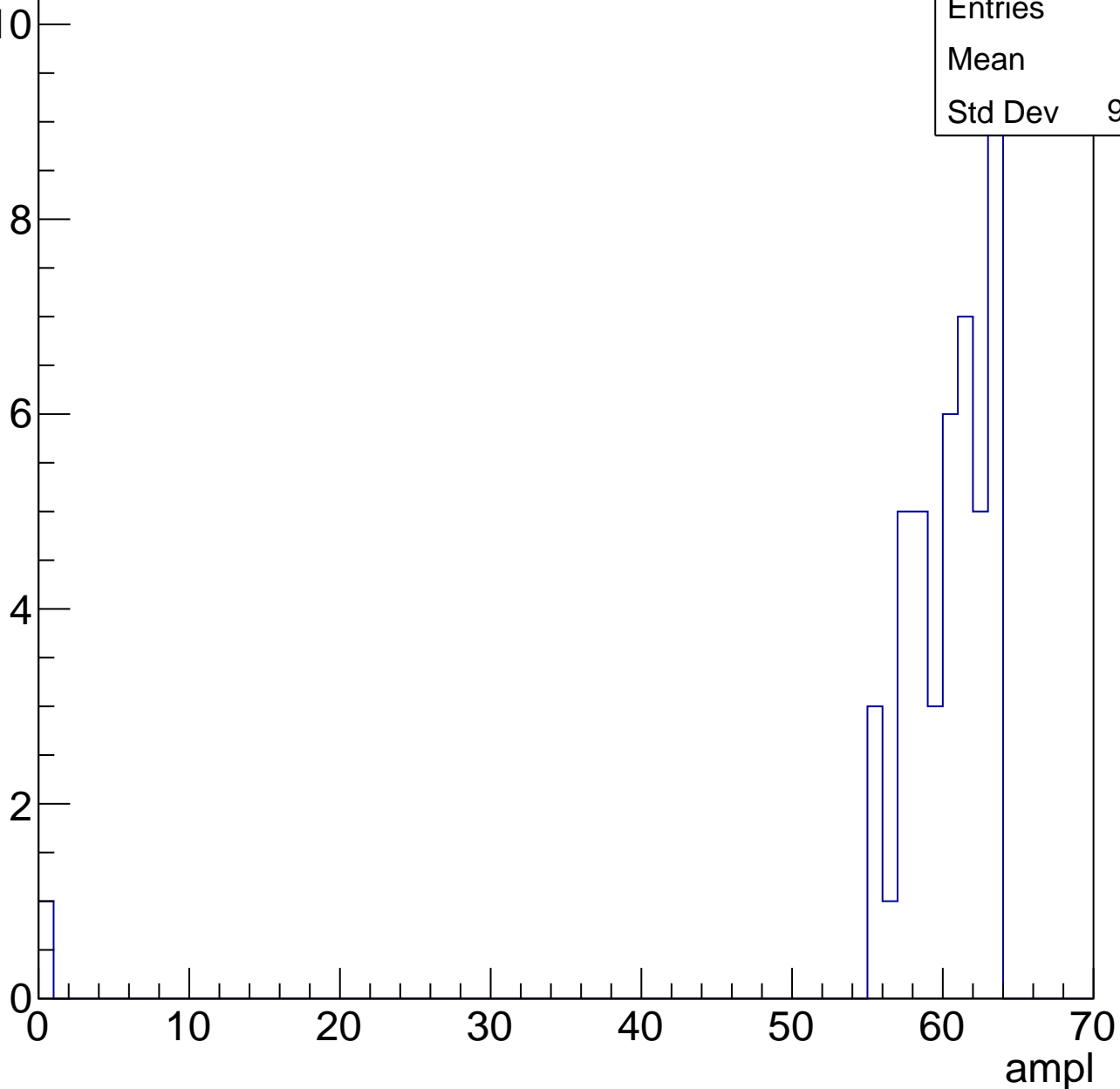
Entries	55
Mean	56.49
Std Dev	3.026

# B0L000S, U13-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.7
Std Dev	9.086



# B0L000S, U13-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	20
Std Dev	0

# B0L000S, U13-ch48, adc0

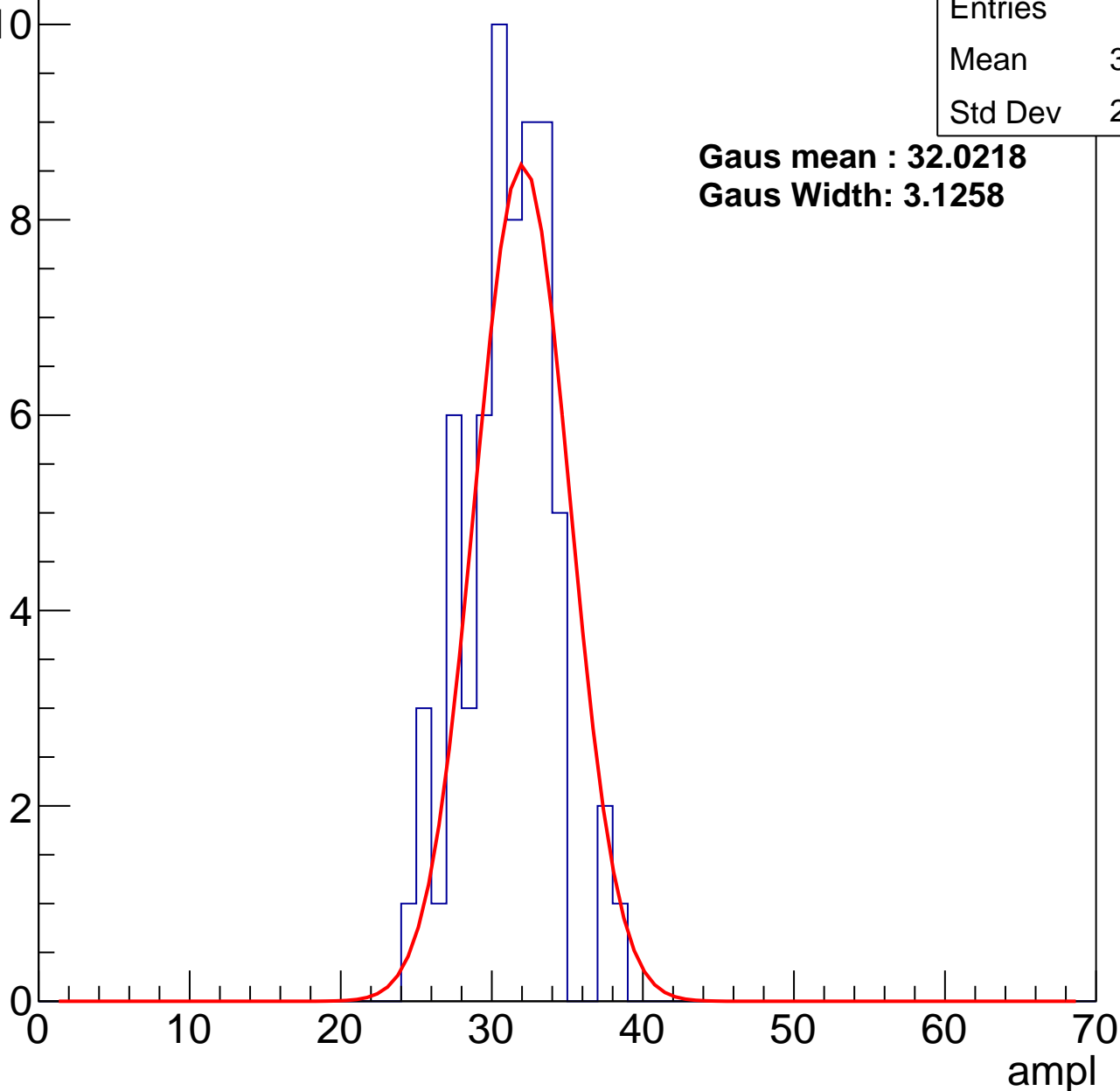
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.62
Std Dev	2.907

**Gaus mean : 32.0218**

**Gaus Width: 3.1258**

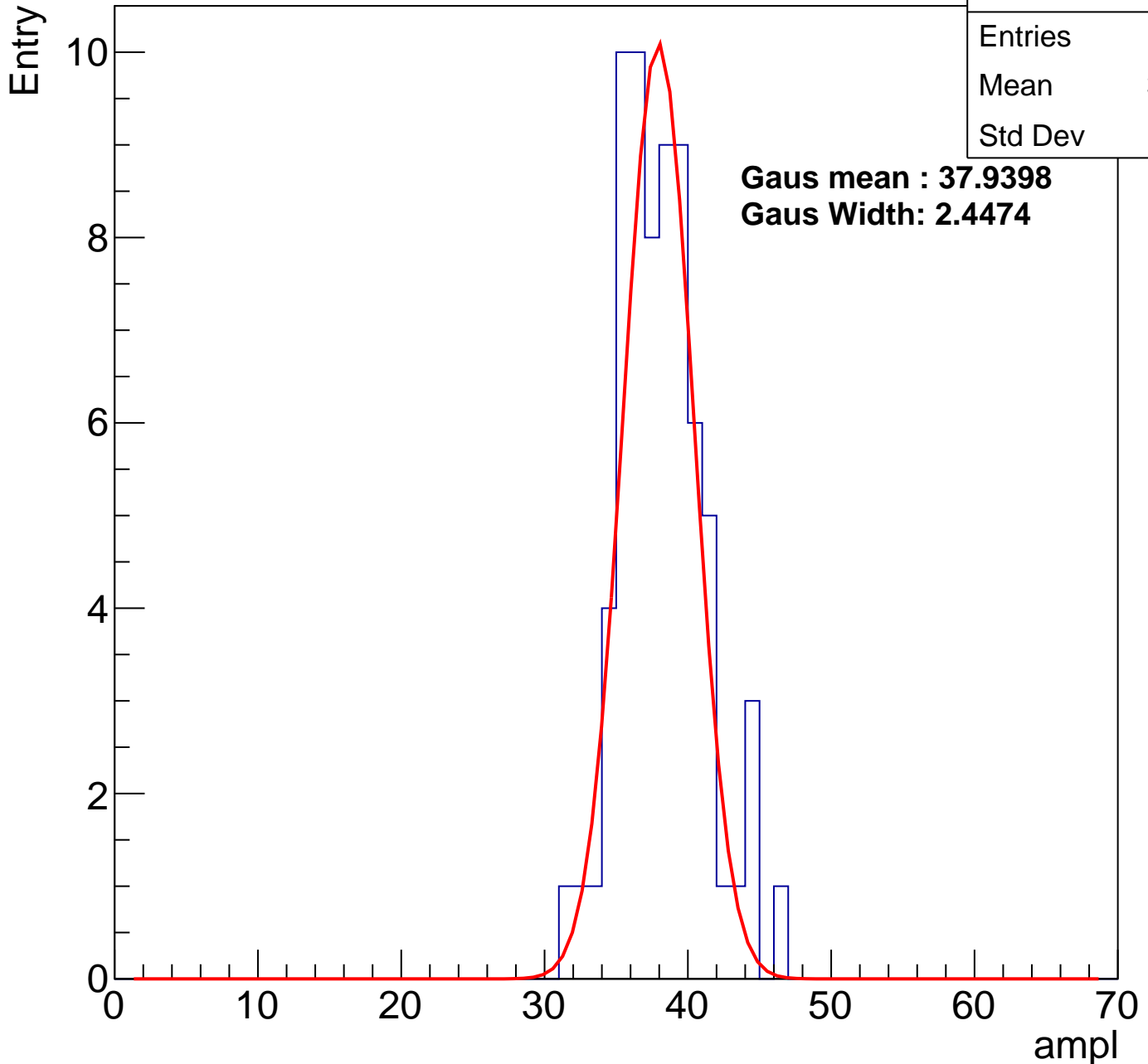


# B0L000S, U13-ch48, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	37.7
Std Dev	2.92

**Gaus mean : 37.9398**  
**Gaus Width: 2.4474**



# B0L000S, U13-ch48, adc2

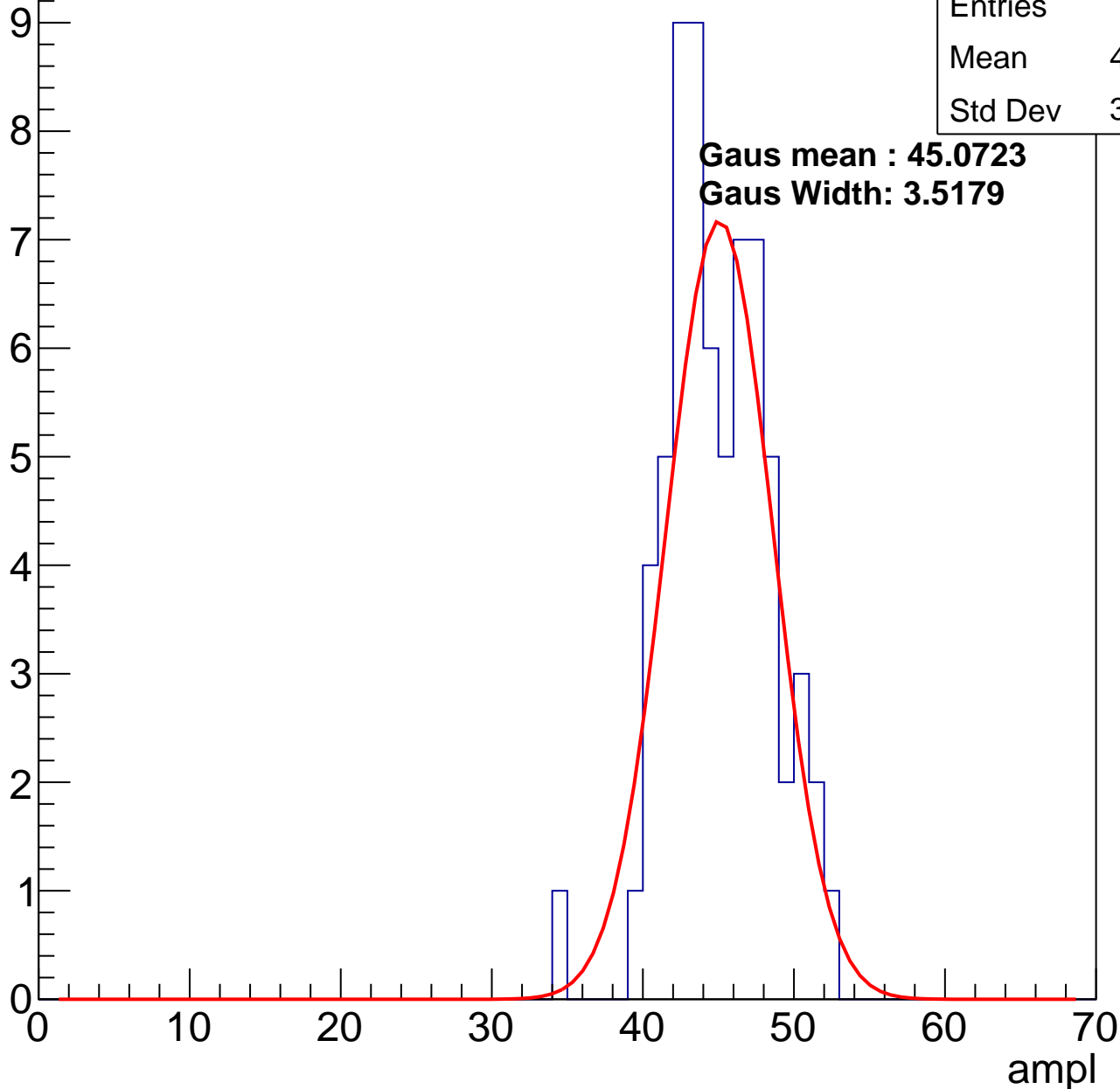
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.55
Std Dev	3.356

**Gaus mean : 45.0723**

**Gaus Width: 3.5179**

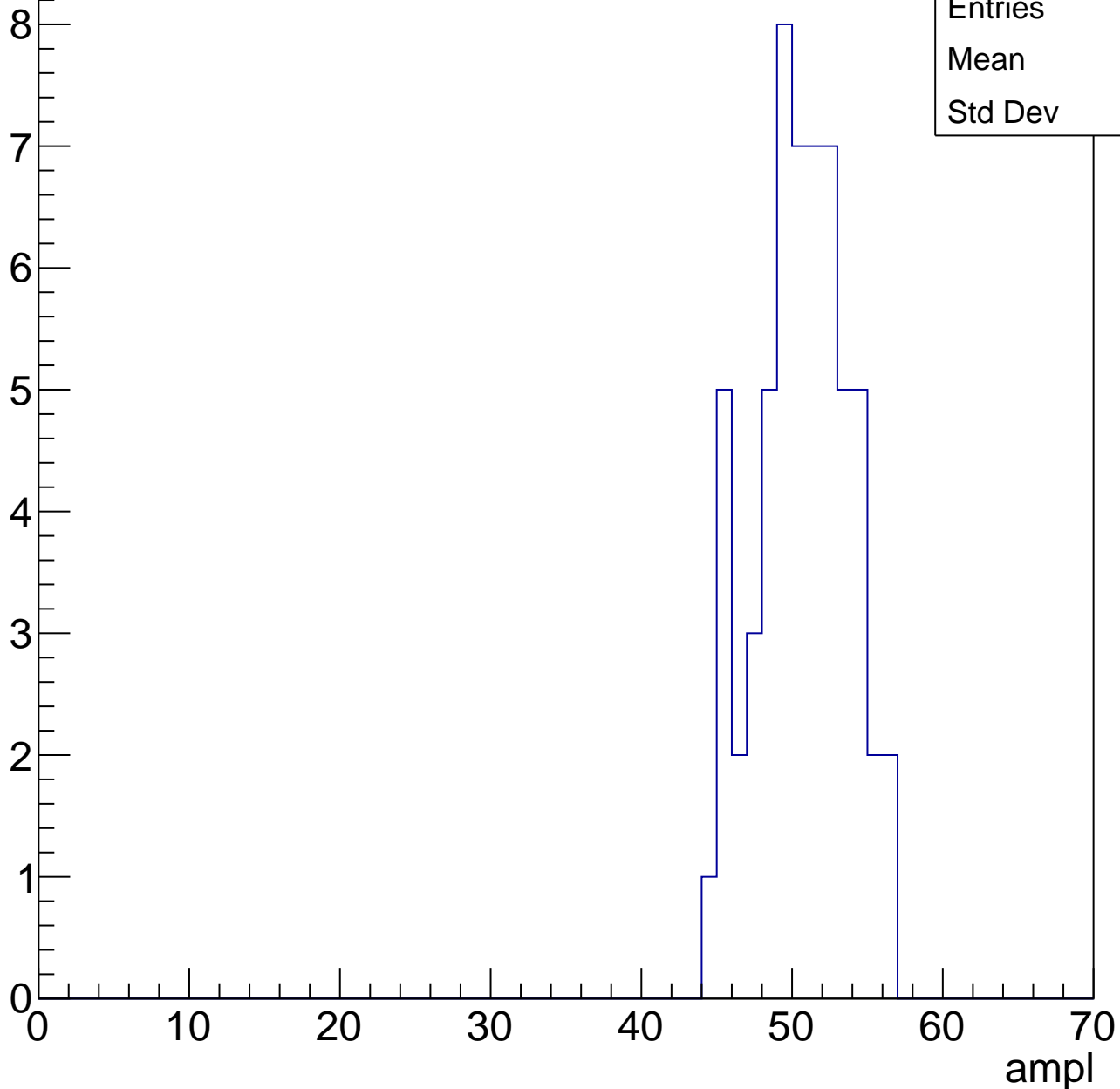


# B0L000S, U13-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

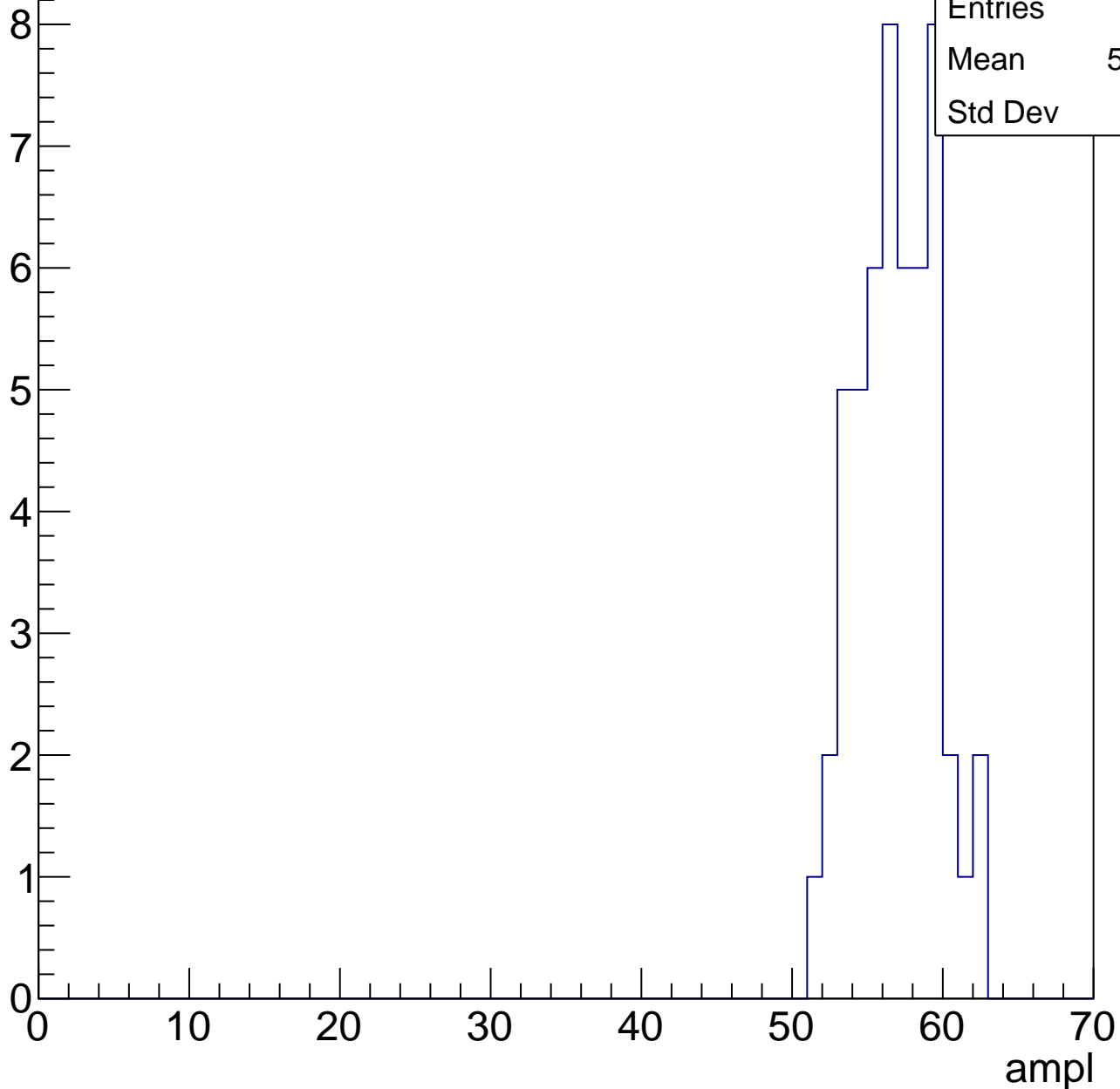
Entries	59
Mean	50.2
Std Dev	2.99



# B0L000S, U13-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

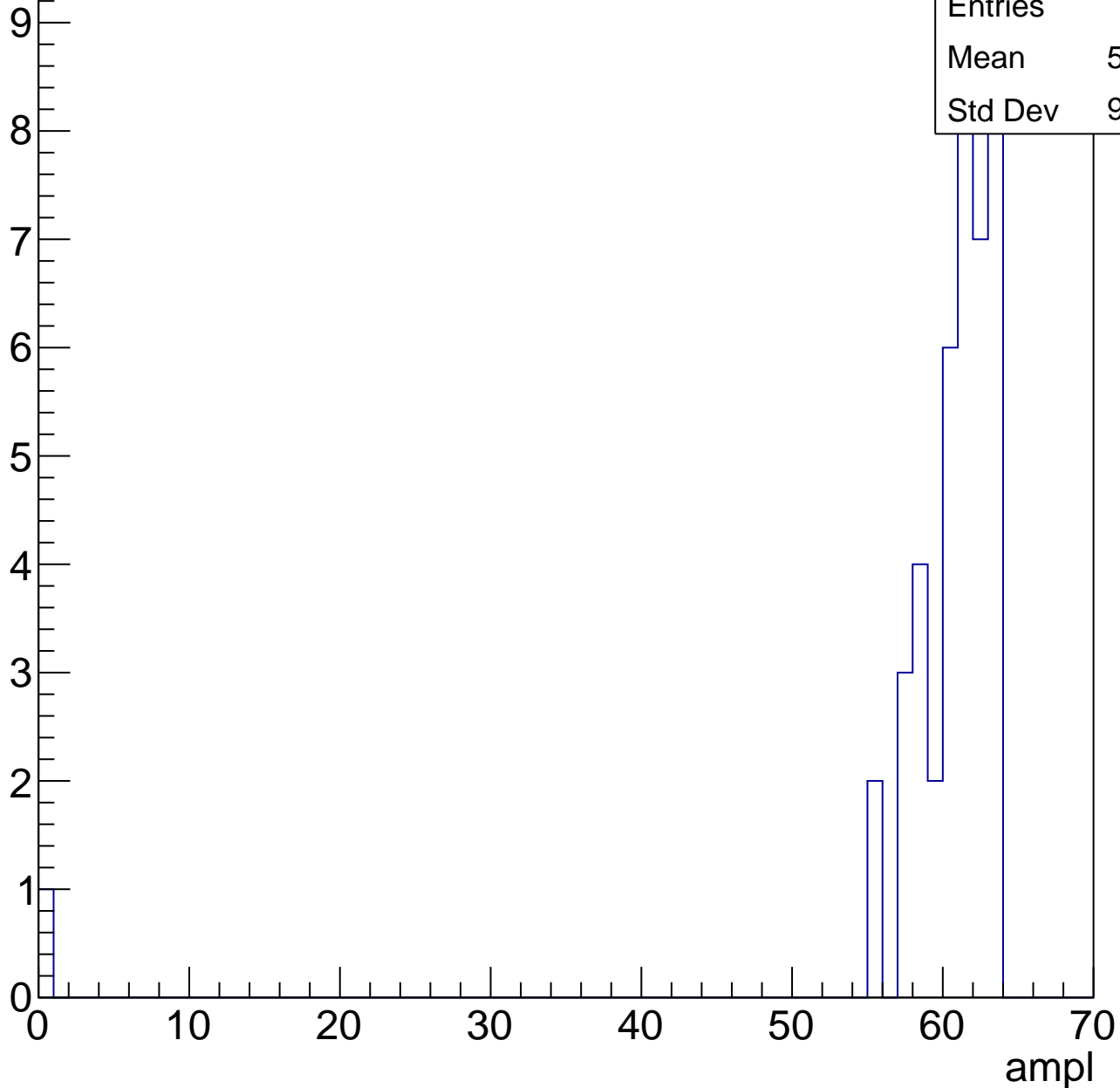


Entries	52
Mean	56.44
Std Dev	2.59

# B0L000S, U13-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

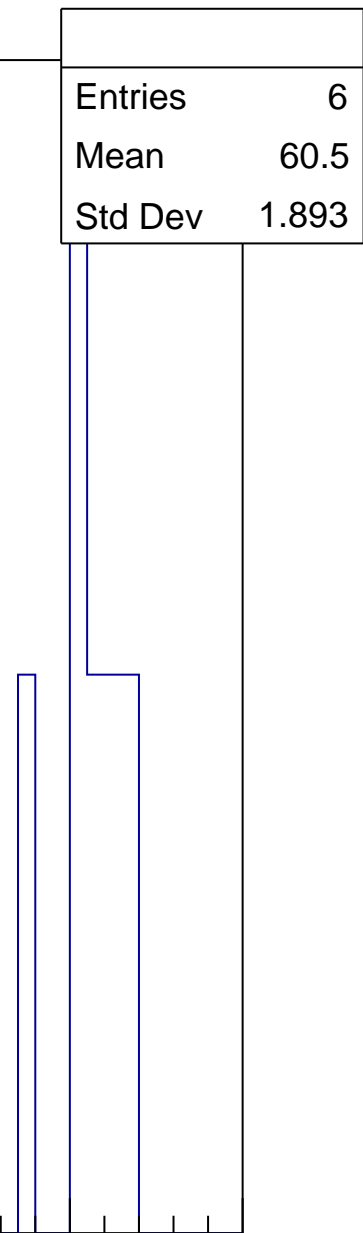
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	60.5
Std Dev	1.893

ampl

0 10 20 30 40 50 60 70





# B0L000S, U13-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch49, adc0

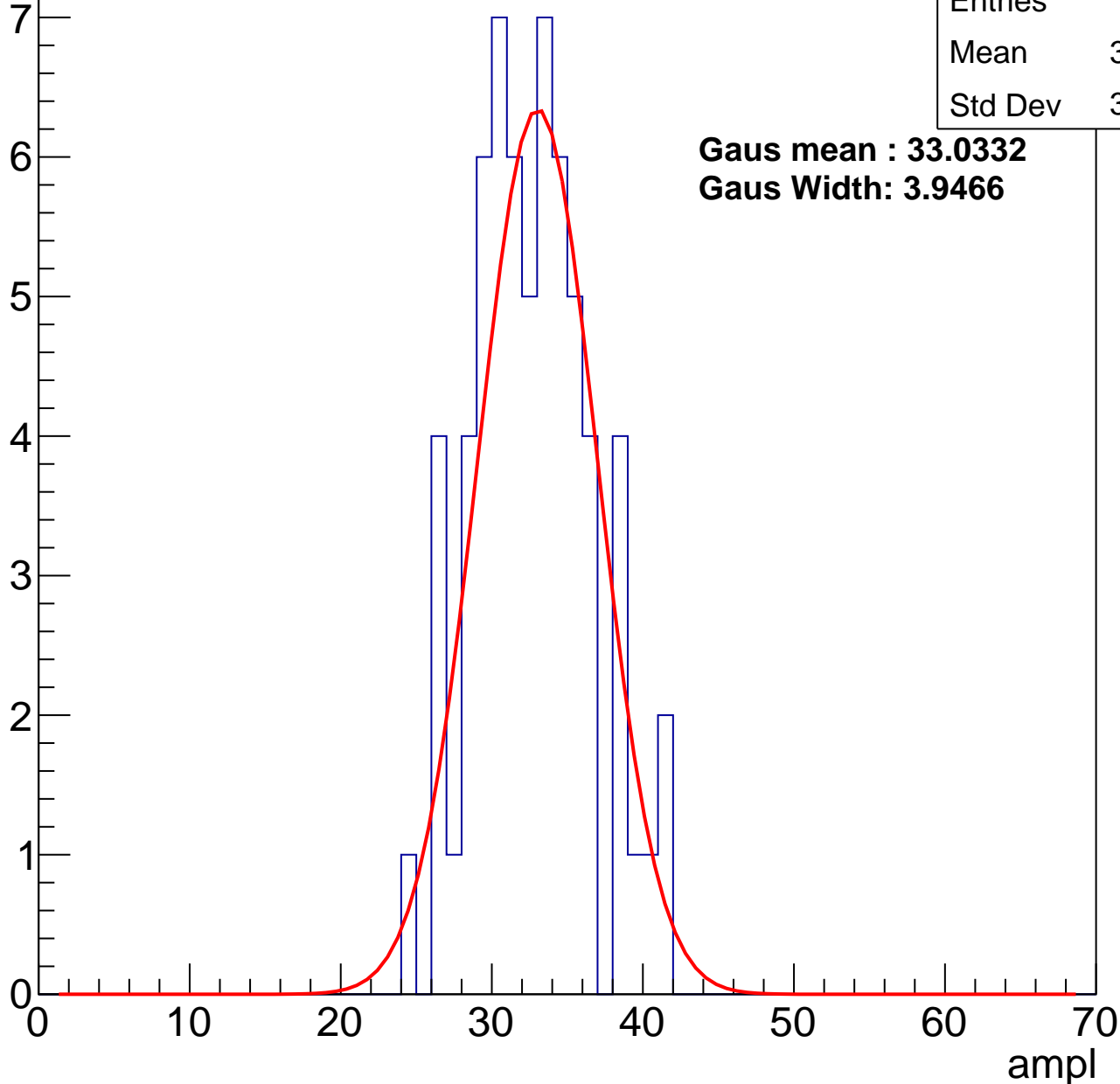
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	32.25
Std Dev	3.832

**Gaus mean : 33.0332**

**Gaus Width: 3.9466**



# B0L000S, U13-ch49, adc1

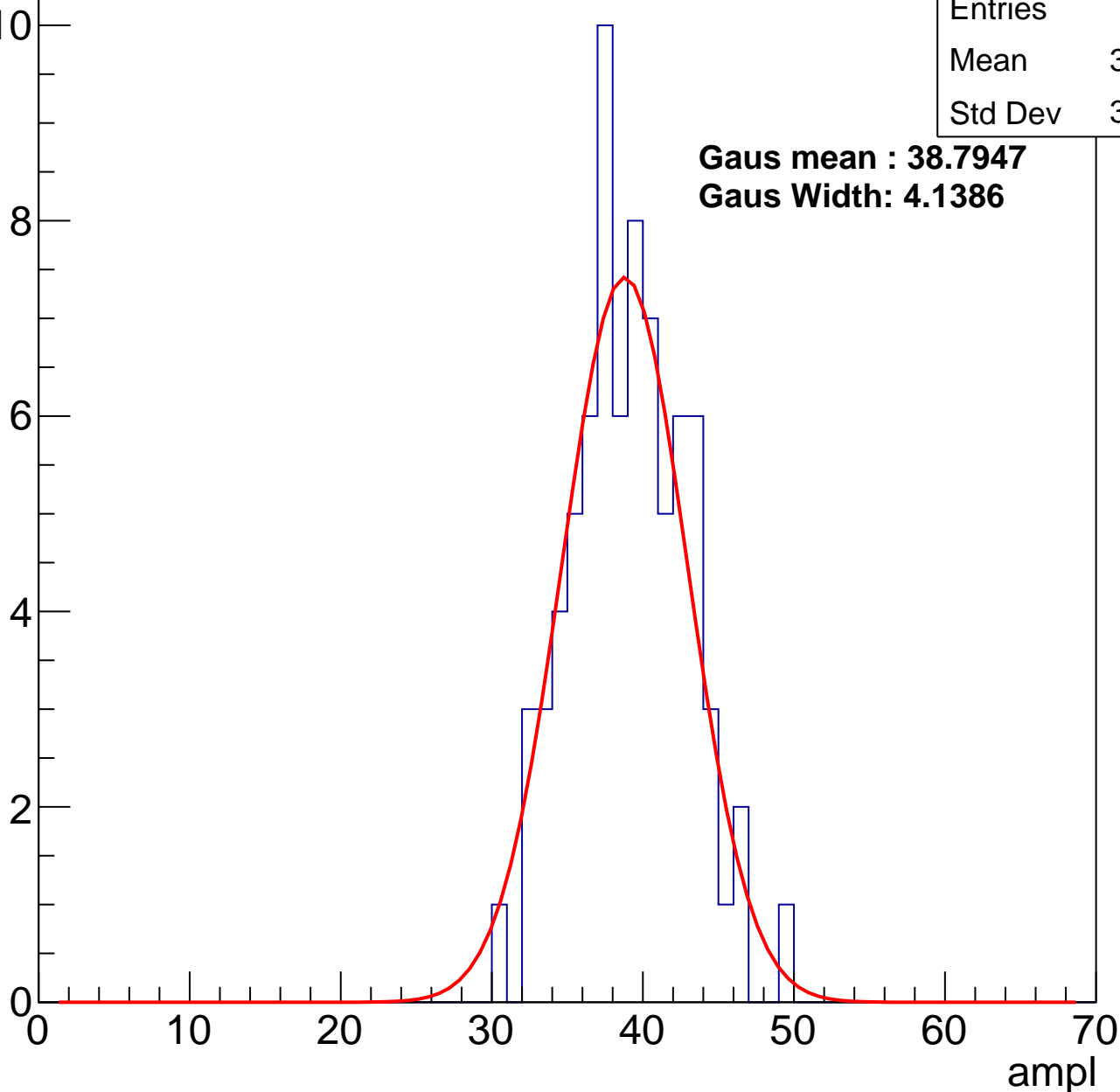
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	38.64
Std Dev	3.766

**Gaus mean : 38.7947**

**Gaus Width: 4.1386**

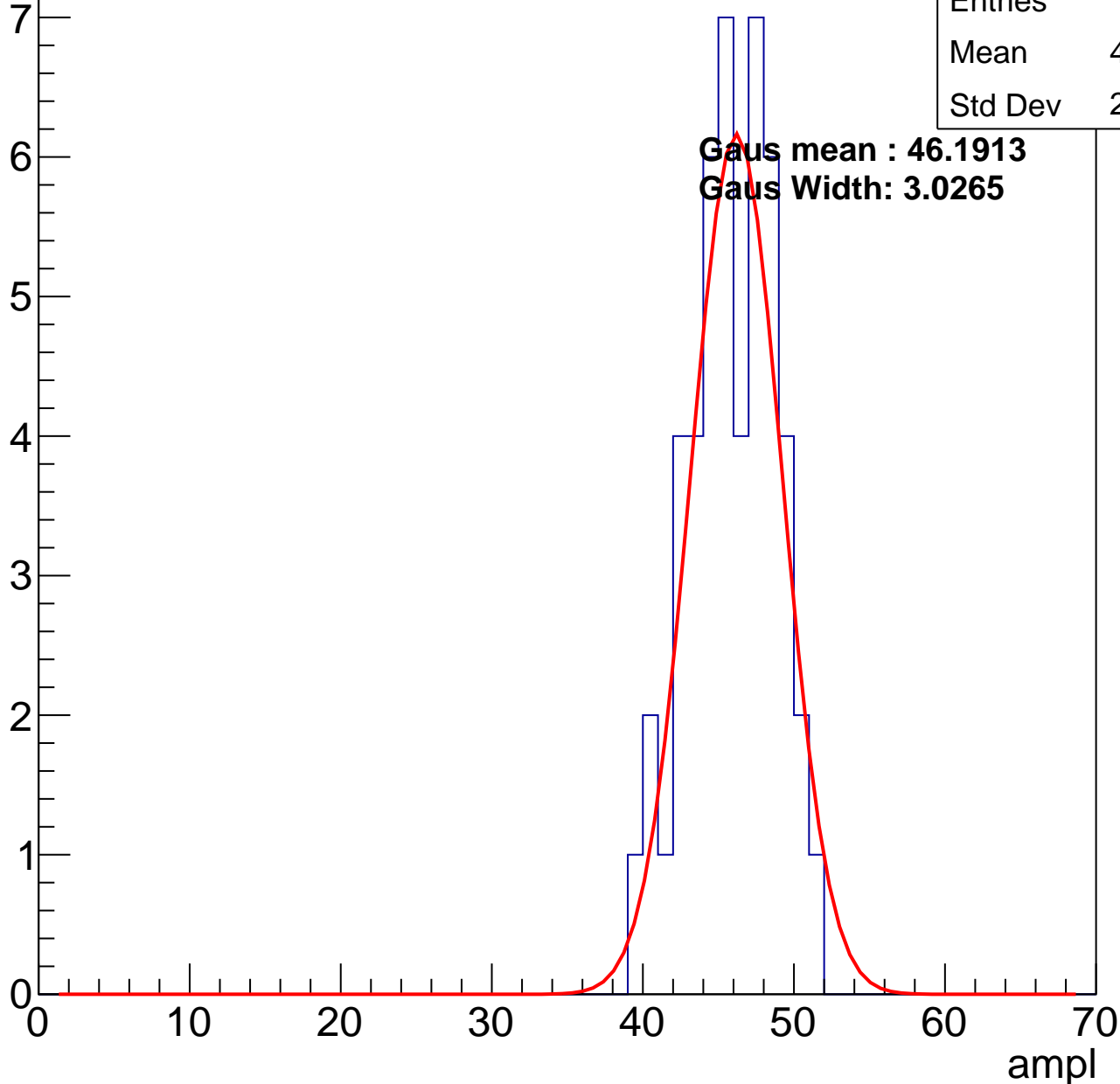


# B0L000S, U13-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	45.45
Std Dev	2.807

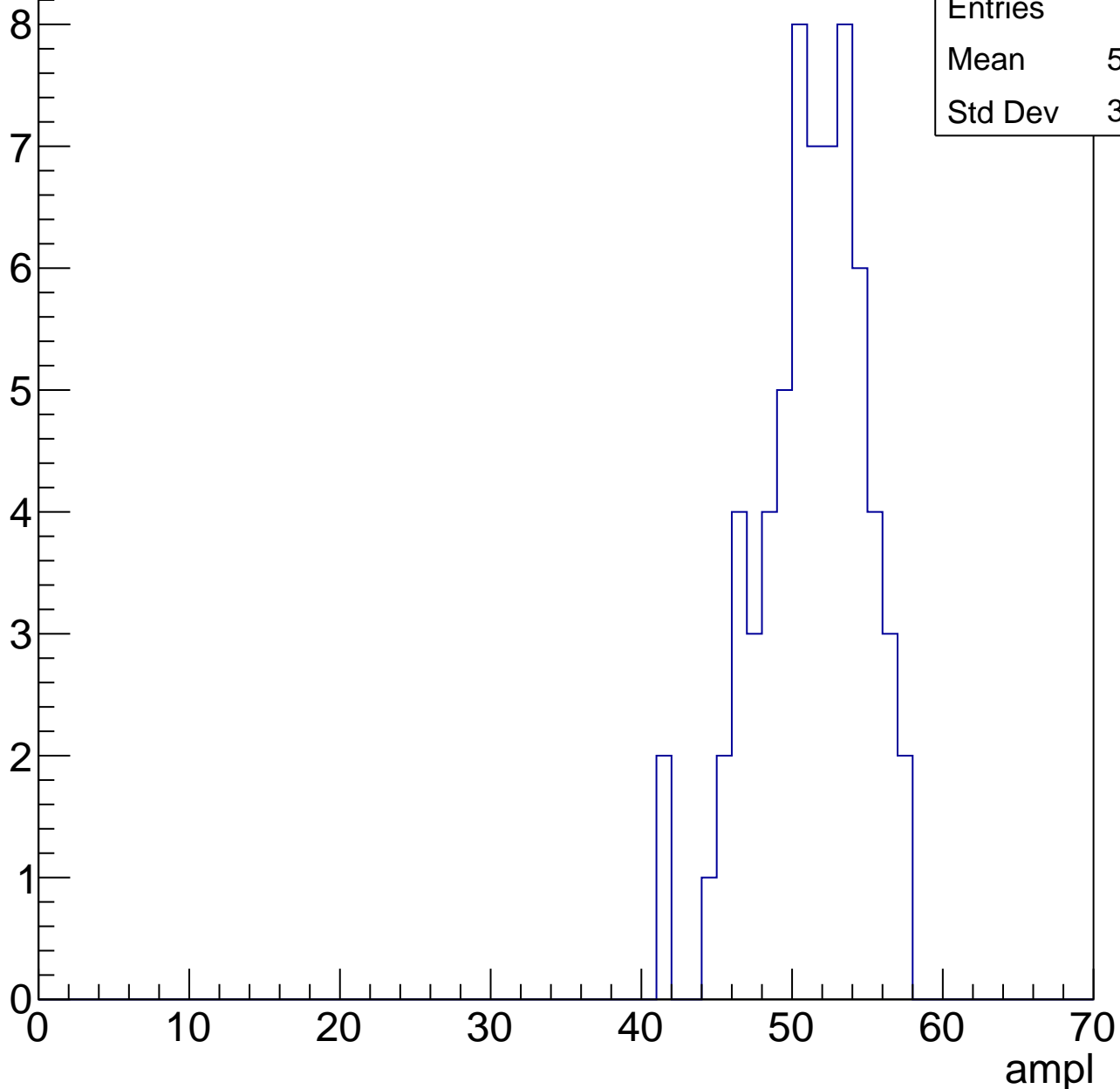


# B0L000S, U13-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.74
Std Dev	3.552



# B0L000S, U13-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

Entries 78

Mean 57.26

Std Dev 7.23

8

6

4

2

0

ampl

0

10

20

30

40

50

60

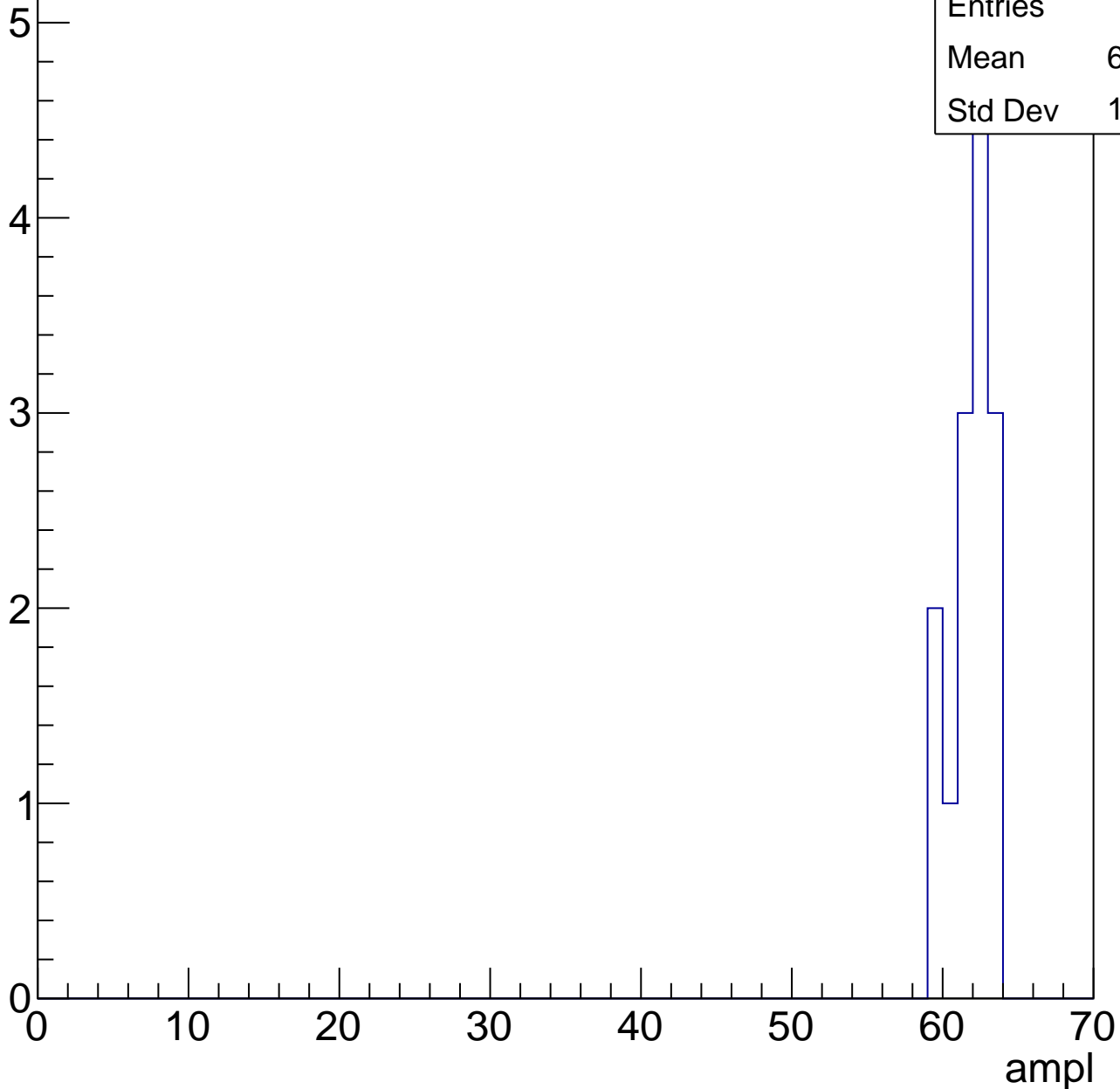
70

# B0L000S, U13-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.43
Std Dev	1.294



# B0L000S, U13-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch50, adc0

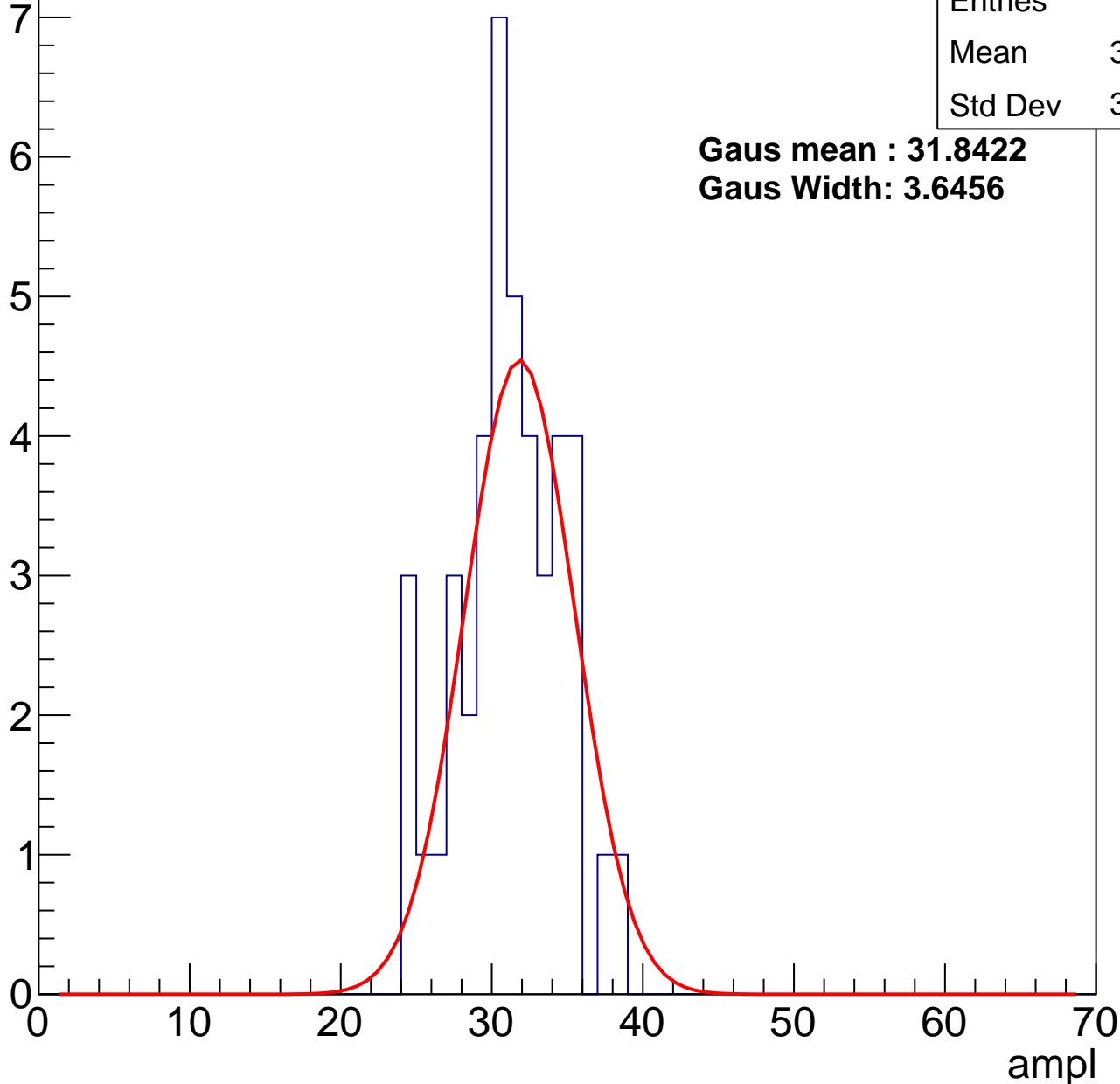
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	30.67
Std Dev	3.388

**Gaus mean : 31.8422**

**Gaus Width: 3.6456**



# B0L000S, U13-ch50, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	36.88
Std Dev	3.89

**Gaus mean : 37.0338**

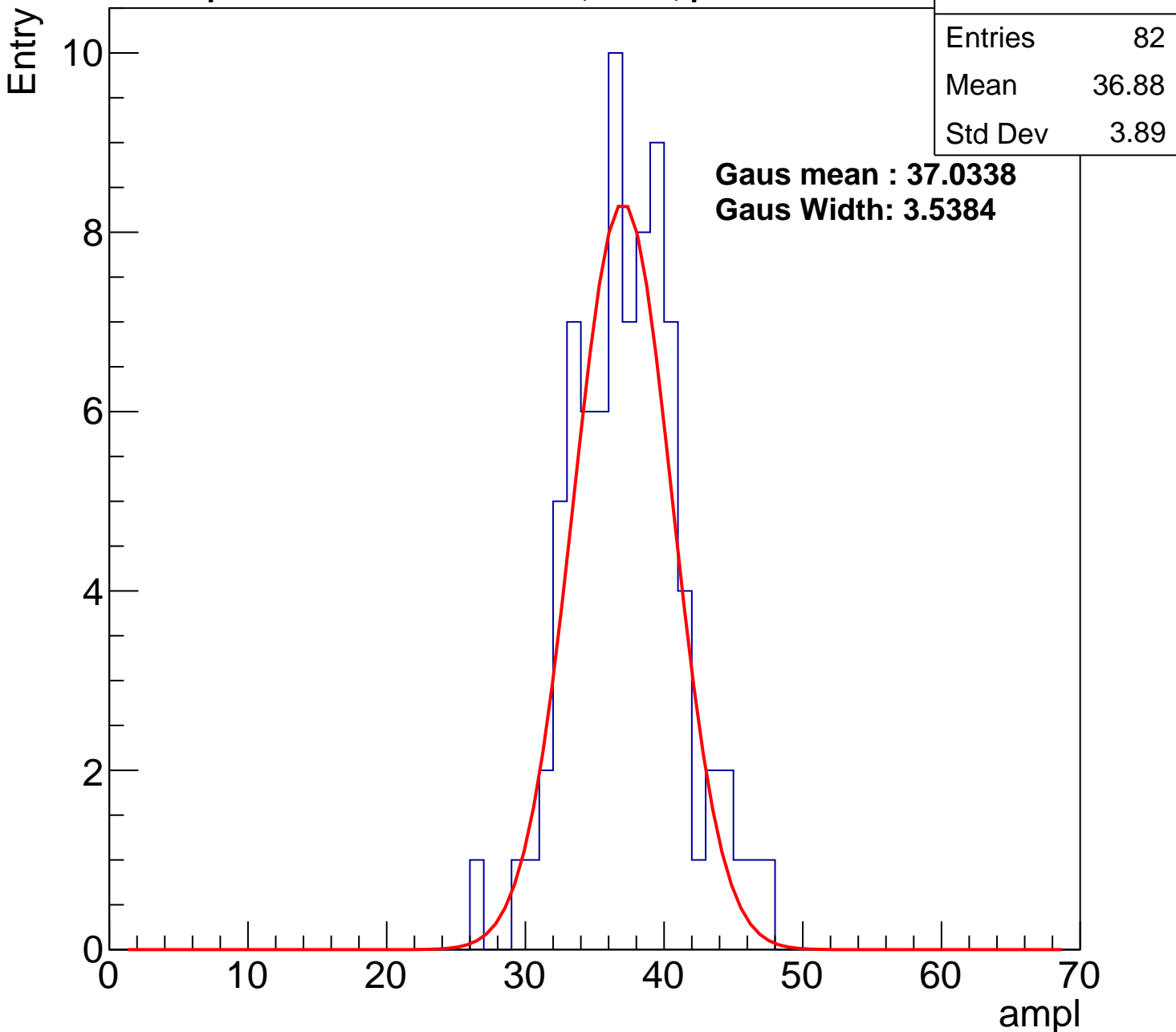
**Gaus Width: 3.5384**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch50, adc2

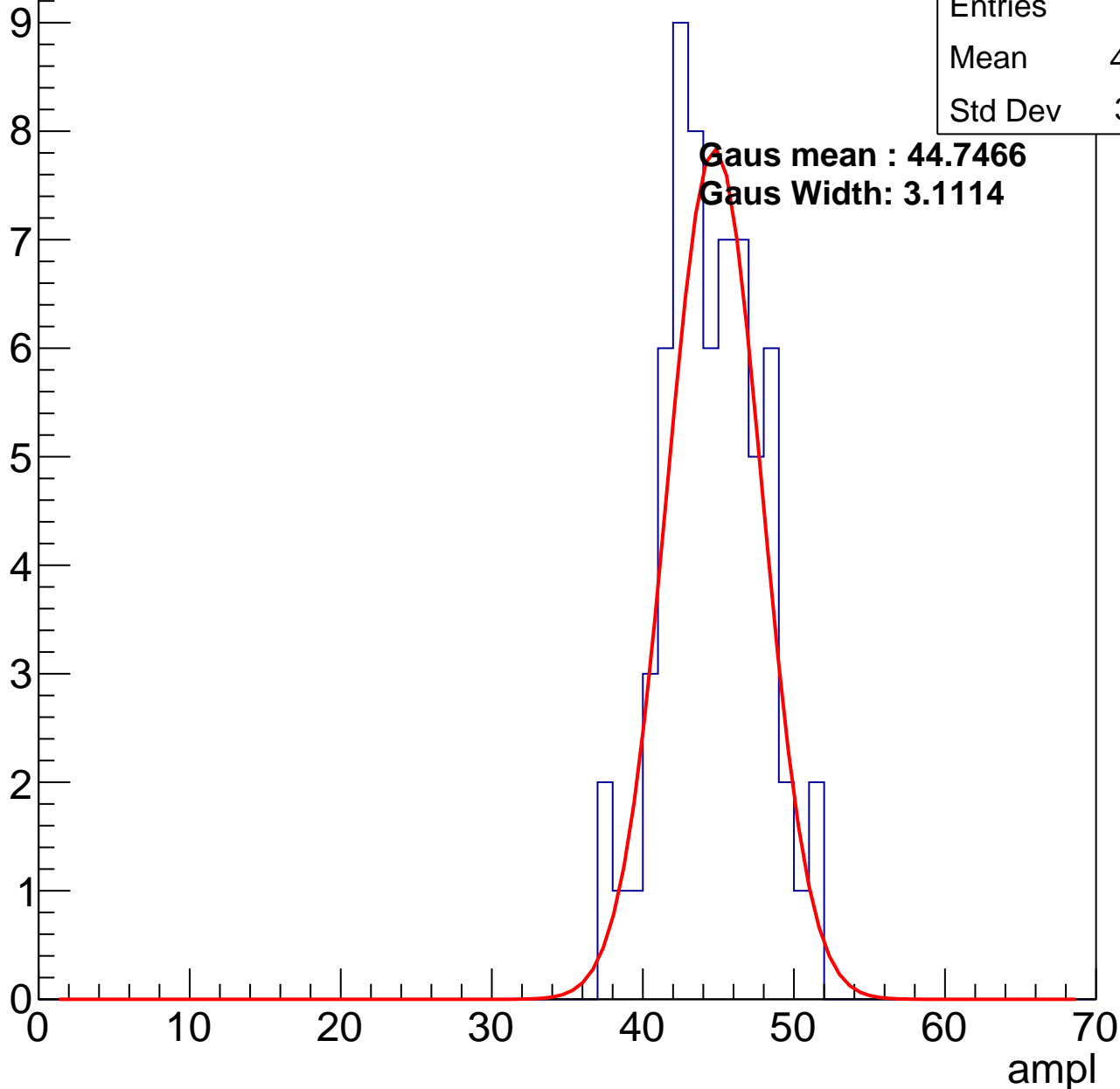
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.14
Std Dev	3.171

**Gaus mean : 44.7466**

**Gaus Width: 3.1114**

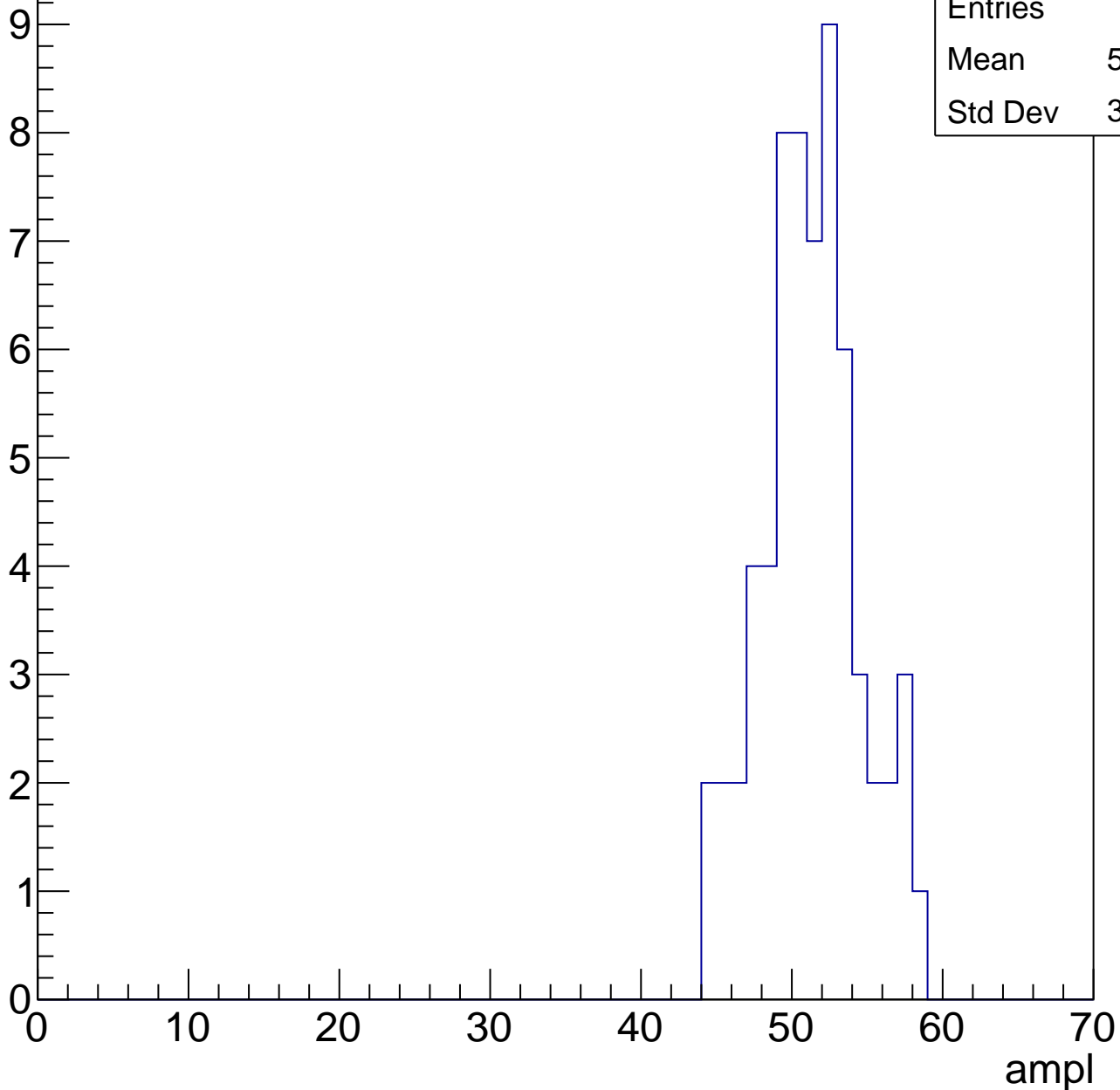


# B0L000S, U13-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

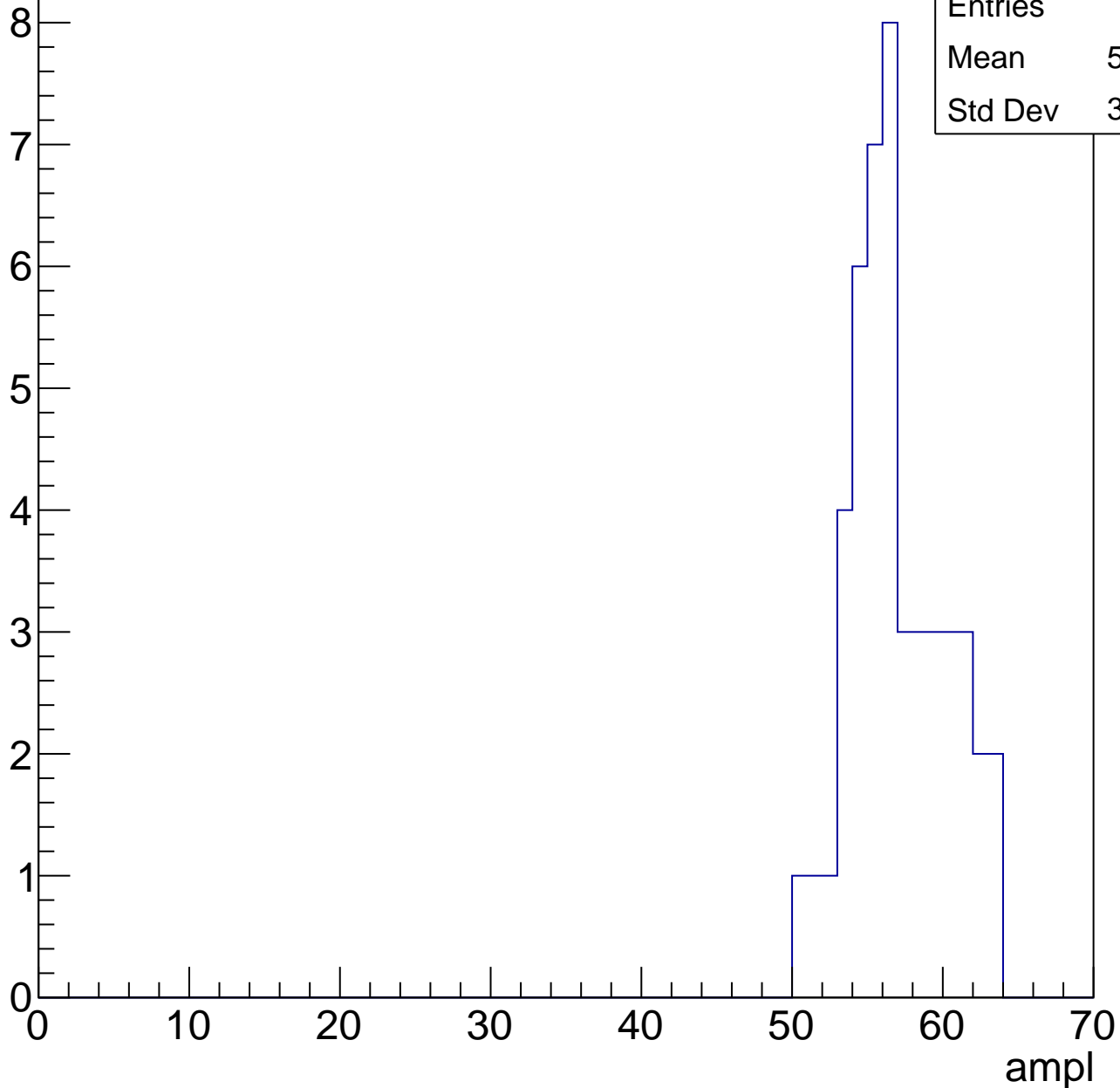
Entries	63
Mean	50.76
Std Dev	3.225



# B0L000S, U13-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 48

Mean 58.75

Std Dev 8.84

ampl

0

10

20

30

40

50

60

70

# B0L000S, U13-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U13-ch51, adc0

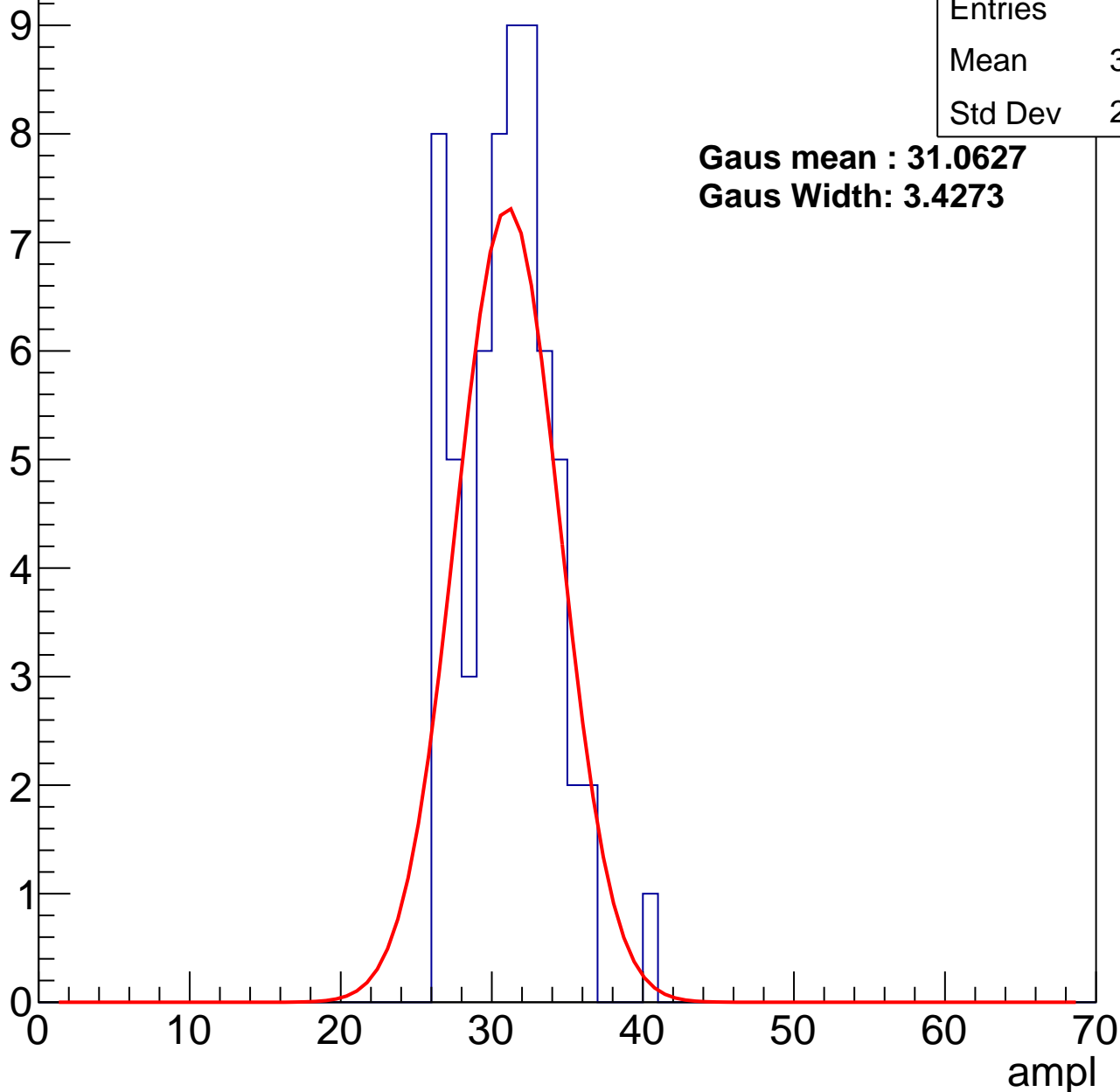
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.59
Std Dev	2.983

**Gaus mean : 31.0627**

**Gaus Width: 3.4273**



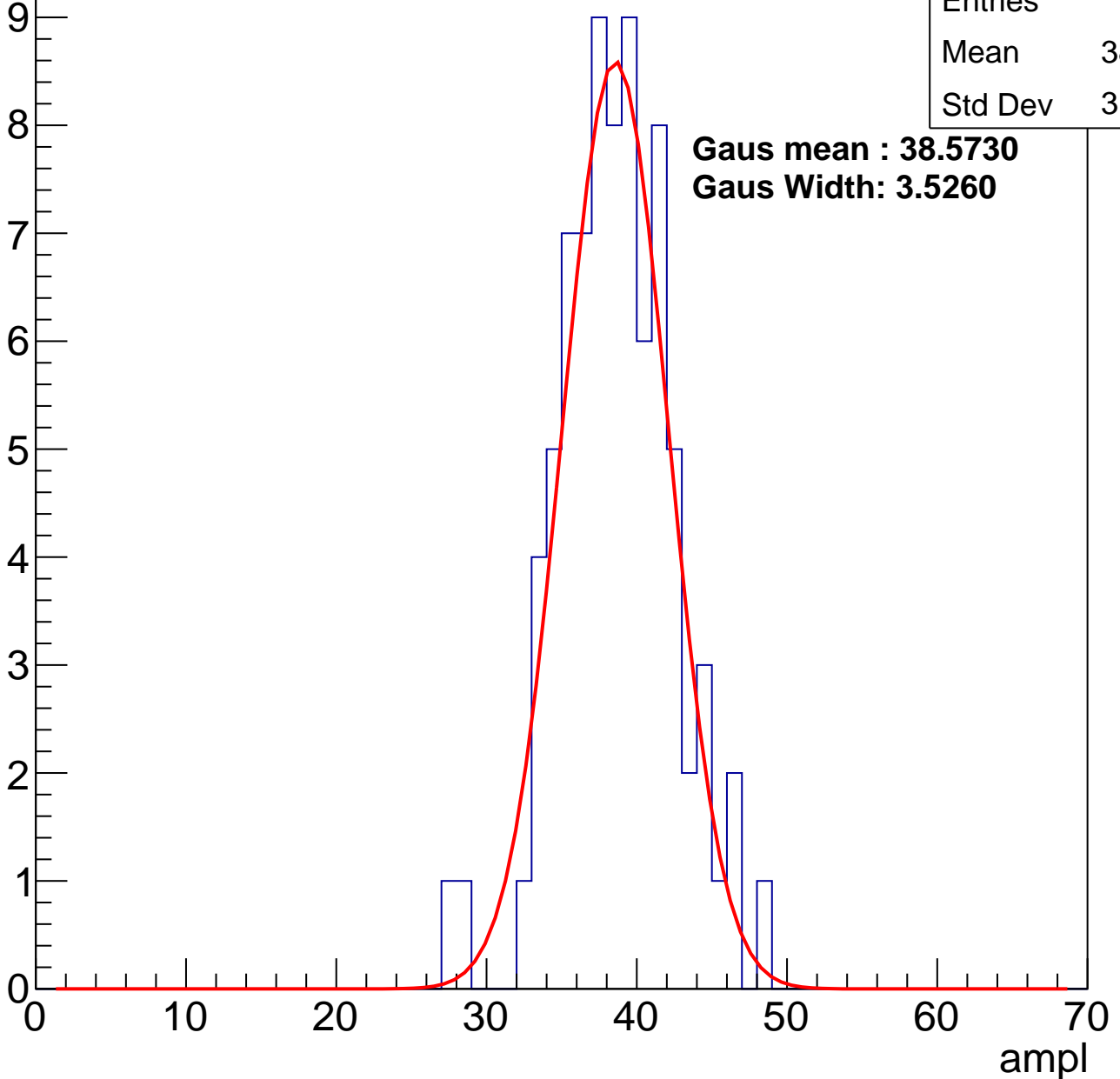
# B0L000S, U13-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	38.19
Std Dev	3.798

**Gaus mean : 38.5730**  
**Gaus Width: 3.5260**



# B0L000S, U13-ch51, adc2

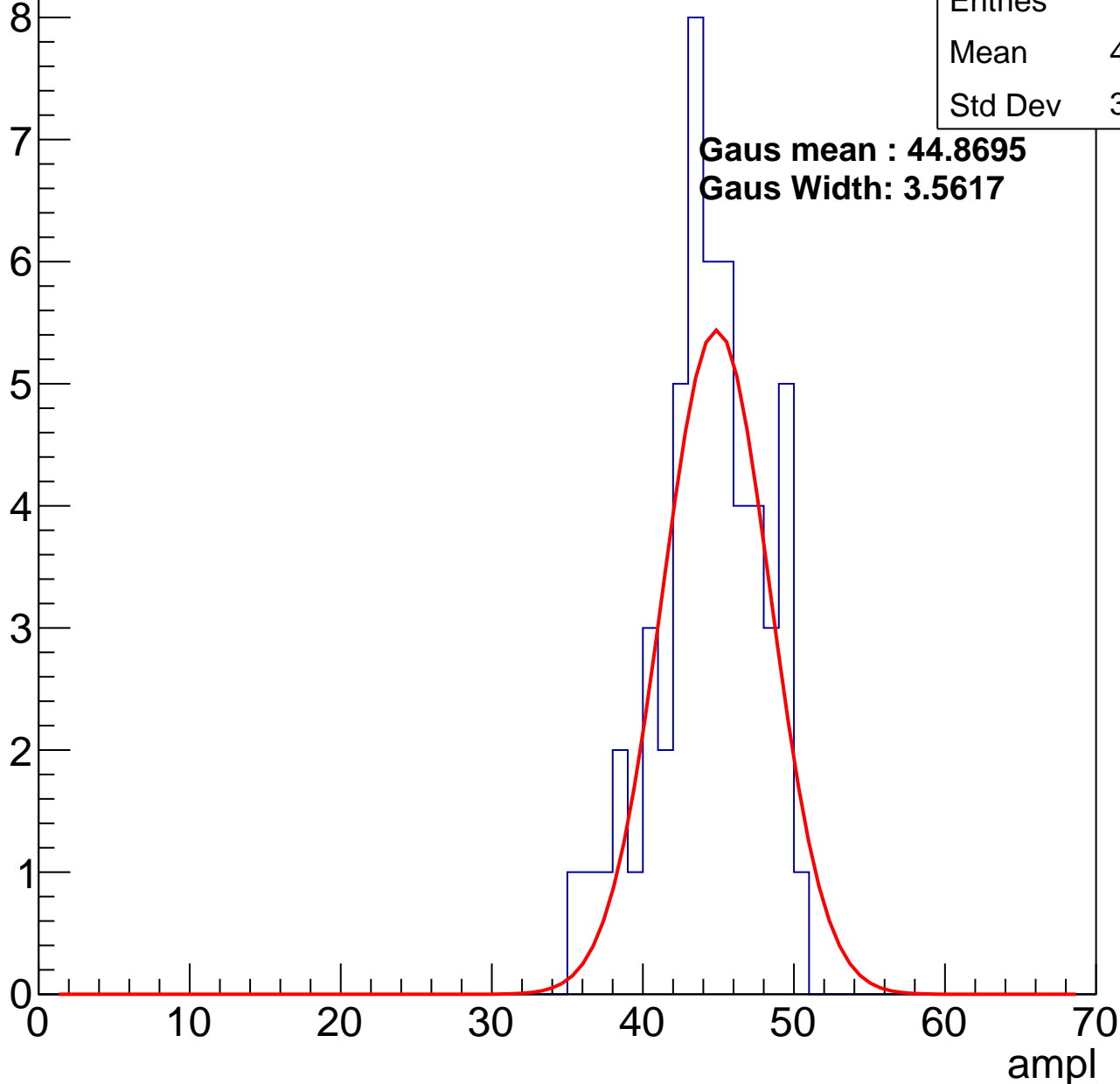
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.85
Std Dev	3.504

**Gaus mean : 44.8695**

**Gaus Width: 3.5617**

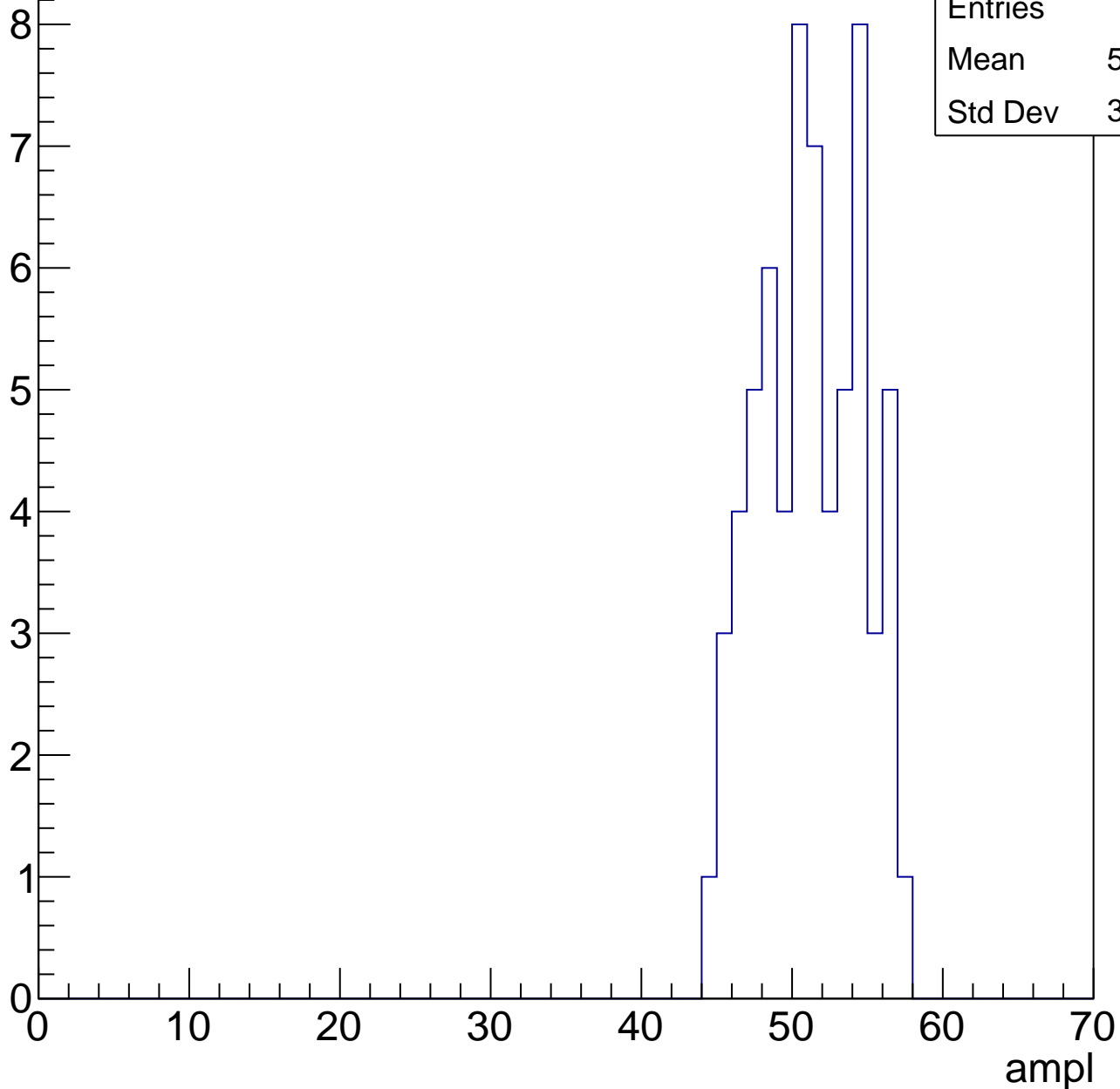


# B0L000S, U13-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	50.72
Std Dev	3.342

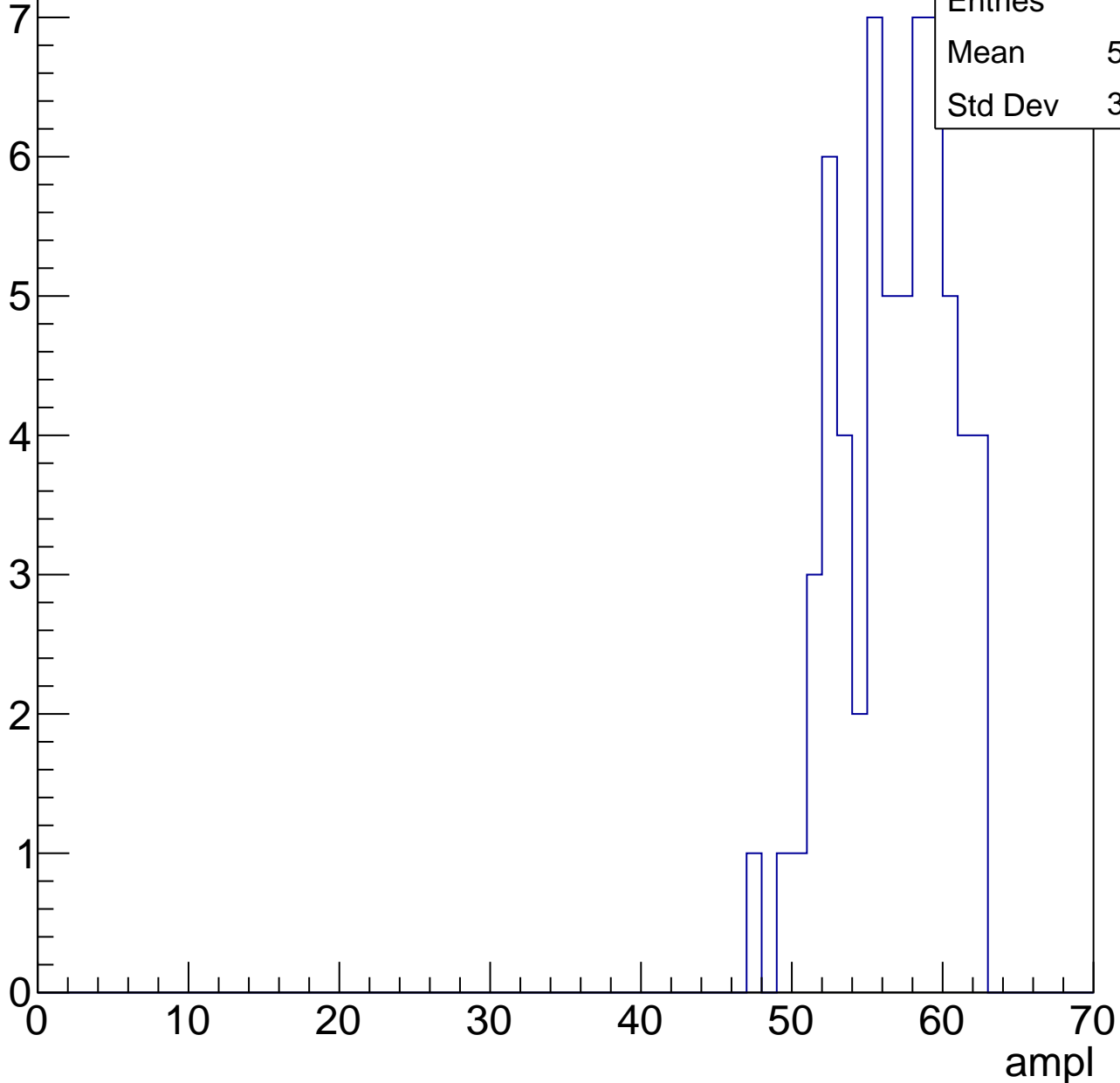


# B0L000S, U13-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.32
Std Dev	3.596

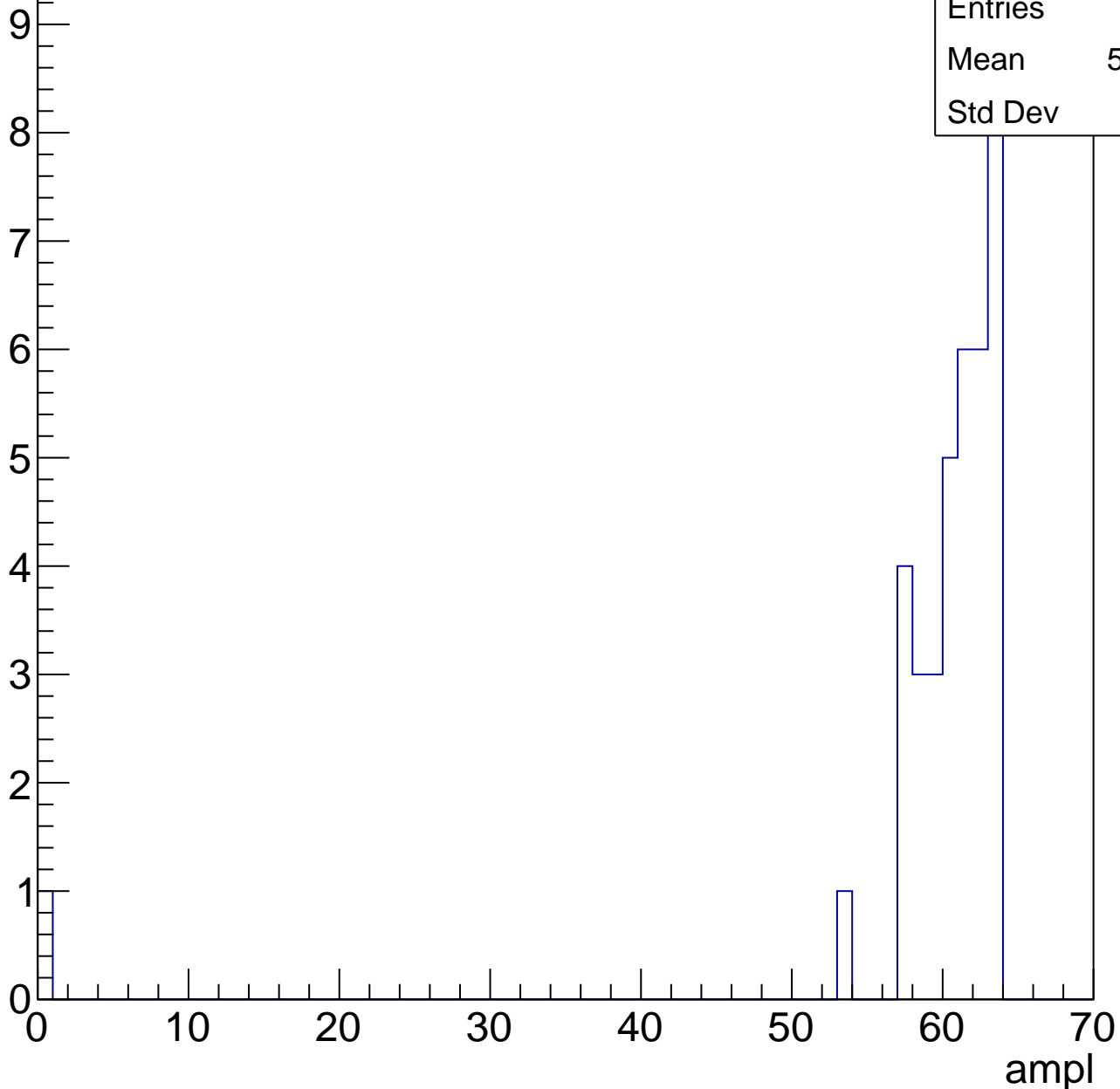


# B0L000S, U13-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.87
Std Dev	9.95



# B0L000S, U13-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U13-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L000S, U13-ch52, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	30.63
Std Dev	4.691

**Gaus mean : 31.6188**

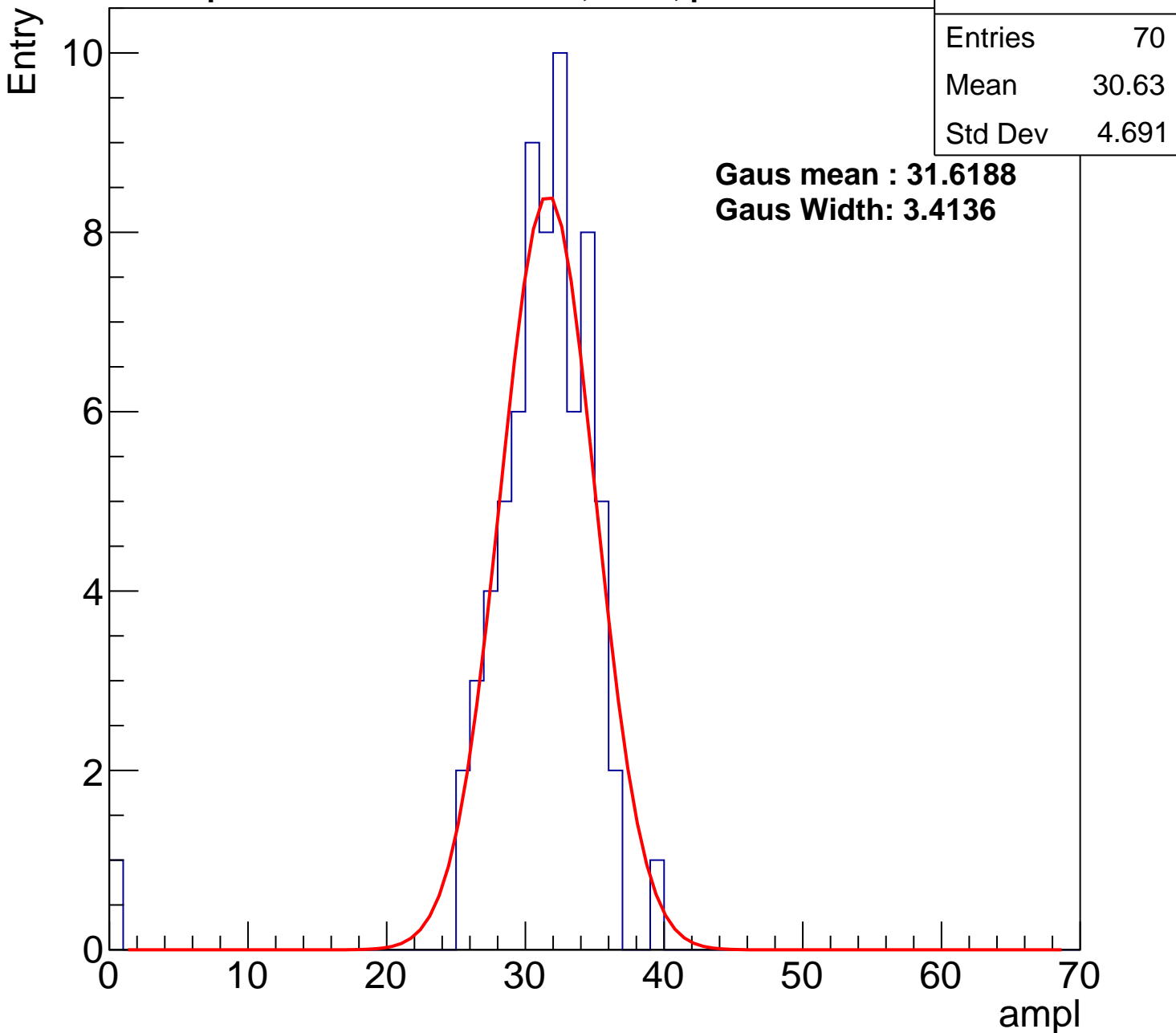
**Gaus Width: 3.4136**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch52, adc1

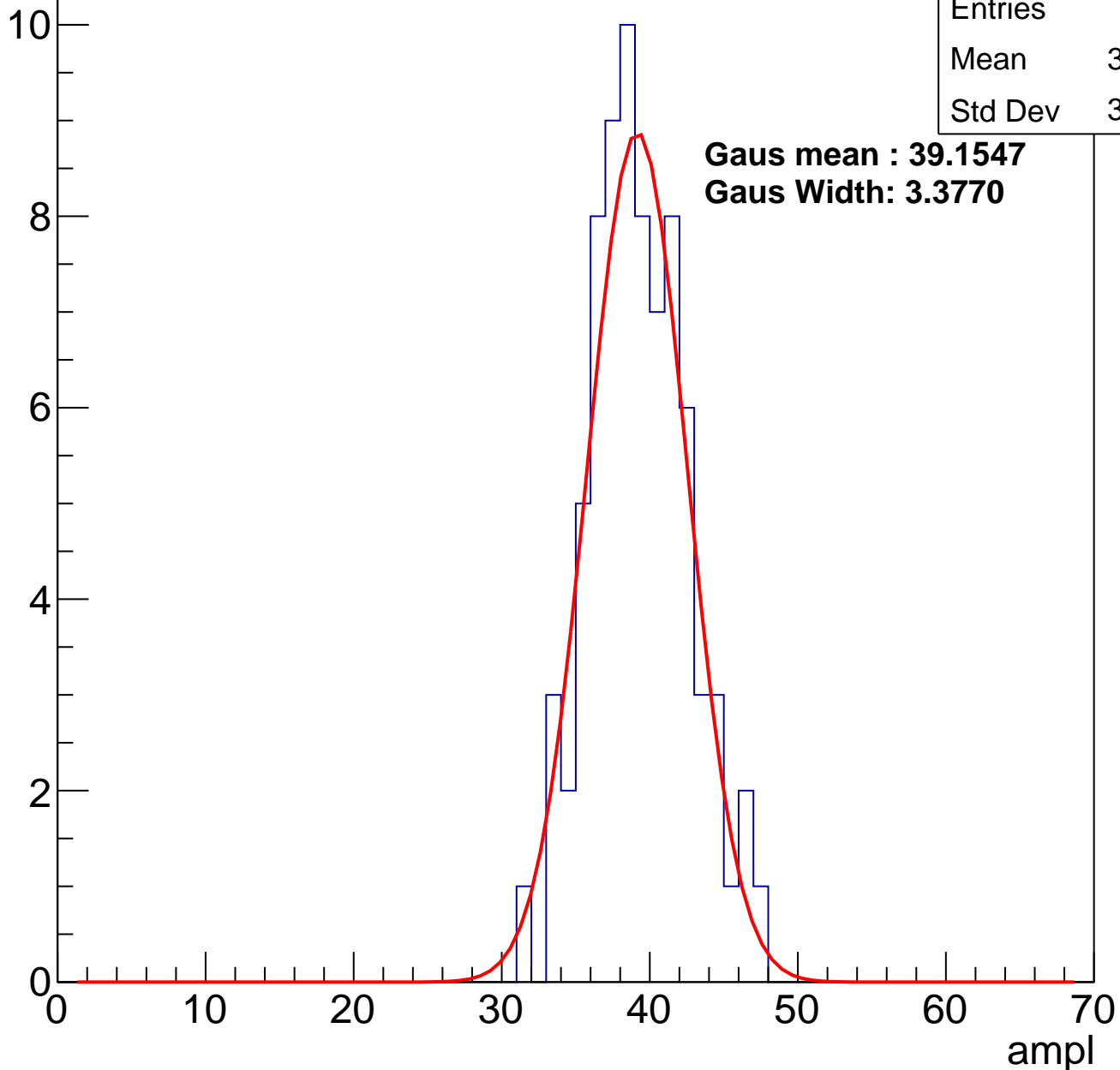
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	38.84
Std Dev	3.287

**Gaus mean : 39.1547**

**Gaus Width: 3.3770**

Entry



# B0L000S, U13-ch52, adc2

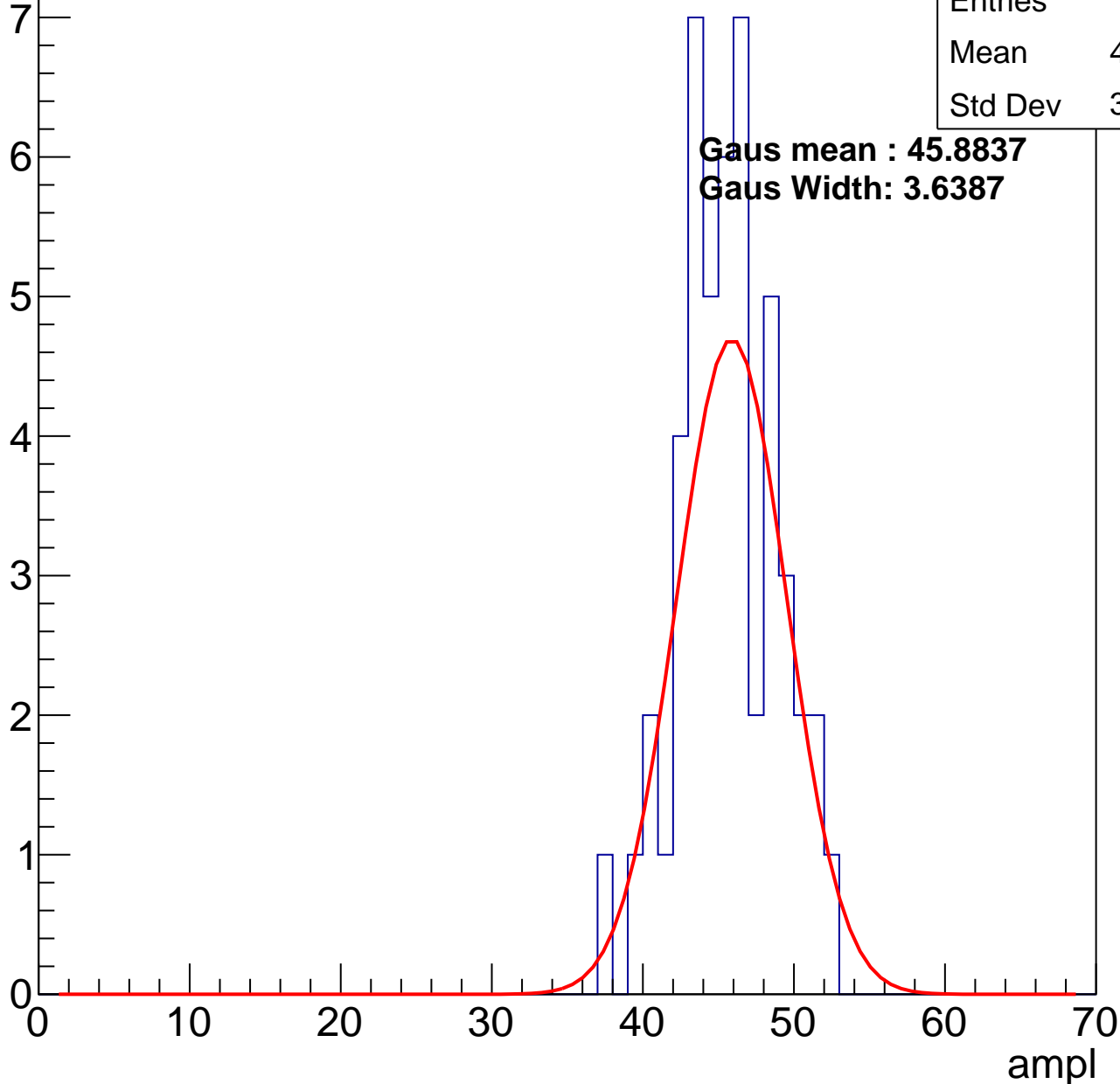
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	45.16
Std Dev	3.235

**Gaus mean : 45.8837**

**Gaus Width: 3.6387**

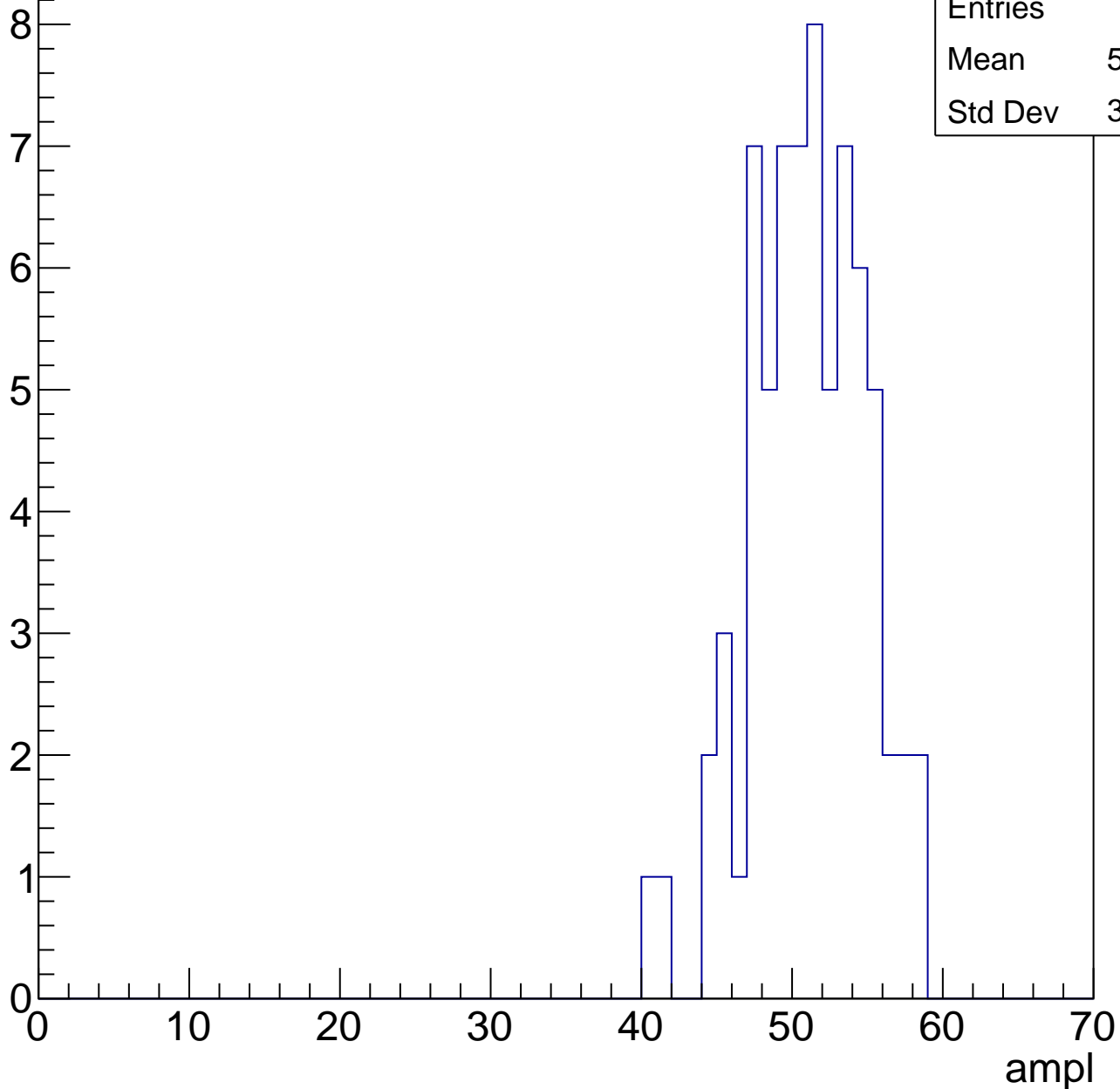


# B0L000S, U13-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	50.59
Std Dev	3.789

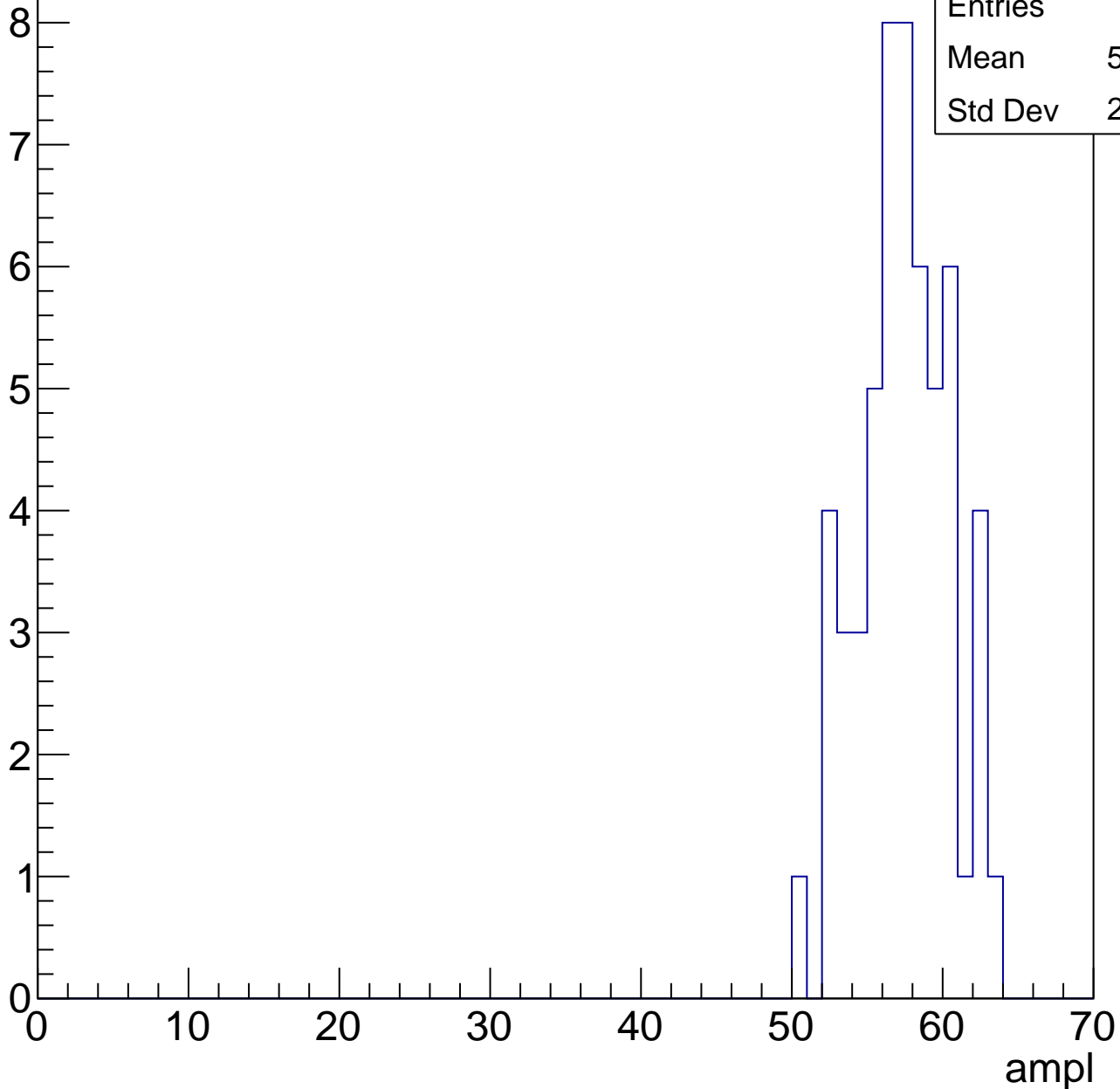


# B0L000S, U13-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.96
Std Dev	2.966

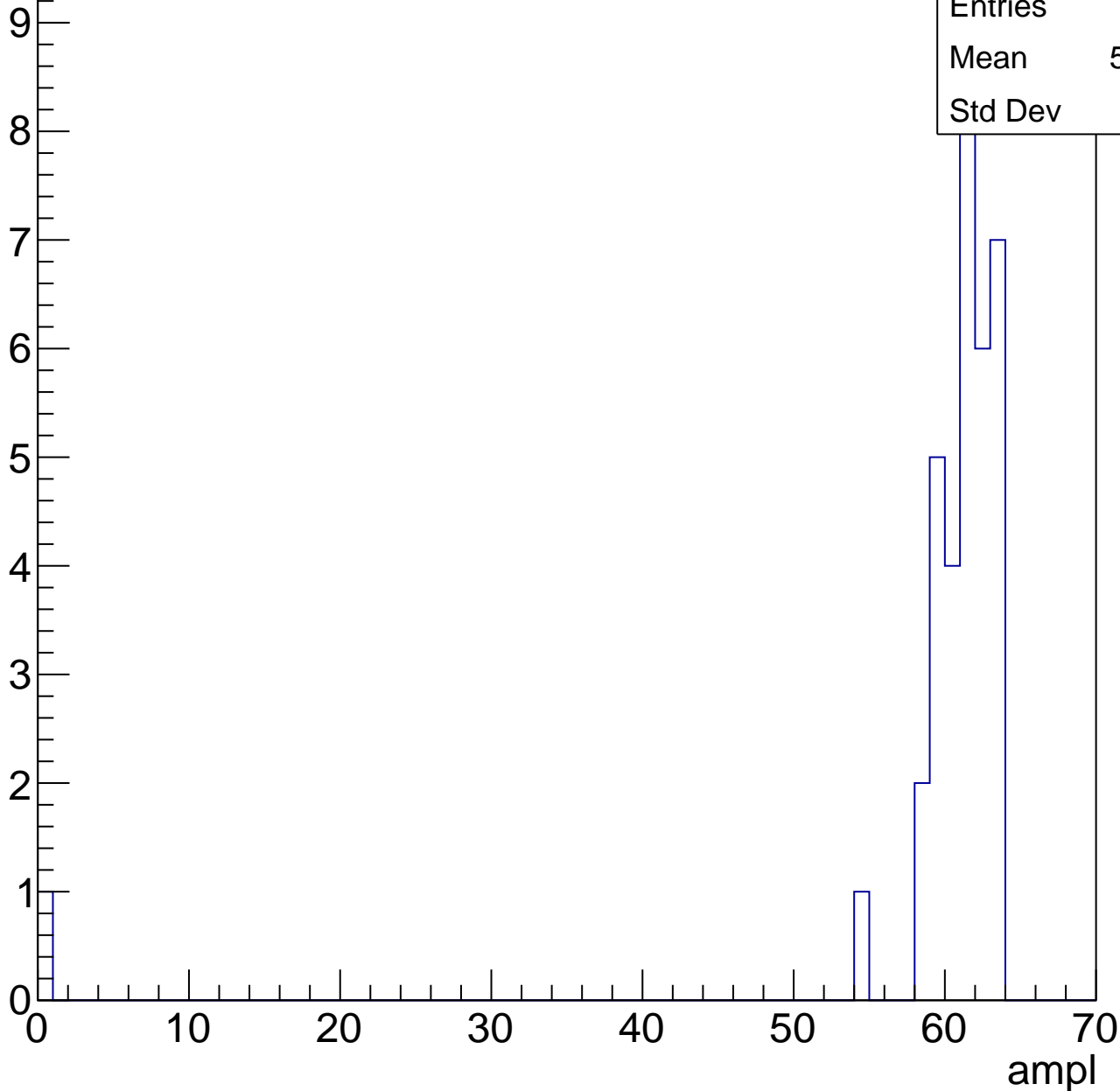


# B0L000S, U13-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	59.06
Std Dev	10.3



# B0L000S, U13-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch53, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	29.49
Std Dev	3.059

**Gaus mean : 29.4775**

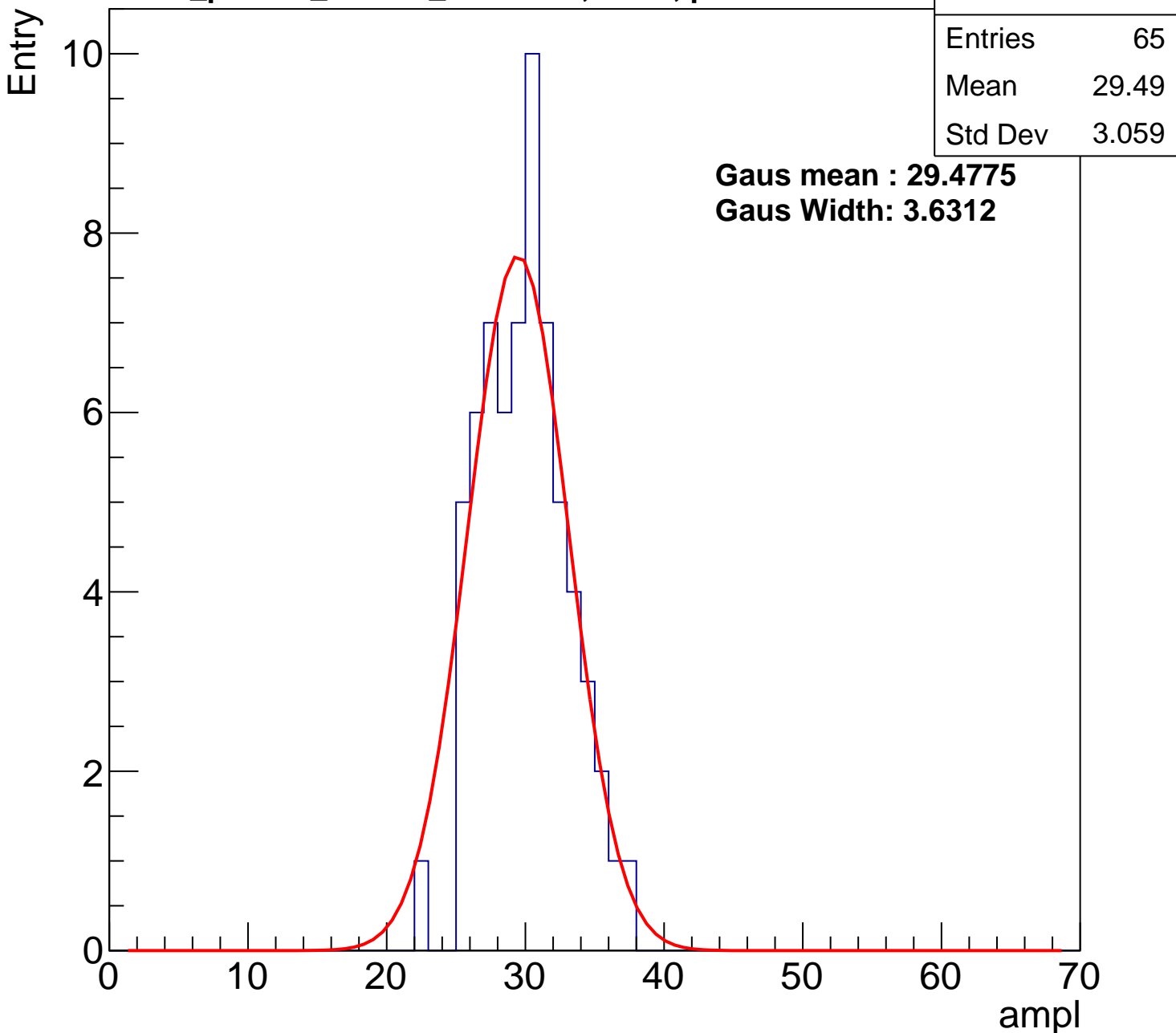
**Gaus Width: 3.6312**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch53, adc1

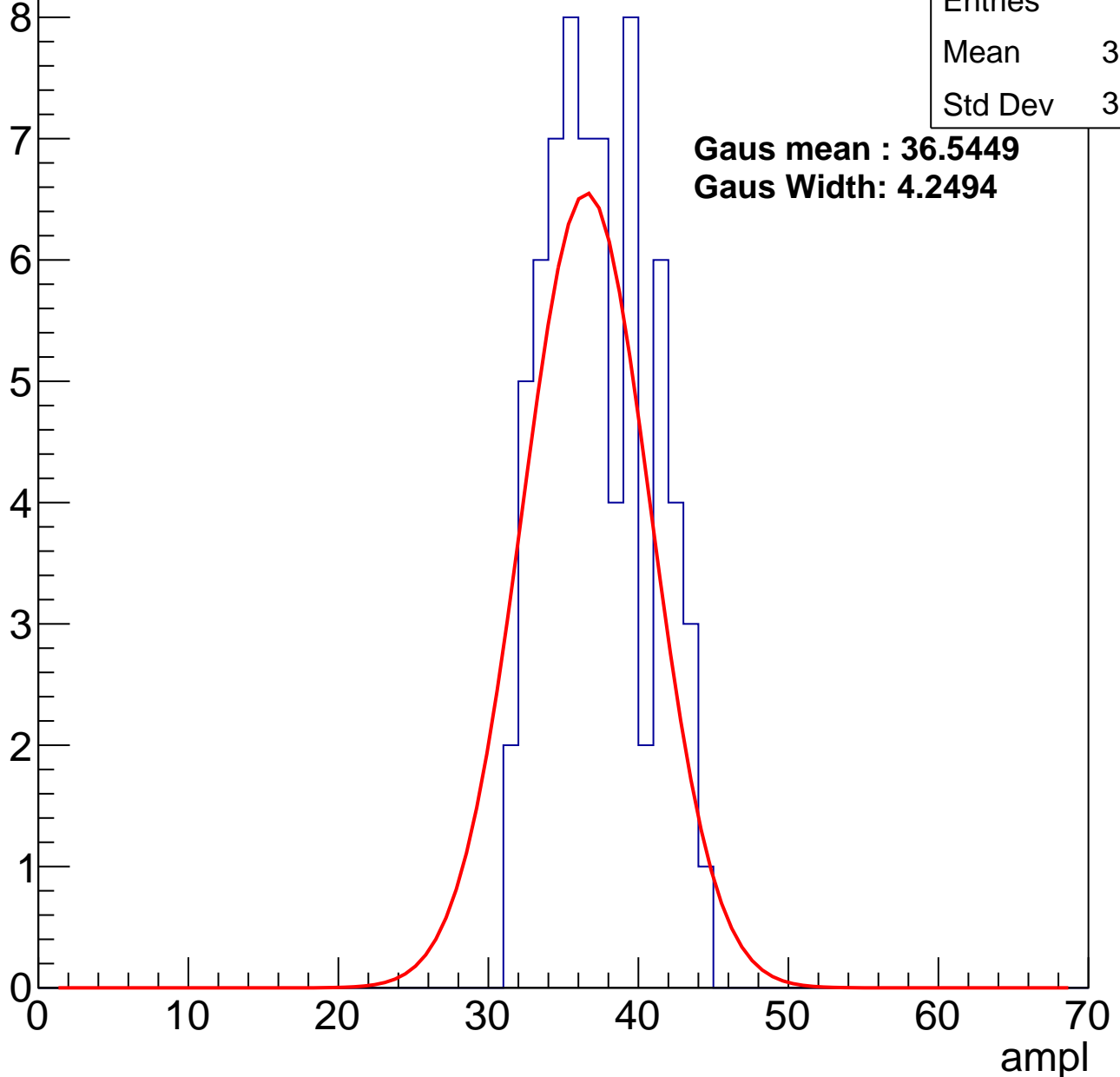
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.86
Std Dev	3.382

**Gaus mean : 36.5449**

**Gaus Width: 4.2494**



# B0L000S, U13-ch53, adc2

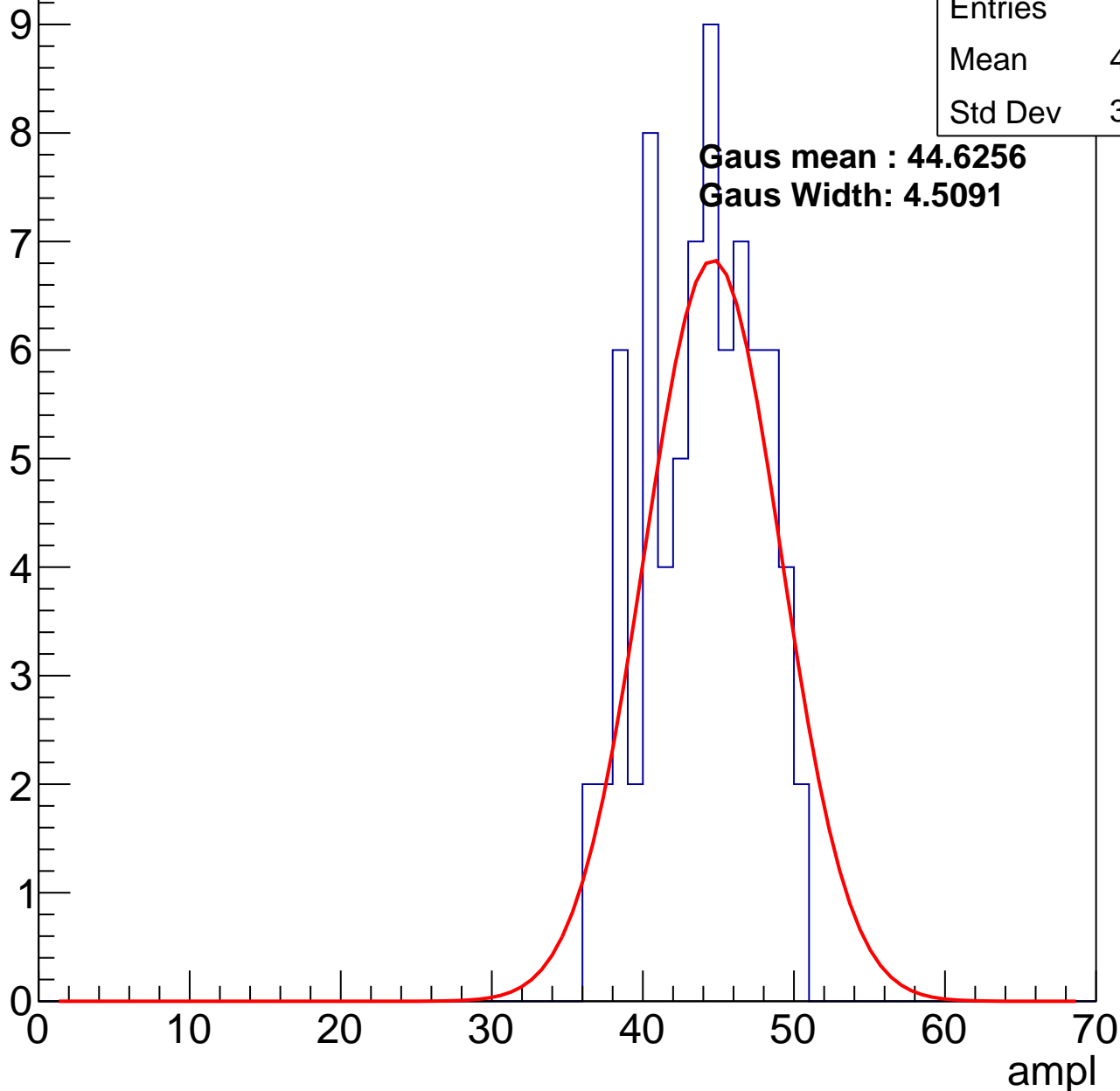
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.43
Std Dev	3.654

**Gaus mean : 44.6256**

**Gaus Width: 4.5091**

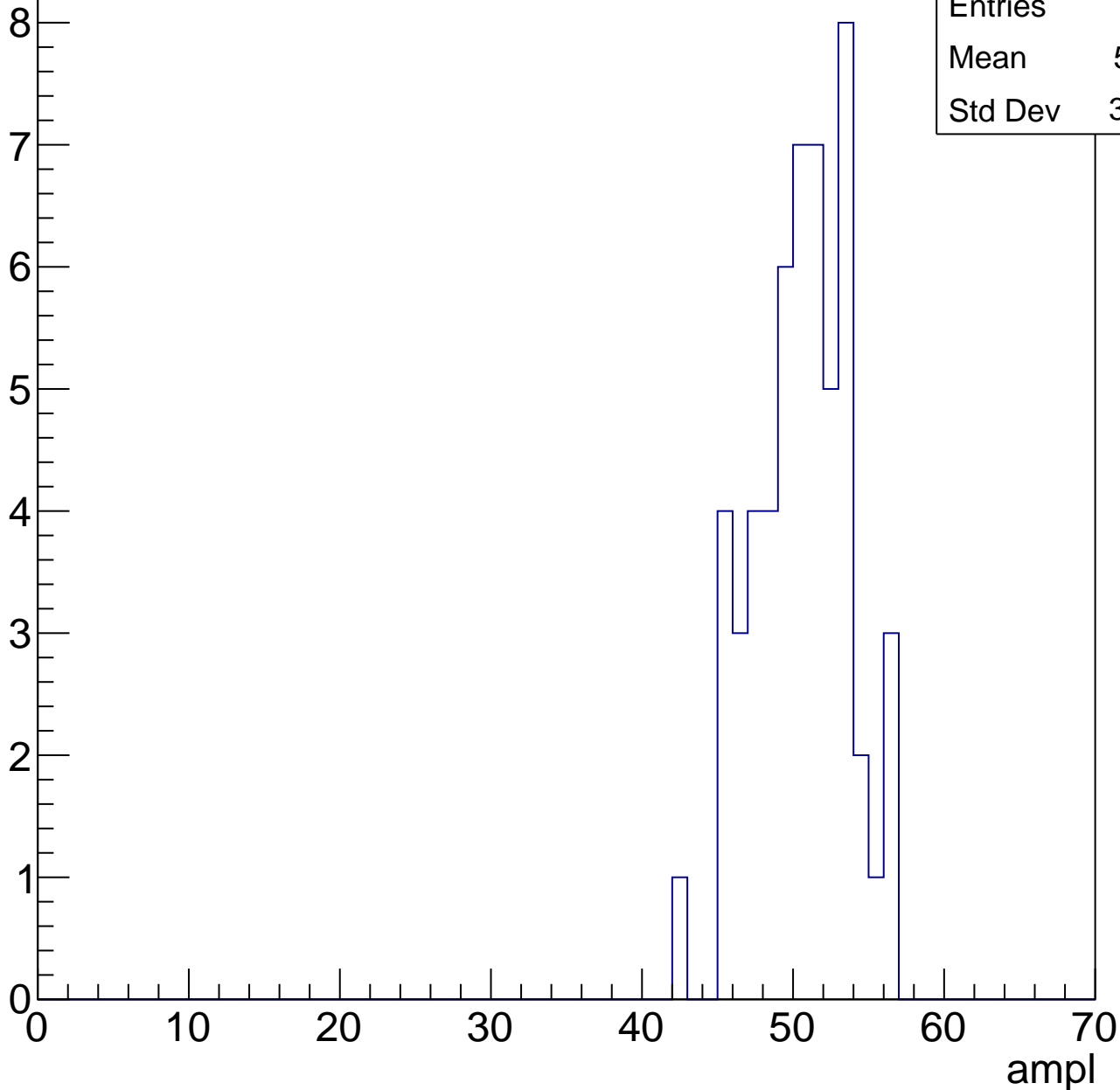


# B0L000S, U13-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	50.11
Std Dev	3.114



# B0L000S, U13-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	59
Mean	55.9
Std Dev	2.748

Entry

10

8

6

4

2

0

0

10

20

30

40

50

ampl

60

70

# B0L000S, U13-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	42
Mean	59.17
Std Dev	9.502

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

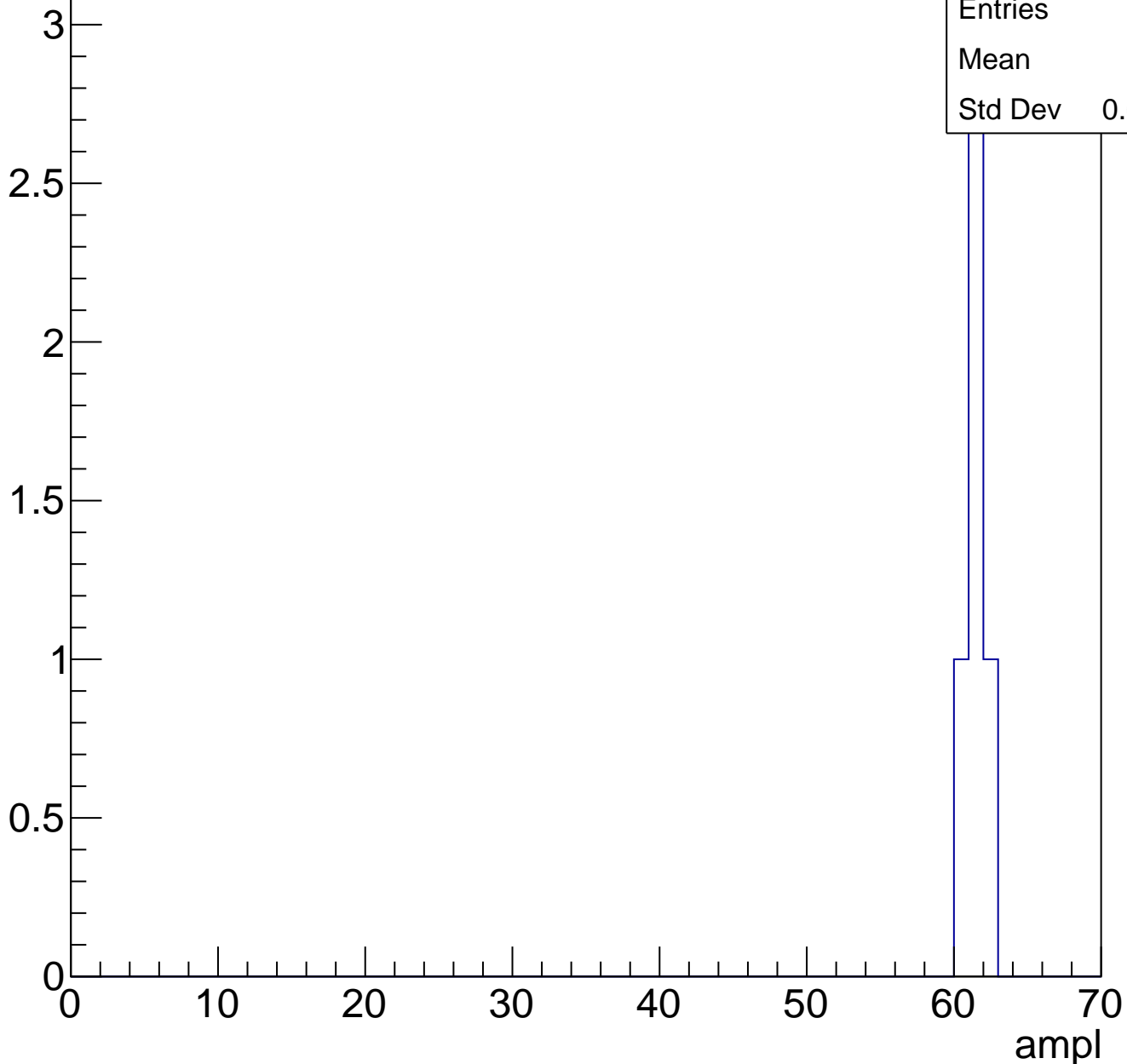
ampl

70

# B0L000S, U13-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

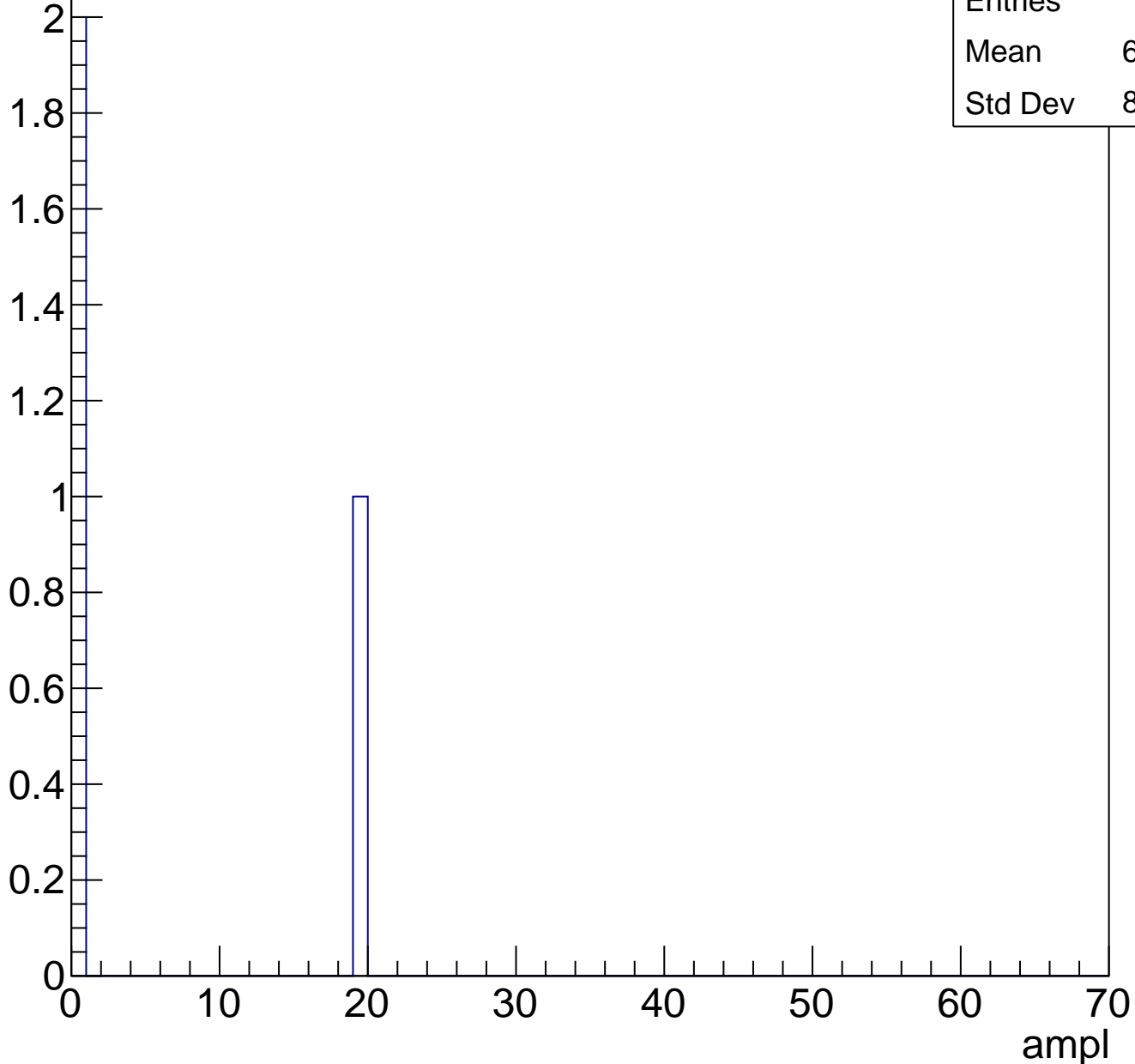




# B0L000S, U13-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

# B0L000S, U13-ch54, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	30.92
Std Dev	3.601

**Gaus mean : 31.4902**

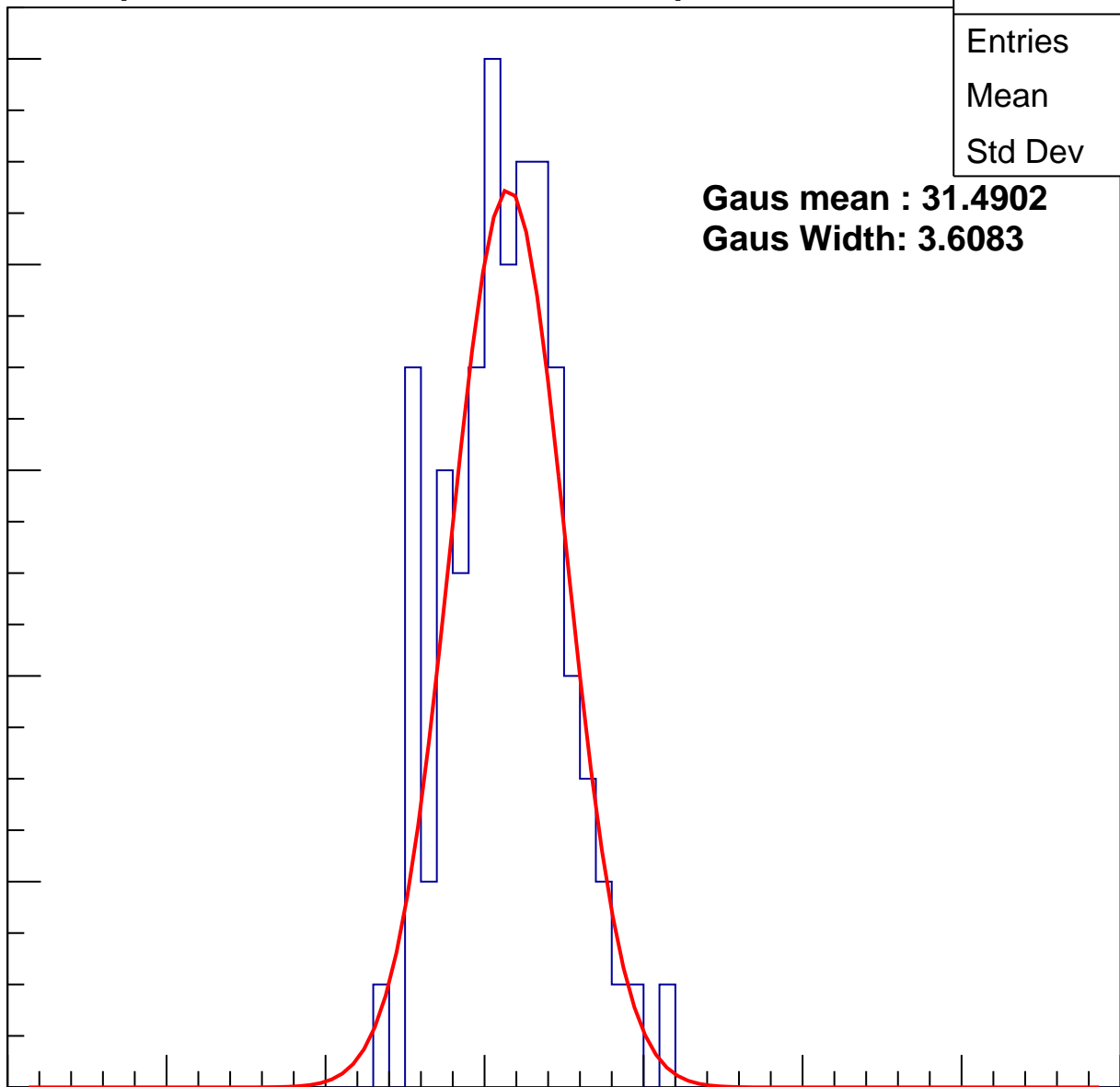
**Gaus Width: 3.6083**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch54, adc1

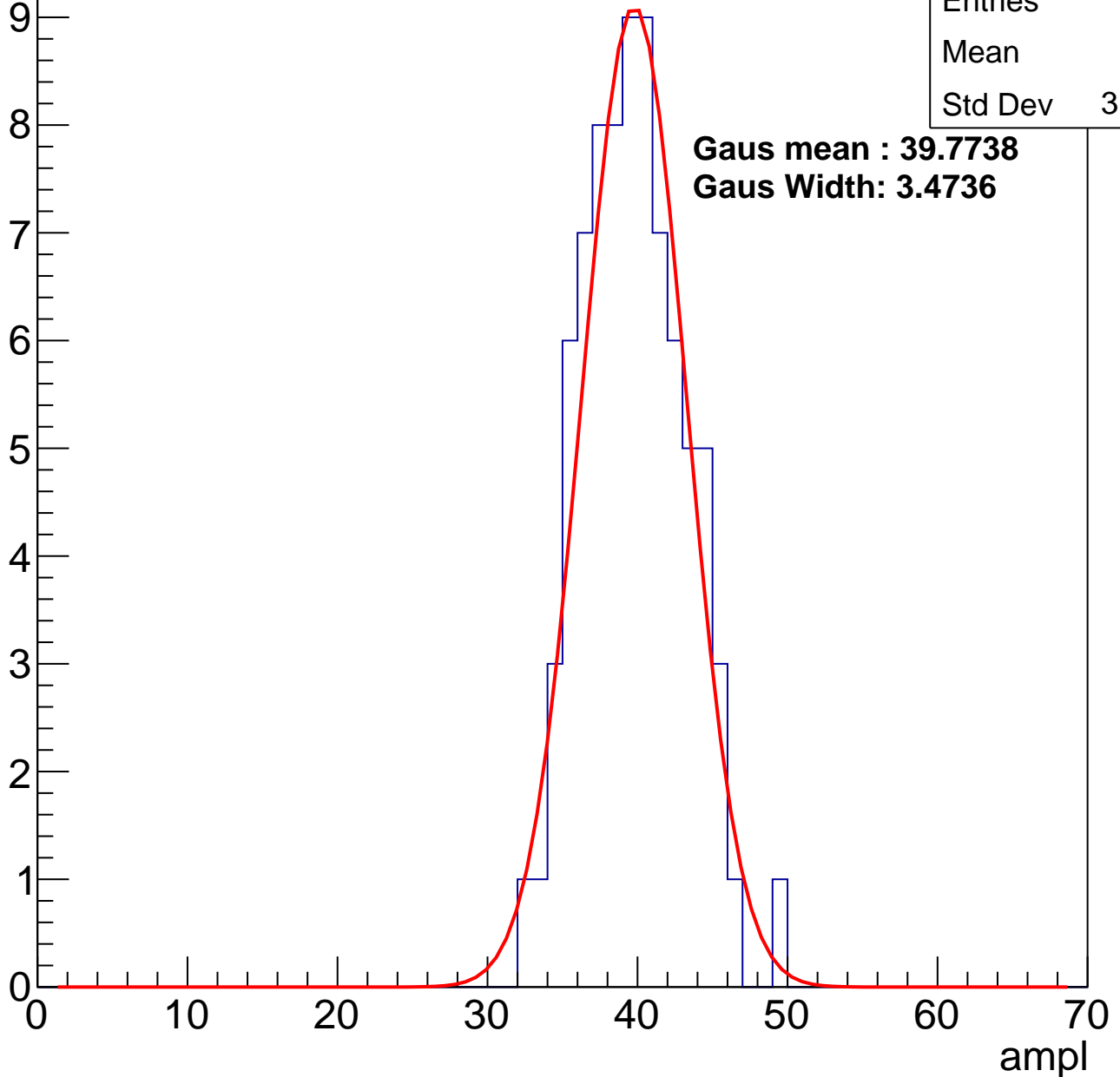
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	39.3
Std Dev	3.363

**Gaus mean : 39.7738**

**Gaus Width: 3.4736**



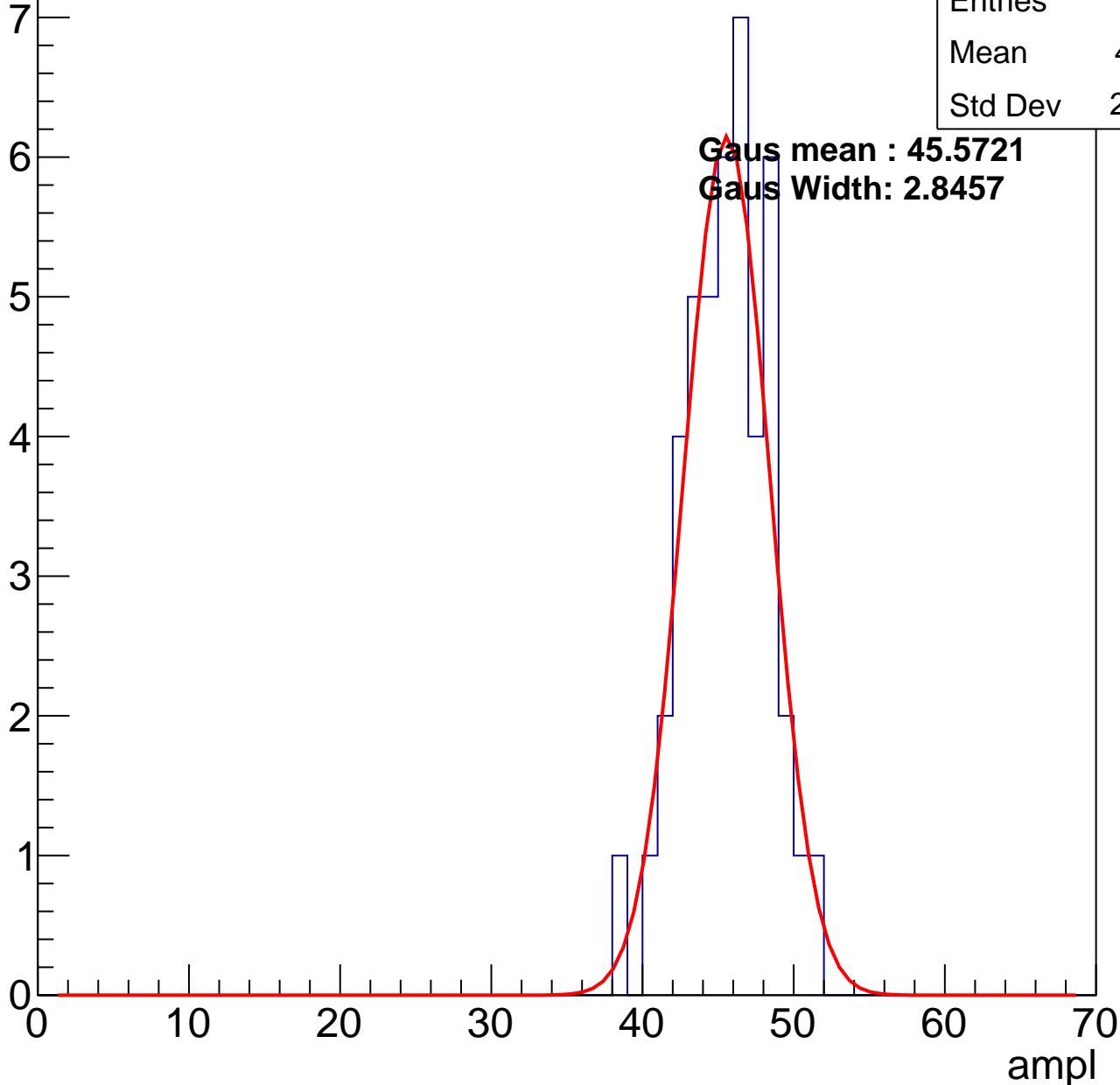
# B0L000S, U13-ch54, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	45.11
Std Dev	2.734

Gaus mean : 45.5721  
Gaus Width: 2.8457

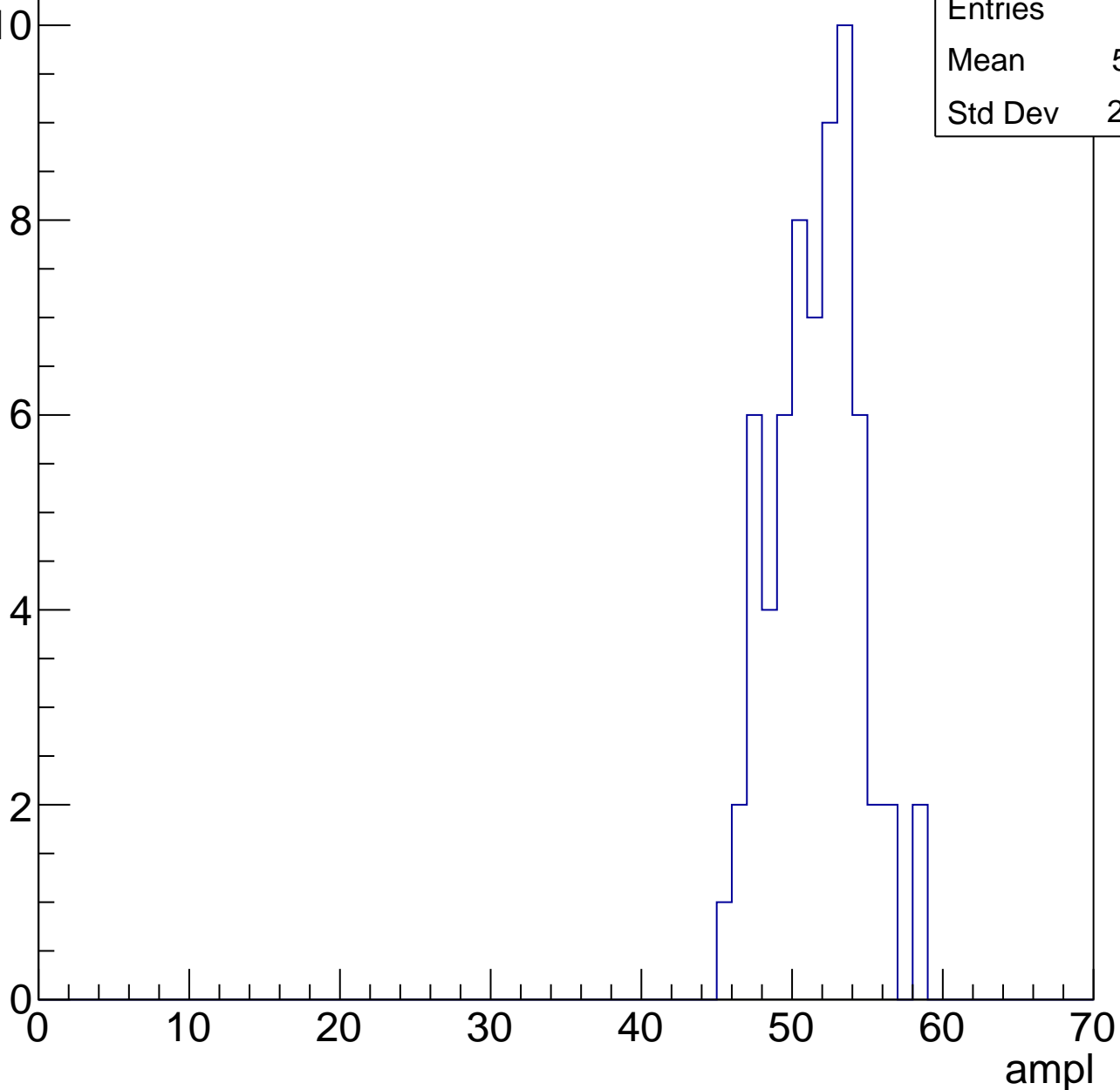


# B0L000S, U13-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	51.11
Std Dev	2.862

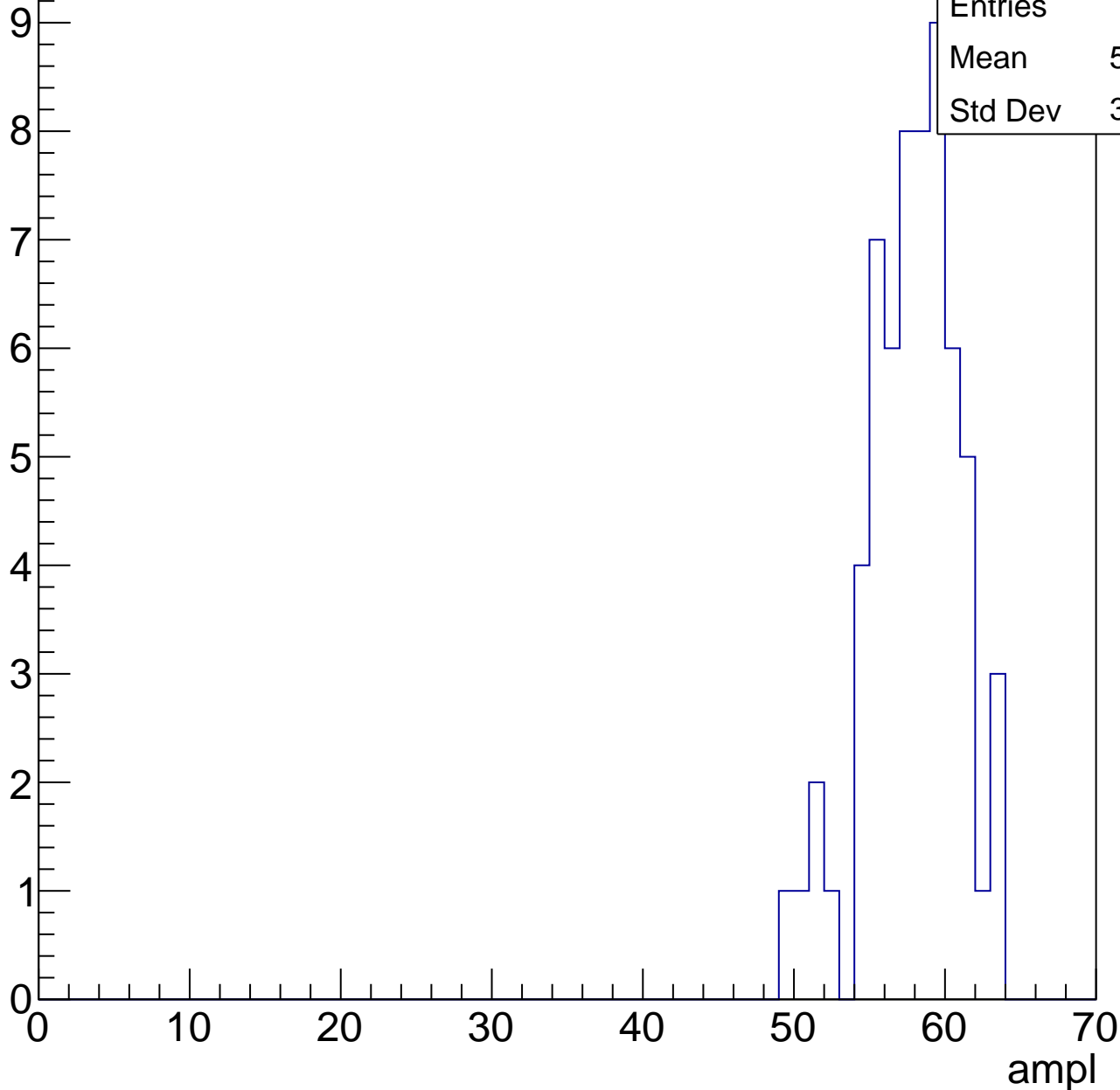


# B0L000S, U13-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	57.37
Std Dev	3.065

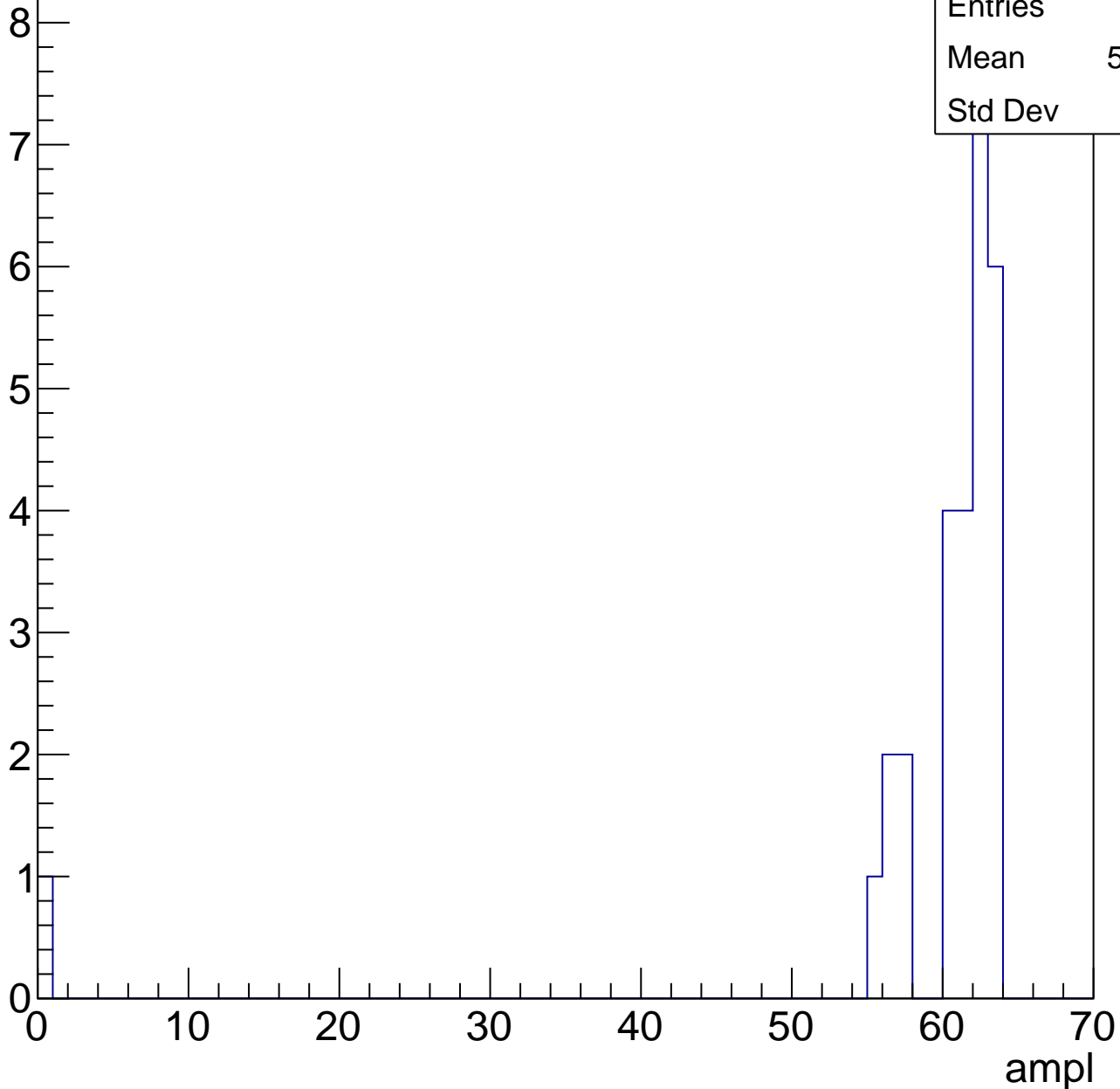


# B0L000S, U13-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

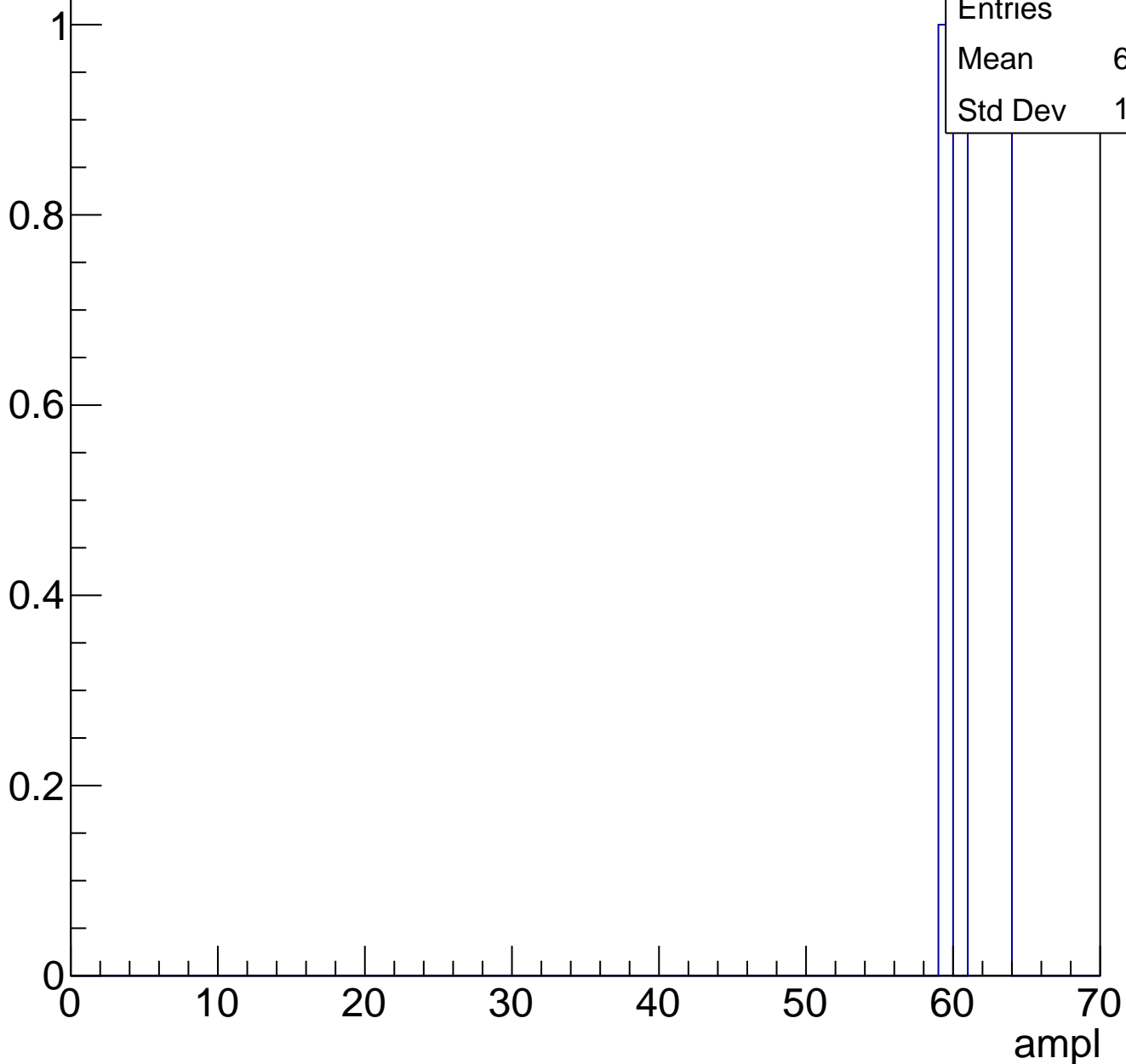
Entries	28
Mean	58.54
Std Dev	11.5



# B0L000S, U13-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

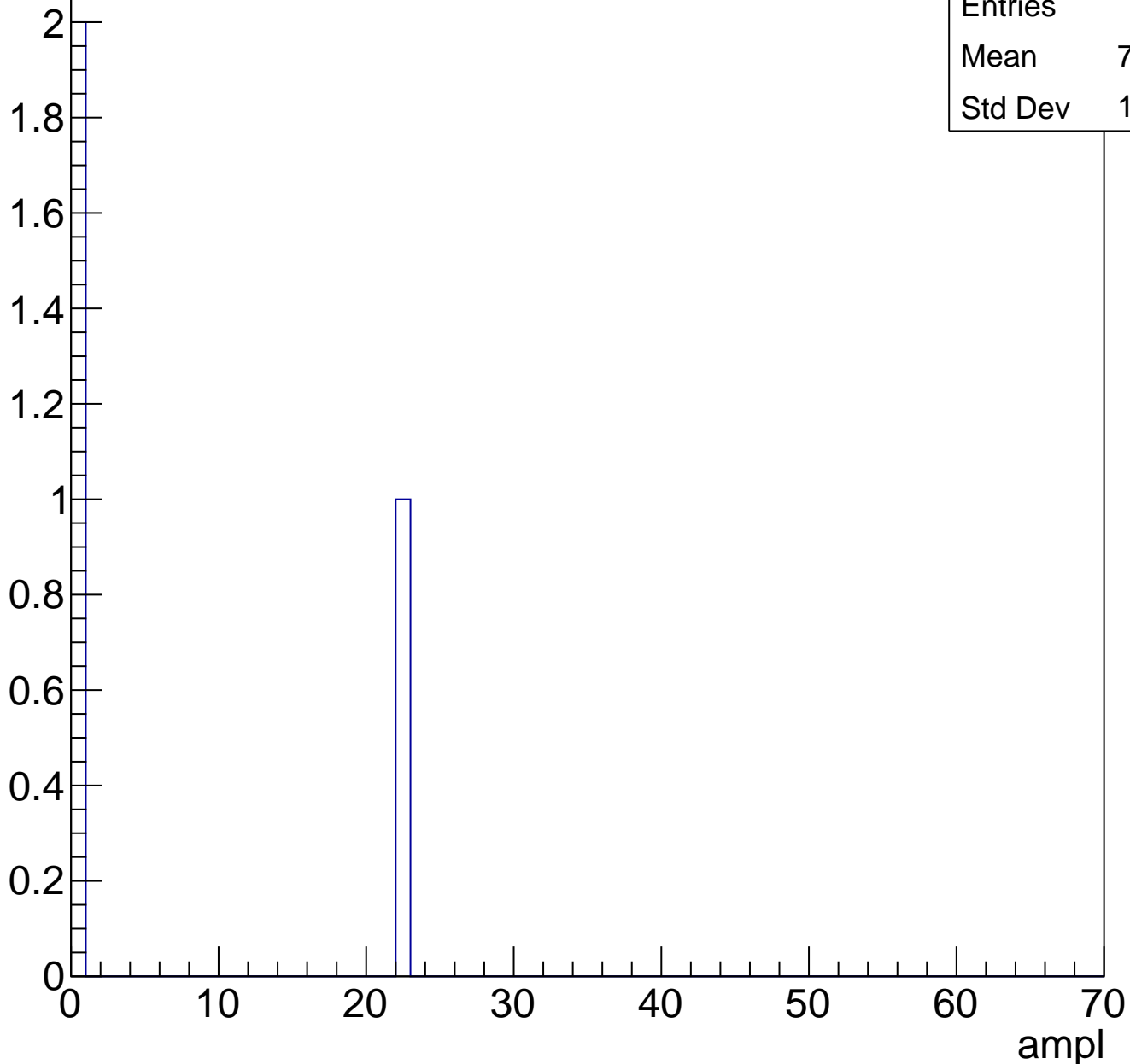




# B0L000S, U13-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U13-ch55, adc0

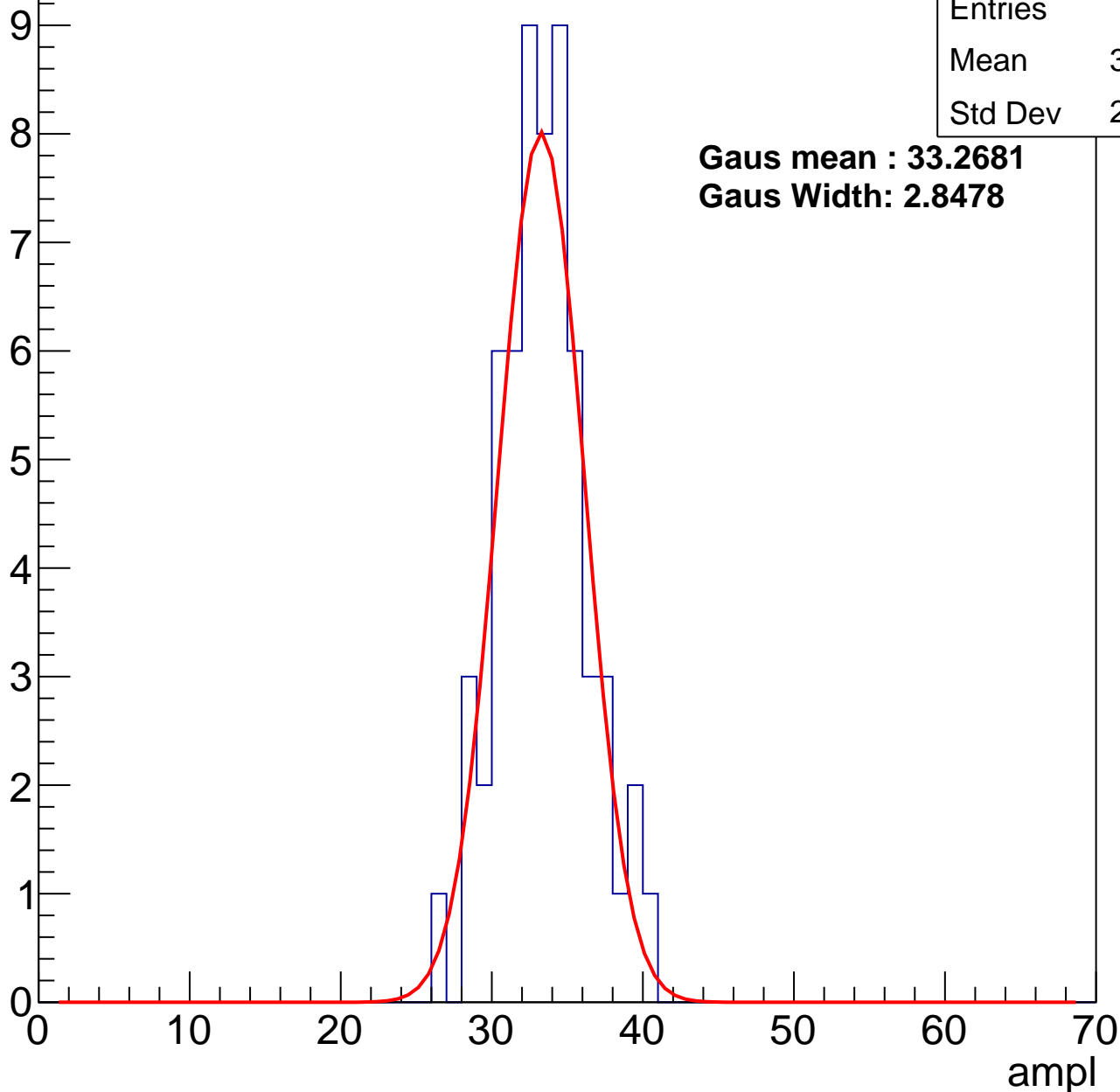
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	32.95
Std Dev	2.878

**Gaus mean : 33.2681**

**Gaus Width: 2.8478**



# B0L000S, U13-ch55, adc1

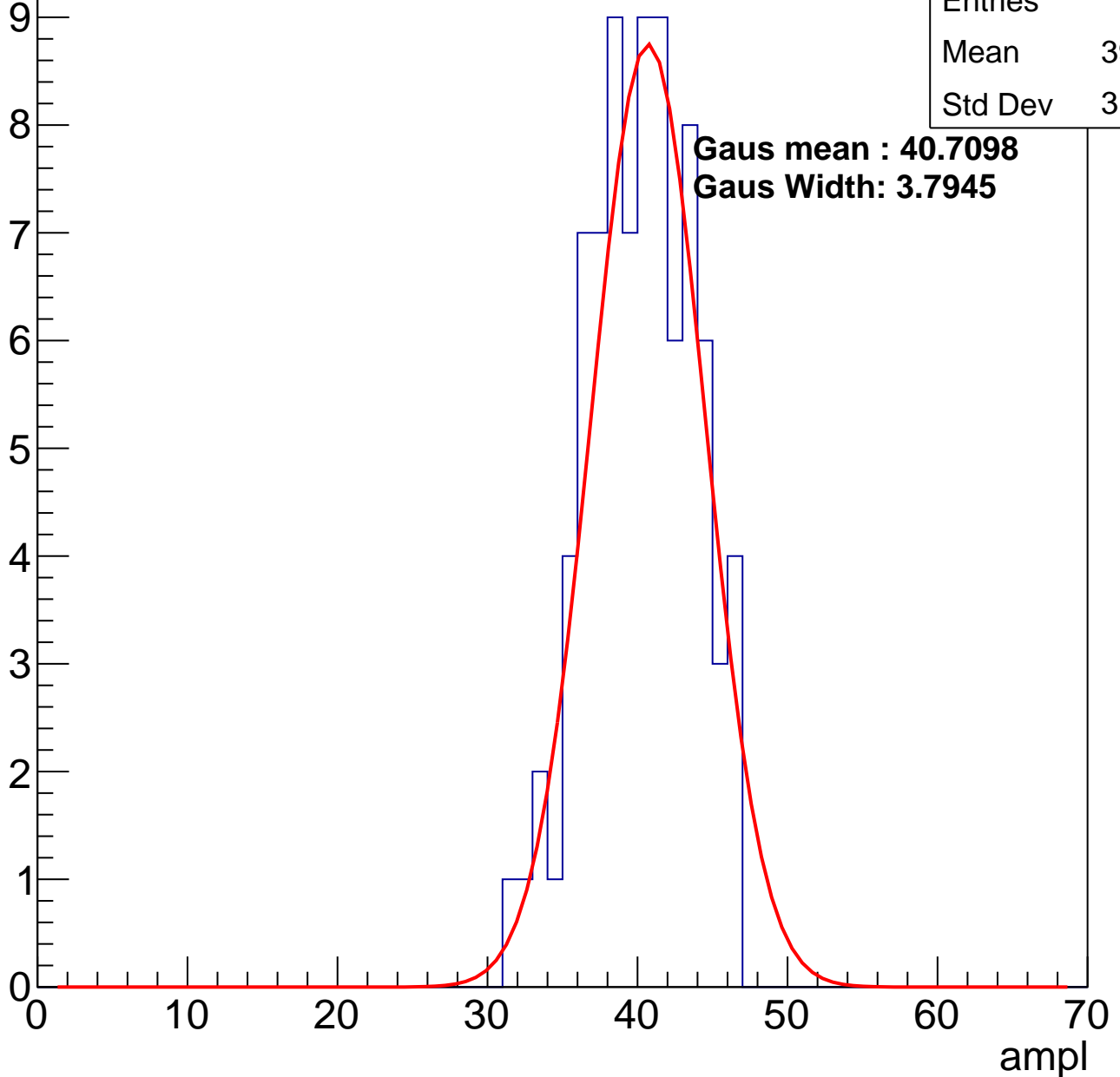
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	39.73
Std Dev	3.469

**Gaus mean : 40.7098**

**Gaus Width: 3.7945**



# B0L000S, U13-ch55, adc2

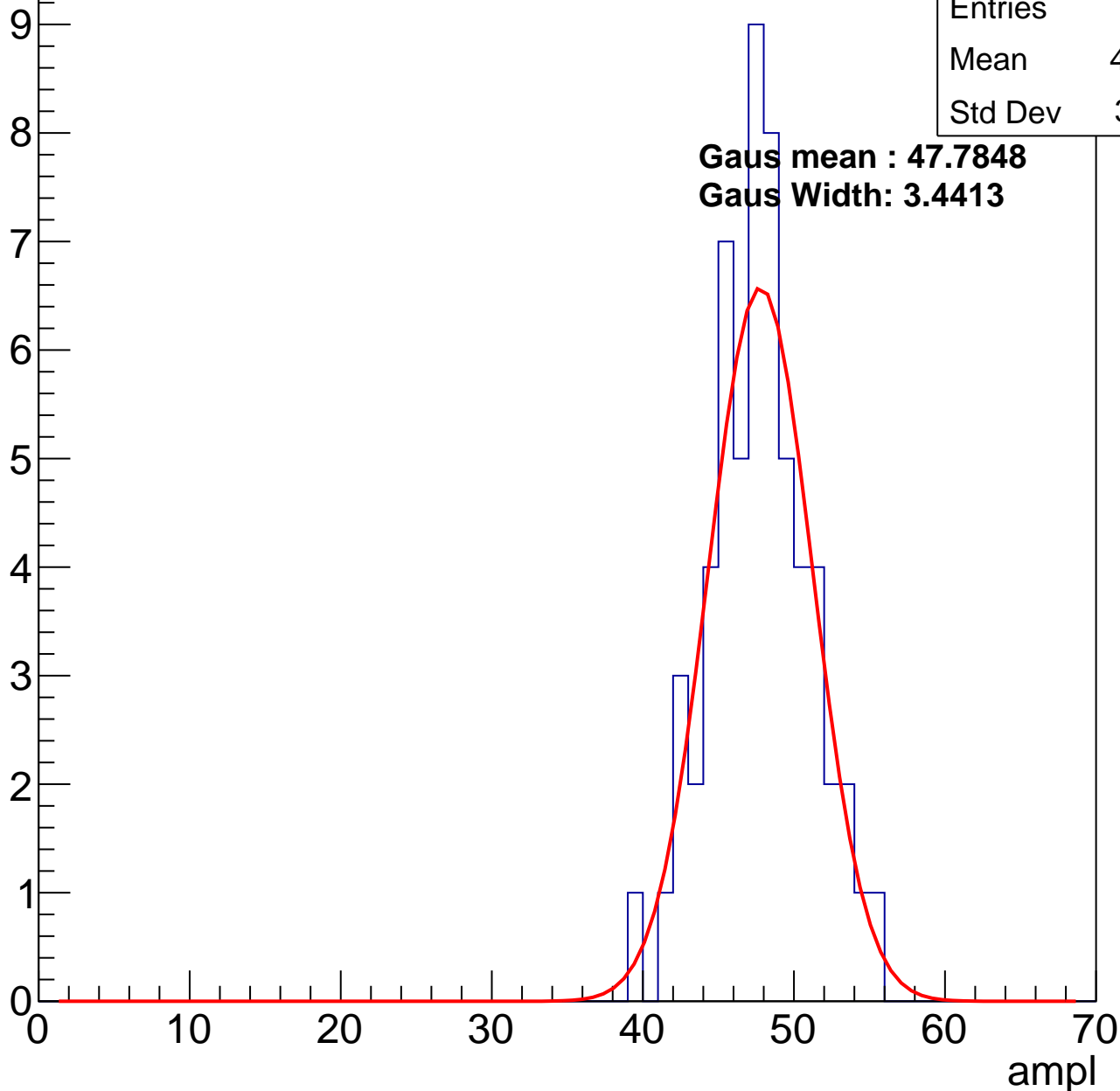
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	47.25
Std Dev	3.281

**Gaus mean : 47.7848**

**Gaus Width: 3.4413**

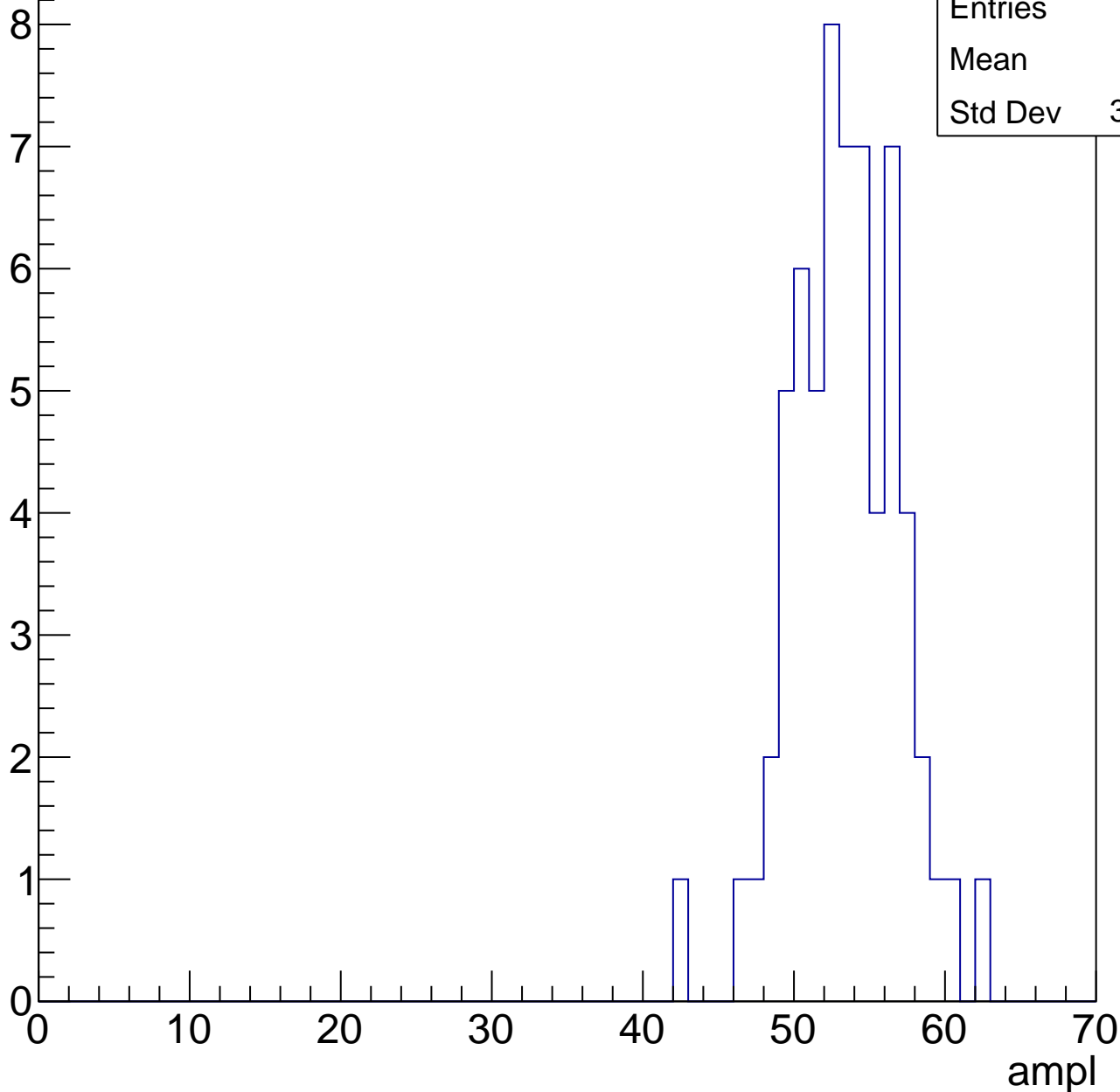


# B0L000S, U13-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

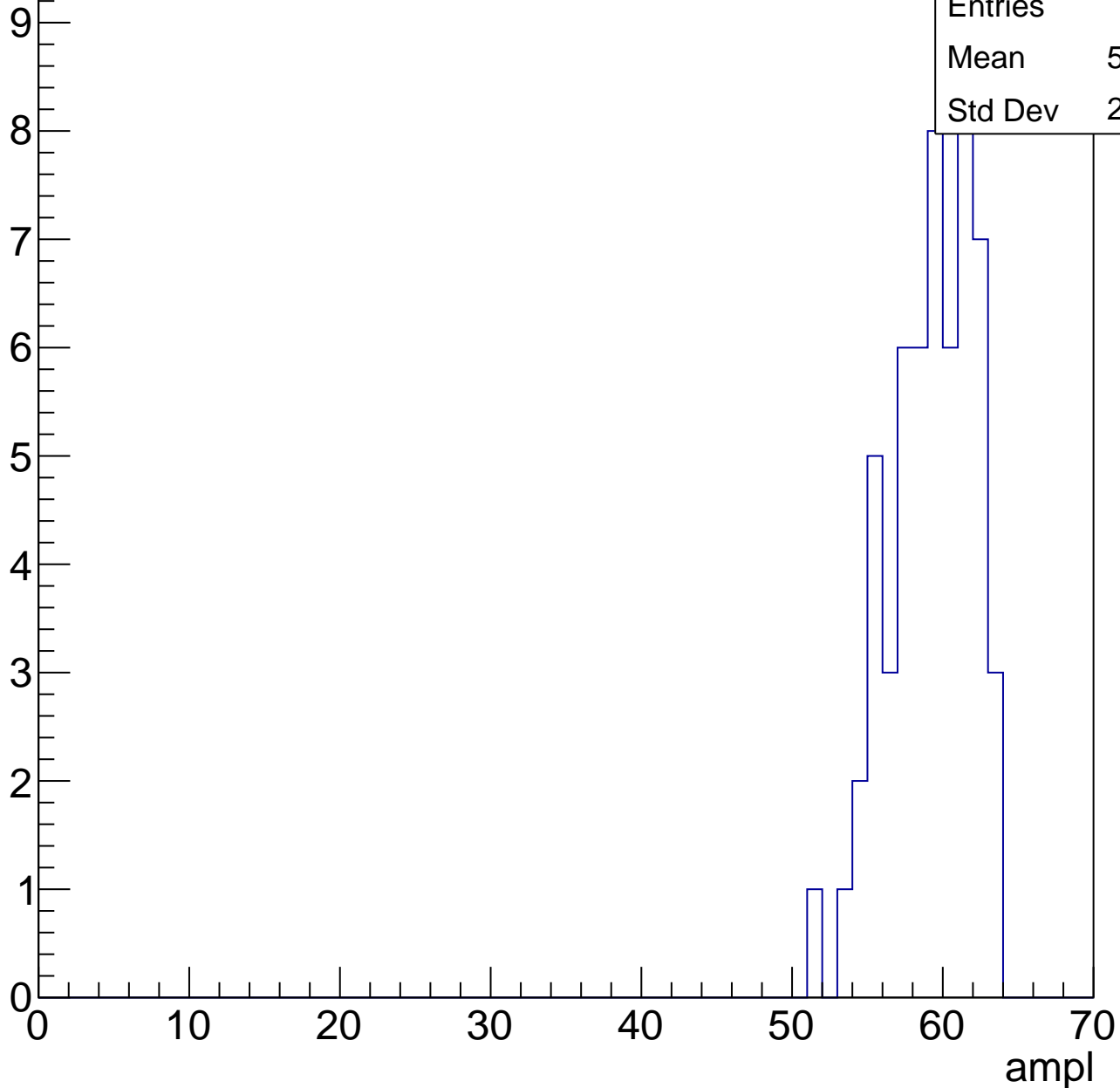
Entries	63
Mean	52.9
Std Dev	3.526



# B0L000S, U13-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

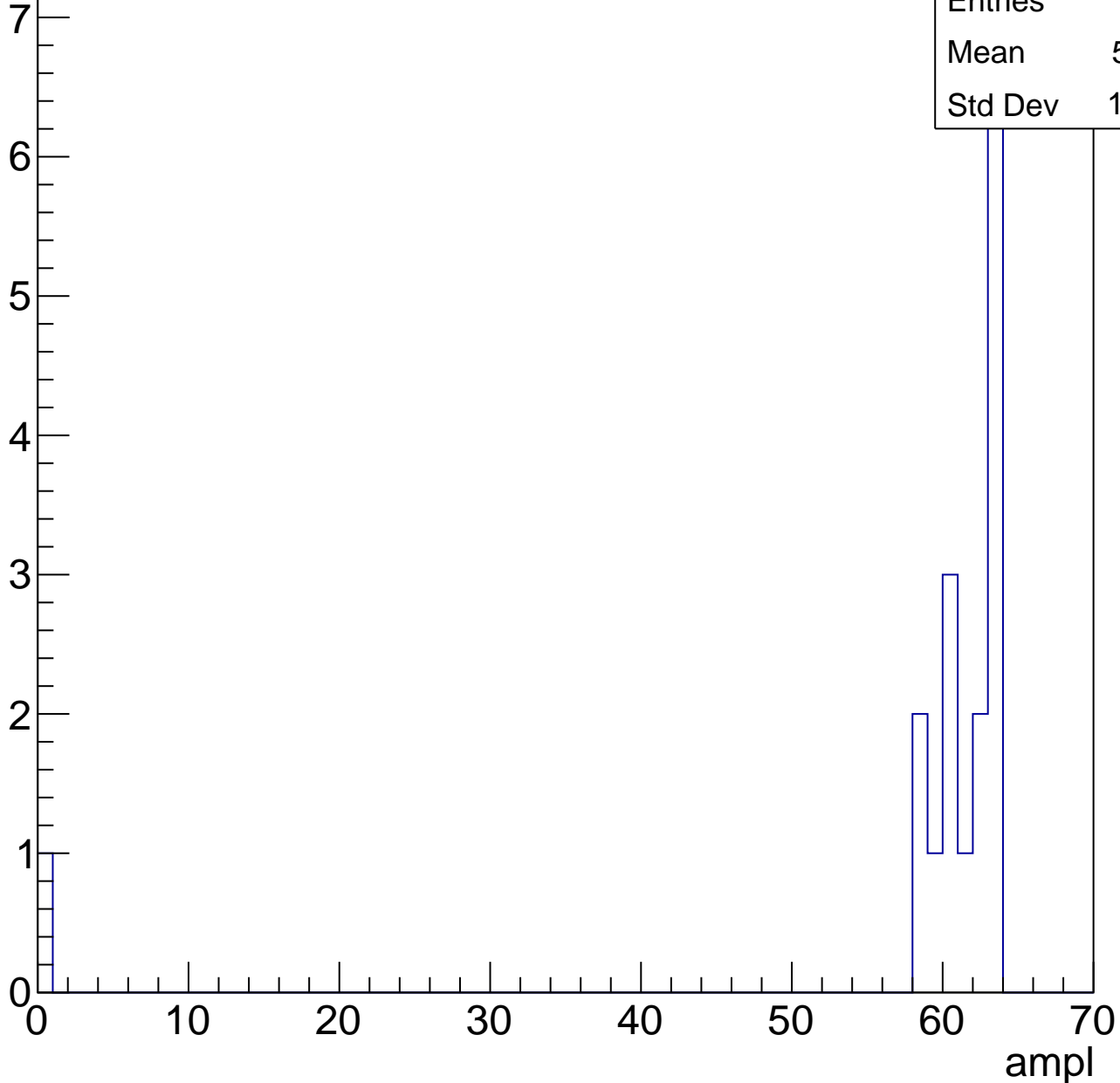


# B0L000S, U13-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.71
Std Dev	14.54



# B0L000S, U13-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



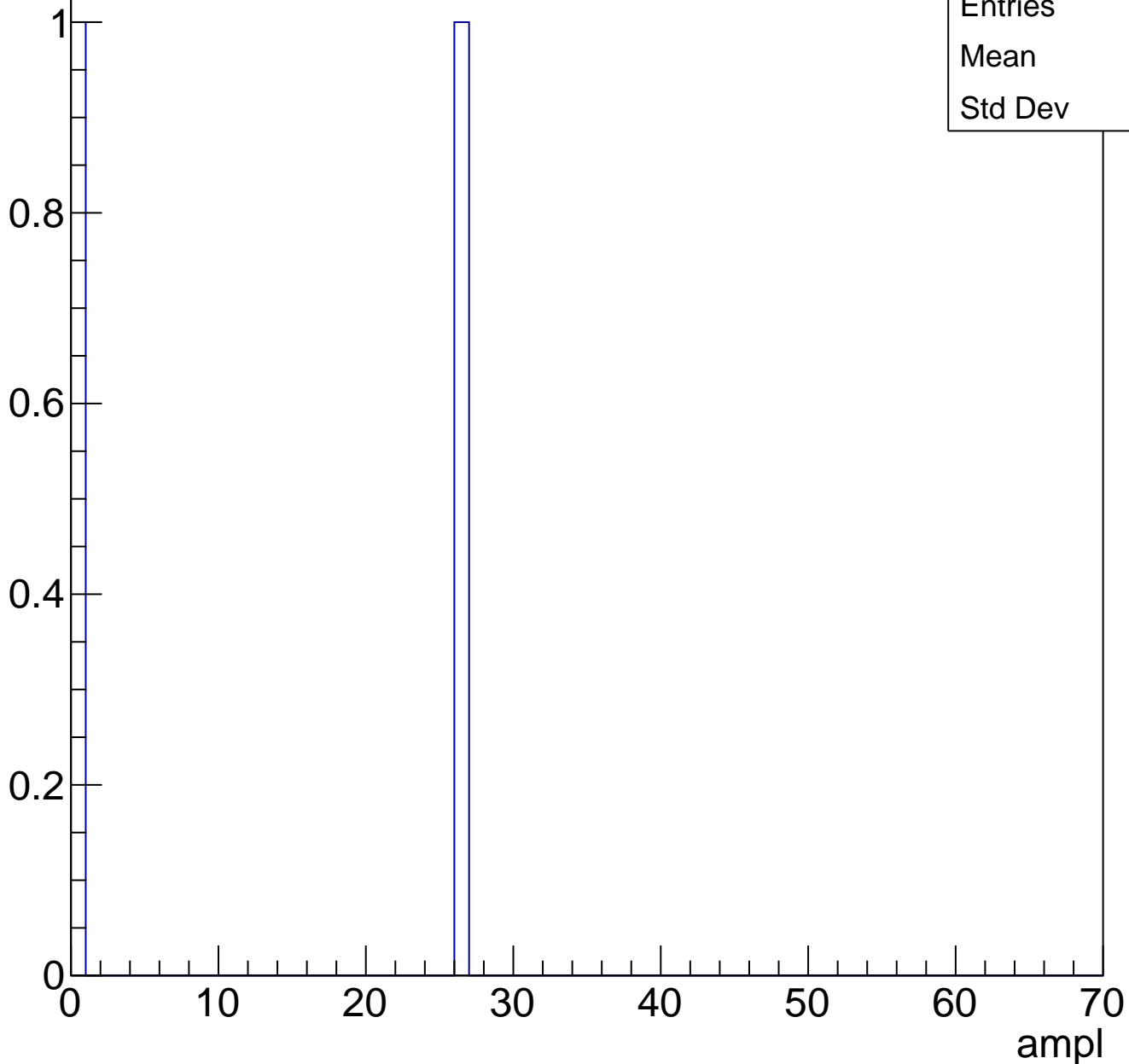
Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch56, adc0

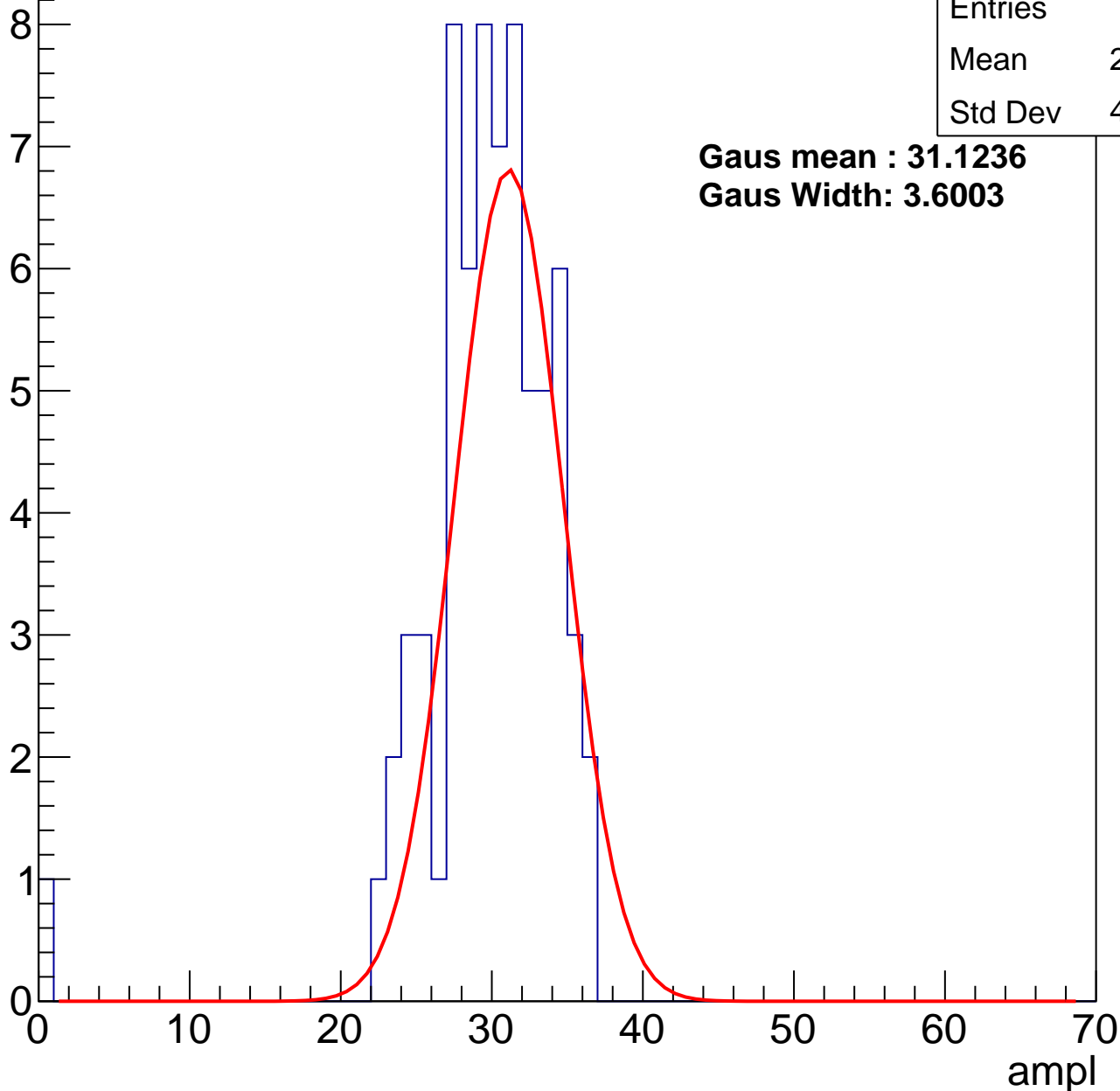
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.29
Std Dev	4.884

**Gaus mean : 31.1236**

**Gaus Width: 3.6003**



# B0L000S, U13-ch56, adc1

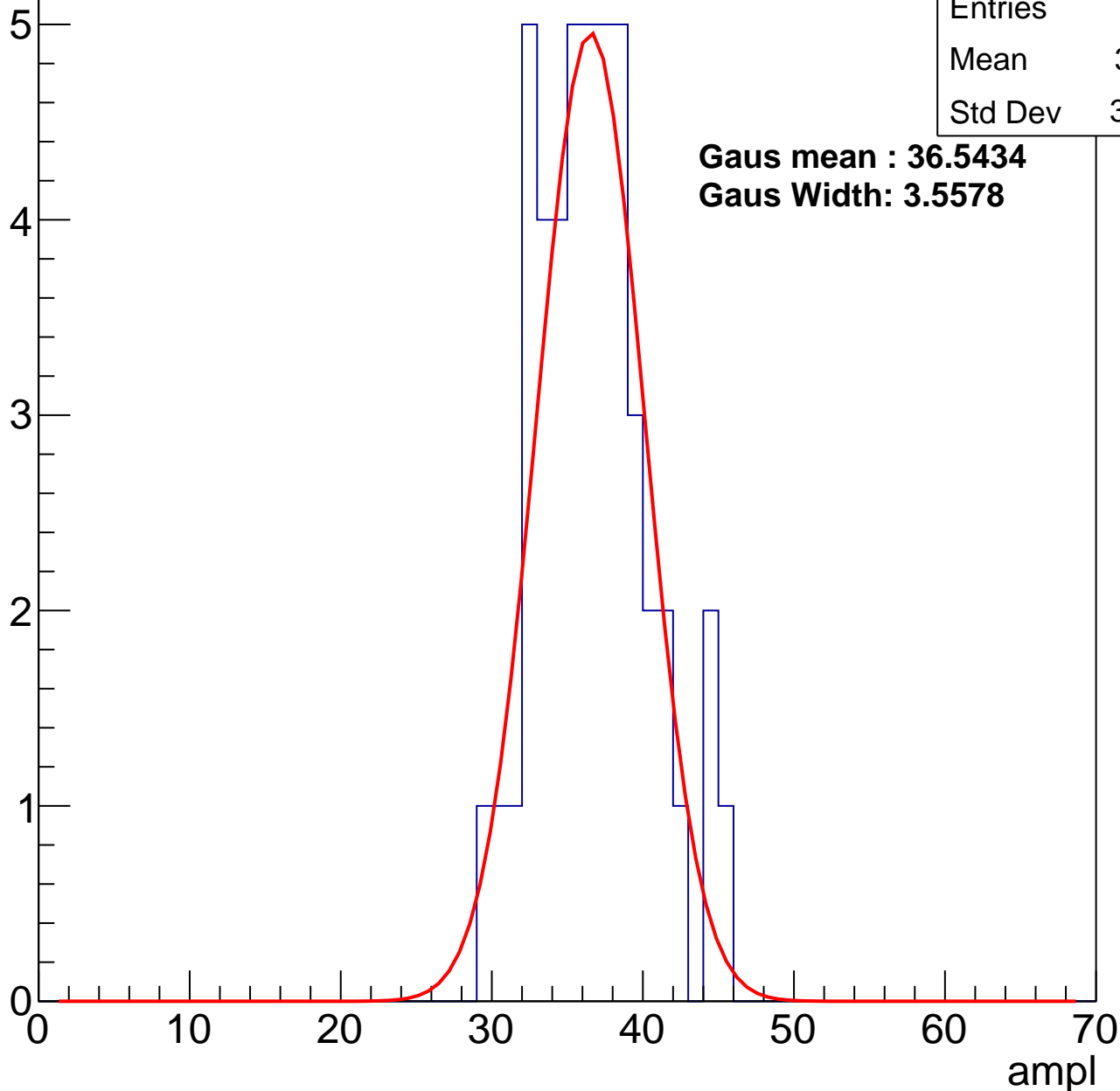
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	36.21
Std Dev	3.643

**Gaus mean : 36.5434**

**Gaus Width: 3.5578**



# B0L000S, U13-ch56, adc2

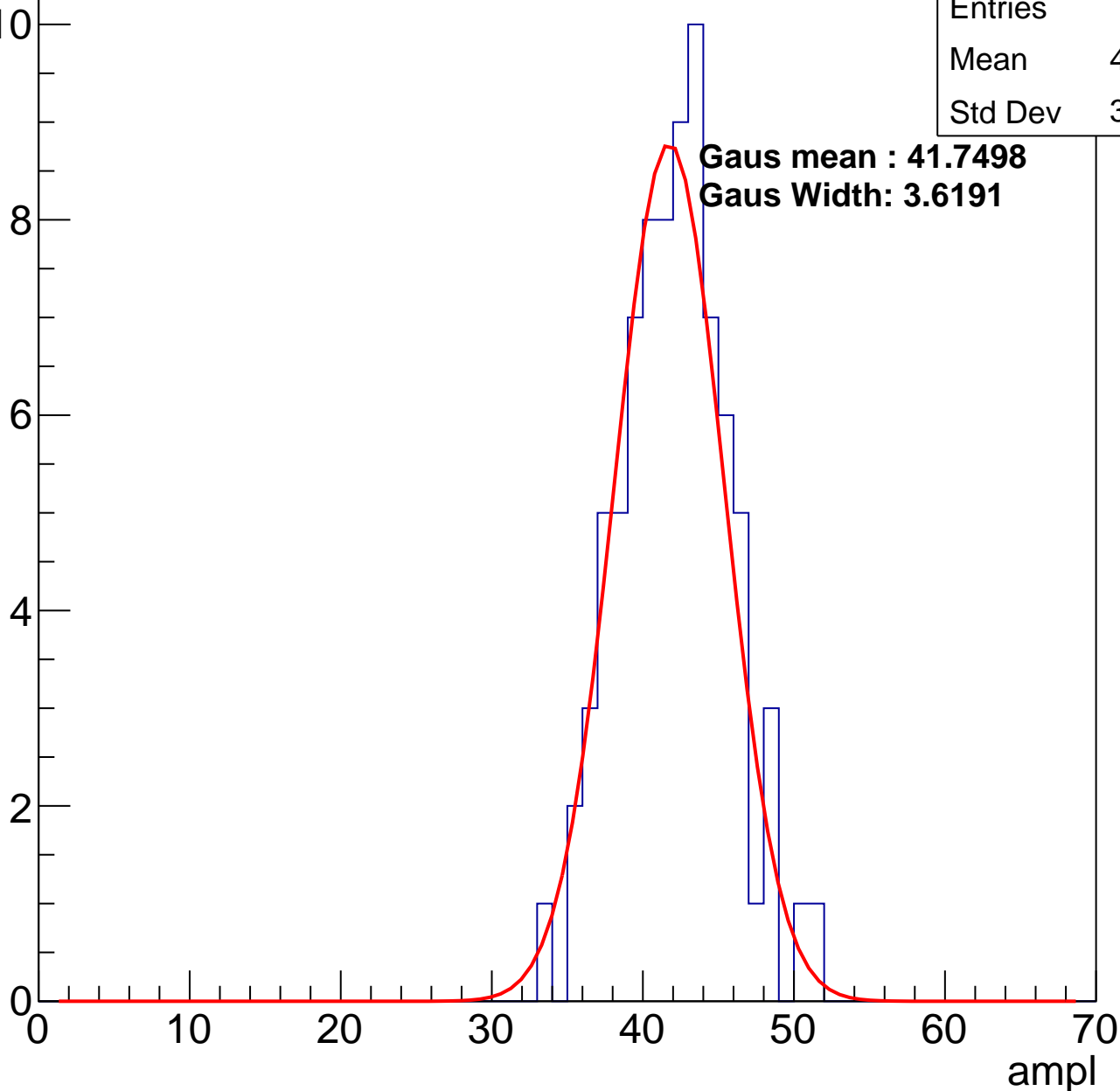
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	41.65
Std Dev	3.552

**Gaus mean : 41.7498**

**Gaus Width: 3.6191**

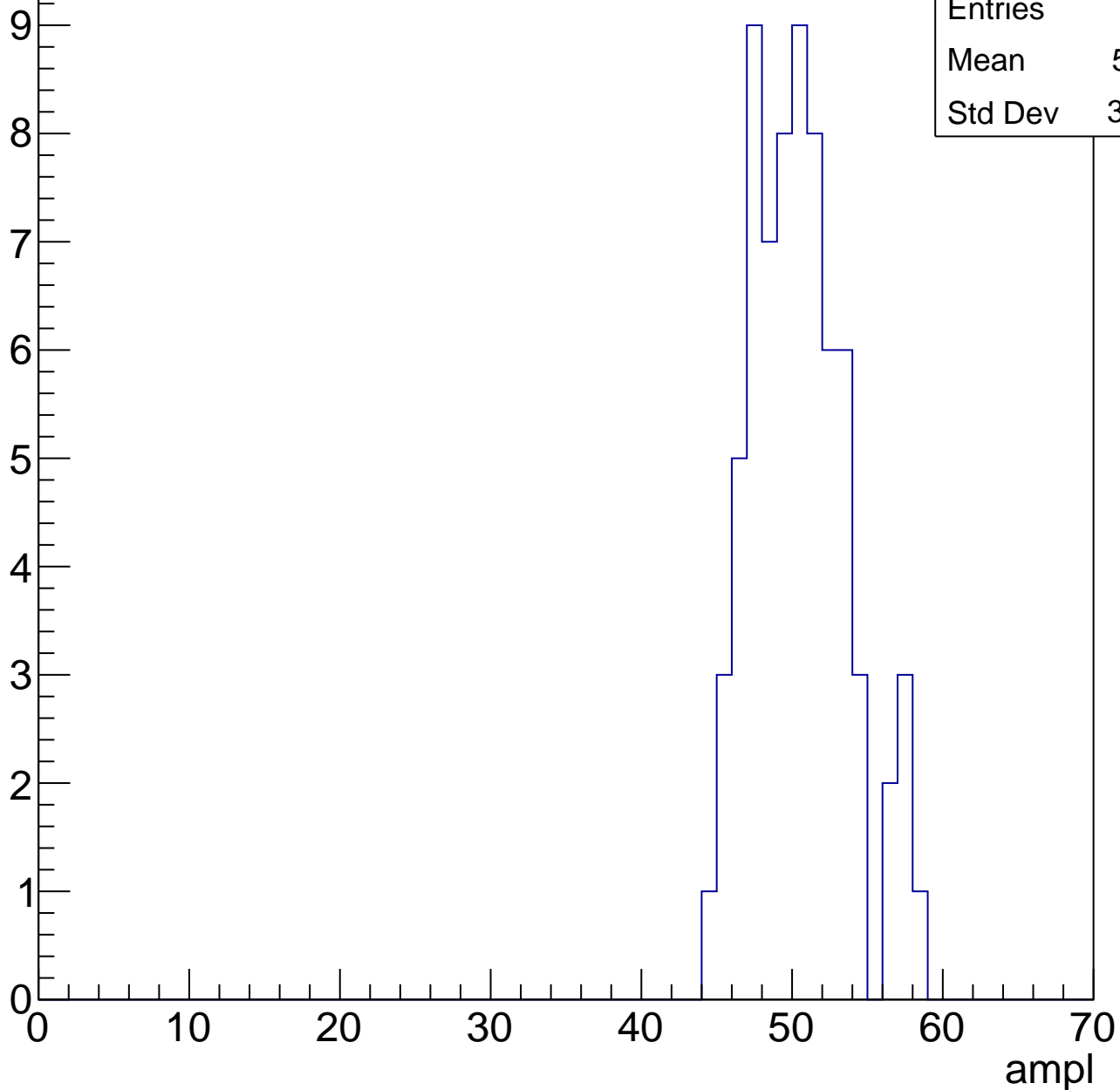


# B0L000S, U13-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

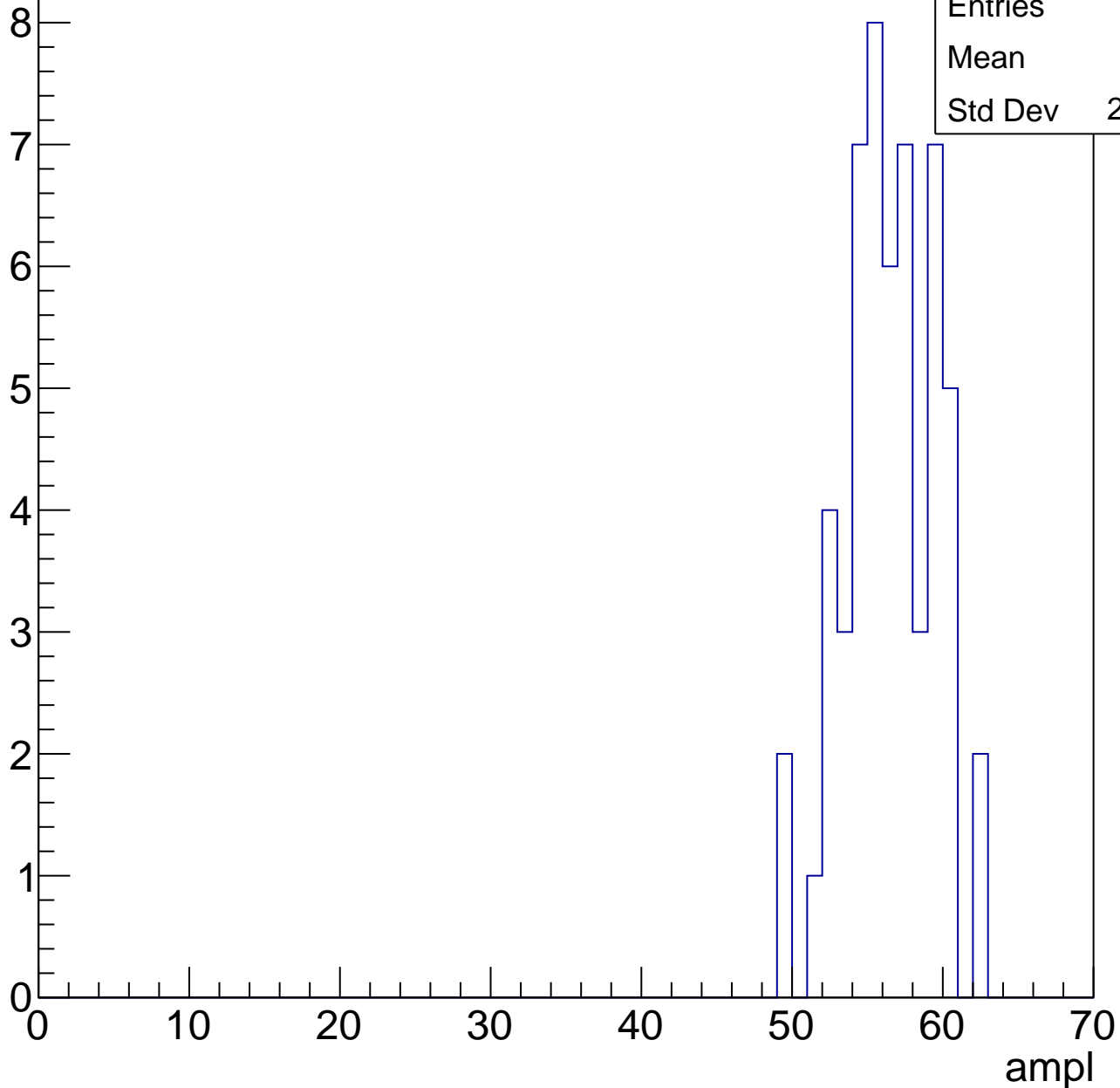
Entries	71
Mean	50.01
Std Dev	3.195



# B0L000S, U13-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

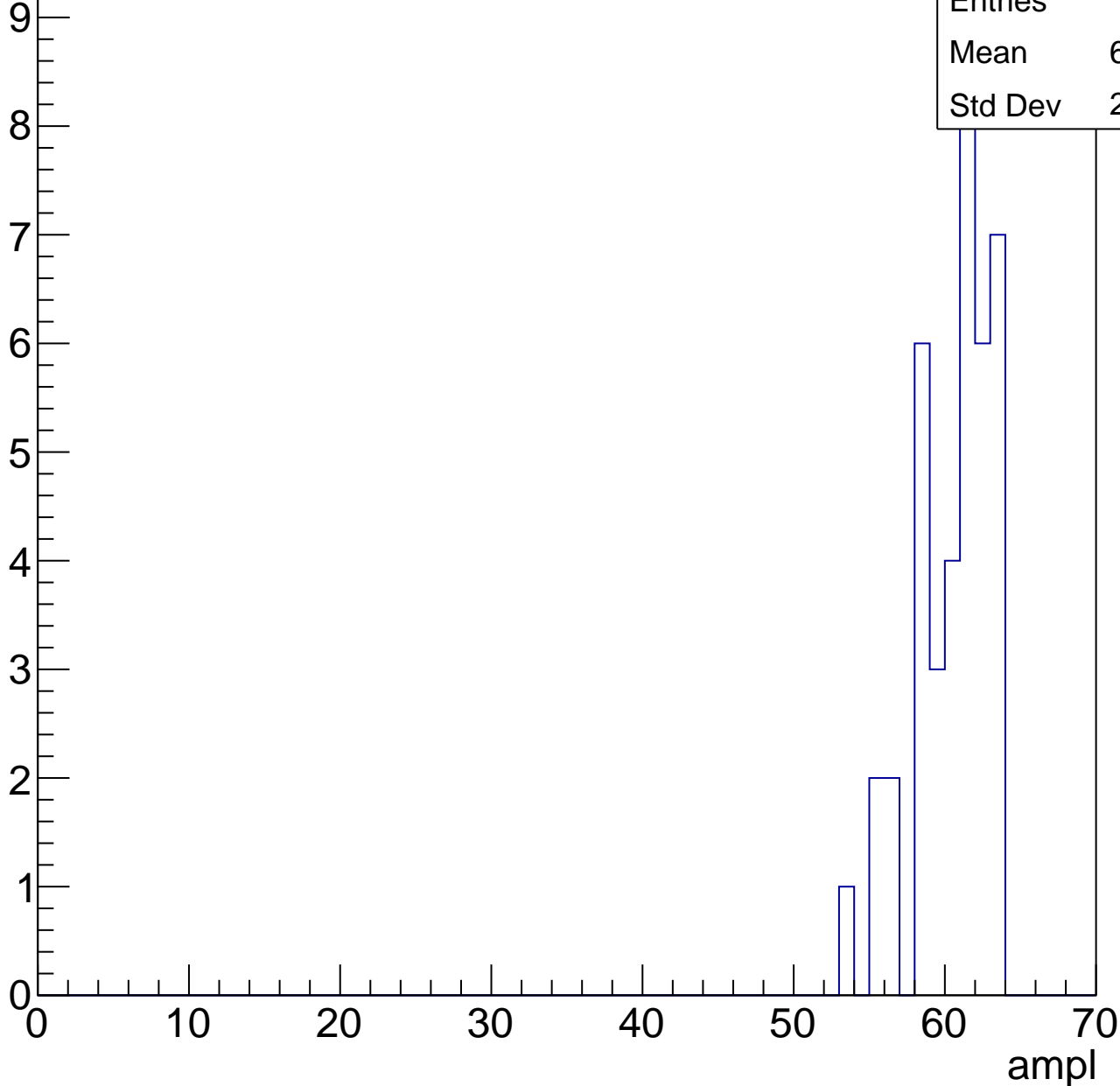


# B0L000S, U13-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

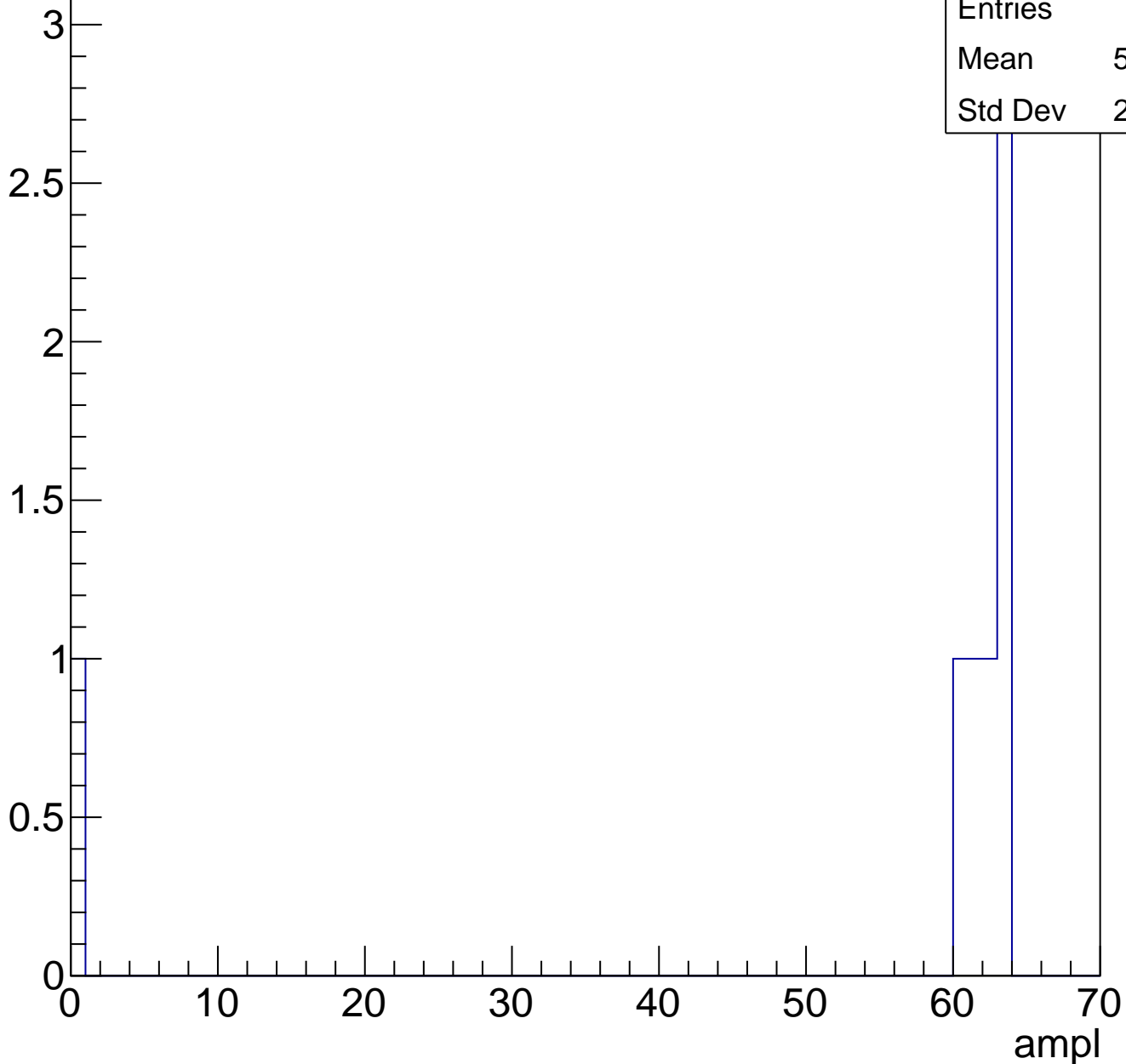
Entries	40
Mean	60.05
Std Dev	2.519



# B0L000S, U13-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch57, adc0

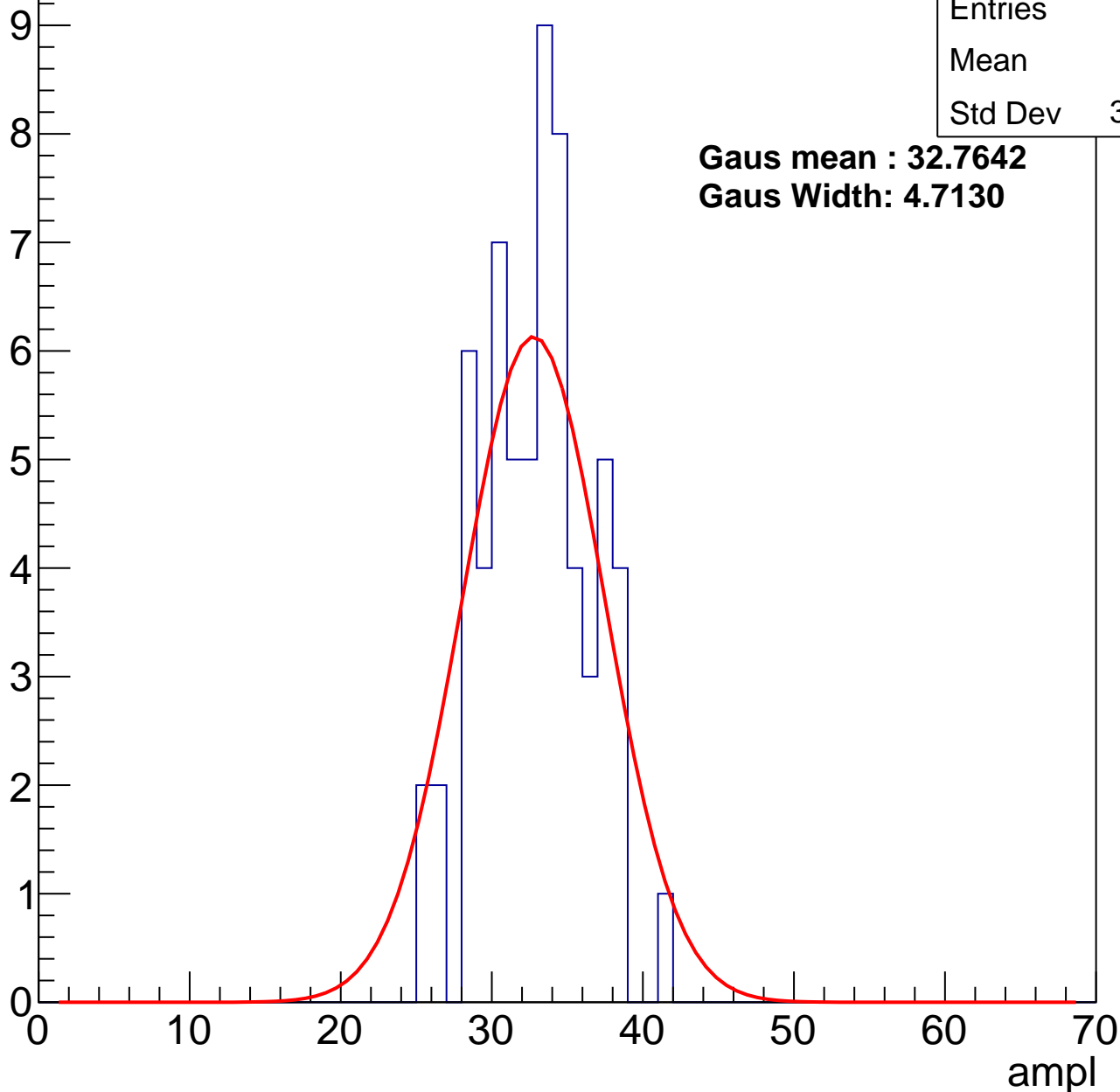
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	32.4
Std Dev	3.507

**Gaus mean : 32.7642**

**Gaus Width: 4.7130**



# B0L000S, U13-ch57, adc1

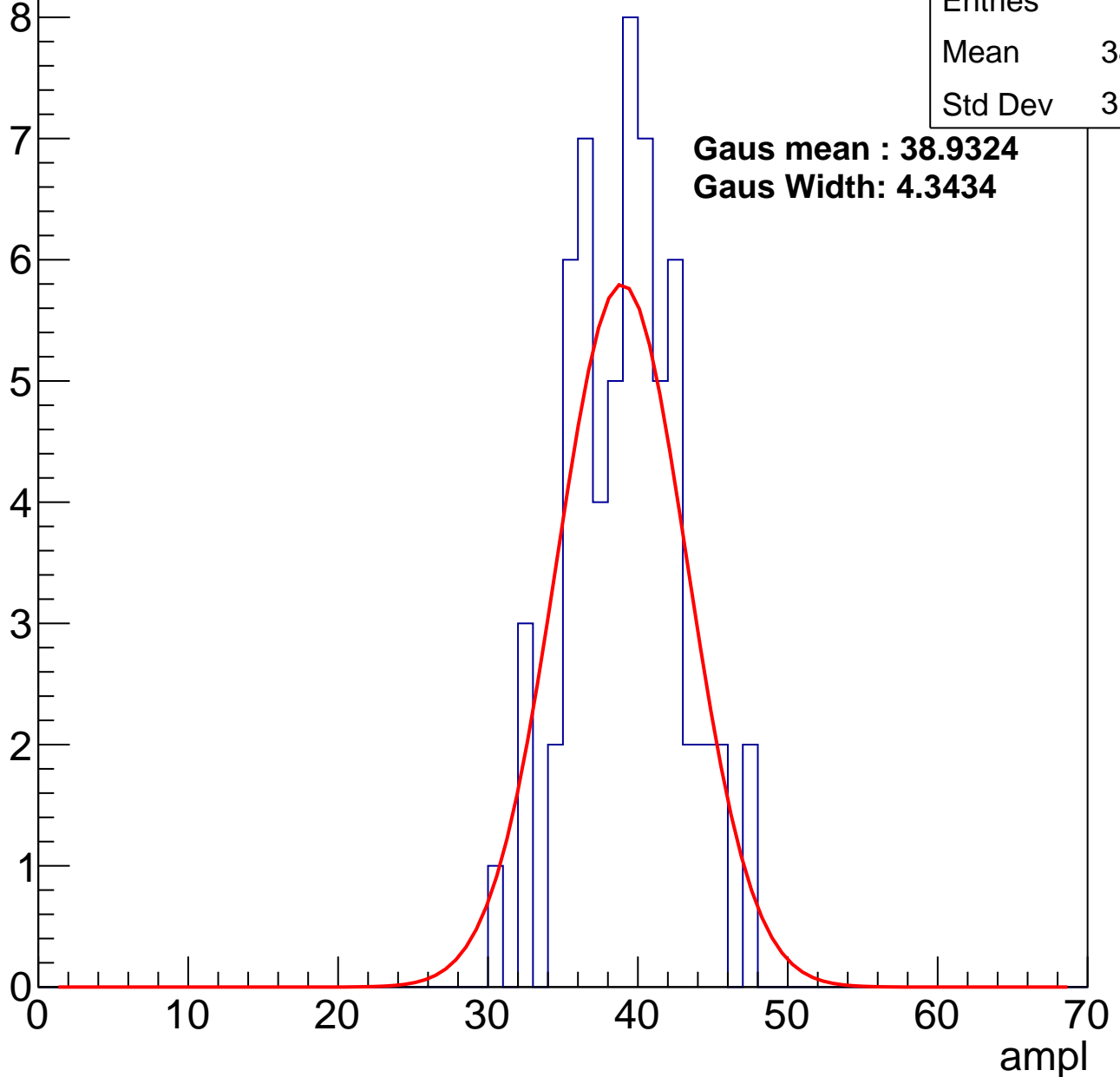
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.73
Std Dev	3.629

**Gaus mean : 38.9324**

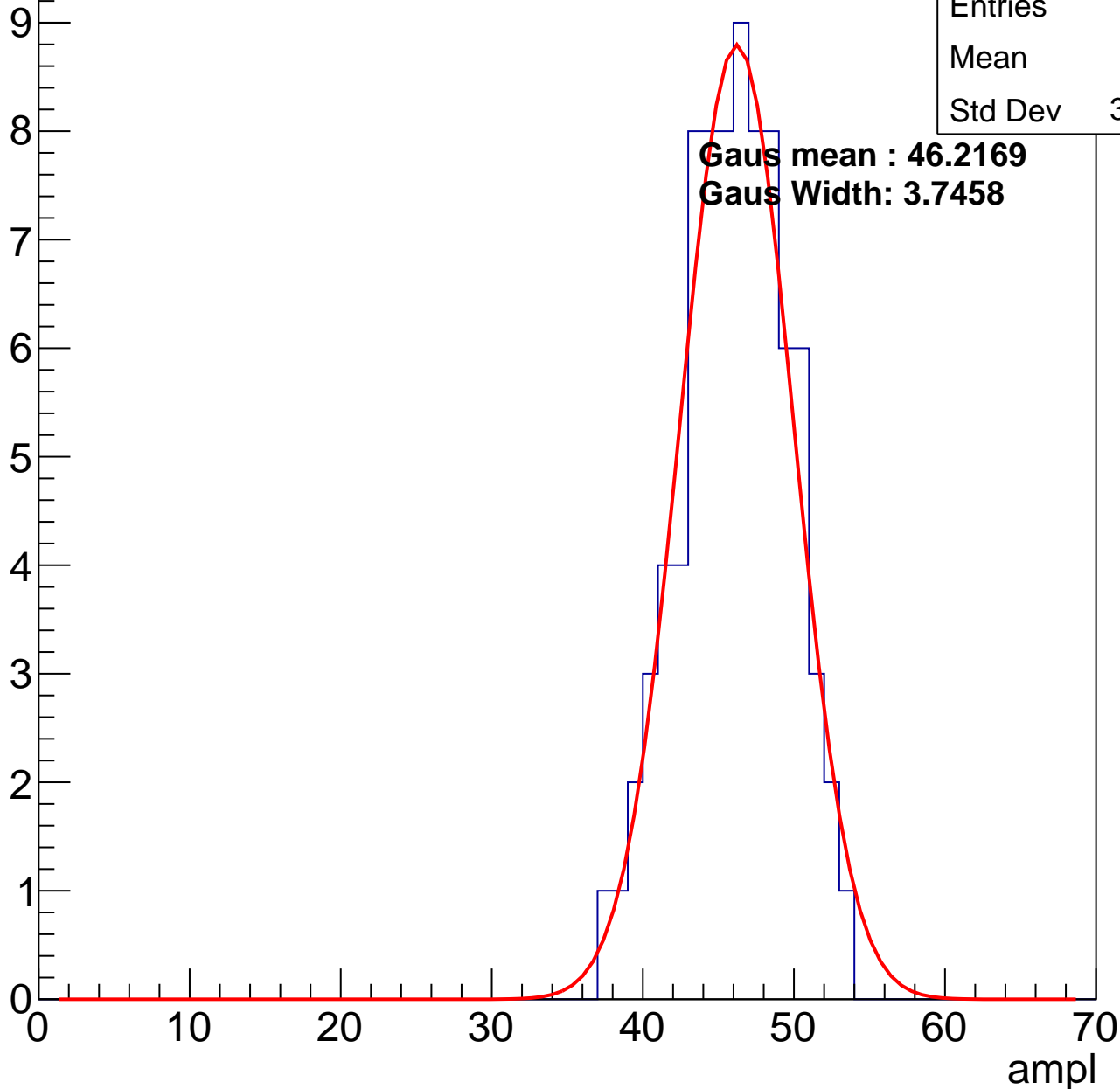
**Gaus Width: 4.3434**



# B0L000S, U13-ch57, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

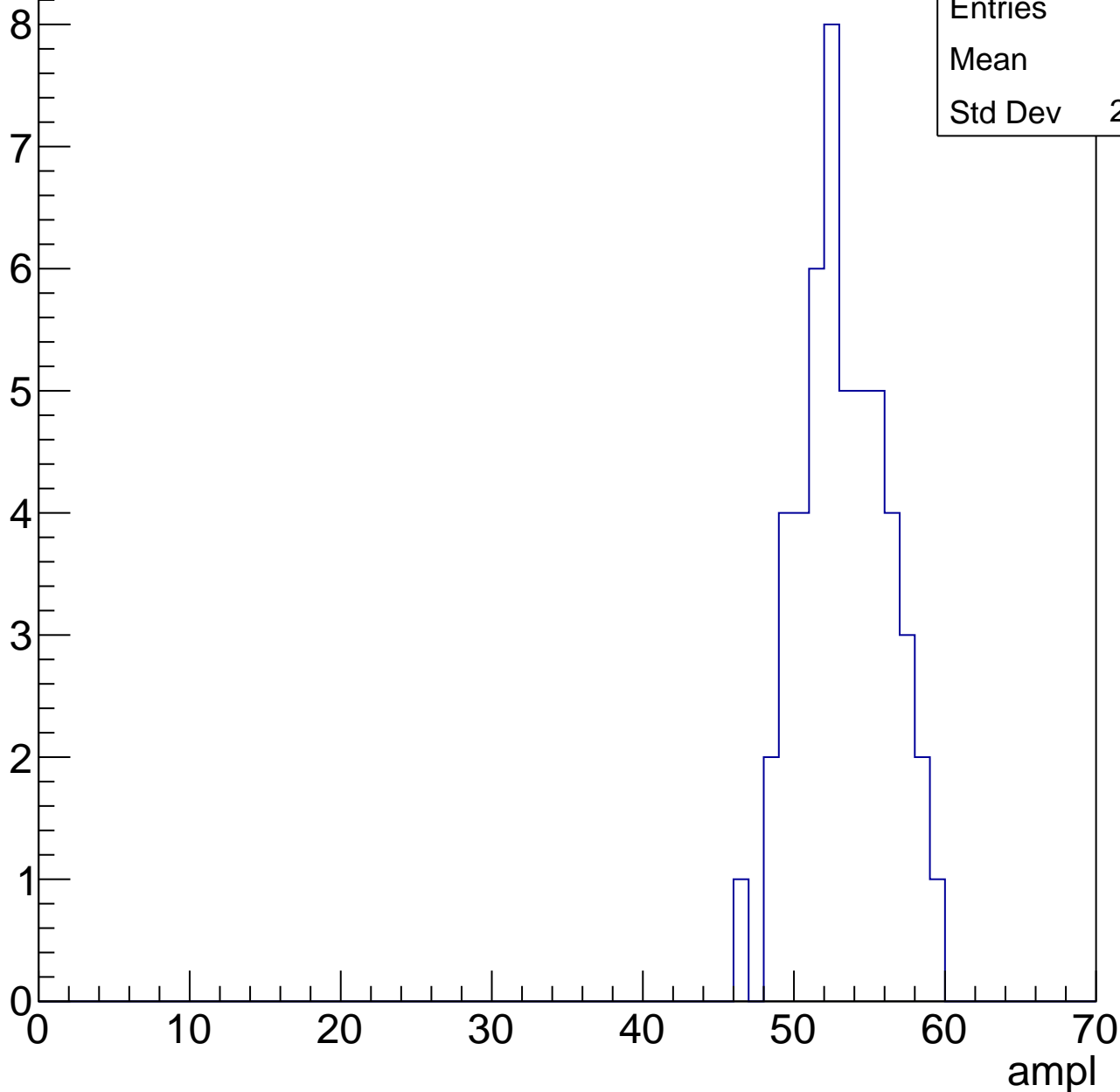


# B0L000S, U13-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	52.8
Std Dev	2.912

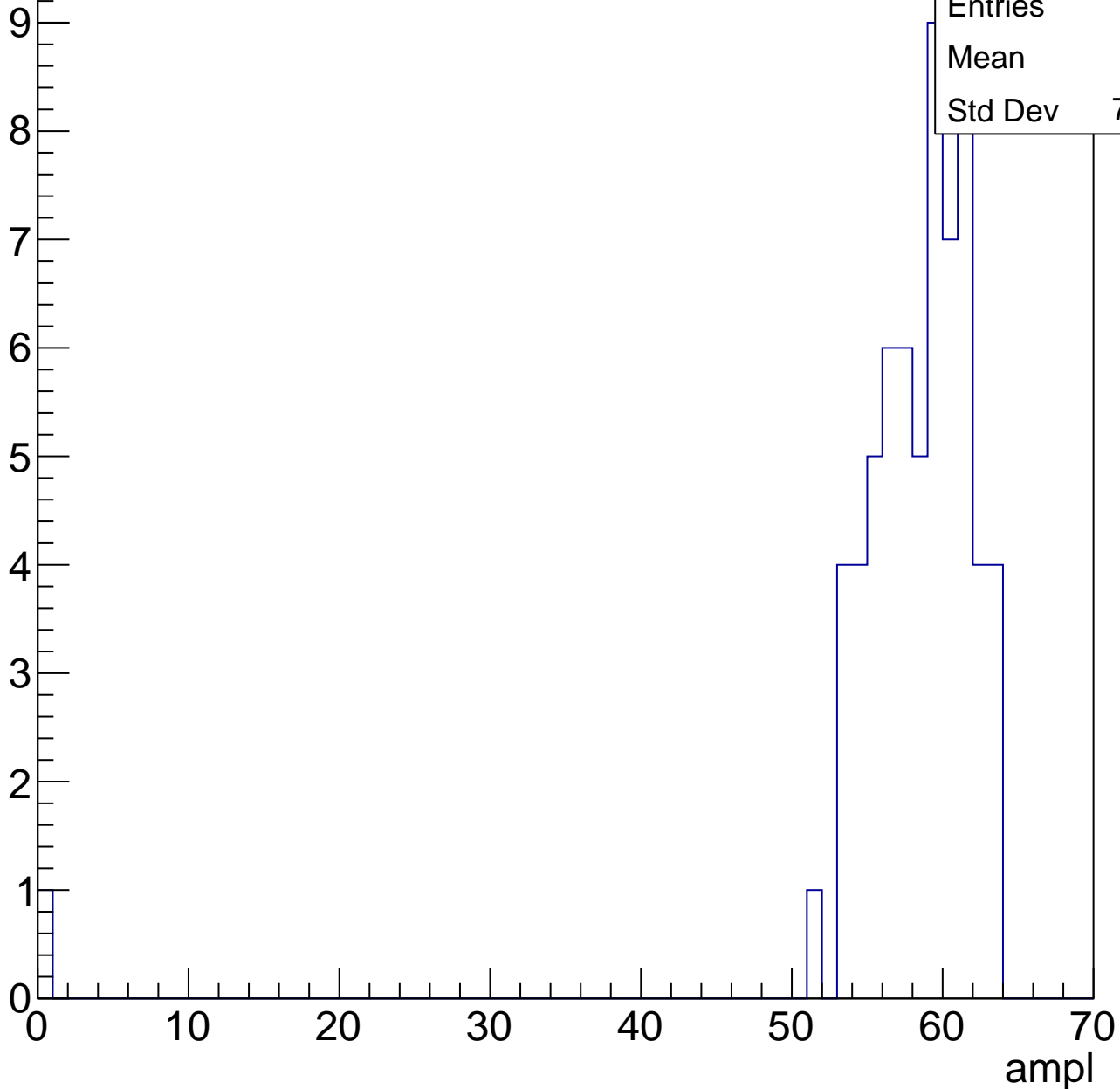


# B0L000S, U13-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	57.2
Std Dev	7.791

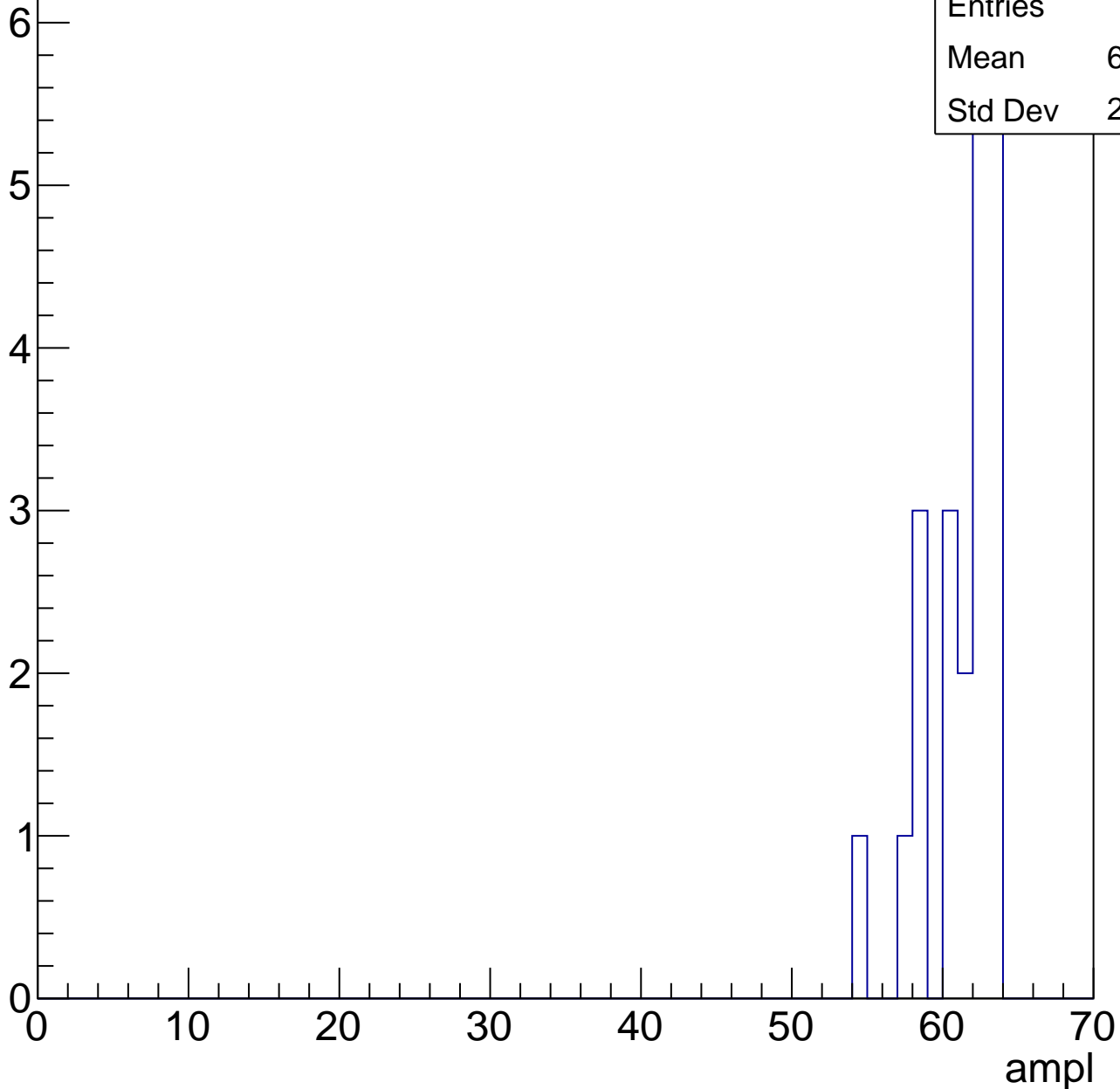


# B0L000S, U13-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	60.77
Std Dev	2.373



# B0L000S, U13-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch58, adc0

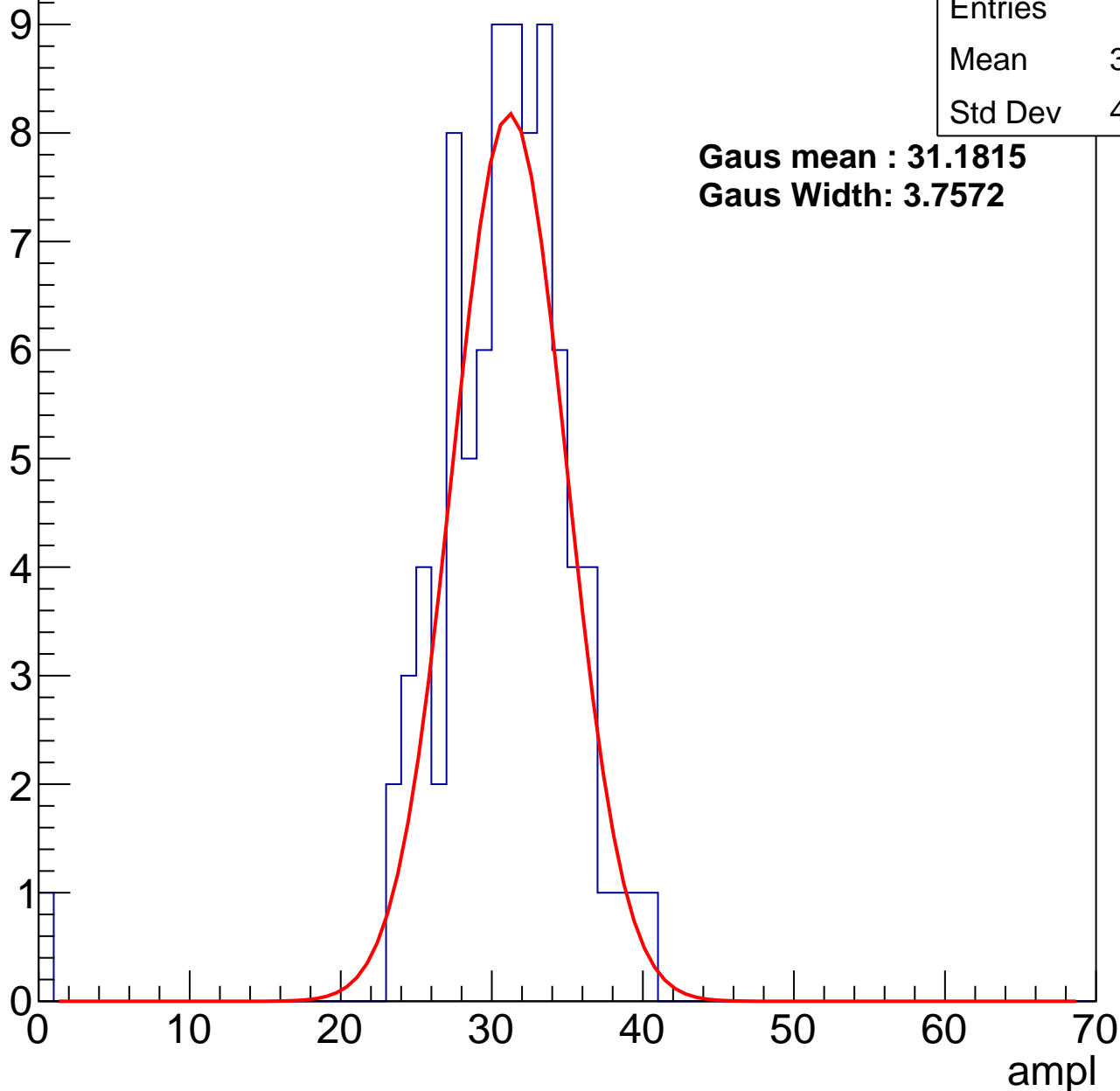
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	30.29
Std Dev	4.982

**Gaus mean : 31.1815**

**Gaus Width: 3.7572**



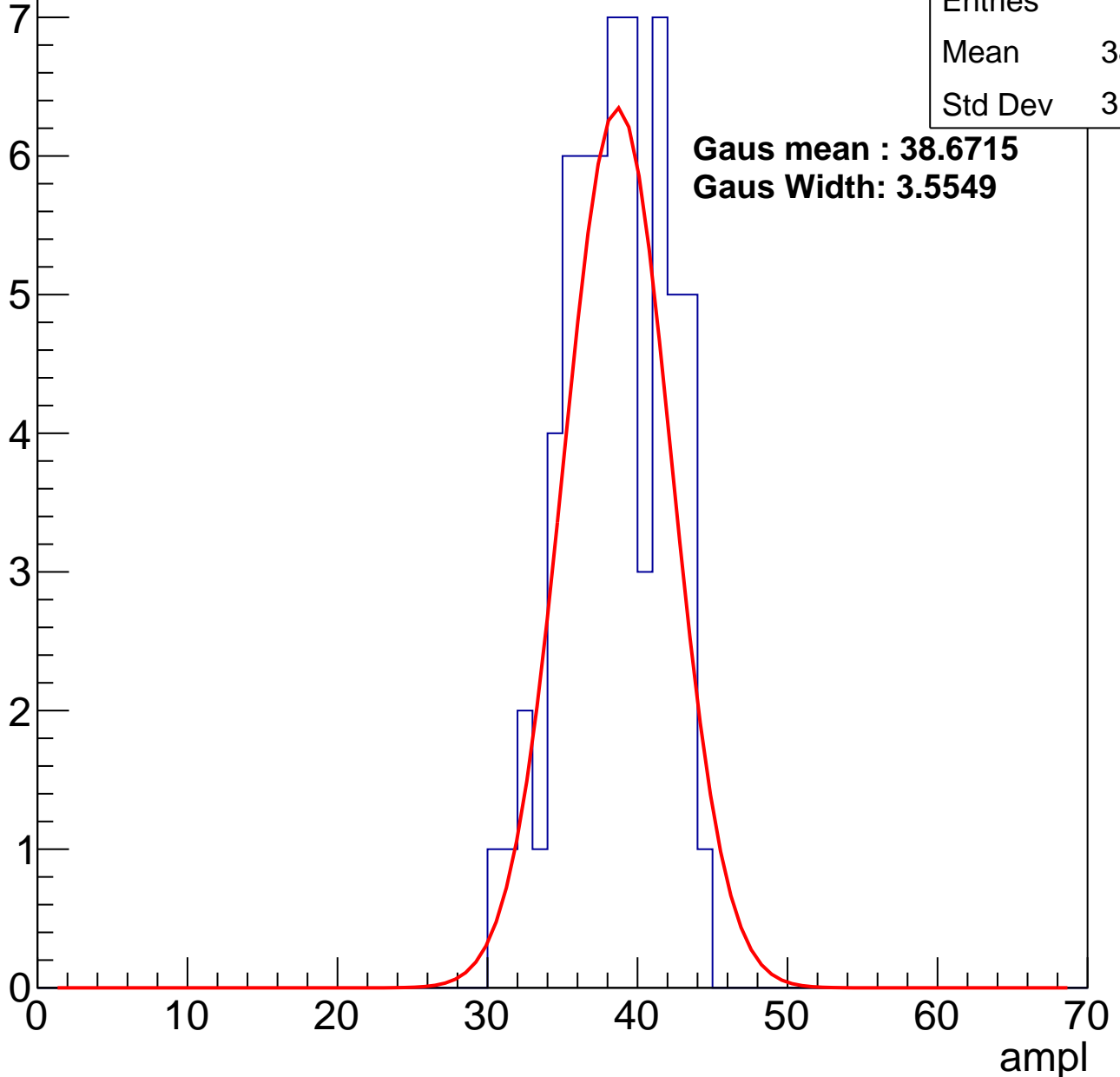
# B0L000S, U13-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.02
Std Dev	3.314

**Gaus mean : 38.6715**  
**Gaus Width: 3.5549**



# B0L000S, U13-ch58, adc2

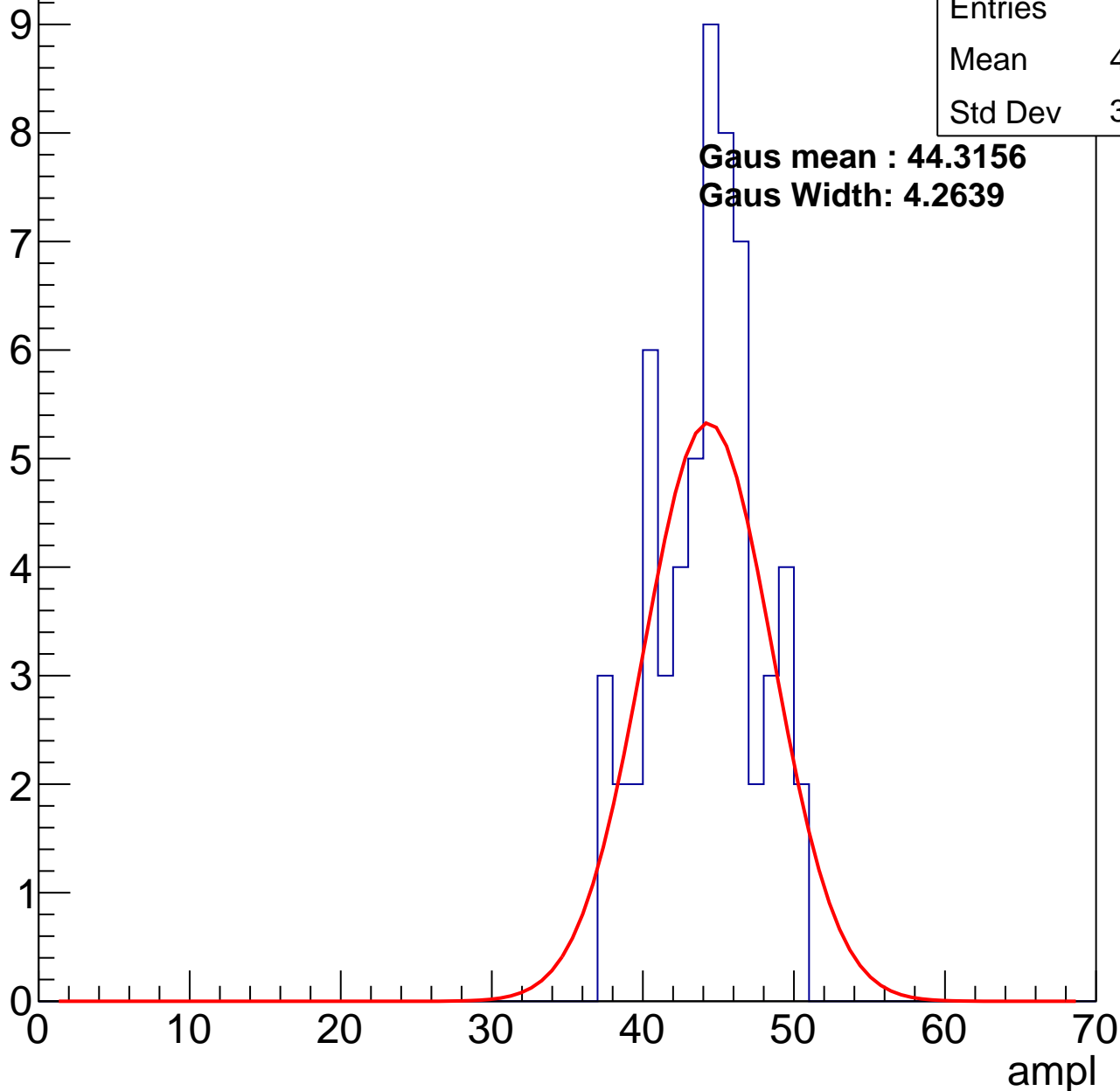
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.72
Std Dev	3.372

**Gaus mean : 44.3156**

**Gaus Width: 4.2639**



# B0L000S, U13-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	50.95
Std Dev	3.412

Entry

10

8

6

4

2

0

0

10

20

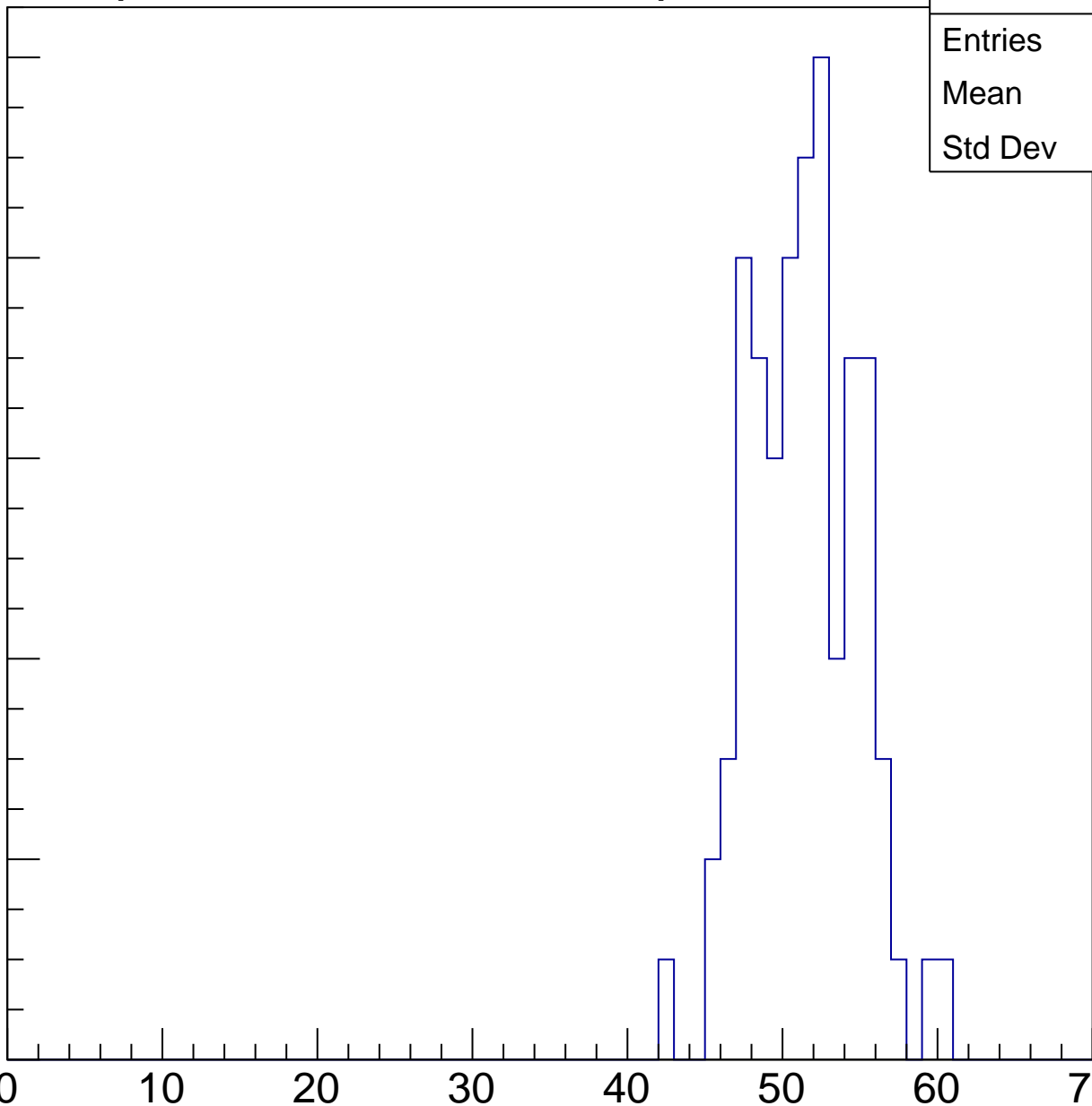
30

40

50

60

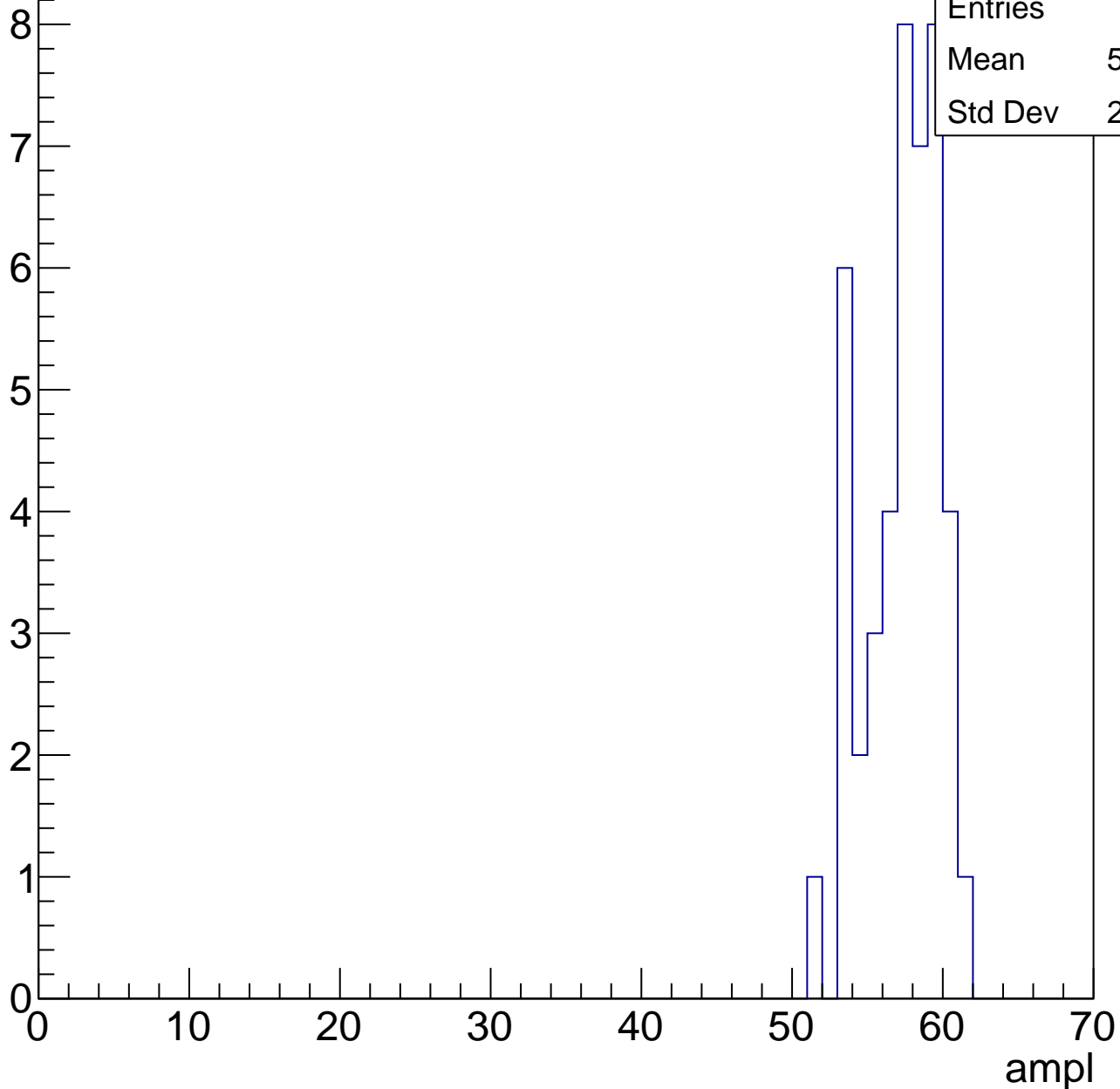
ampl



# B0L000S, U13-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

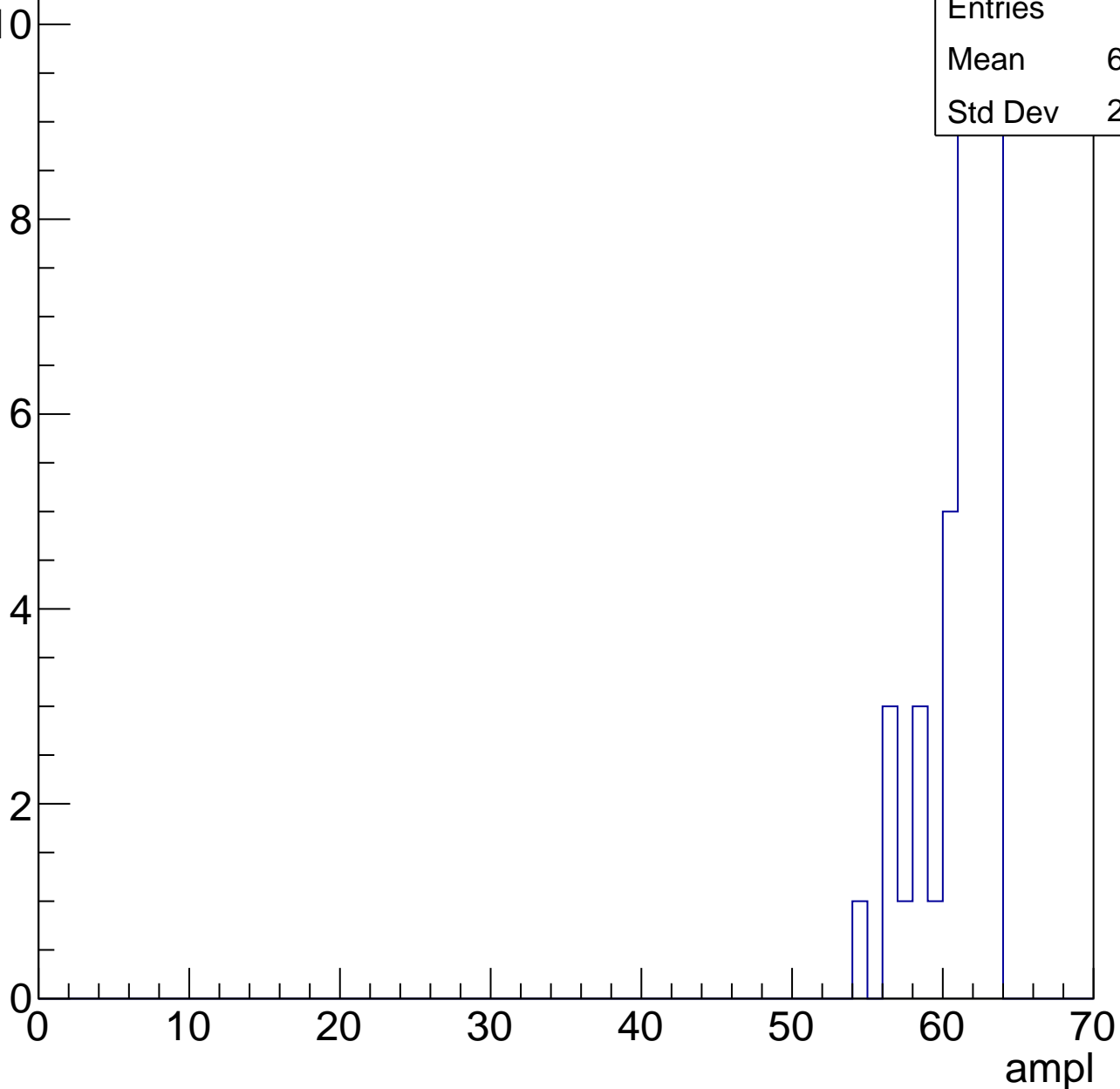


# B0L000S, U13-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

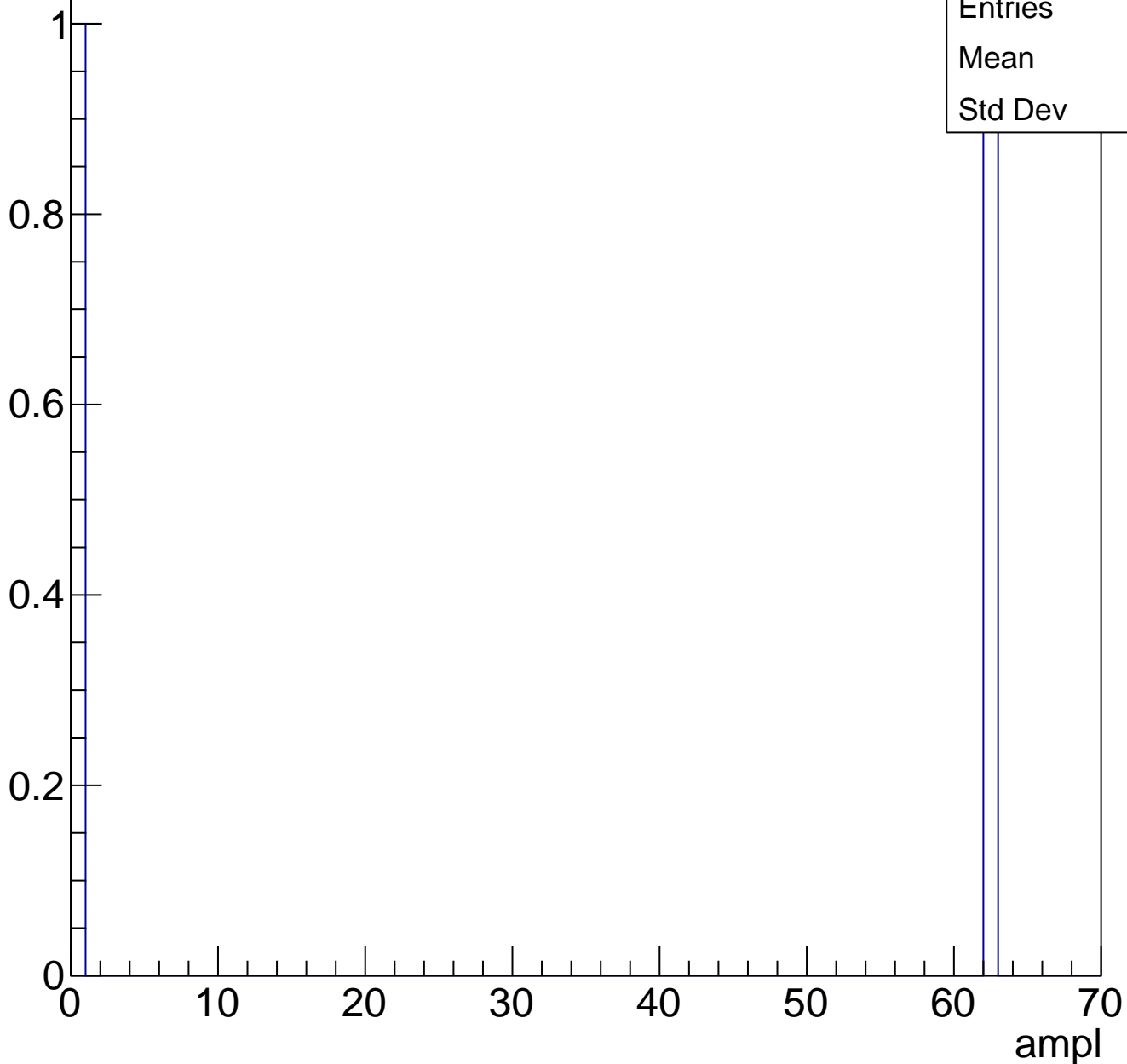
Entries	42
Mean	60.69
Std Dev	2.294



# B0L000S, U13-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

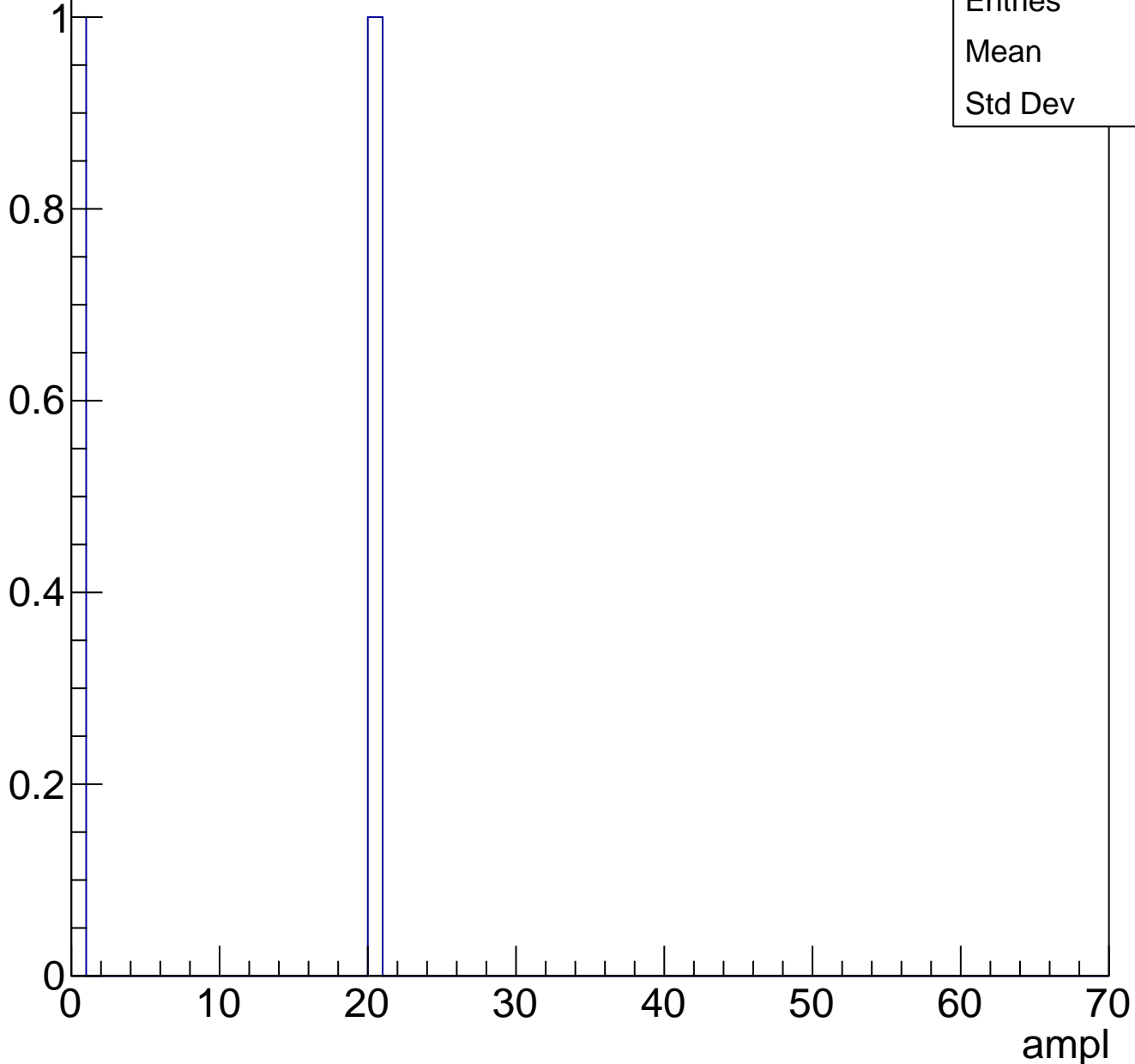




# B0L000S, U13-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U13-ch59, adc0

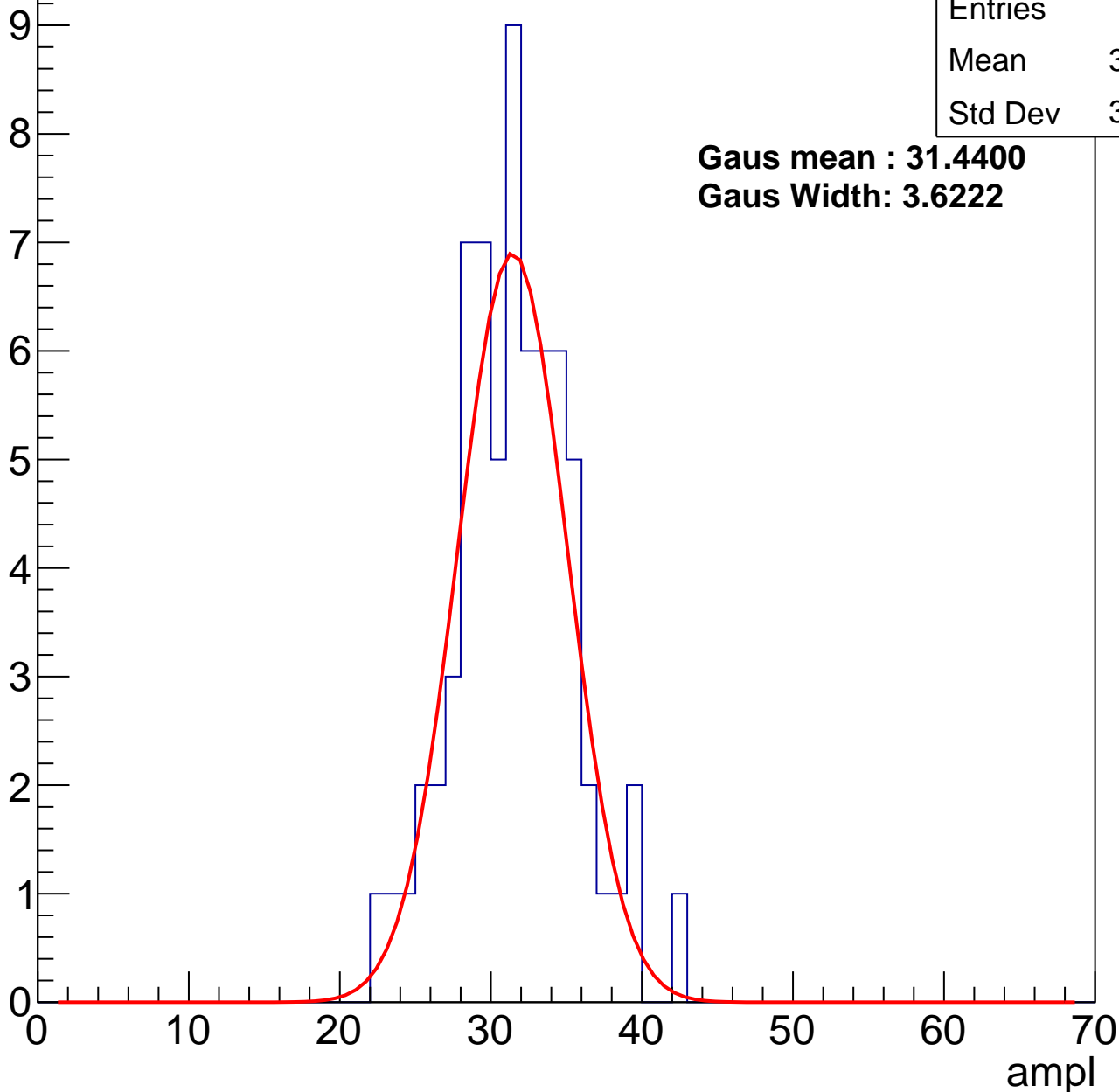
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	31.12
Std Dev	3.848

**Gaus mean : 31.4400**

**Gaus Width: 3.6222**



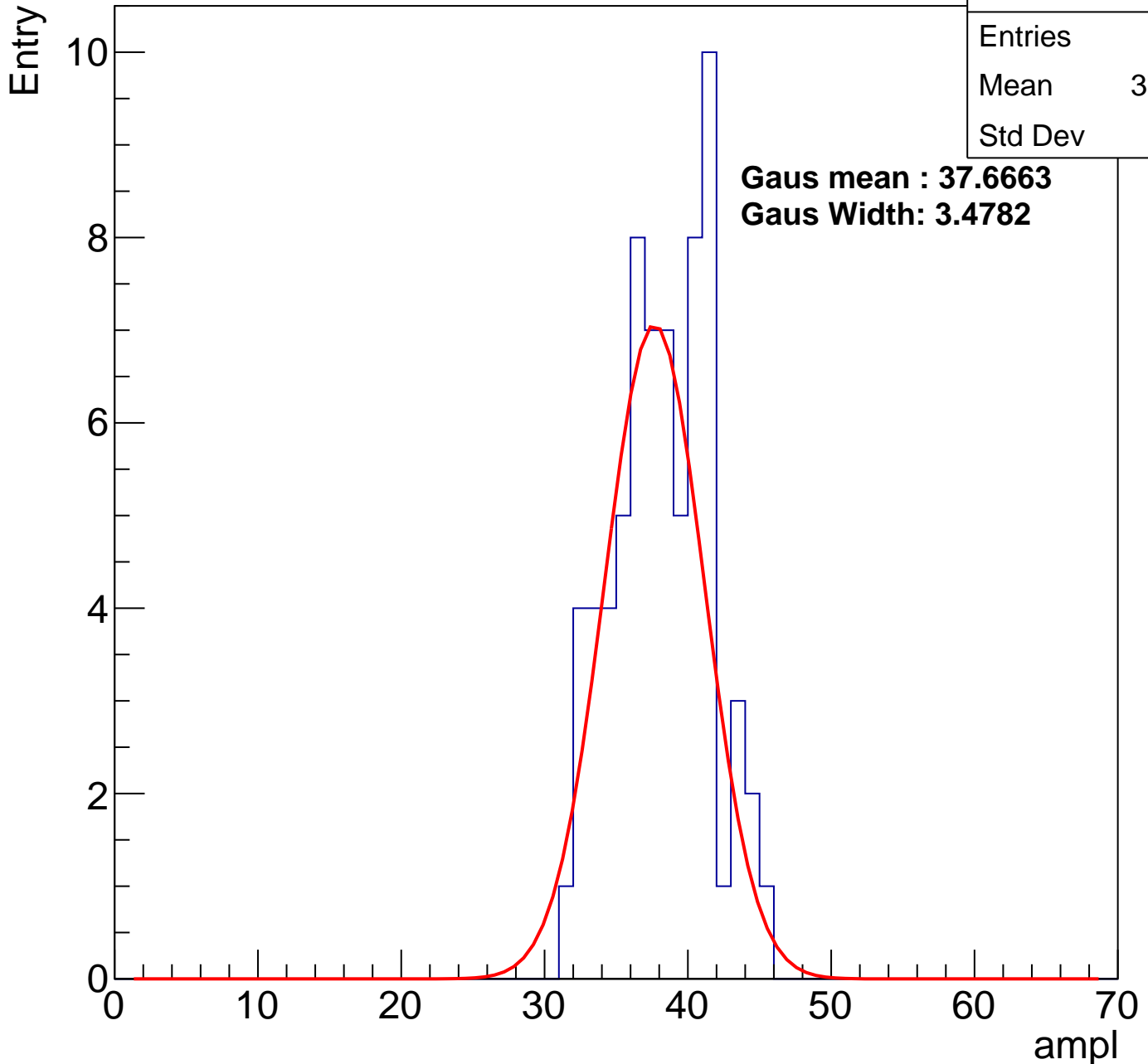
# B0L000S, U13-ch59, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	37.77
Std Dev	3.33

**Gaus mean : 37.6663**

**Gaus Width: 3.4782**



# B0L000S, U13-ch59, adc2

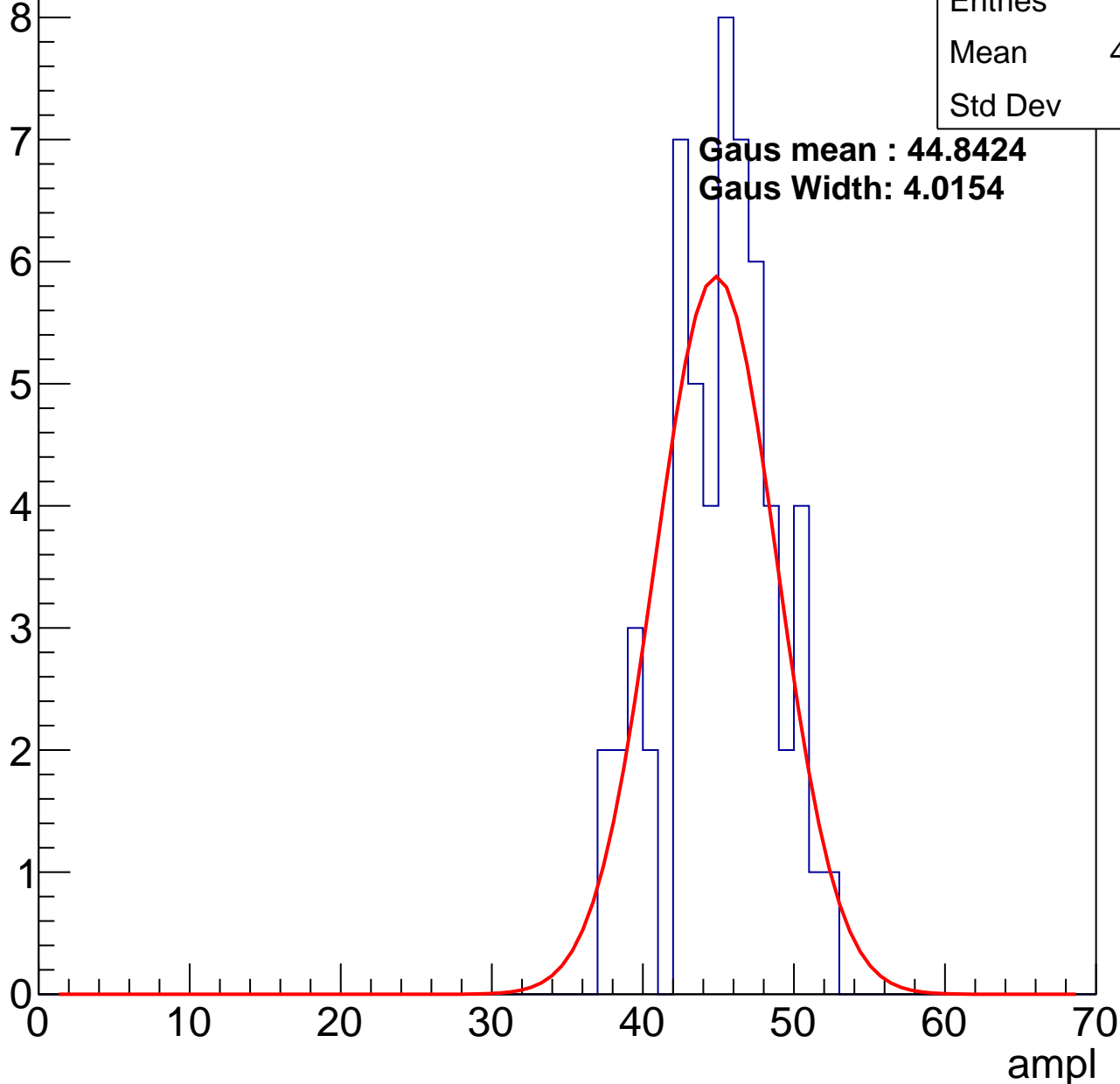
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44.64
Std Dev	3.59

**Gaus mean : 44.8424**

**Gaus Width: 4.0154**

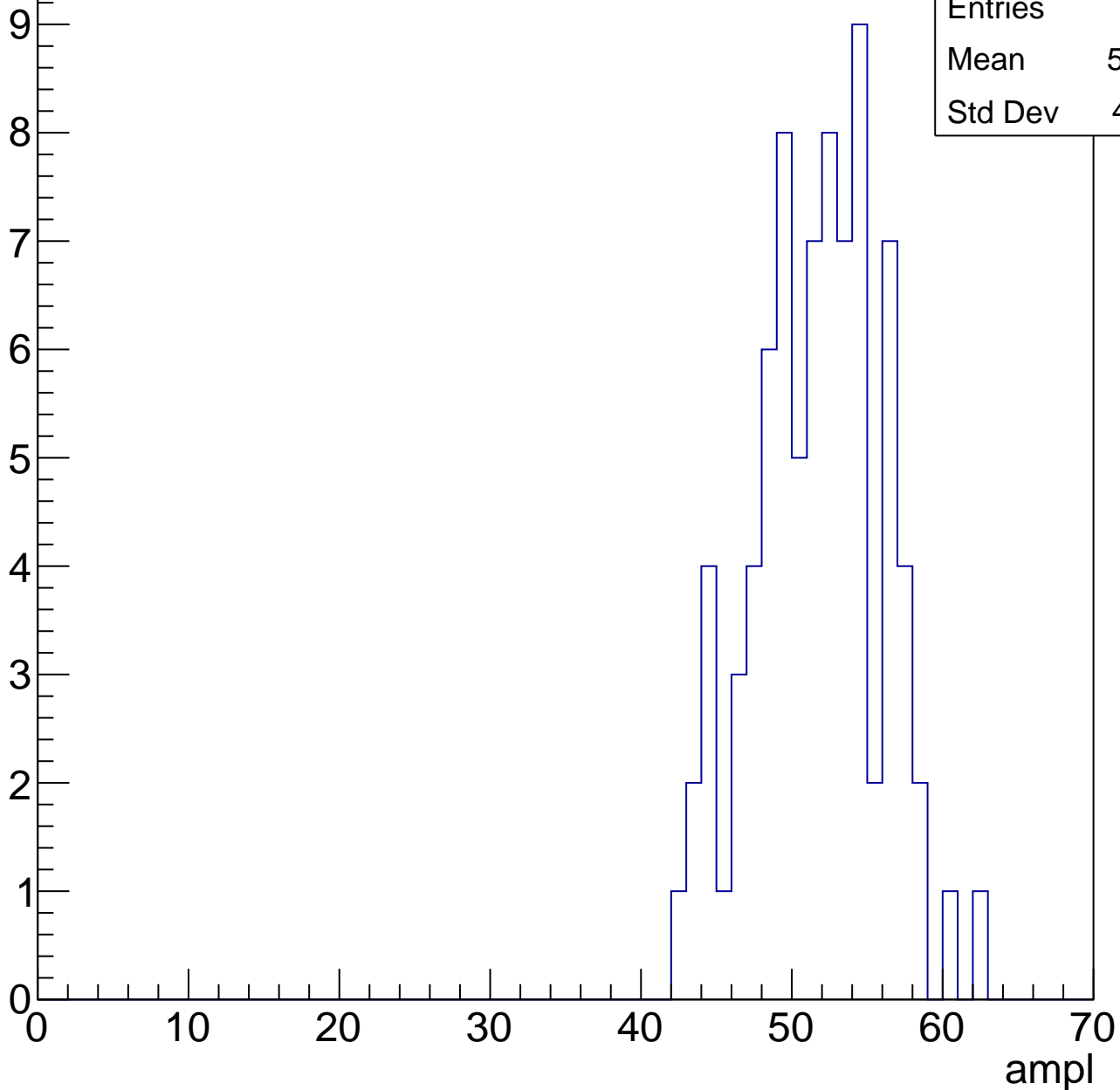


# B0L000S, U13-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	51.26
Std Dev	4.181

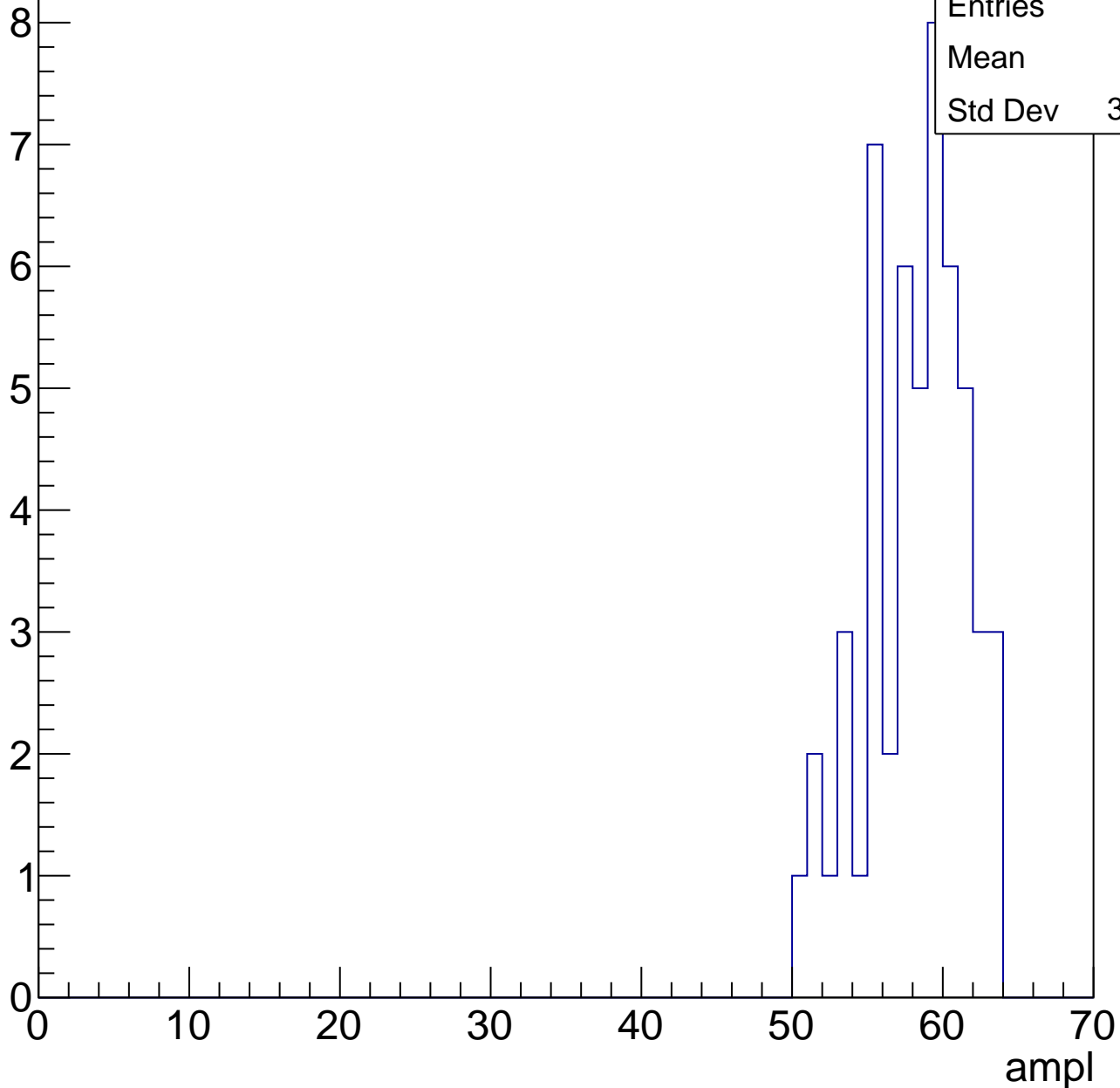


# B0L000S, U13-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.7
Std Dev	3.254

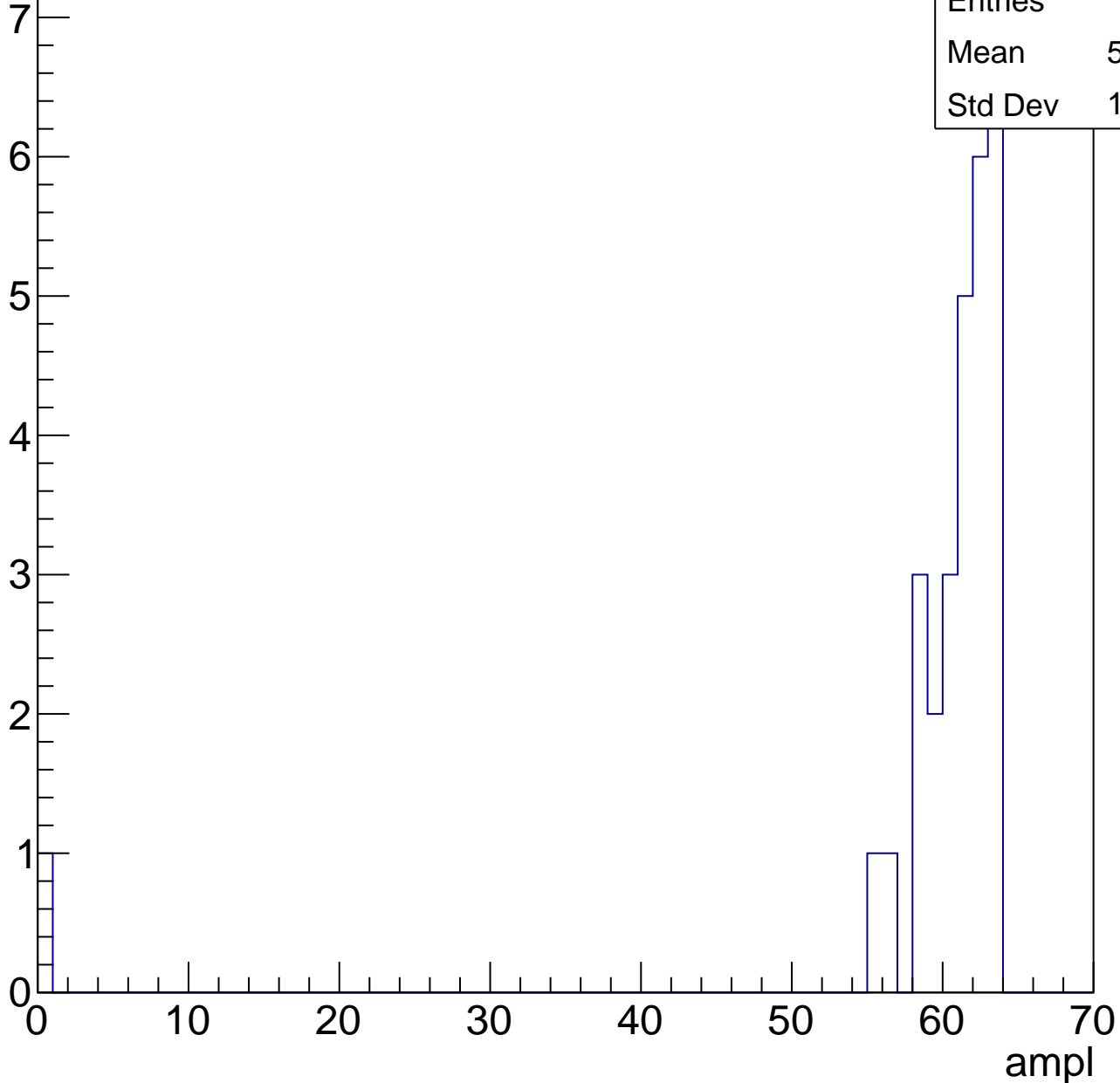


# B0L000S, U13-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	29
Mean	58.66
Std Dev	11.29



# B0L000S, U13-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch60, adc0

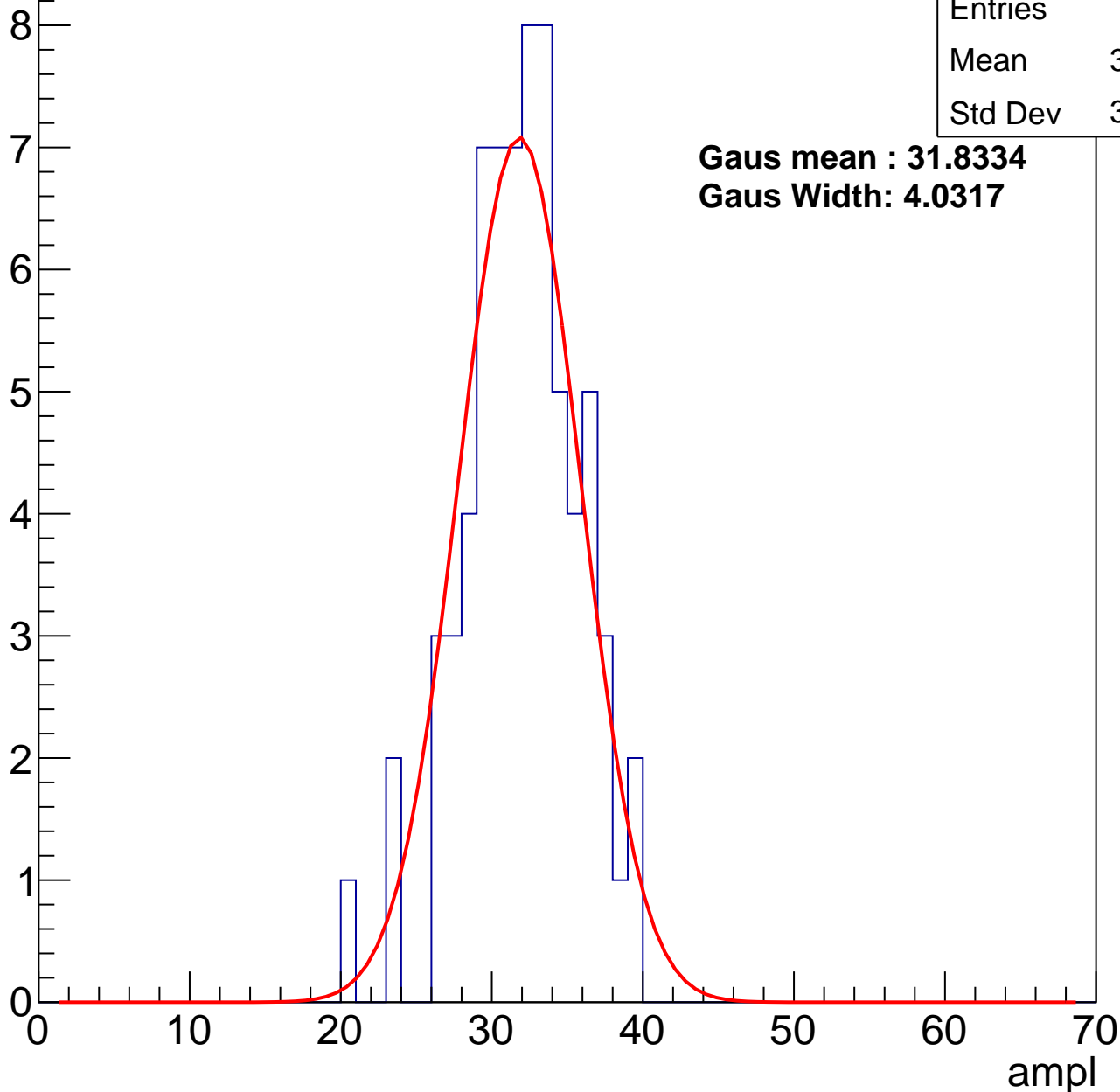
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	31.49
Std Dev	3.752

**Gaus mean : 31.8334**

**Gaus Width: 4.0317**



# B0L000S, U13-ch60, adc1

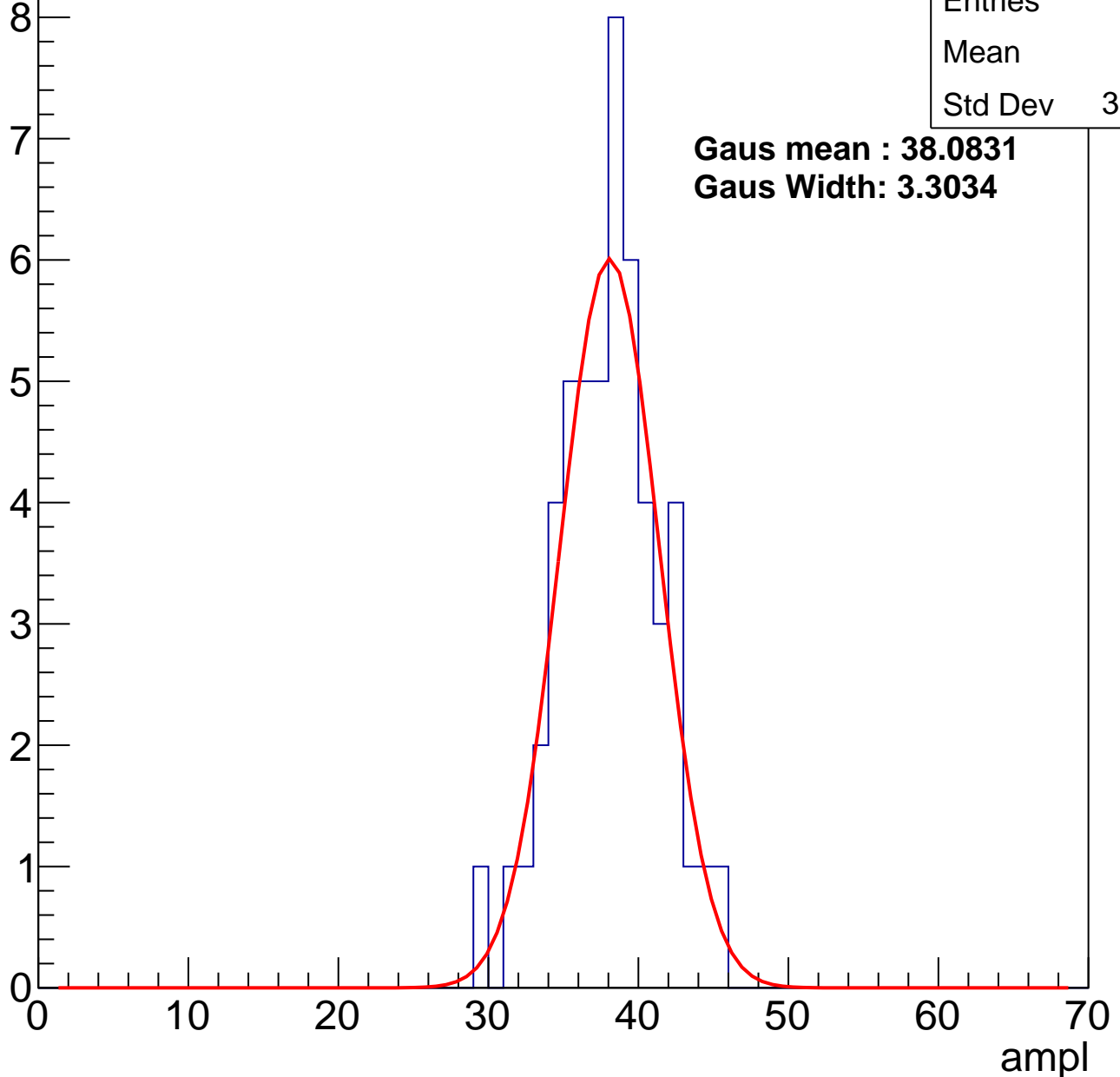
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	37.6
Std Dev	3.295

**Gaus mean : 38.0831**

**Gaus Width: 3.3034**



# B0L000S, U13-ch60, adc2

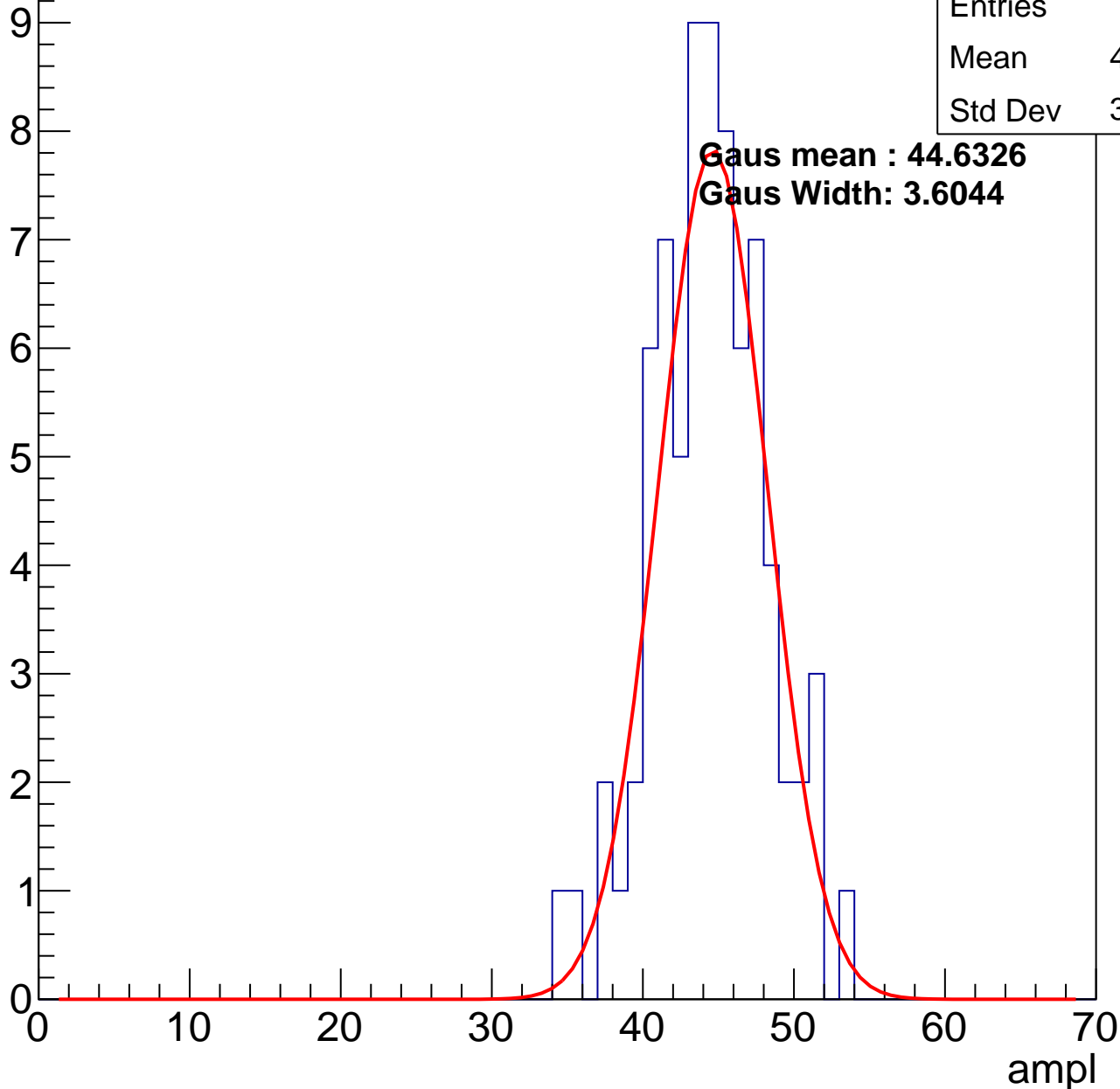
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.95
Std Dev	3.738

**Gaus mean : 44.6326**

**Gaus Width: 3.6044**

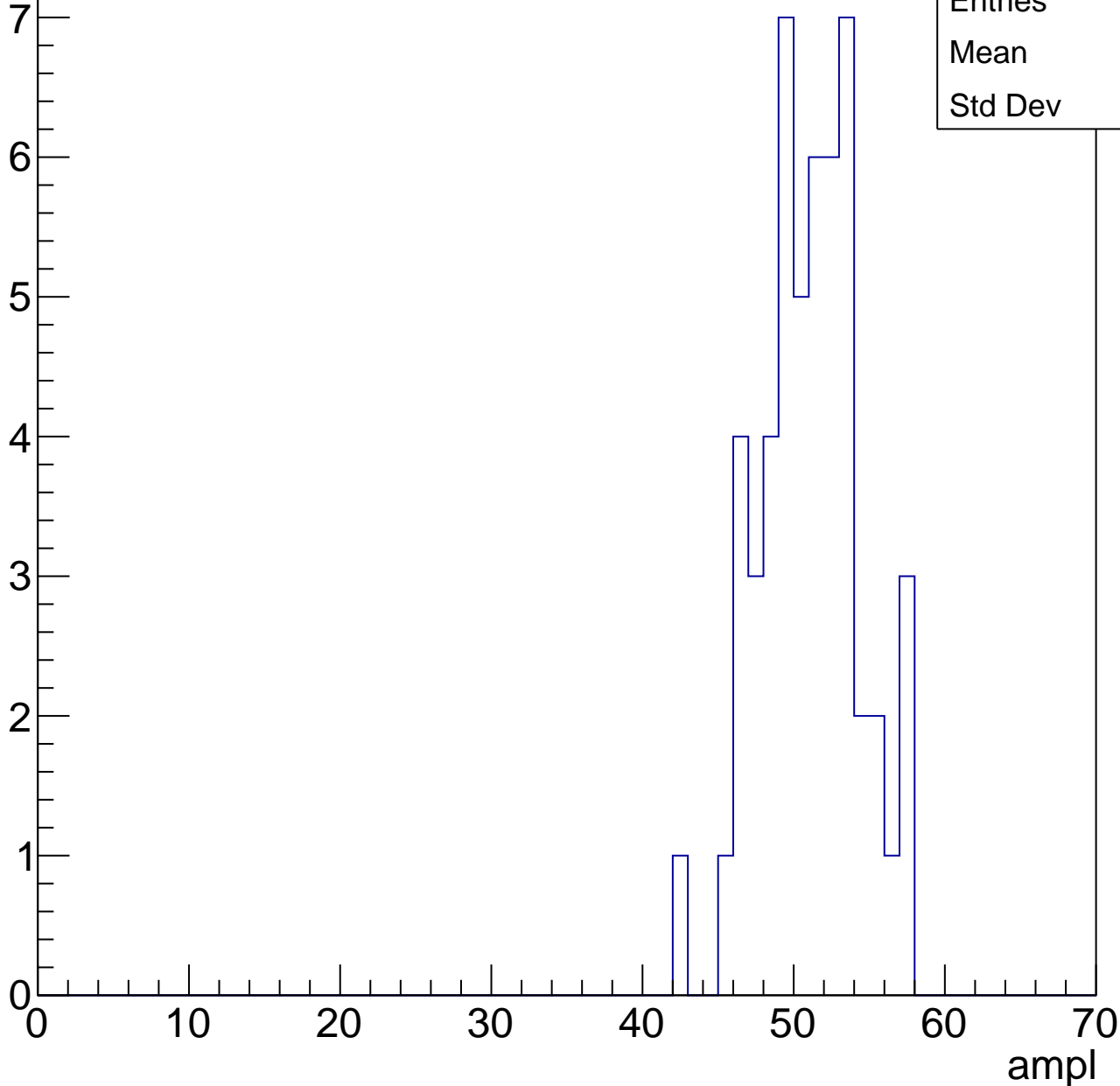


# B0L000S, U13-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

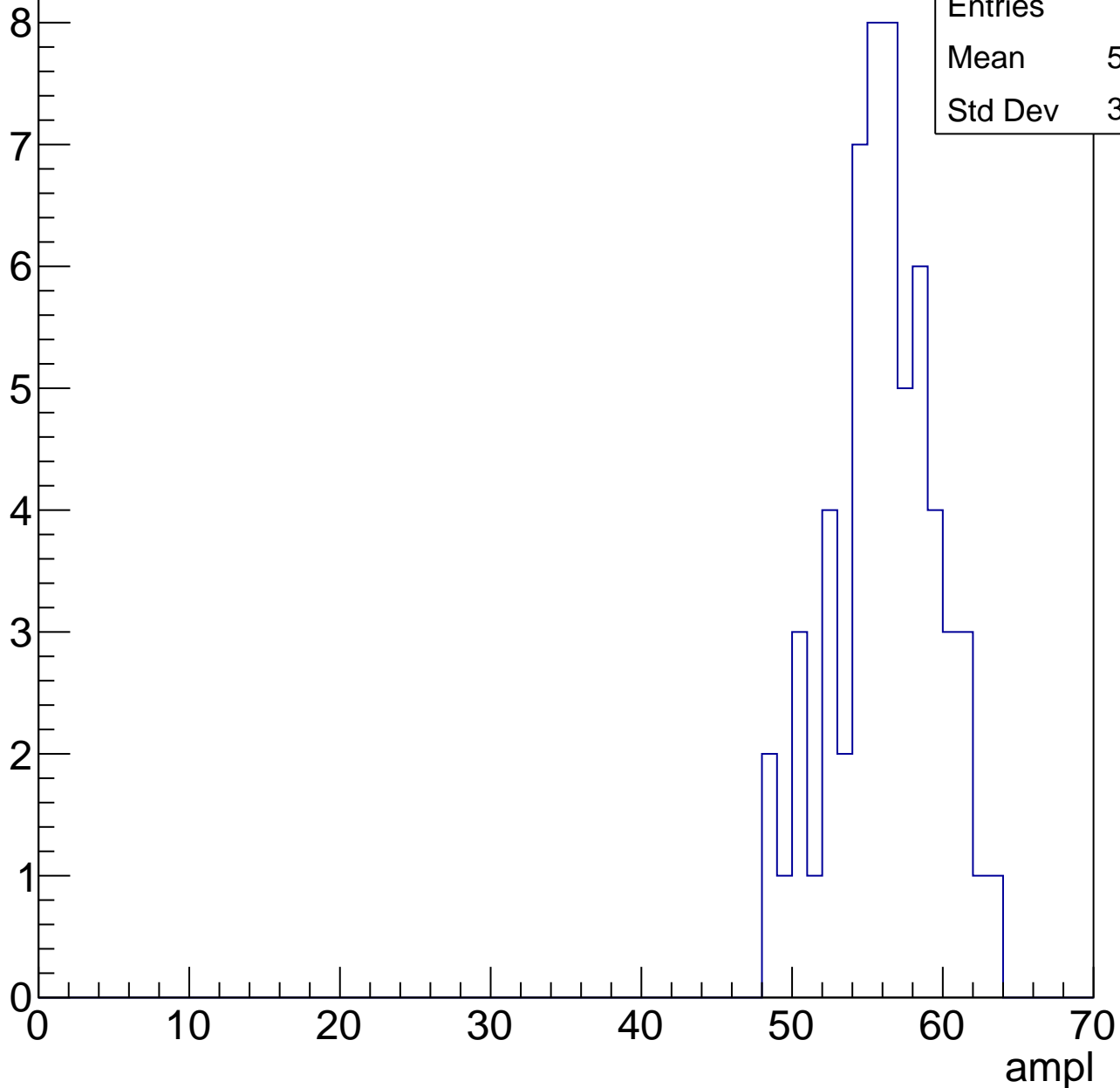
Entries	52
Mean	50.6
Std Dev	3.23



# B0L000S, U13-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



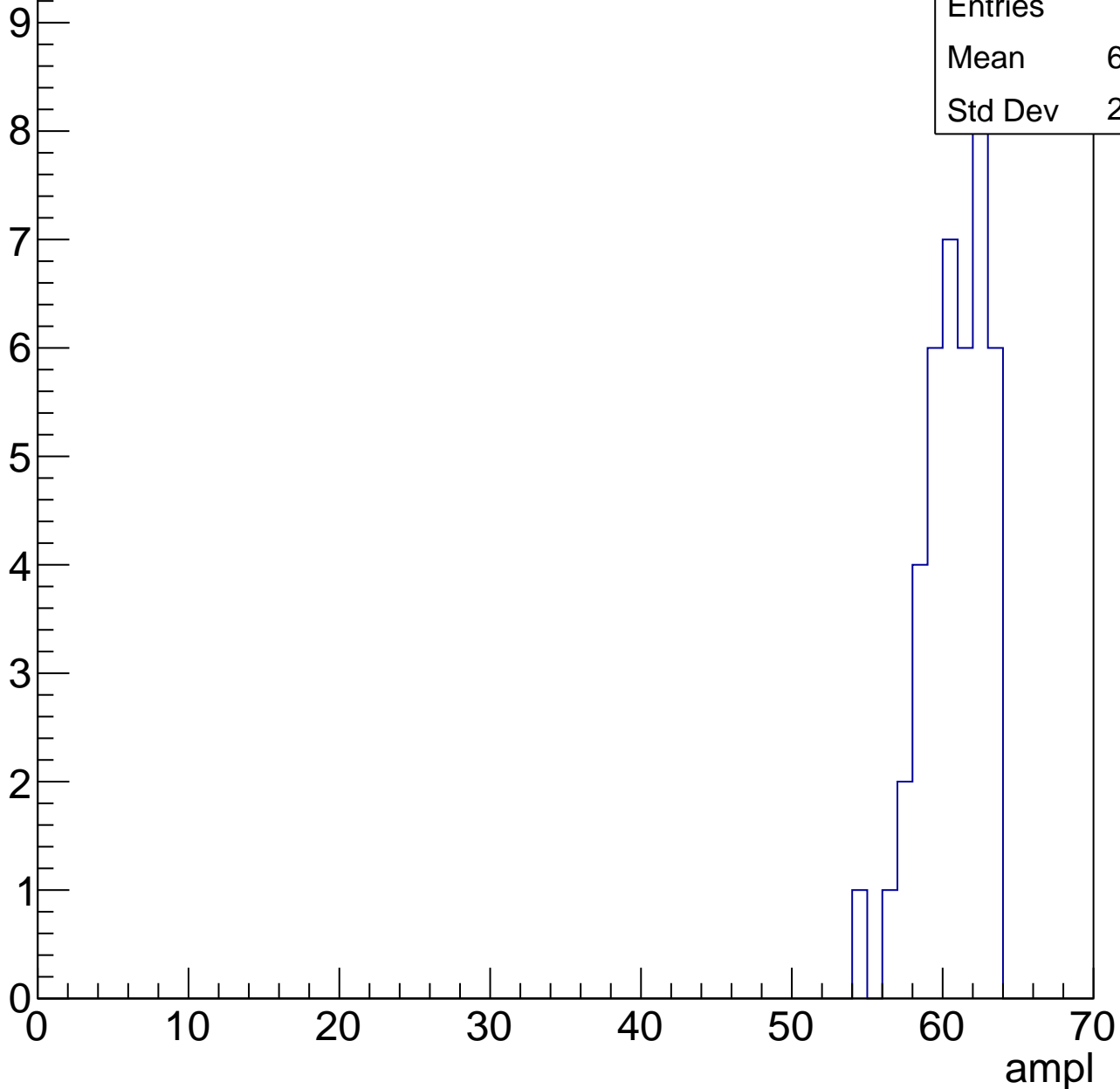
Entries	59
Mean	55.64
Std Dev	3.429

# B0L000S, U13-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

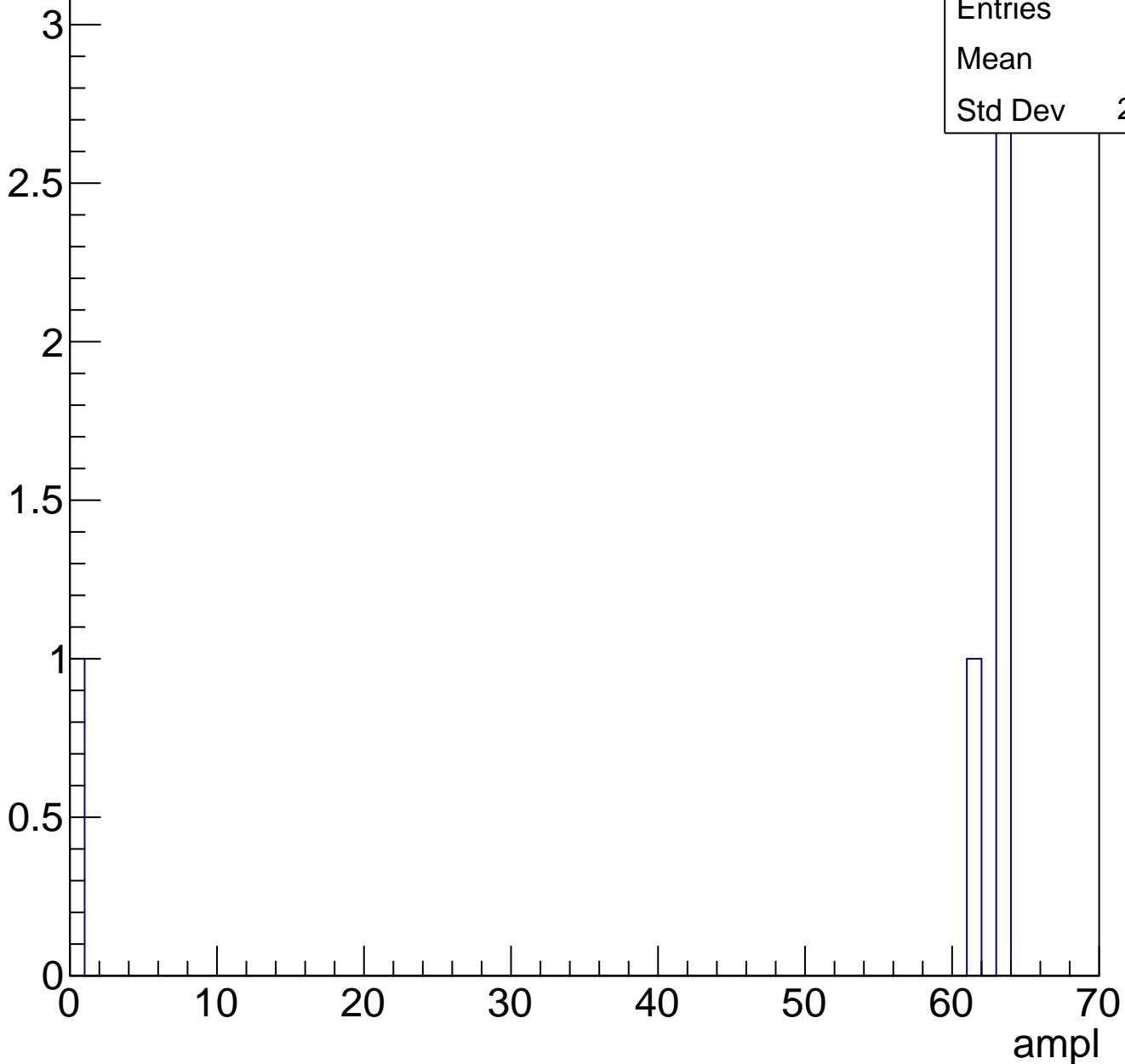
Entries	42
Mean	60.29
Std Dev	2.096



# B0L000S, U13-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch61, adc0

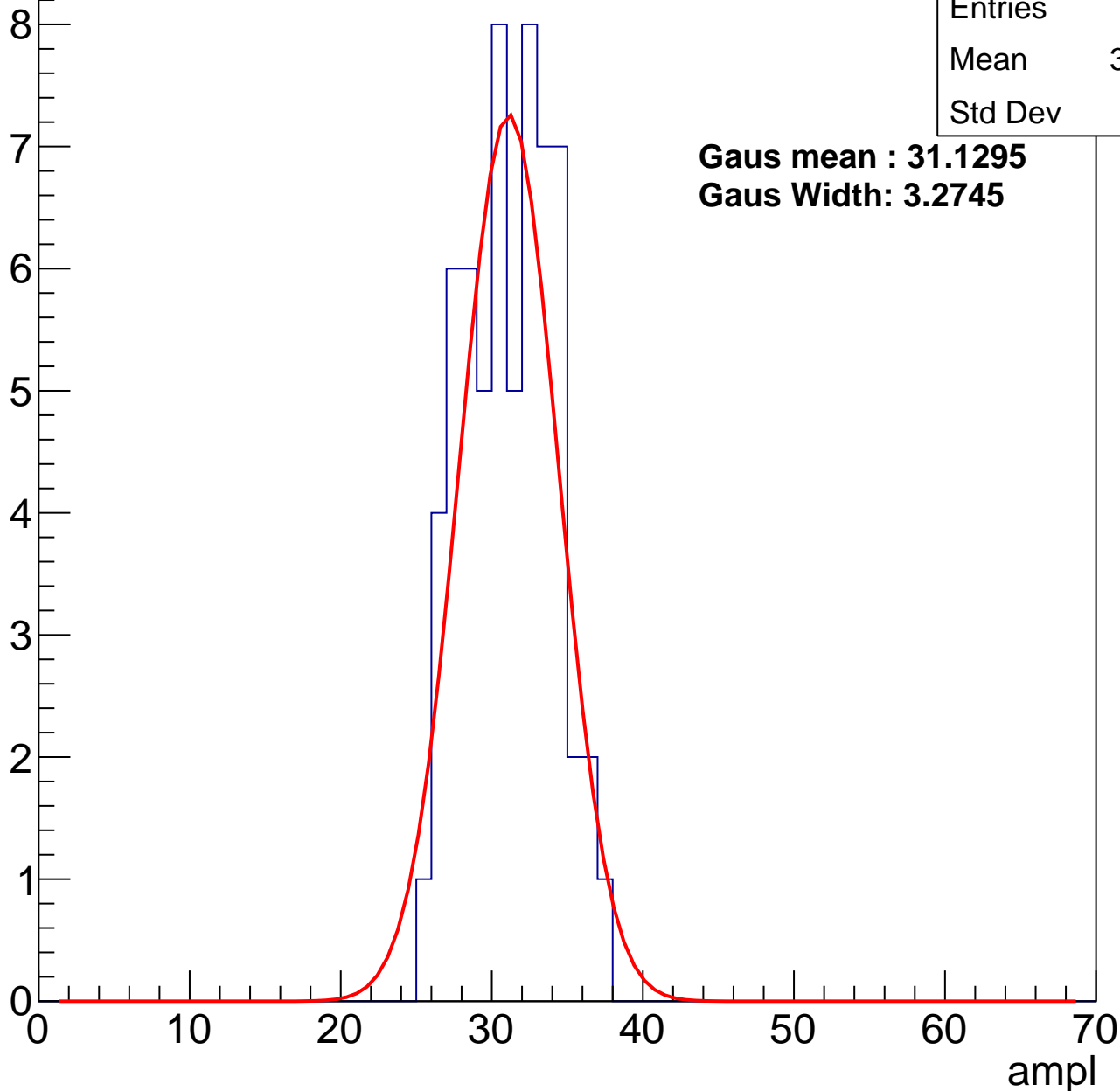
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.69
Std Dev	2.91

**Gaus mean : 31.1295**

**Gaus Width: 3.2745**



# B0L000S, U13-ch61, adc1

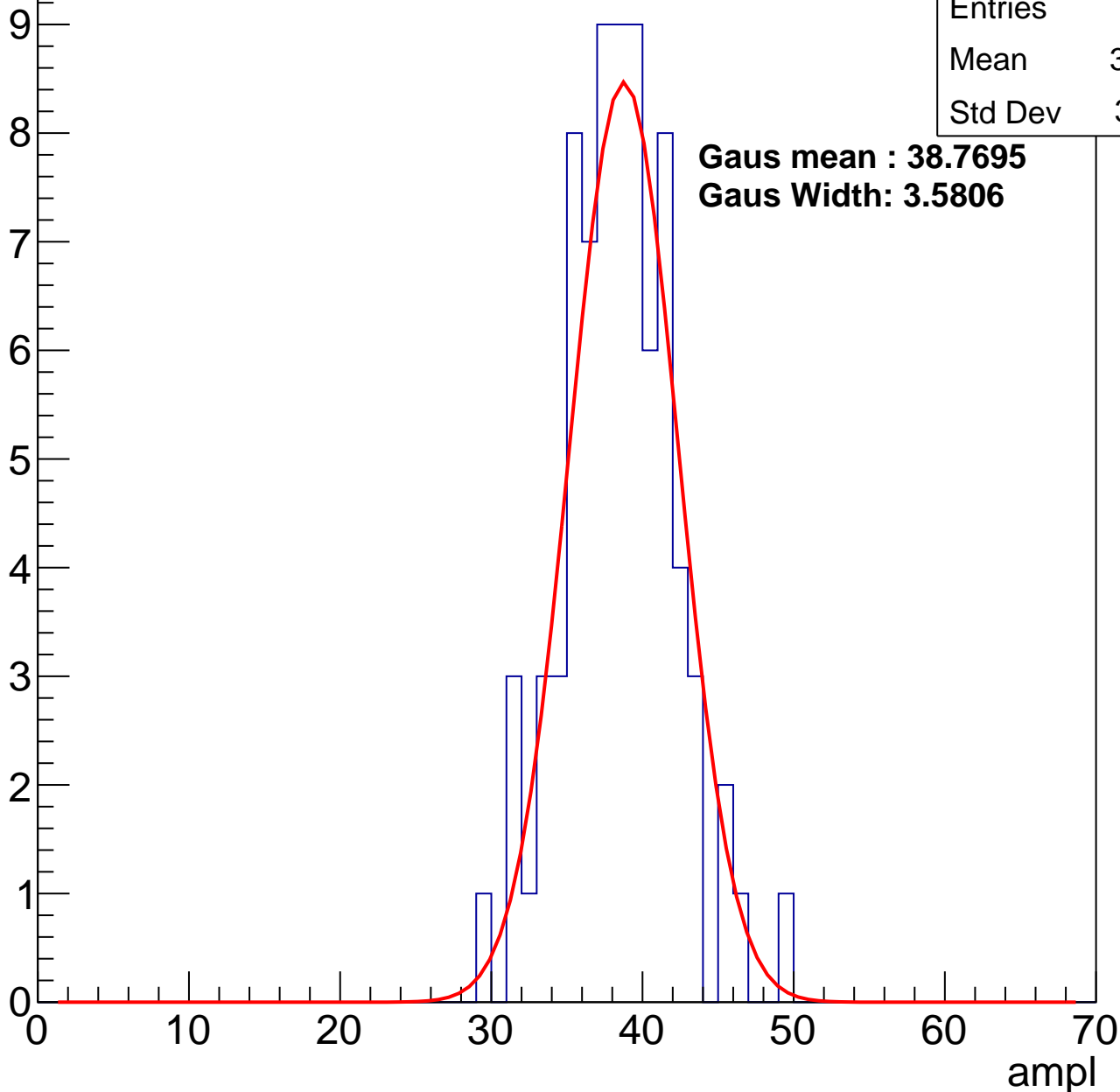
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	37.99
Std Dev	3.621

**Gaus mean : 38.7695**

**Gaus Width: 3.5806**



# B0L000S, U13-ch61, adc2

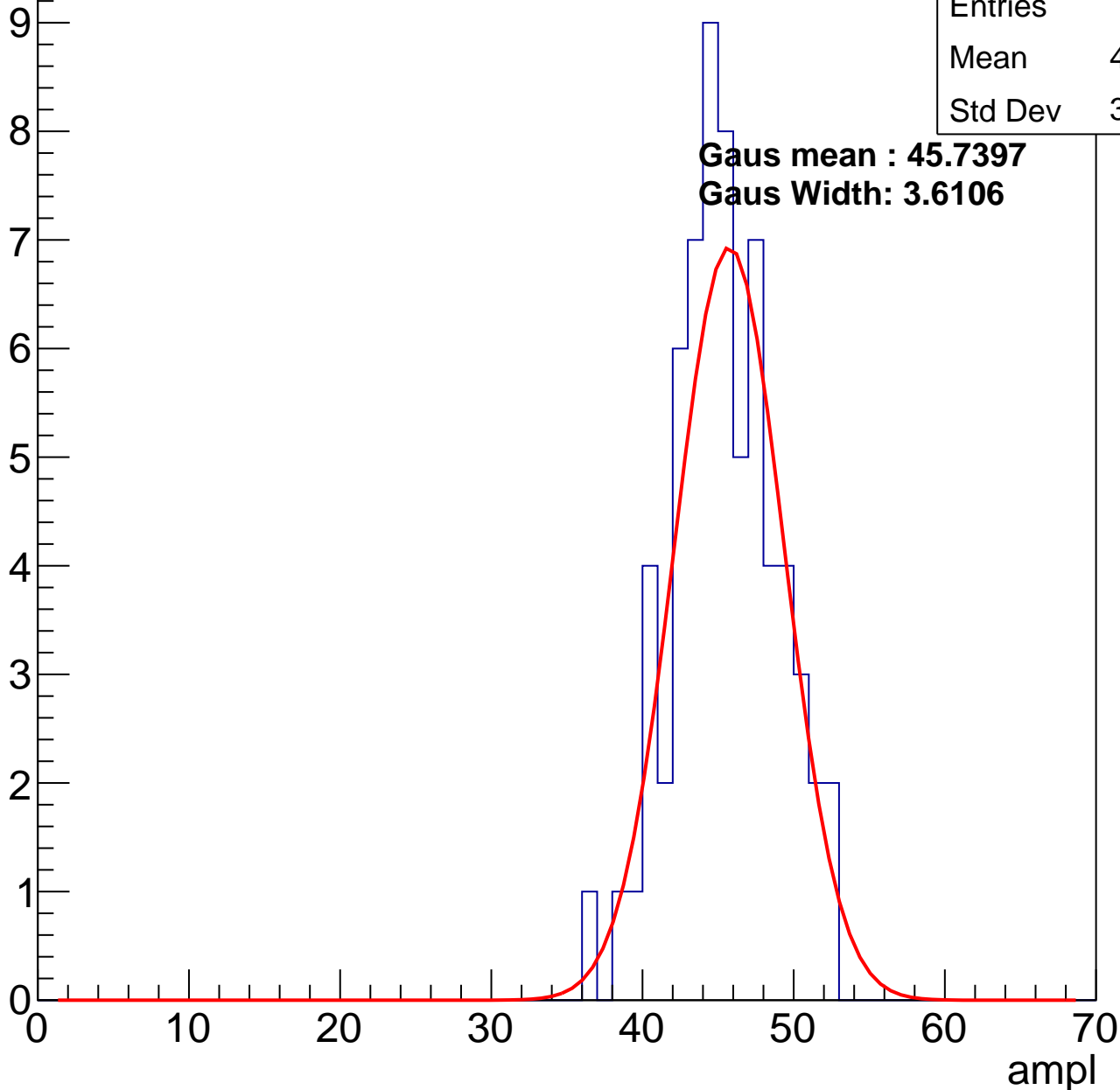
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.95
Std Dev	3.409

**Gaus mean : 45.7397**

**Gaus Width: 3.6106**

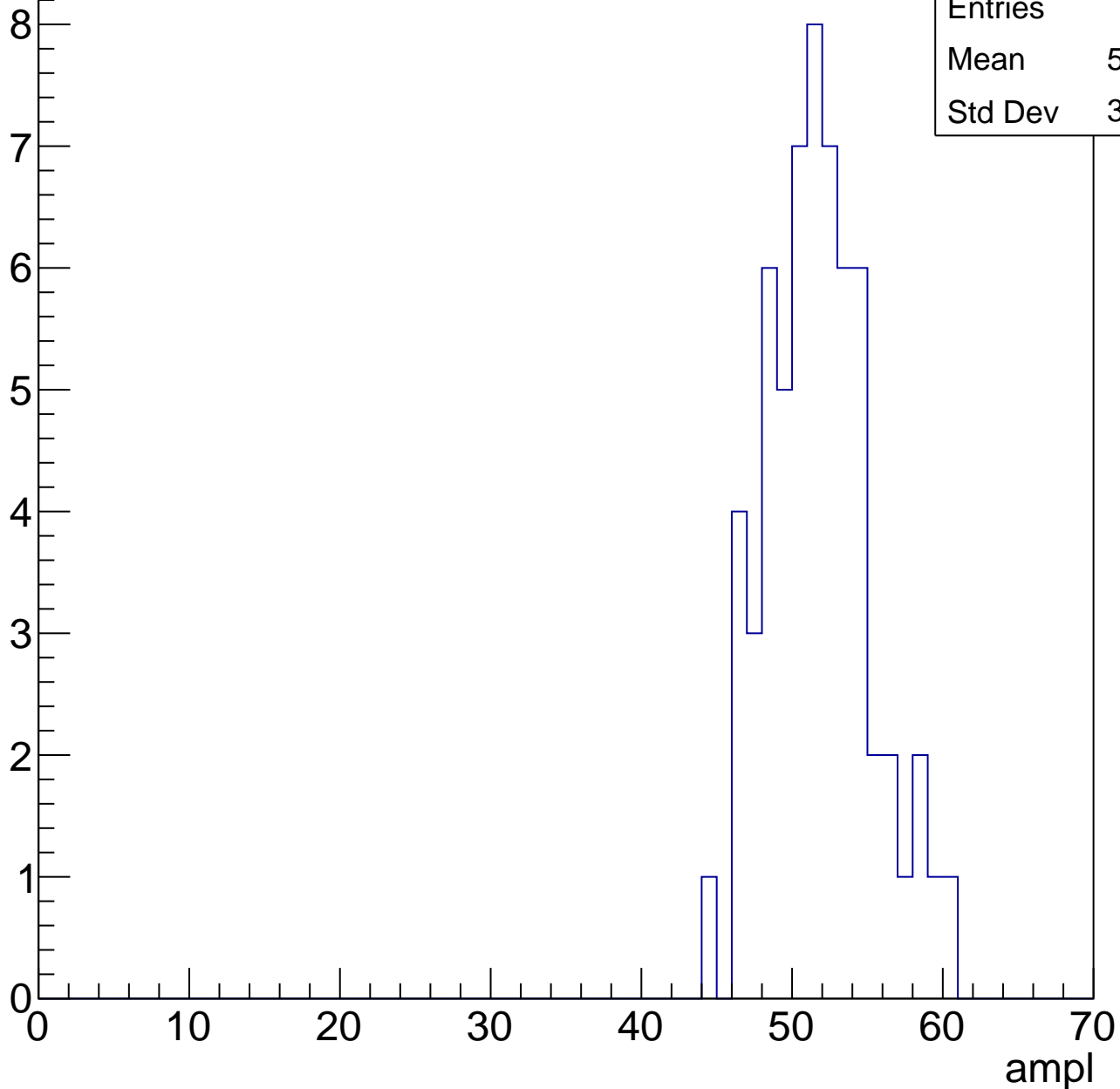


# B0L000S, U13-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	51.29
Std Dev	3.405

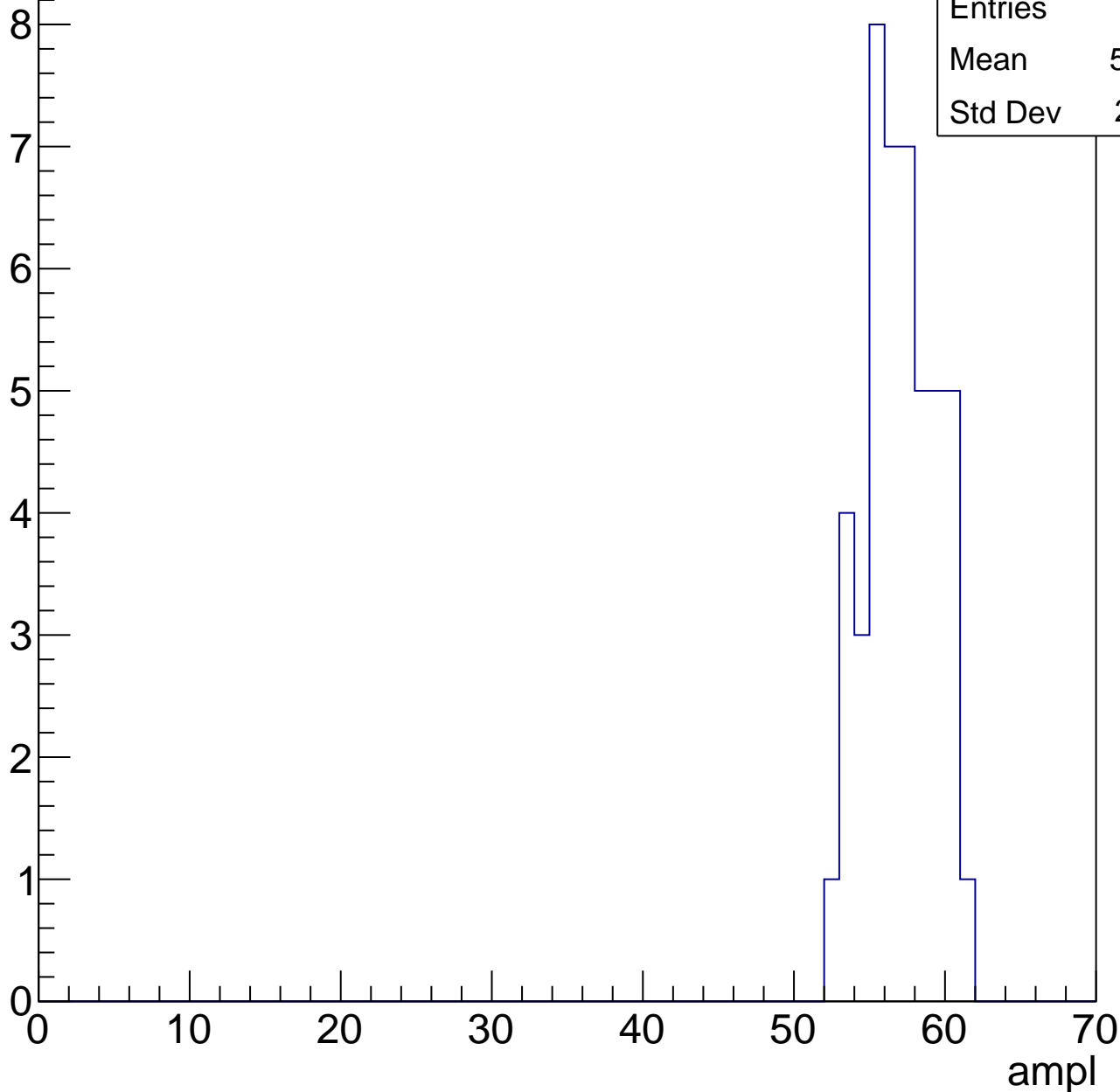


# B0L000S, U13-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	56.59
Std Dev	2.251

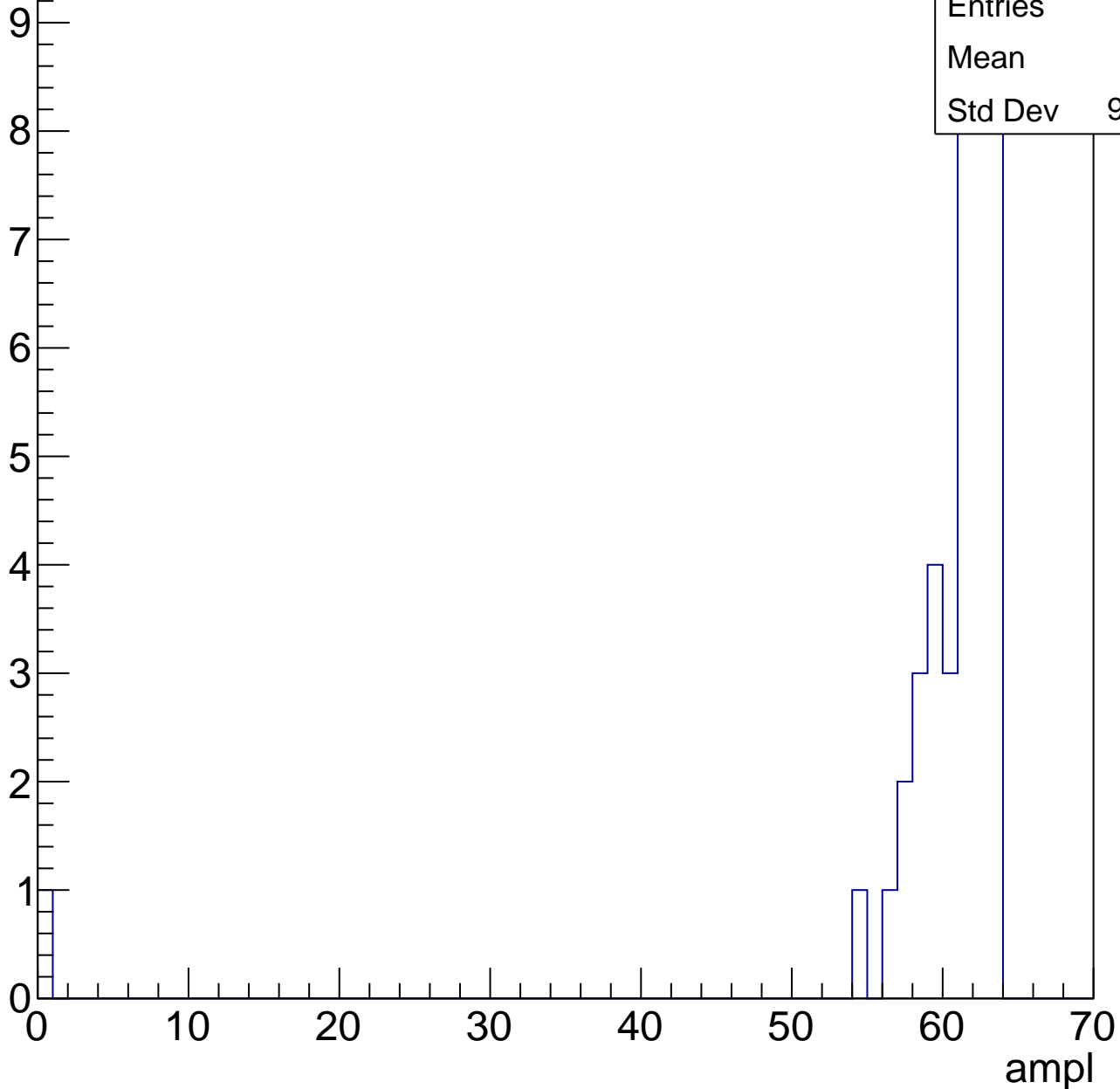


# B0L000S, U13-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	59.2
Std Dev	9.607



# B0L000S, U13-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch62, adc0

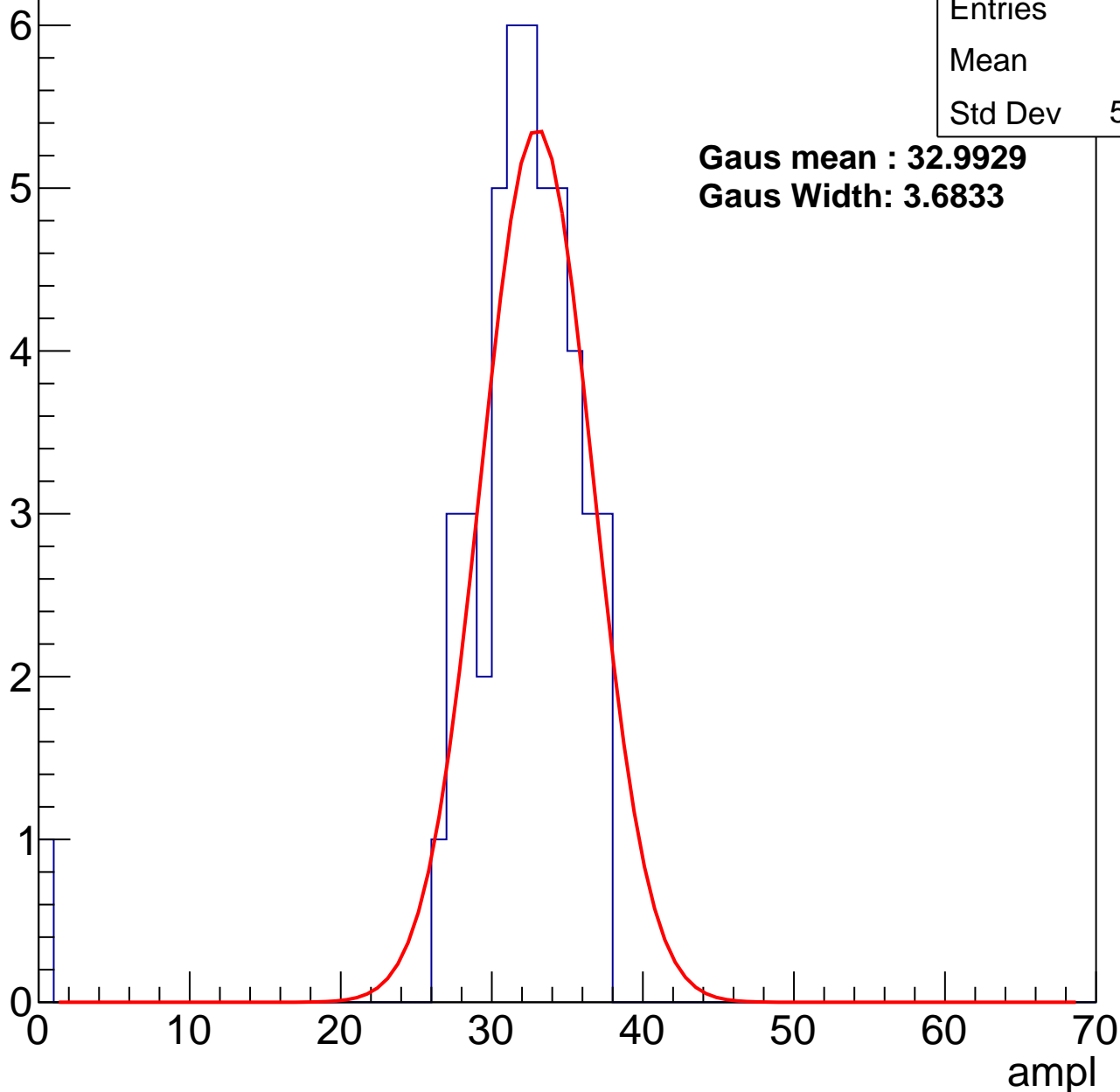
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	31.3
Std Dev	5.434

**Gaus mean : 32.9929**

**Gaus Width: 3.6833**



# B0L000S, U13-ch62, adc1

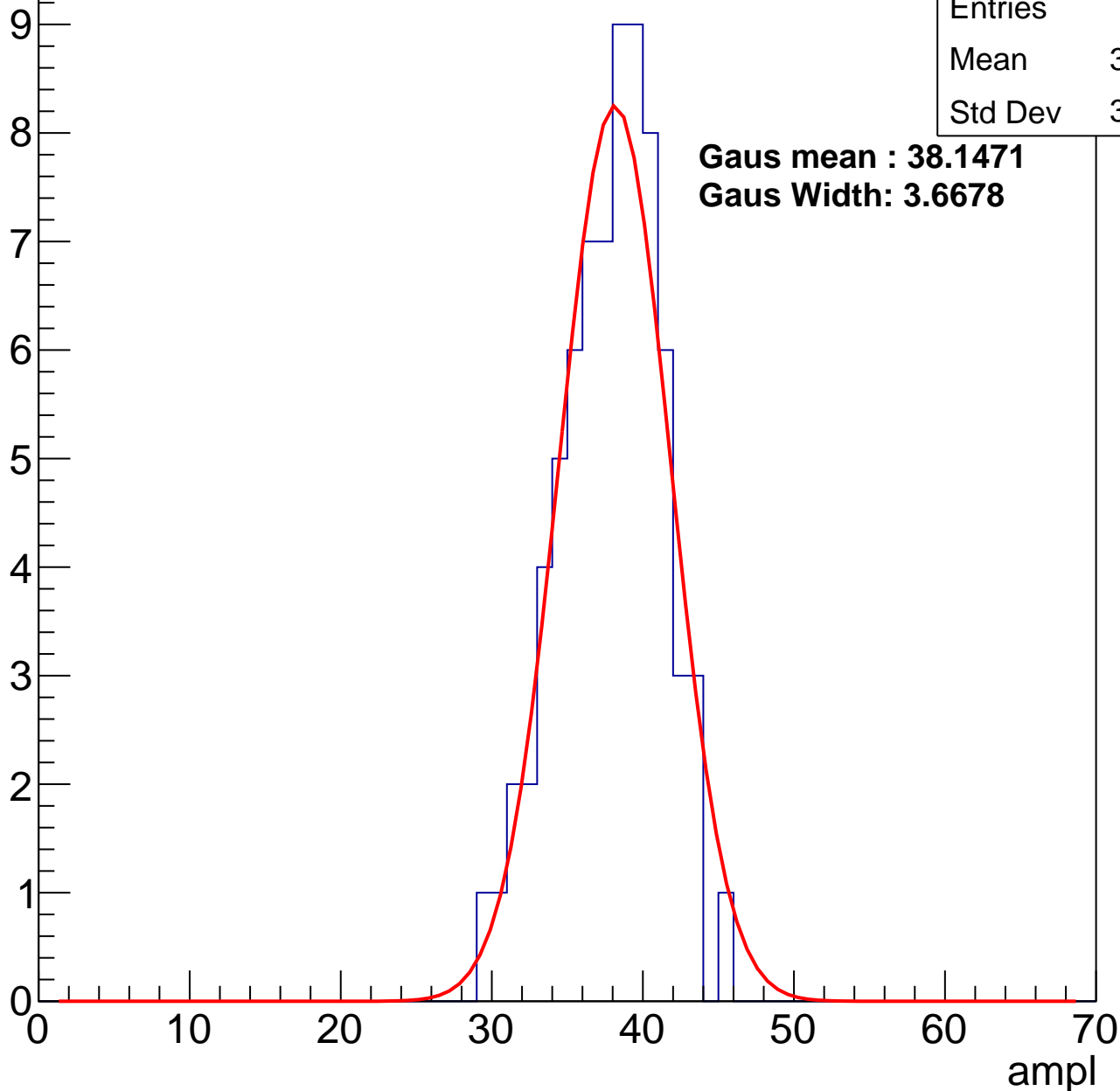
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.39
Std Dev	3.336

**Gaus mean : 38.1471**

**Gaus Width: 3.6678**



# B0L000S, U13-ch62, adc2

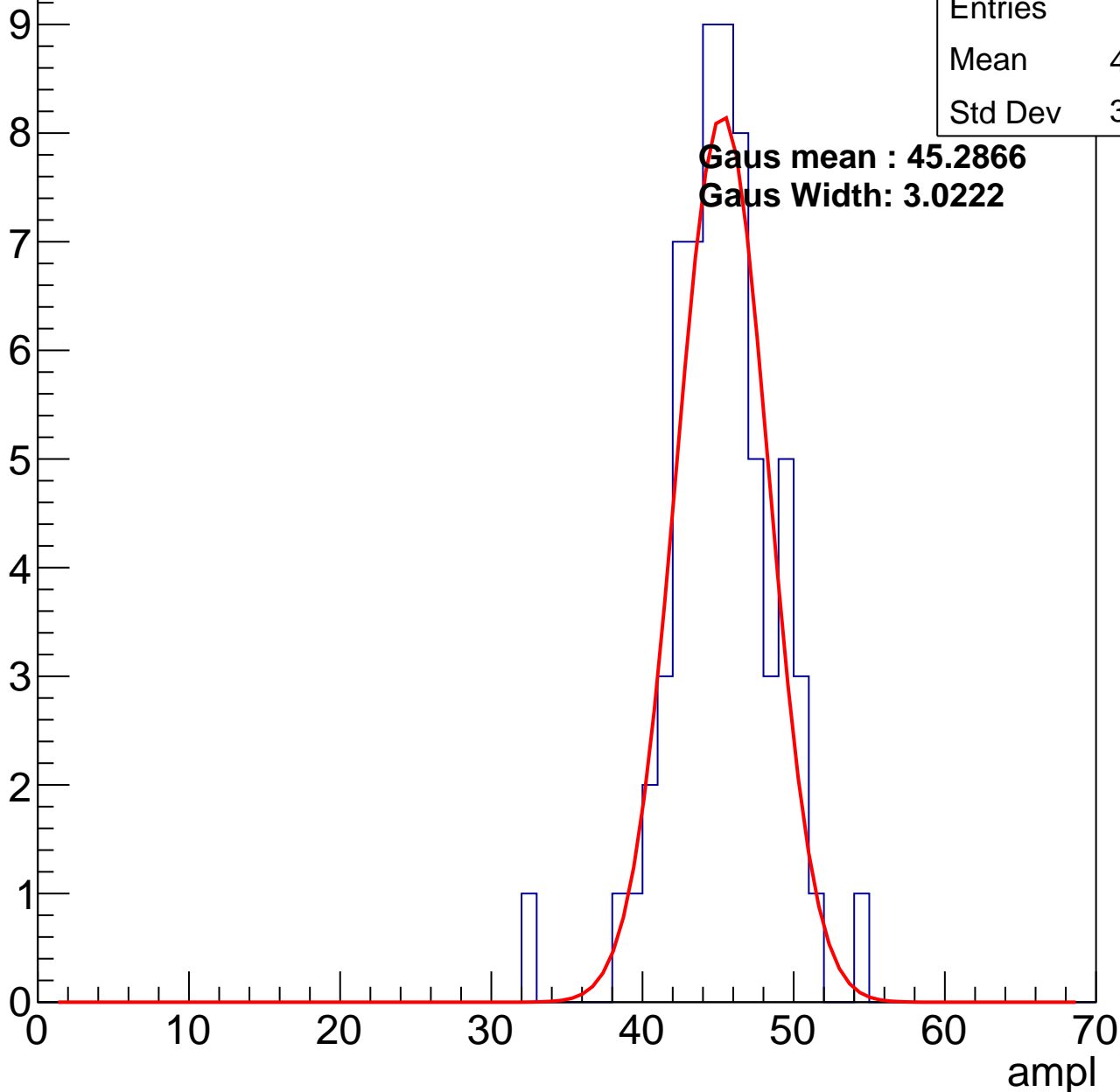
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.77
Std Dev	3.428

**Gaus mean : 45.2866**

**Gaus Width: 3.0222**

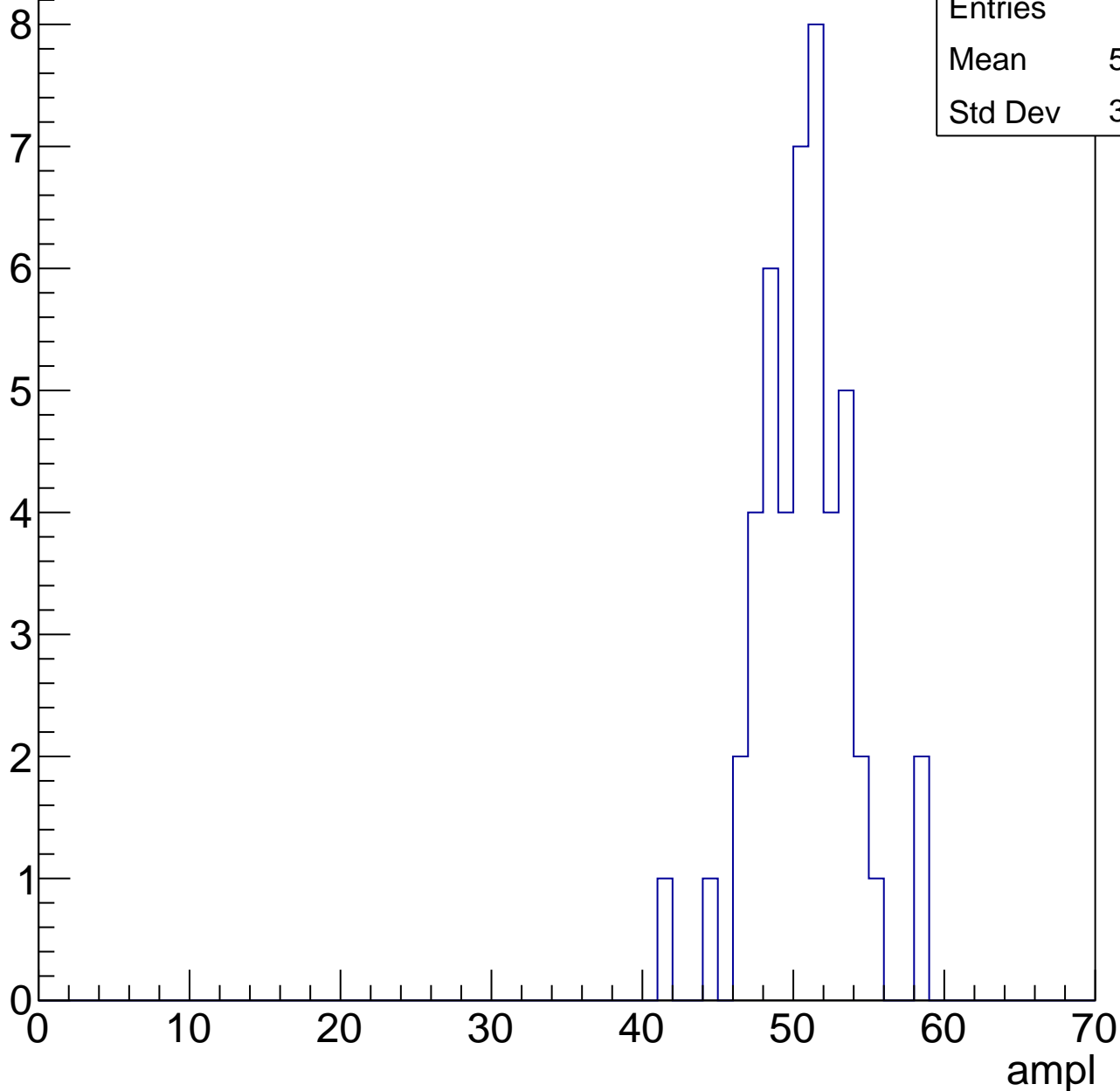


# B0L000S, U13-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	50.19
Std Dev	3.146



# B0L000S, U13-ch62, adc4

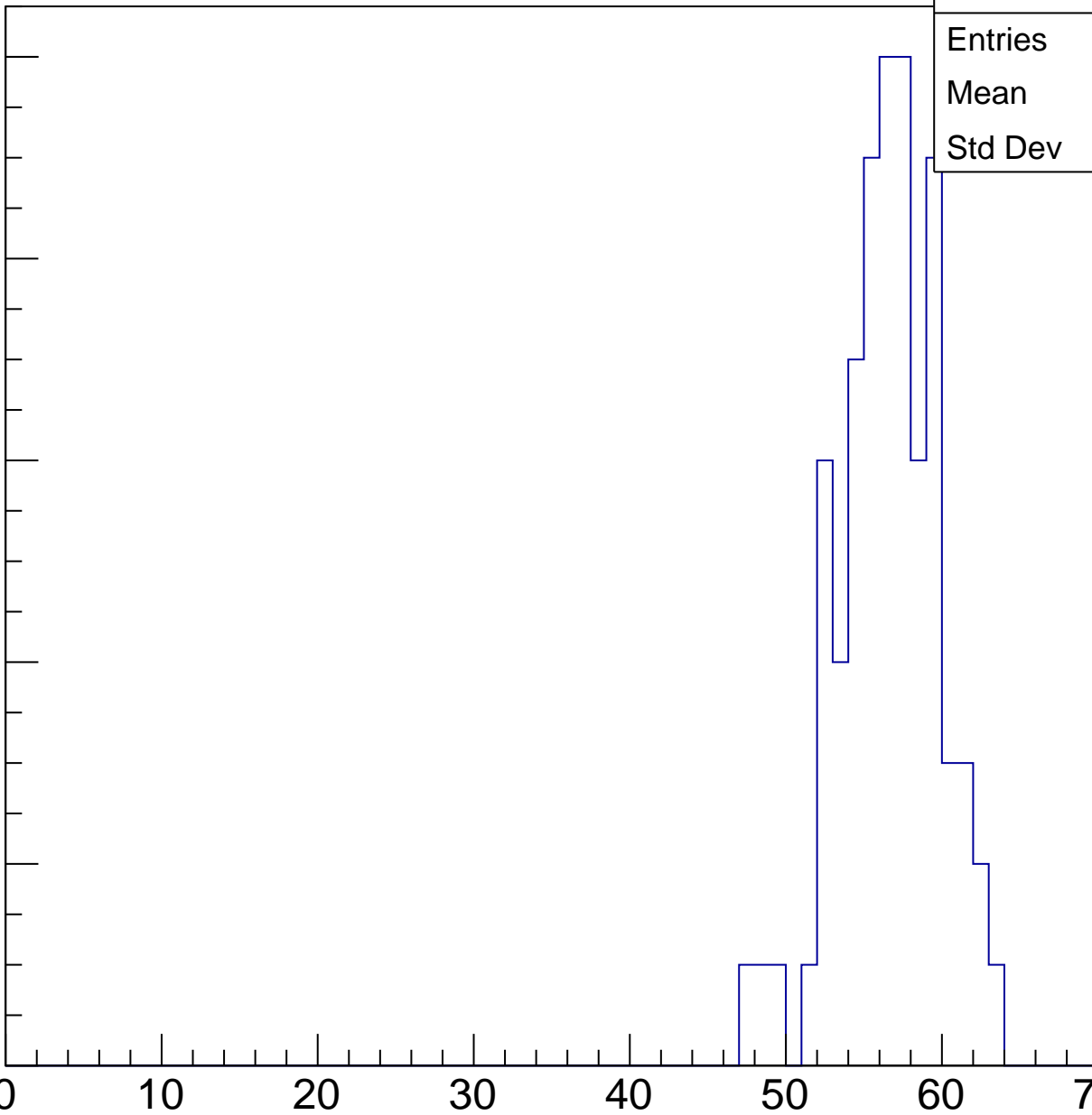
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10  
8  
6  
4  
2  
0

Entries	74
Mean	56.09
Std Dev	3.176

ampl

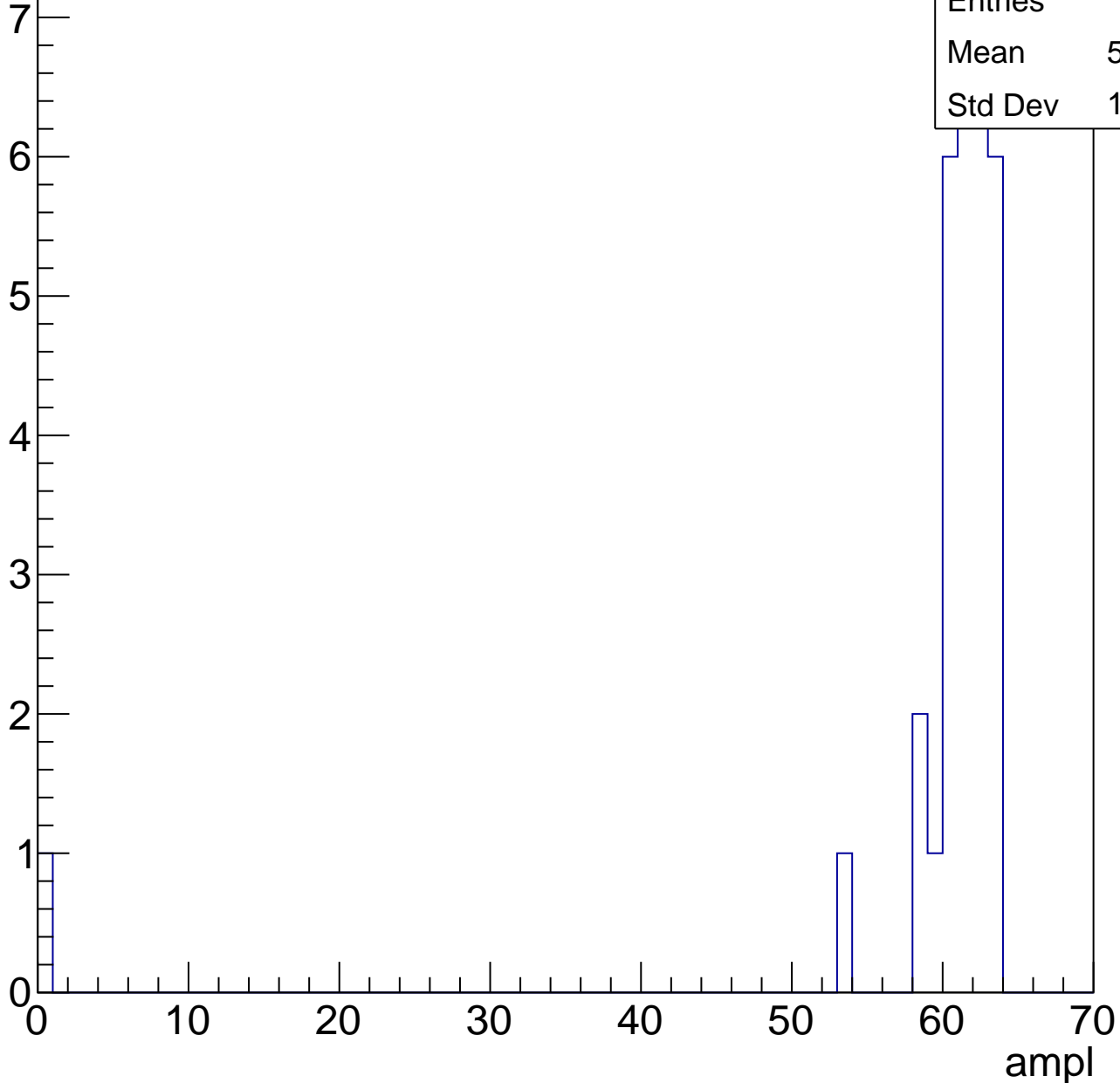


# B0L000S, U13-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	58.94
Std Dev	10.94



# B0L000S, U13-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch63, adc0

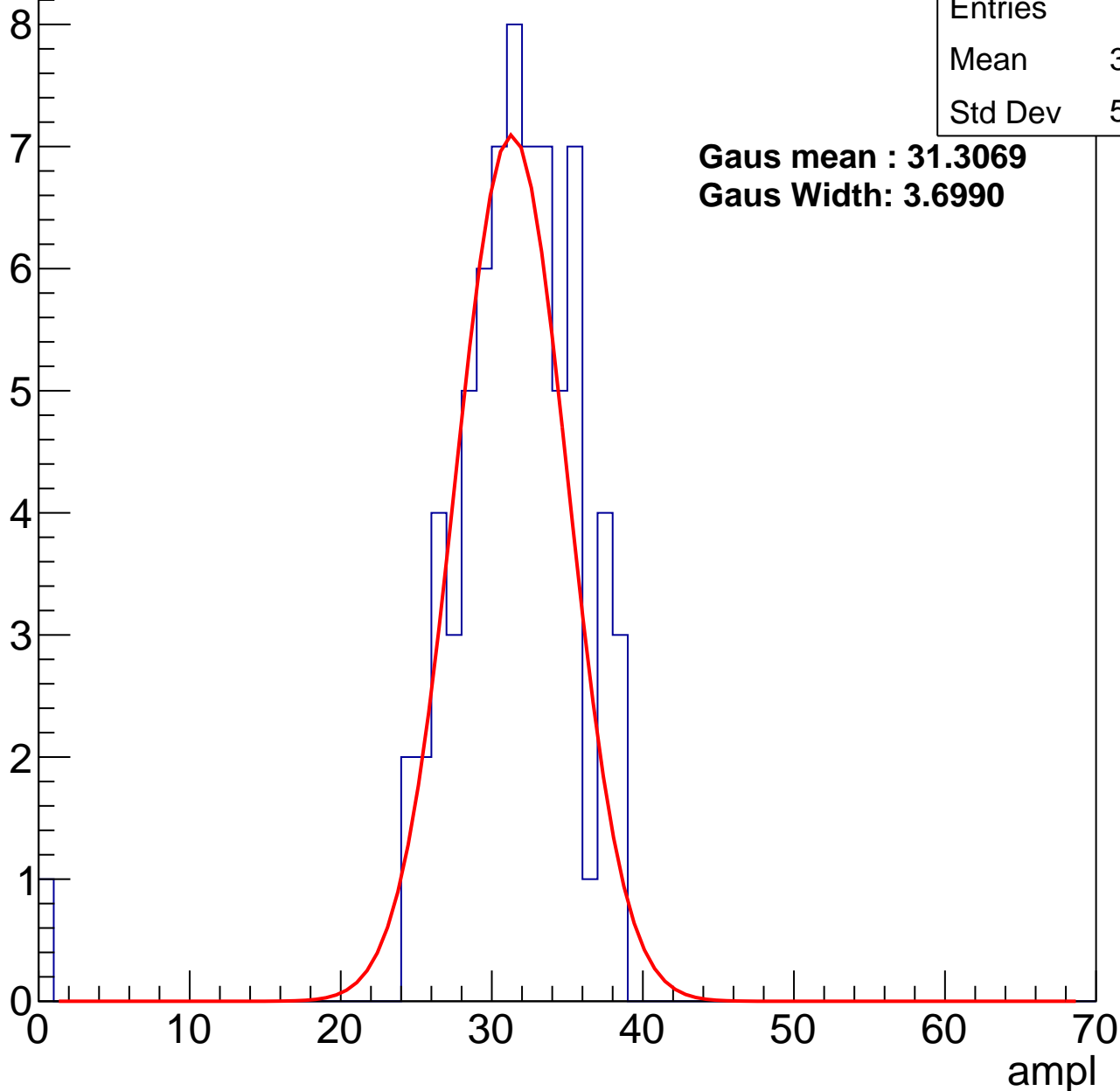
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.88
Std Dev	5.085

**Gaus mean : 31.3069**

**Gaus Width: 3.6990**



# B0L000S, U13-ch63, adc1

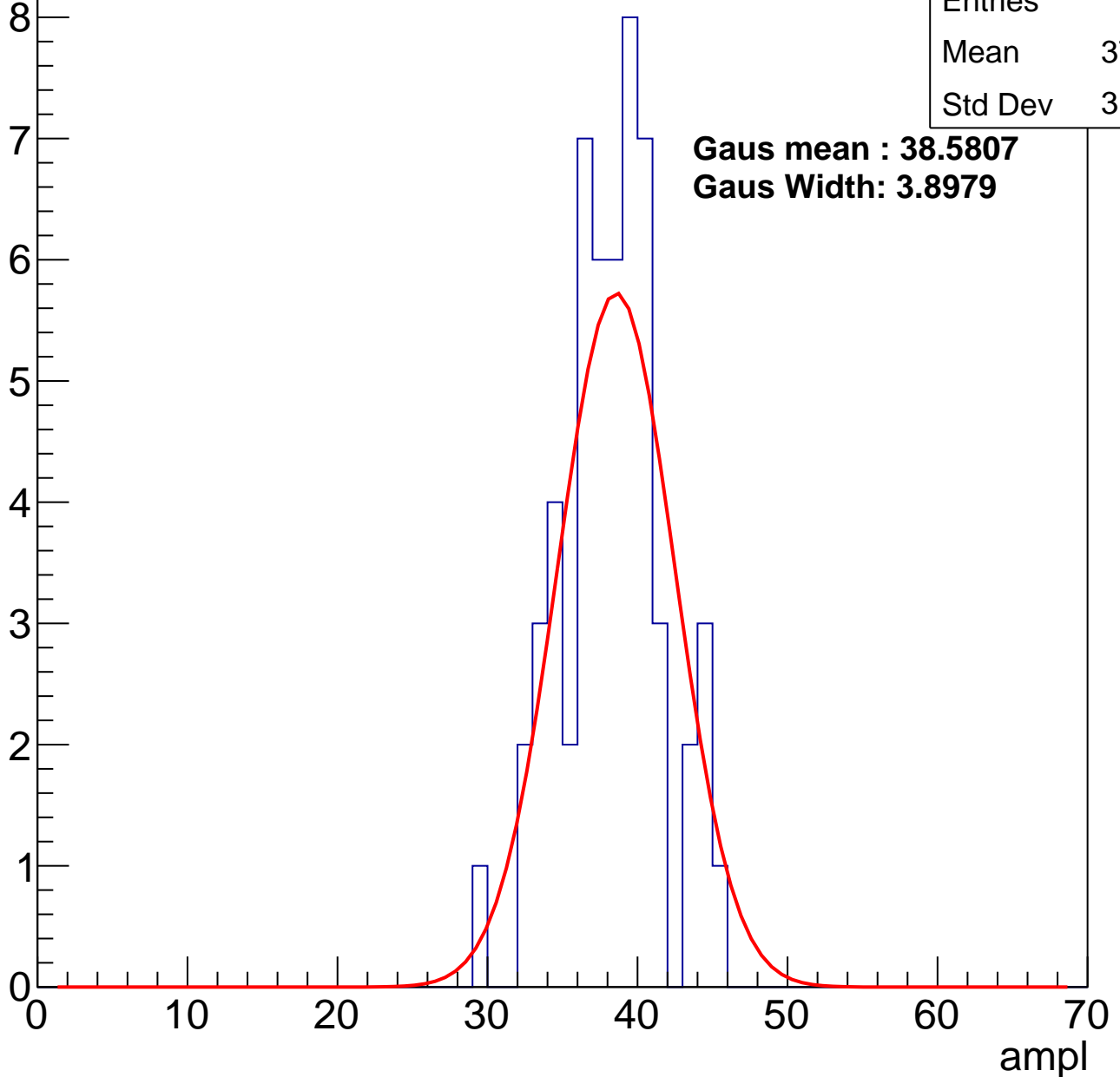
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	37.78
Std Dev	3.334

**Gaus mean : 38.5807**

**Gaus Width: 3.8979**



# B0L000S, U13-ch63, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	44.59
Std Dev	3.537

**Gaus mean : 44.9307**

**Gaus Width: 3.3825**

Entry

10

8

6

4

2

0

0

10

20

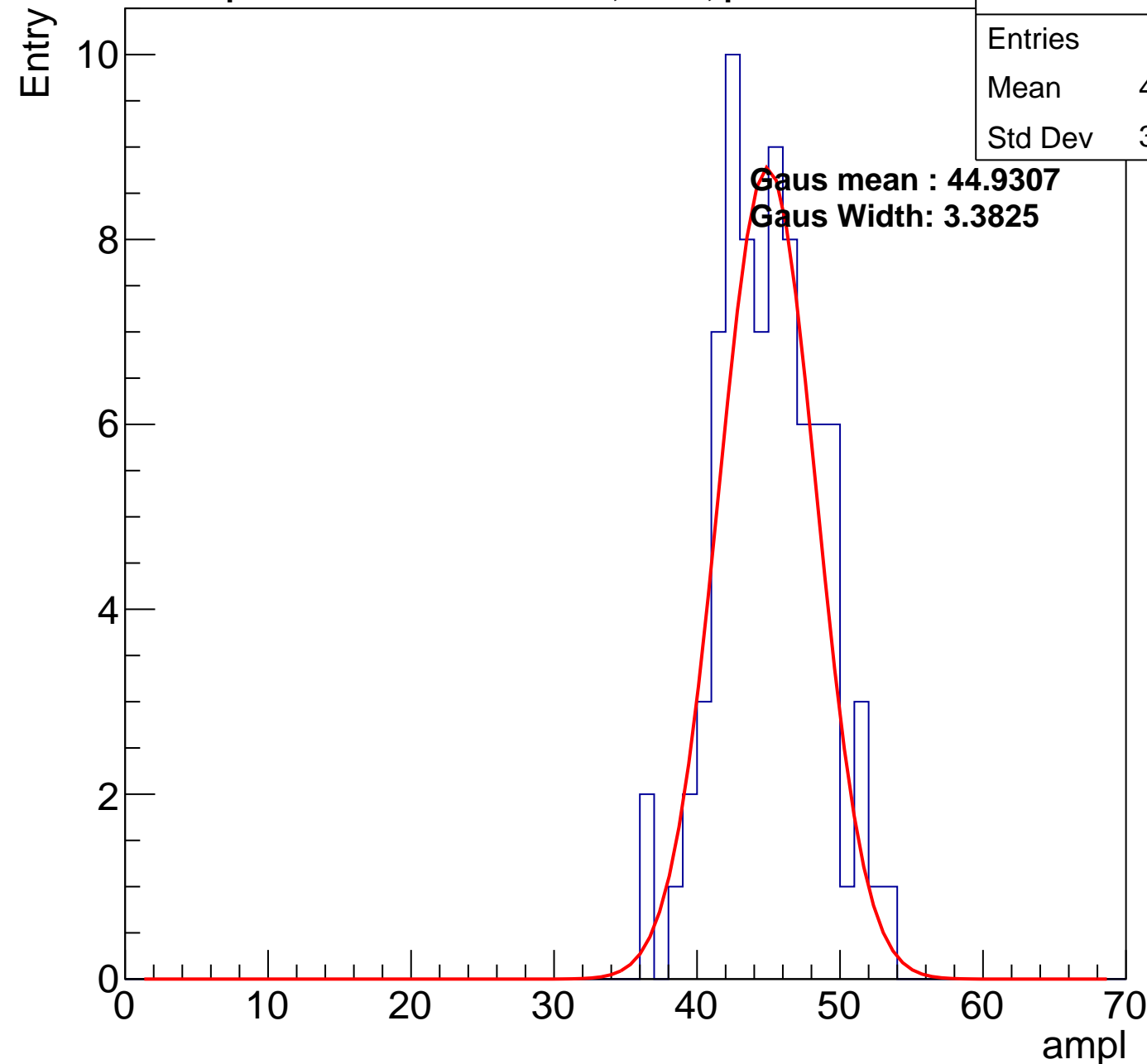
30

40

50

60

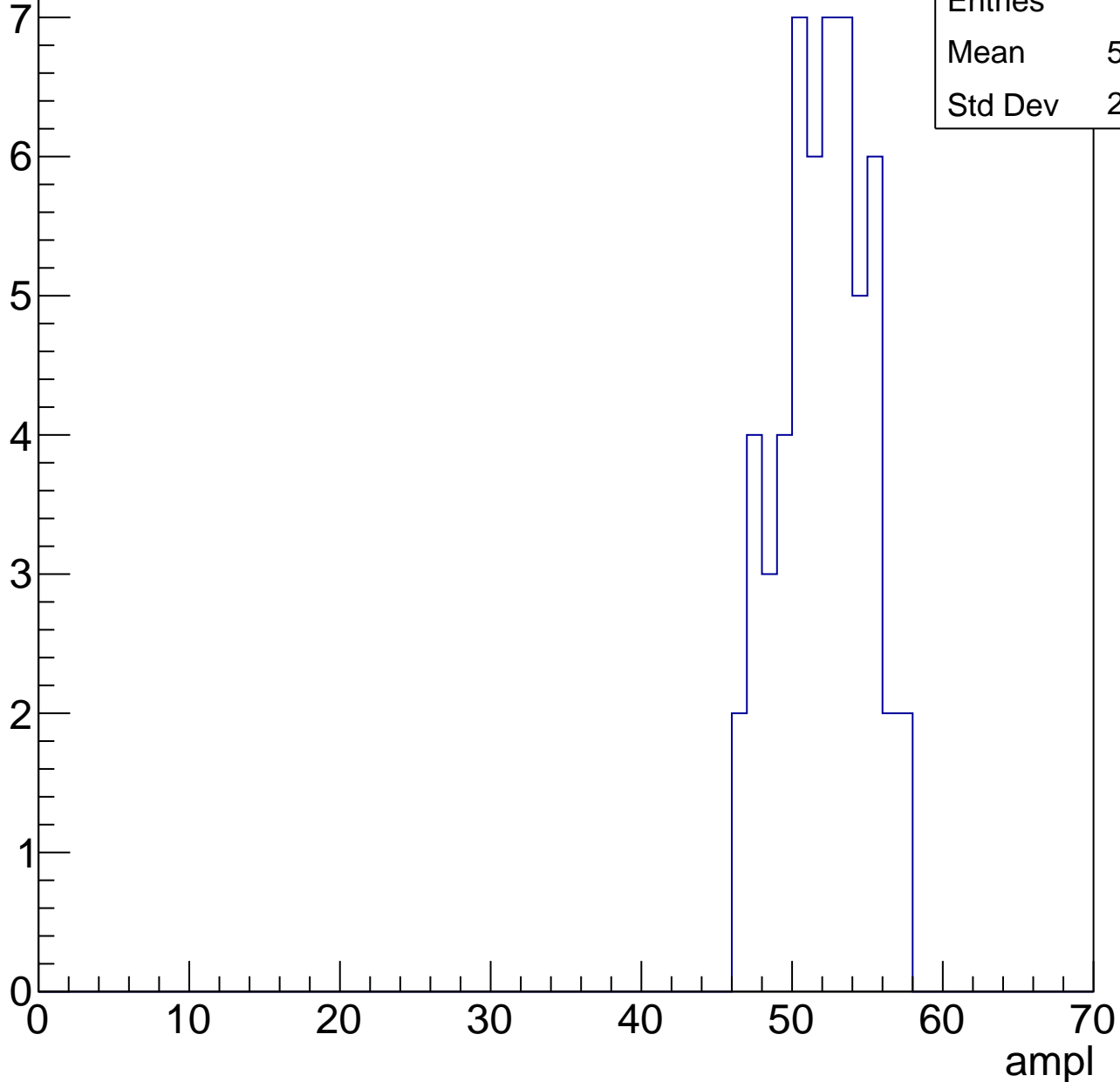
ampl



# B0L000S, U13-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

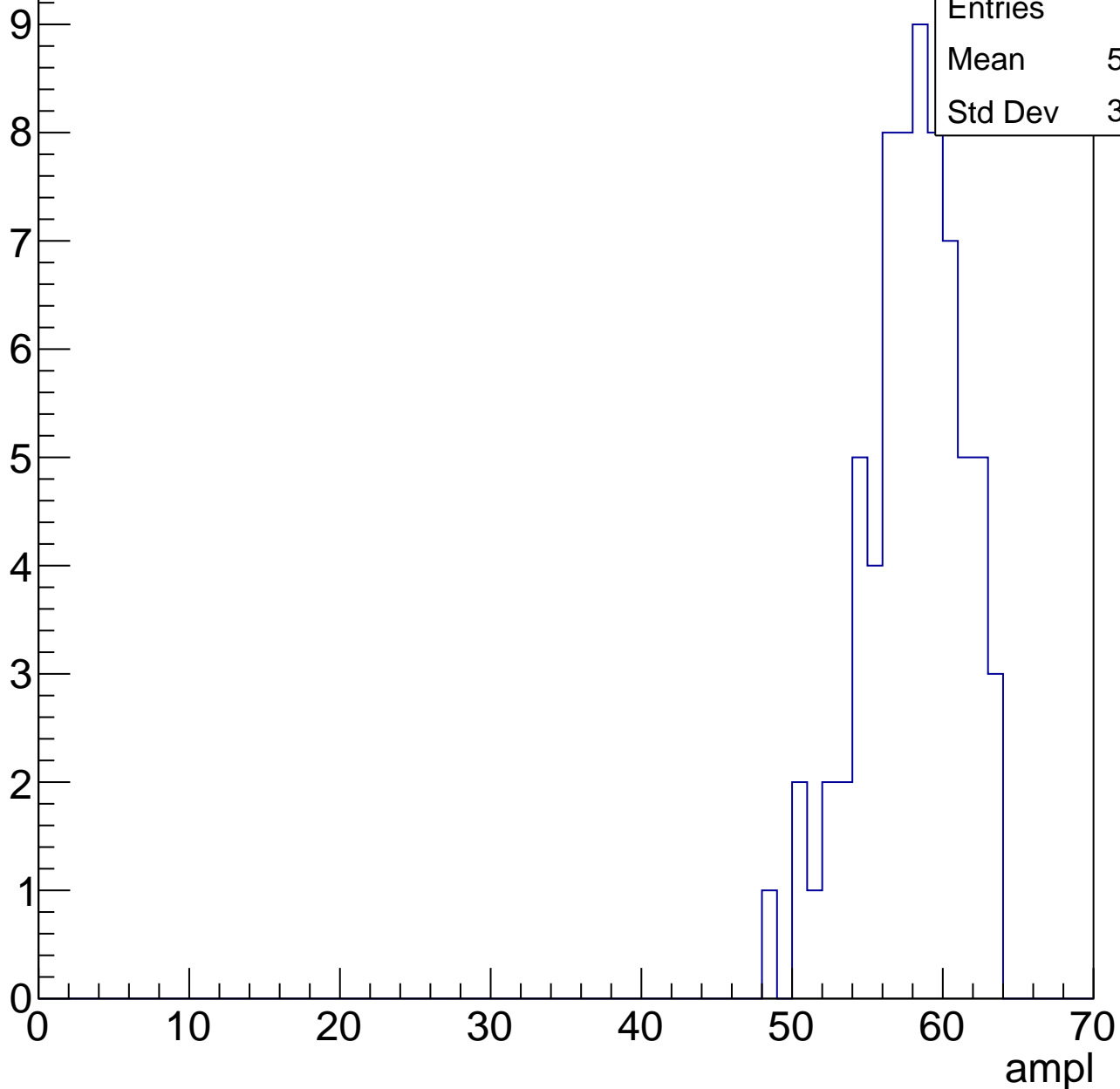


Entries	55
Mean	51.58
Std Dev	2.839

# B0L000S, U13-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

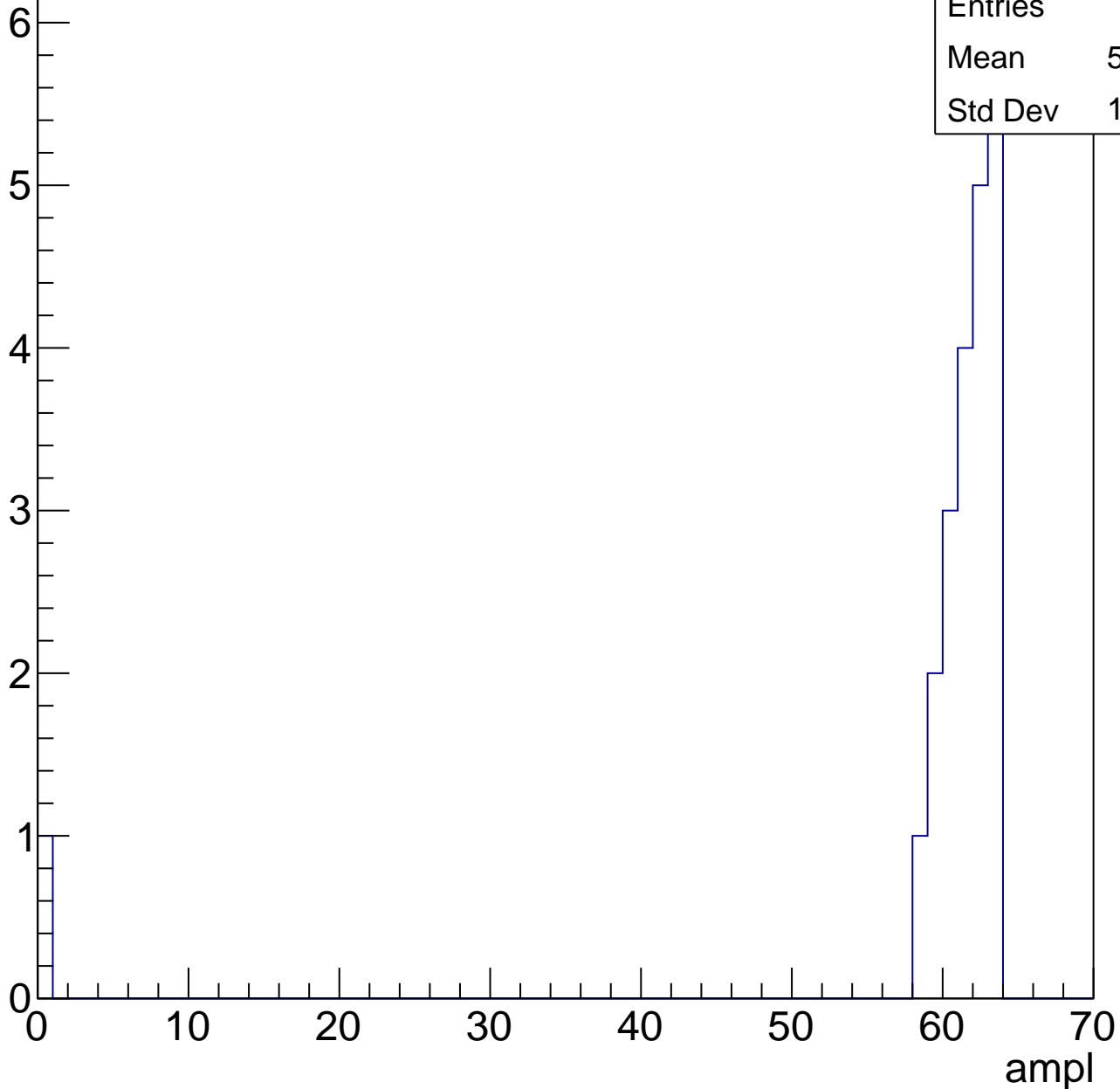


# B0L000S, U13-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	58.55
Std Dev	12.86



# B0L000S, U13-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	22
Std Dev	0

# B0L000S, U13-ch64, adc0

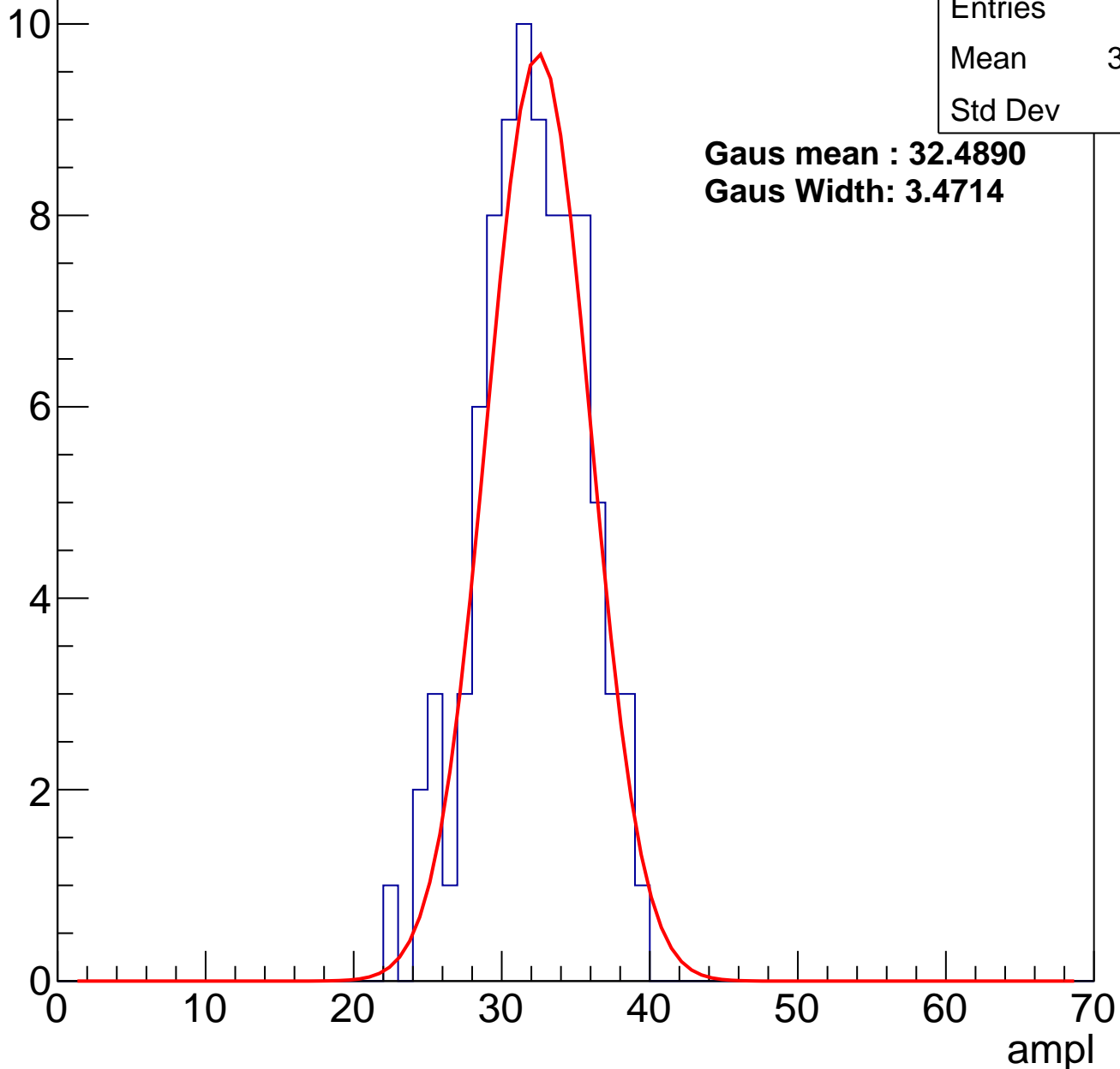
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	88
Mean	31.59
Std Dev	3.55

**Gaus mean : 32.4890**

**Gaus Width: 3.4714**

Entry



# B0L000S, U13-ch64, adc1

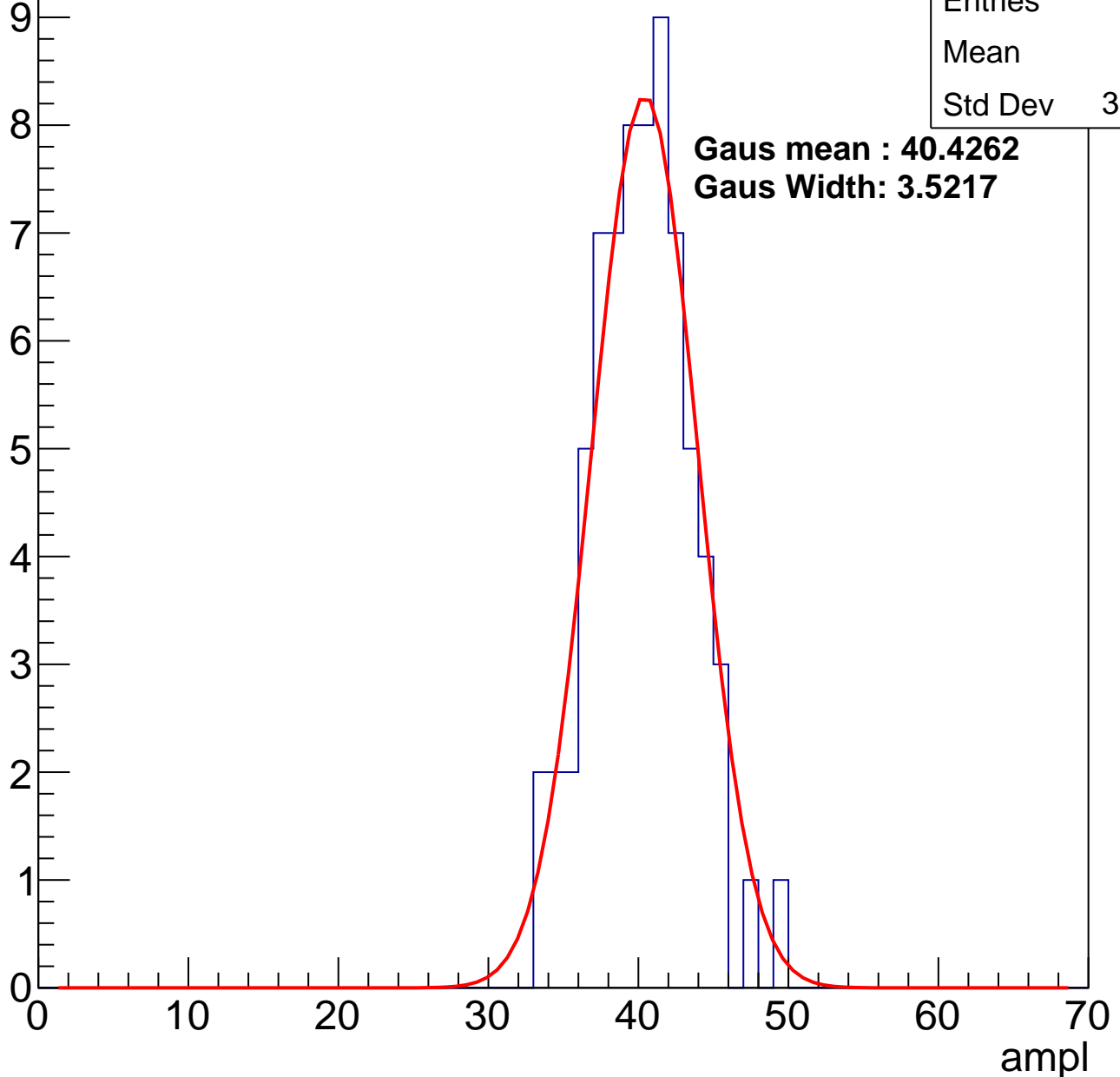
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	39.8
Std Dev	3.244

**Gaus mean : 40.4262**

**Gaus Width: 3.5217**



# B0L000S, U13-ch64, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	46.94
Std Dev	3.779

**Gaus mean : 47.2892**

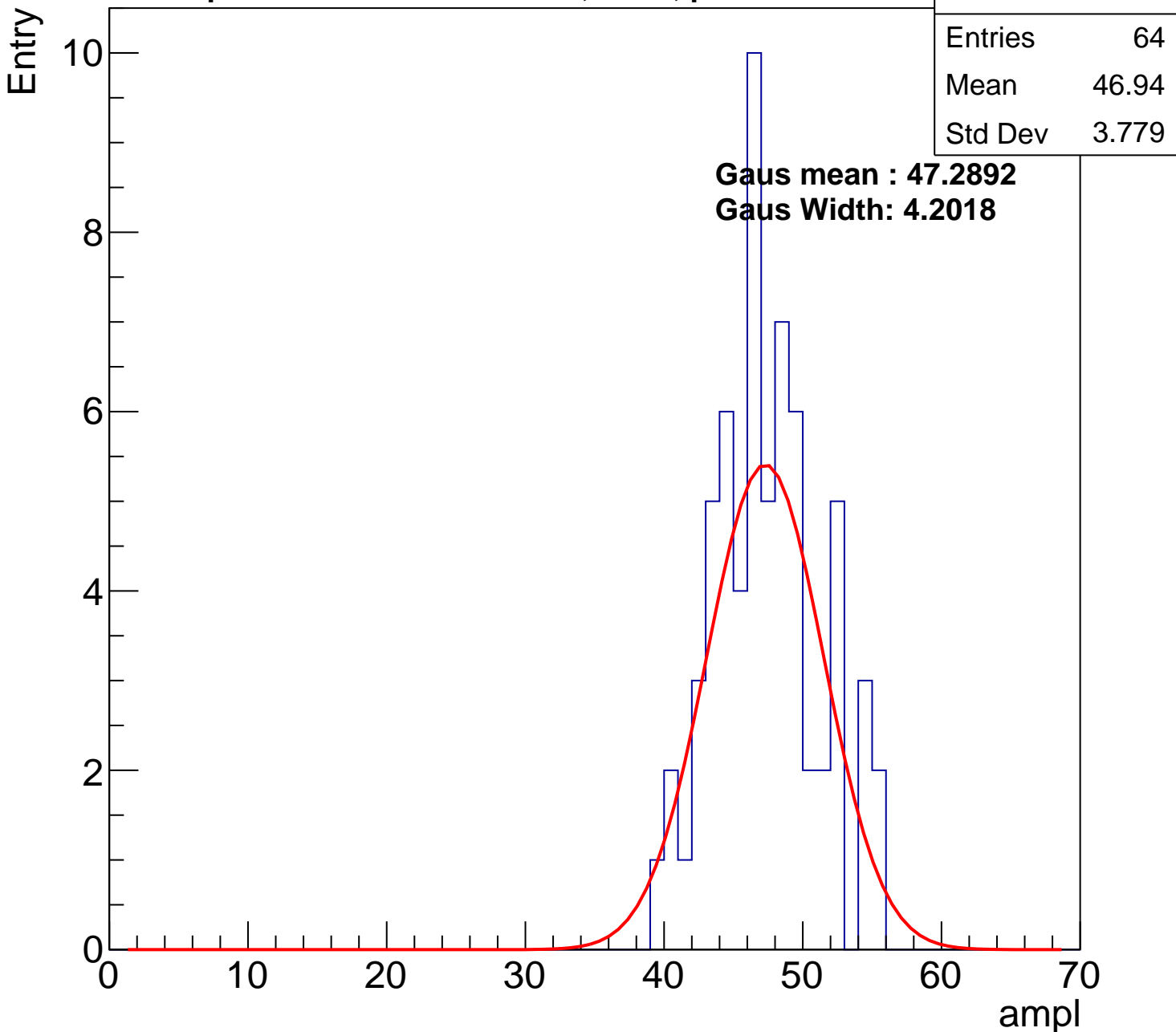
**Gaus Width: 4.2018**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

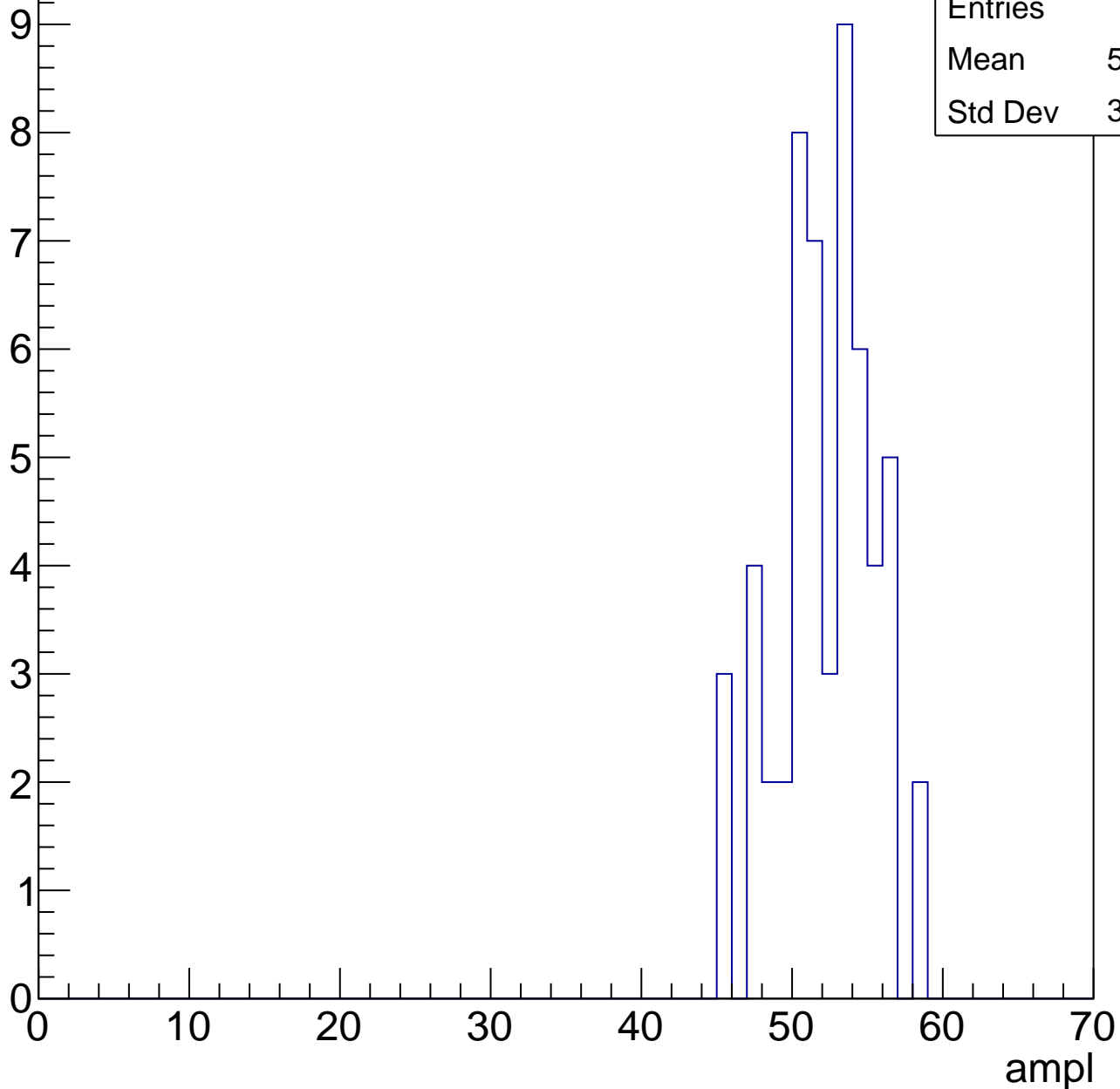


# B0L000S, U13-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

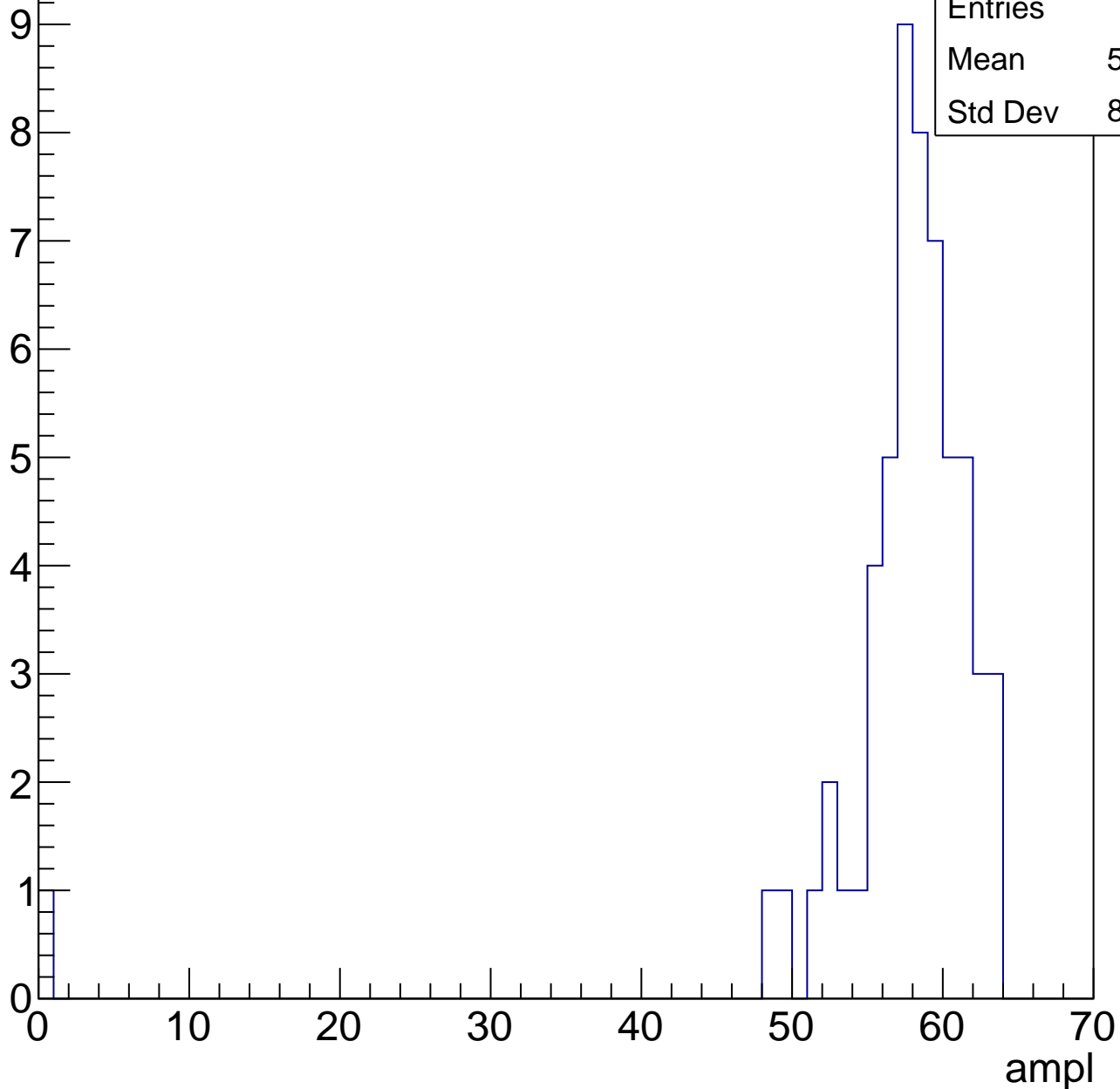
Entries	55
Mean	51.76
Std Dev	3.174



# B0L000S, U13-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

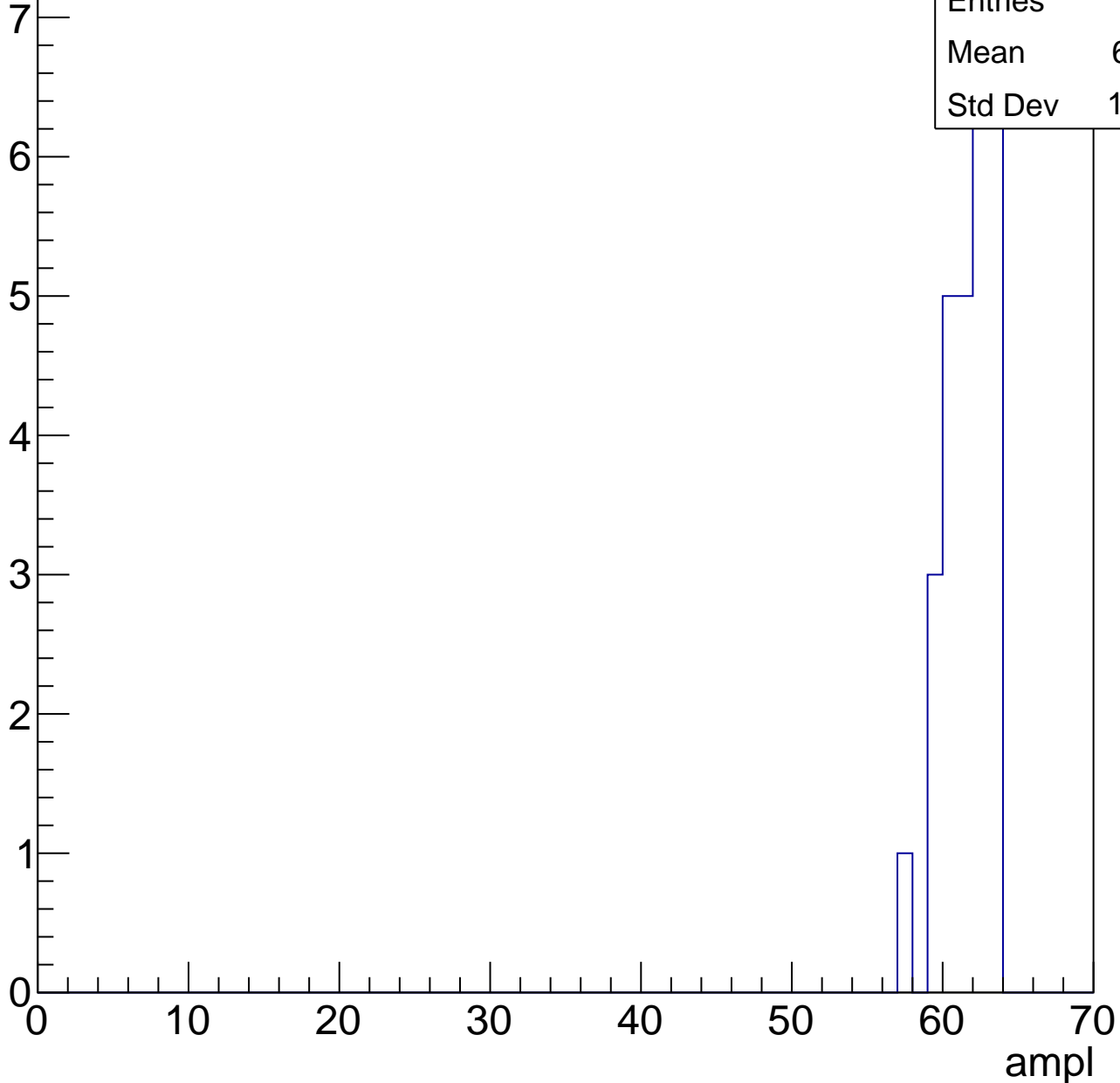


# B0L000S, U13-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	61.21
Std Dev	1.544



# B0L000S, U13-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch65, adc0

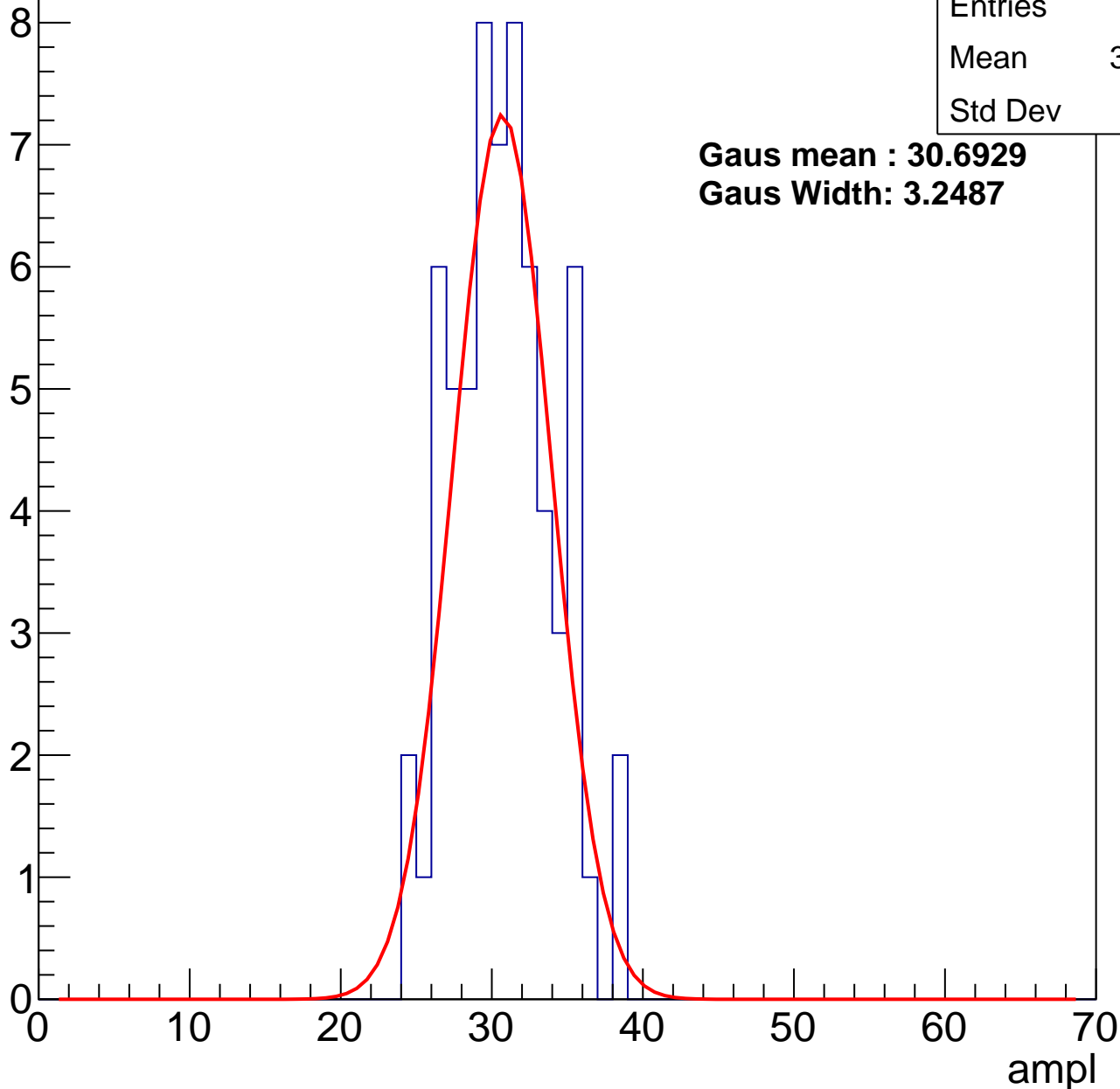
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.34
Std Dev	3.28

**Gaus mean : 30.6929**

**Gaus Width: 3.2487**



# B0L000S, U13-ch65, adc1

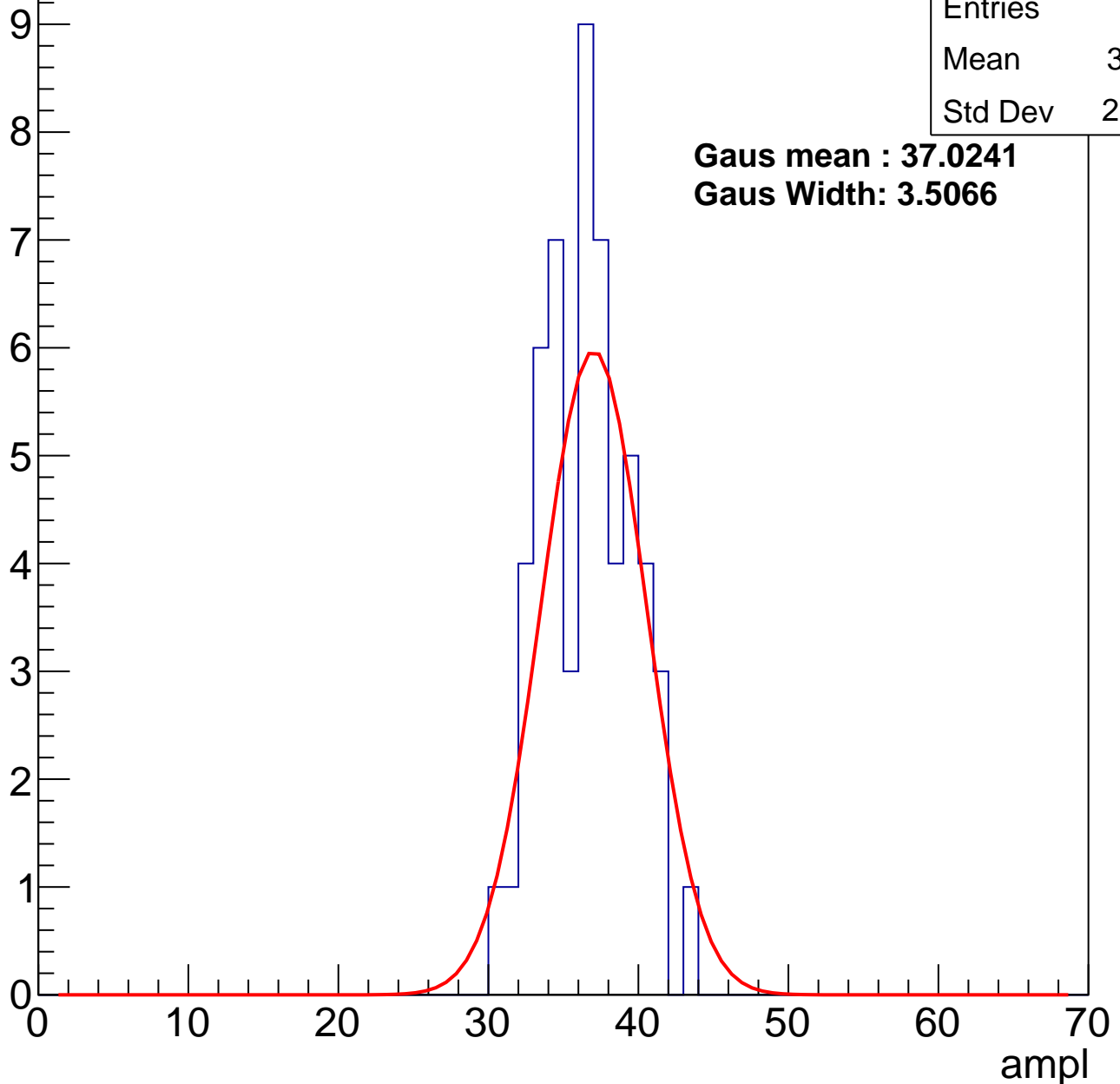
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	36.11
Std Dev	2.909

**Gaus mean : 37.0241**

**Gaus Width: 3.5066**



# B0L000S, U13-ch65, adc2

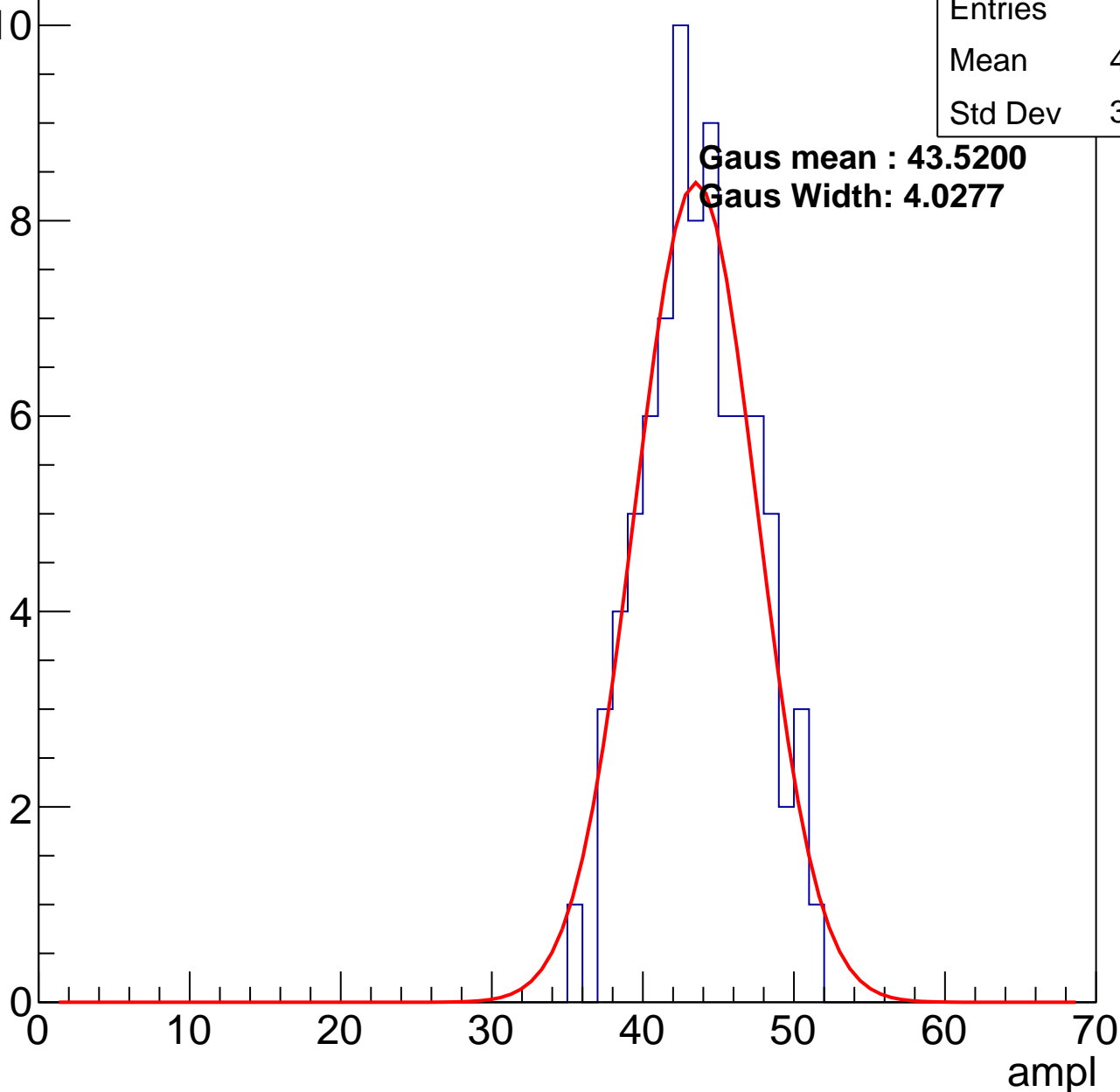
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	43.26
Std Dev	3.543

**Gaus mean : 43.5200**

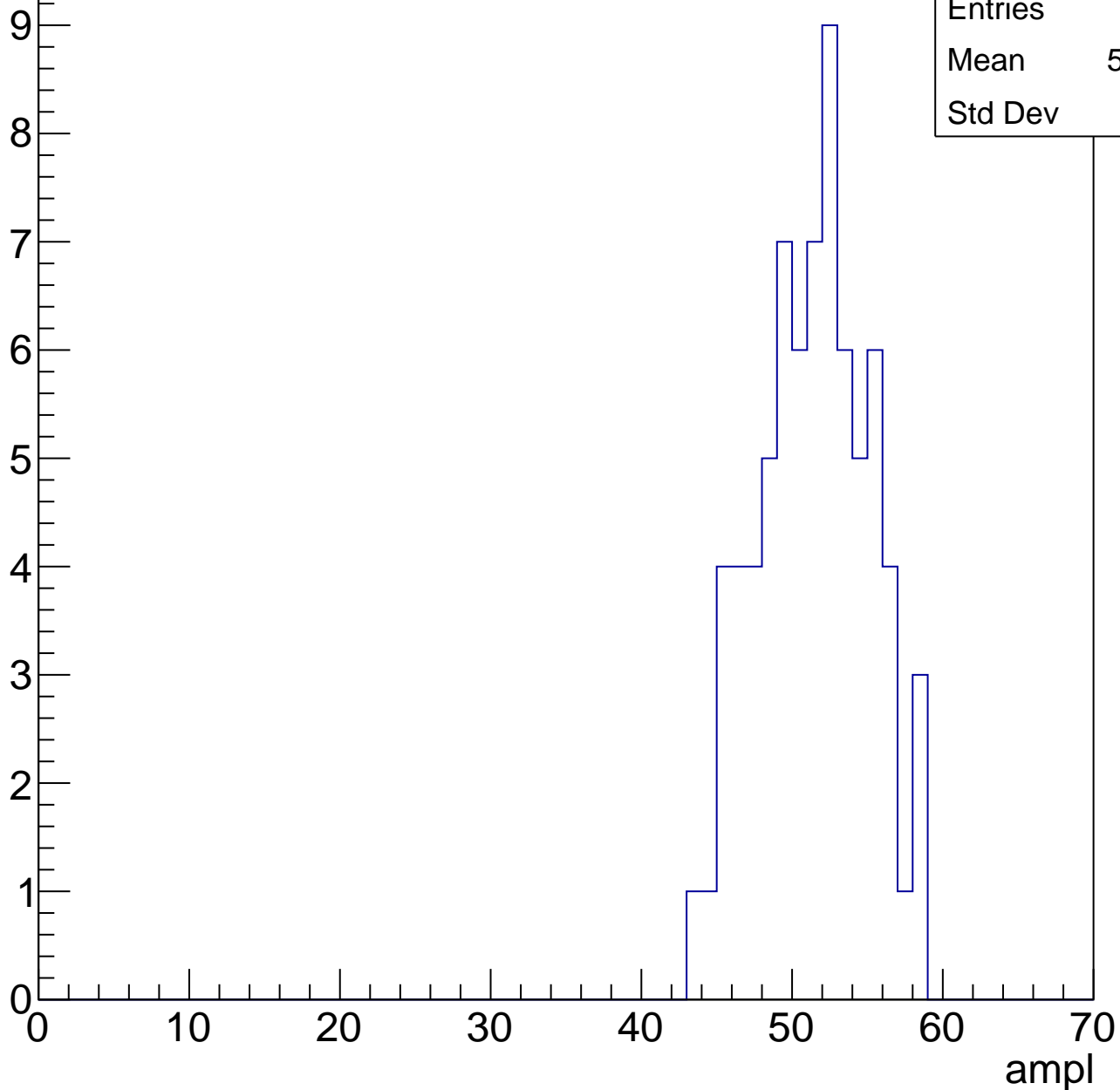
**Gaus Width: 4.0277**



# B0L000S, U13-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

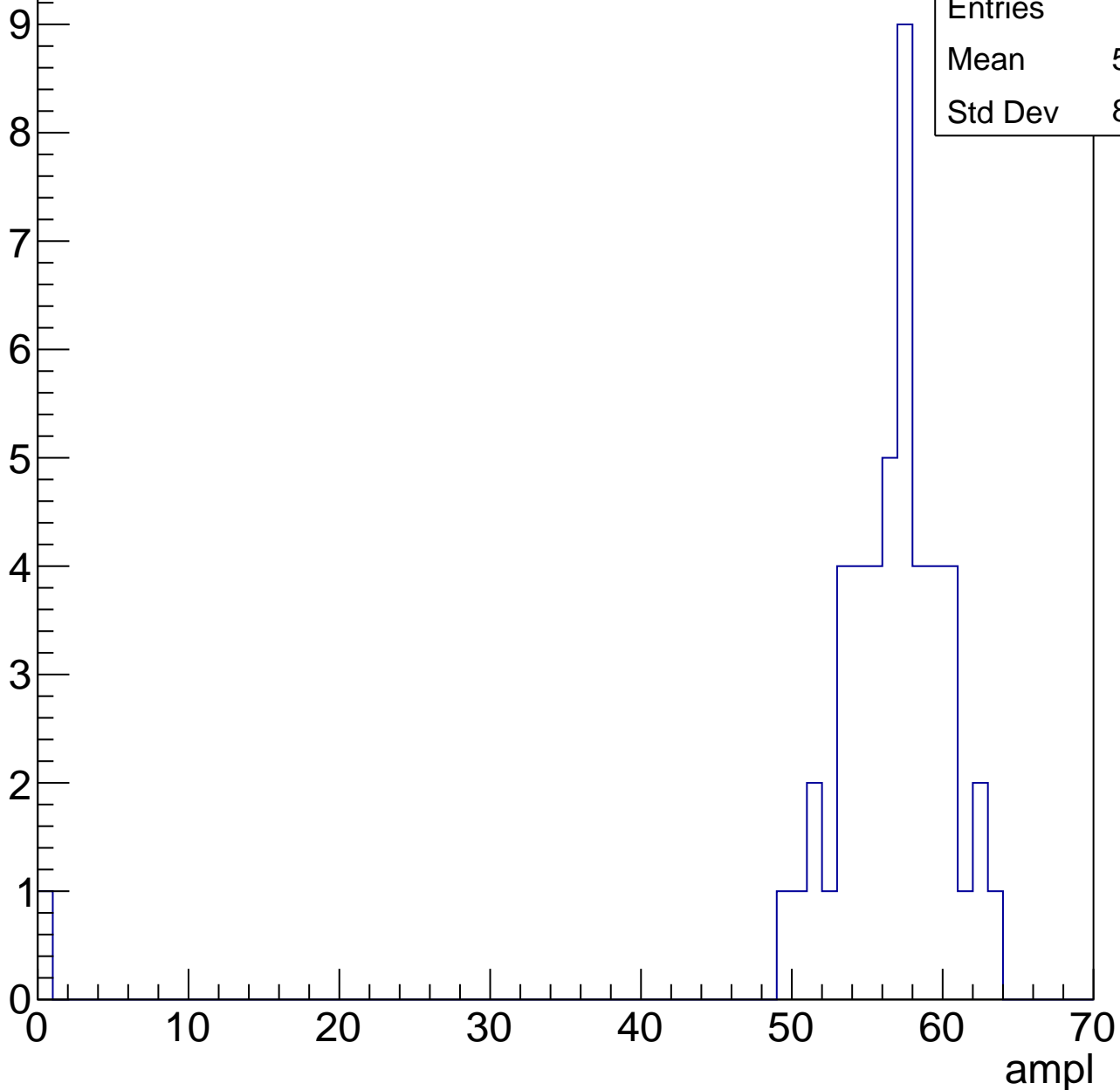


# B0L000S, U13-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

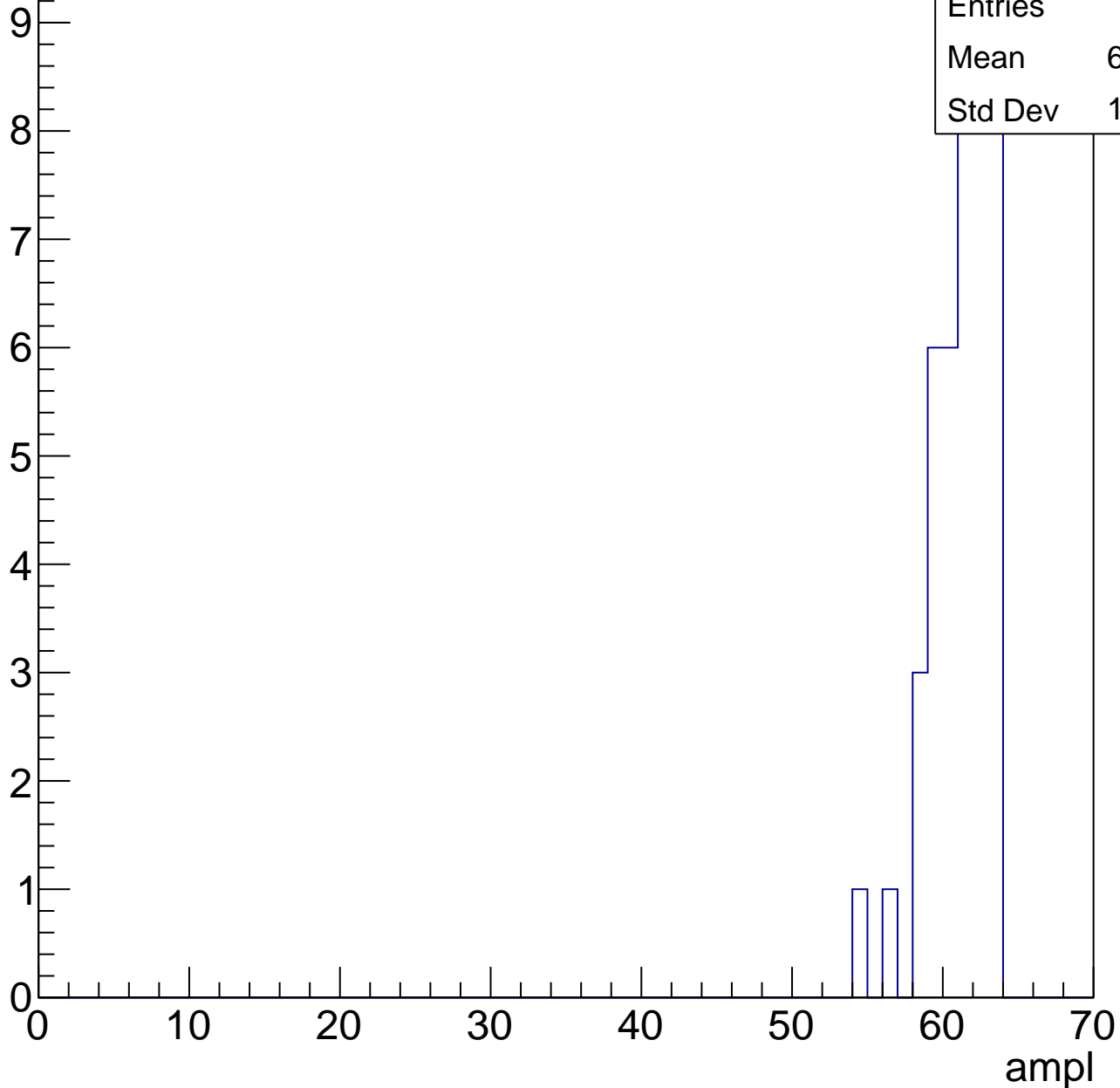
Entries	48
Mean	55.21
Std Dev	8.641



# B0L000S, U13-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch66, adc0

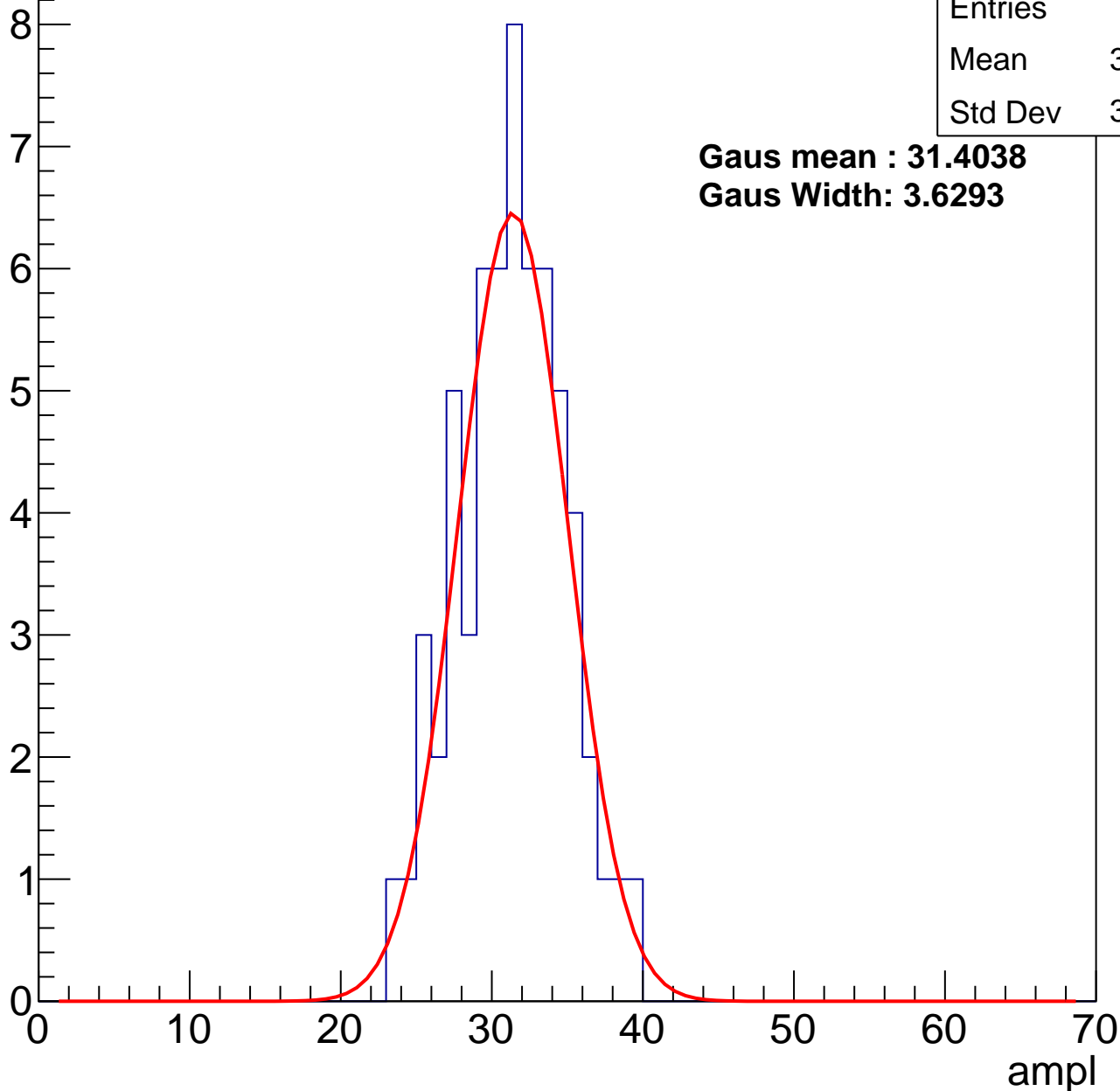
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	30.84
Std Dev	3.493

**Gaus mean : 31.4038**

**Gaus Width: 3.6293**



# B0L000S, U13-ch66, adc1

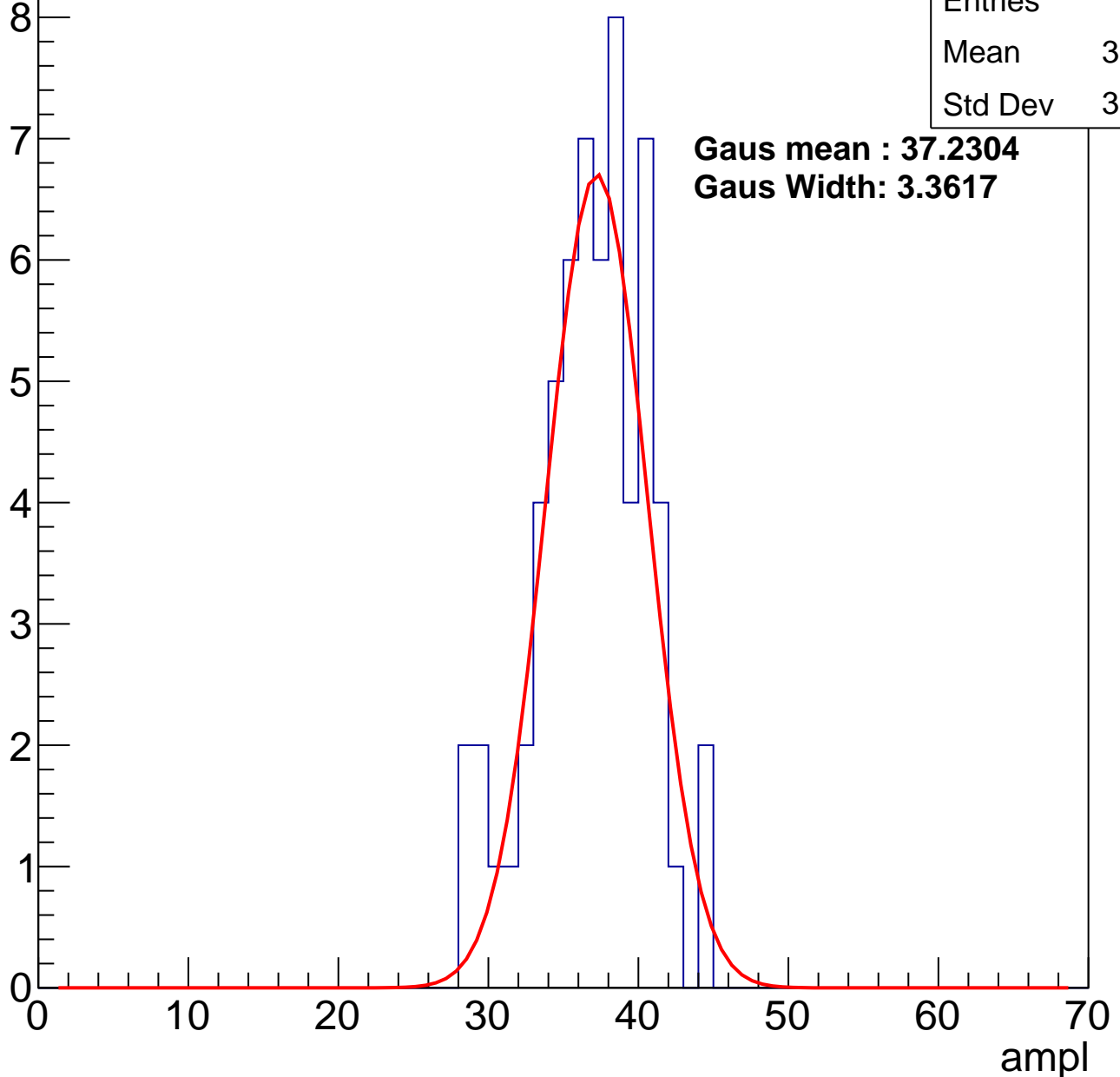
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.44
Std Dev	3.635

**Gaus mean : 37.2304**

**Gaus Width: 3.3617**



# B0L000S, U13-ch66, adc2

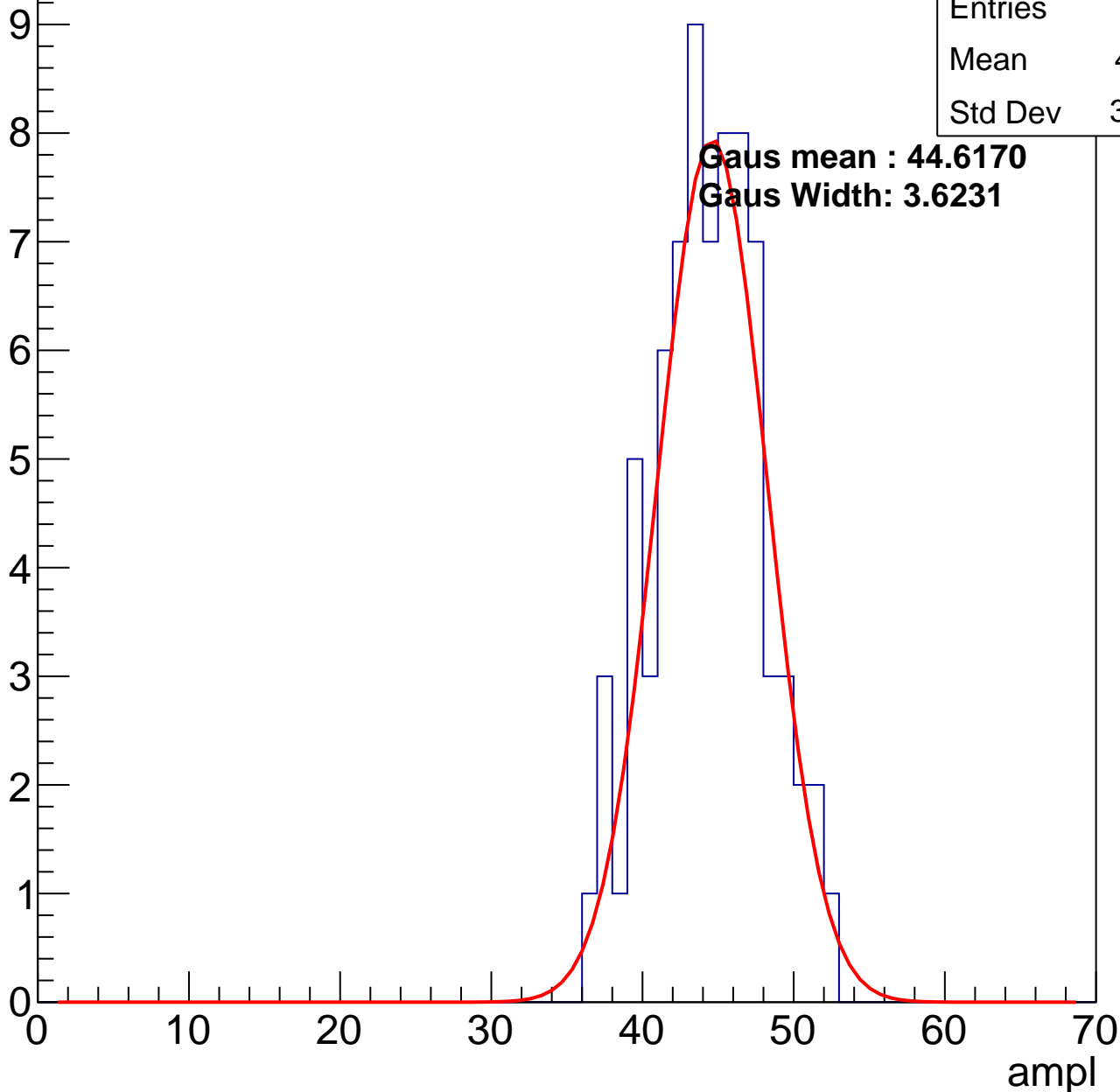
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.91
Std Dev	3.573

**Gaus mean : 44.6170**

**Gaus Width: 3.6231**

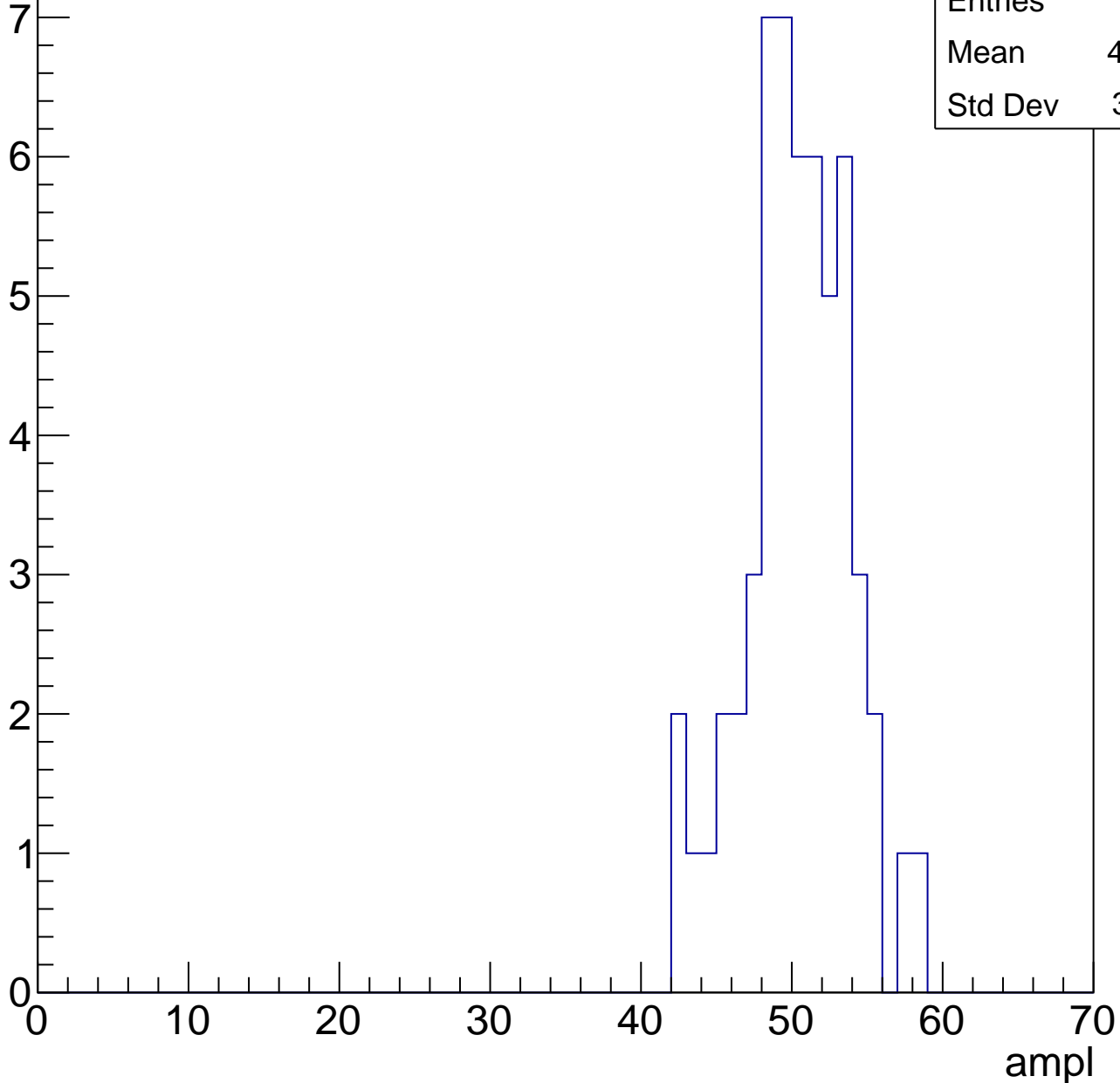


# B0L000S, U13-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

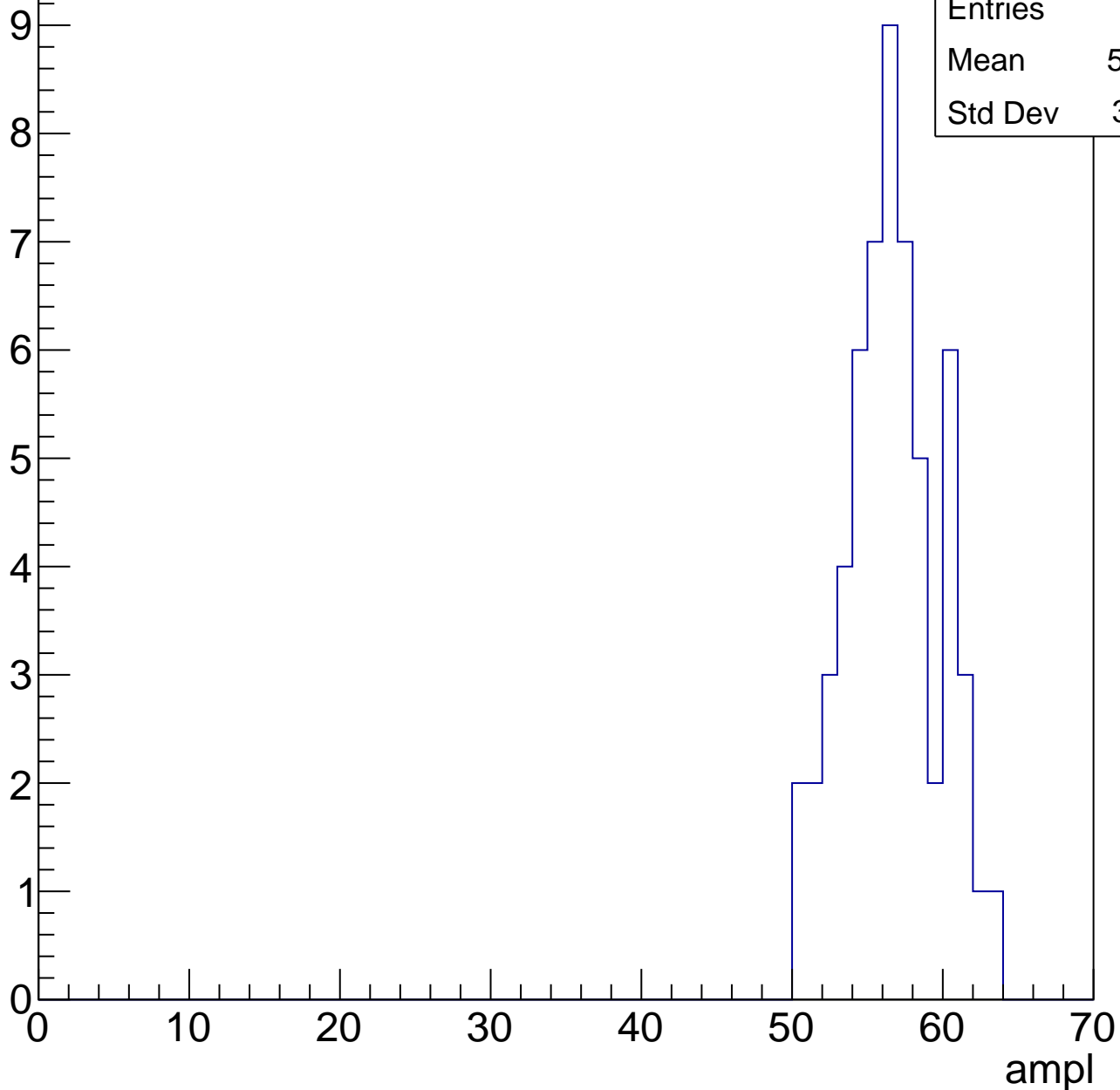
Entries	55
Mean	49.89
Std Dev	3.431



# B0L000S, U13-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

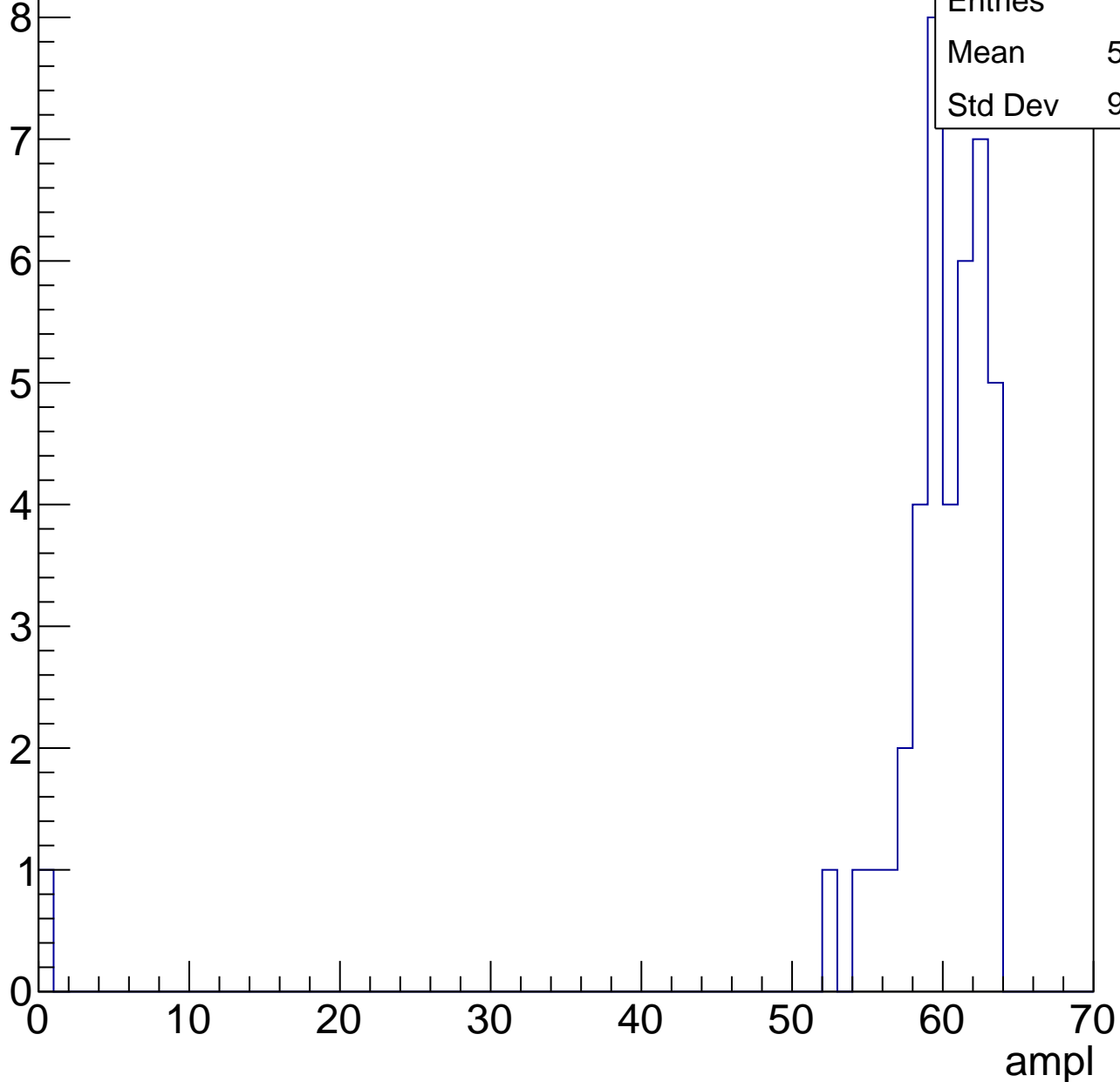


Entries	58
Mean	56.17
Std Dev	3.041

# B0L000S, U13-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	31.49
Std Dev	4.59

**Gaus mean : 32.4609**

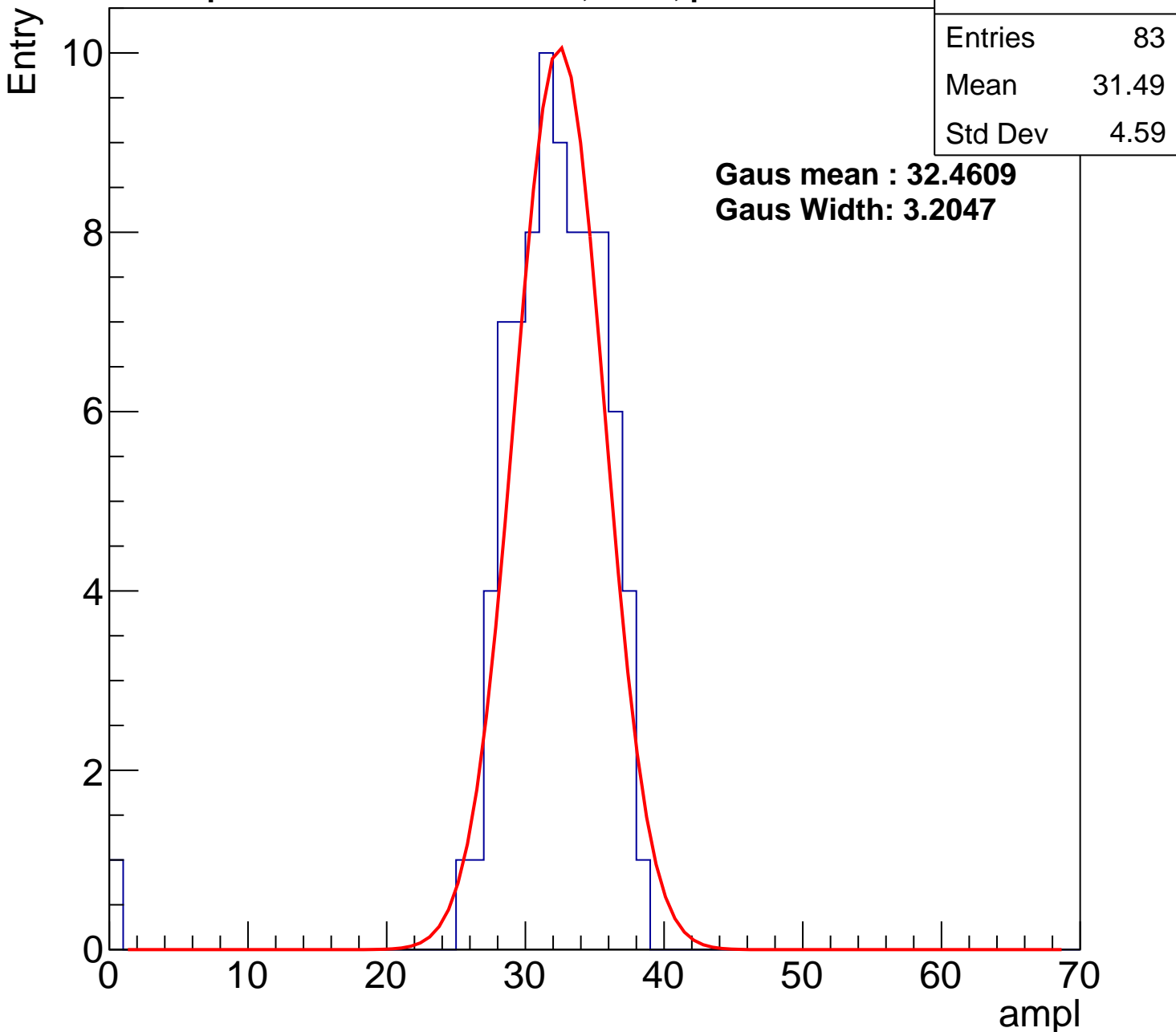
**Gaus Width: 3.2047**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch67, adc1

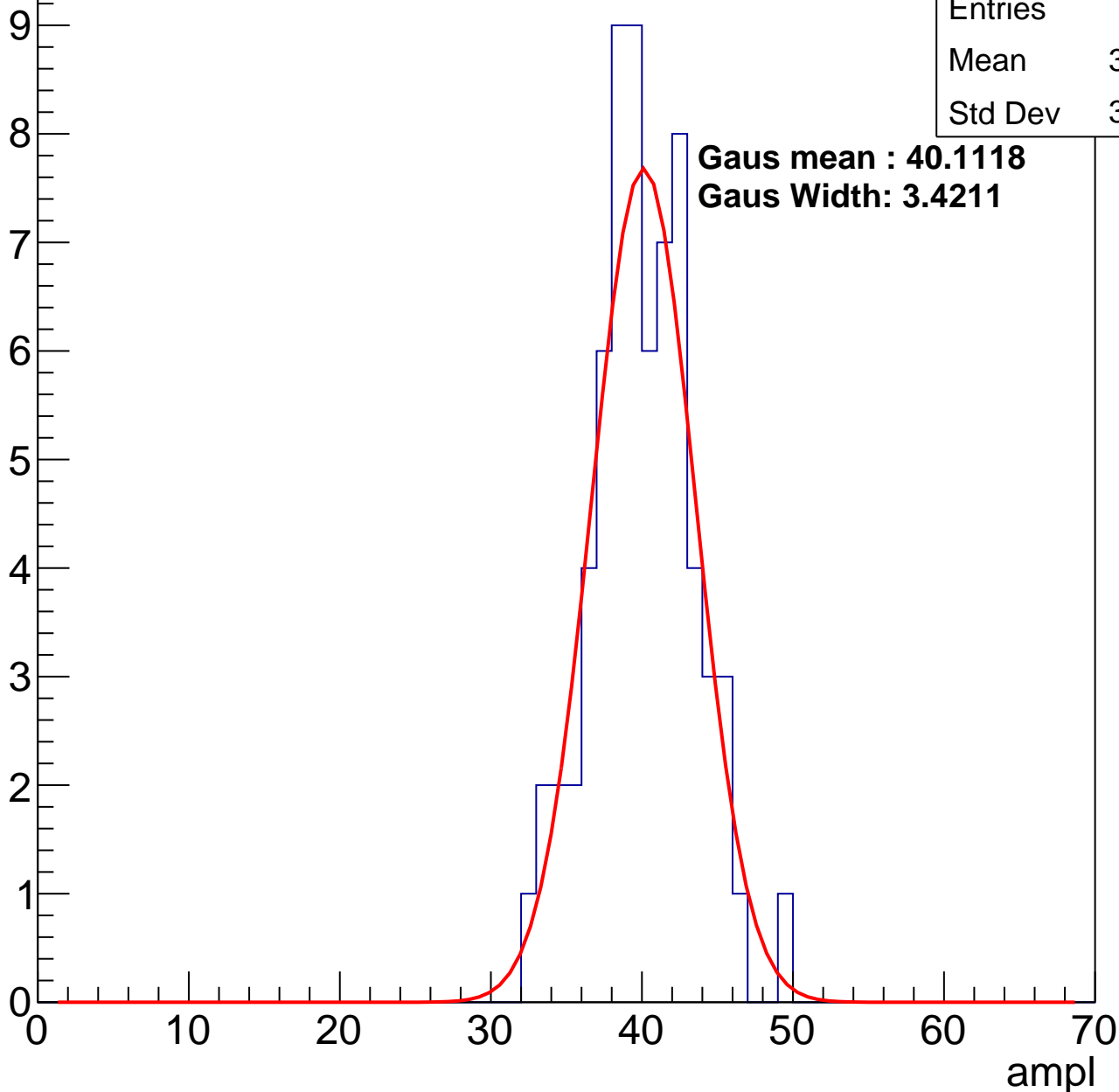
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	39.59
Std Dev	3.322

**Gaus mean : 40.1118**

**Gaus Width: 3.4211**



# B0L000S, U13-ch67, adc2

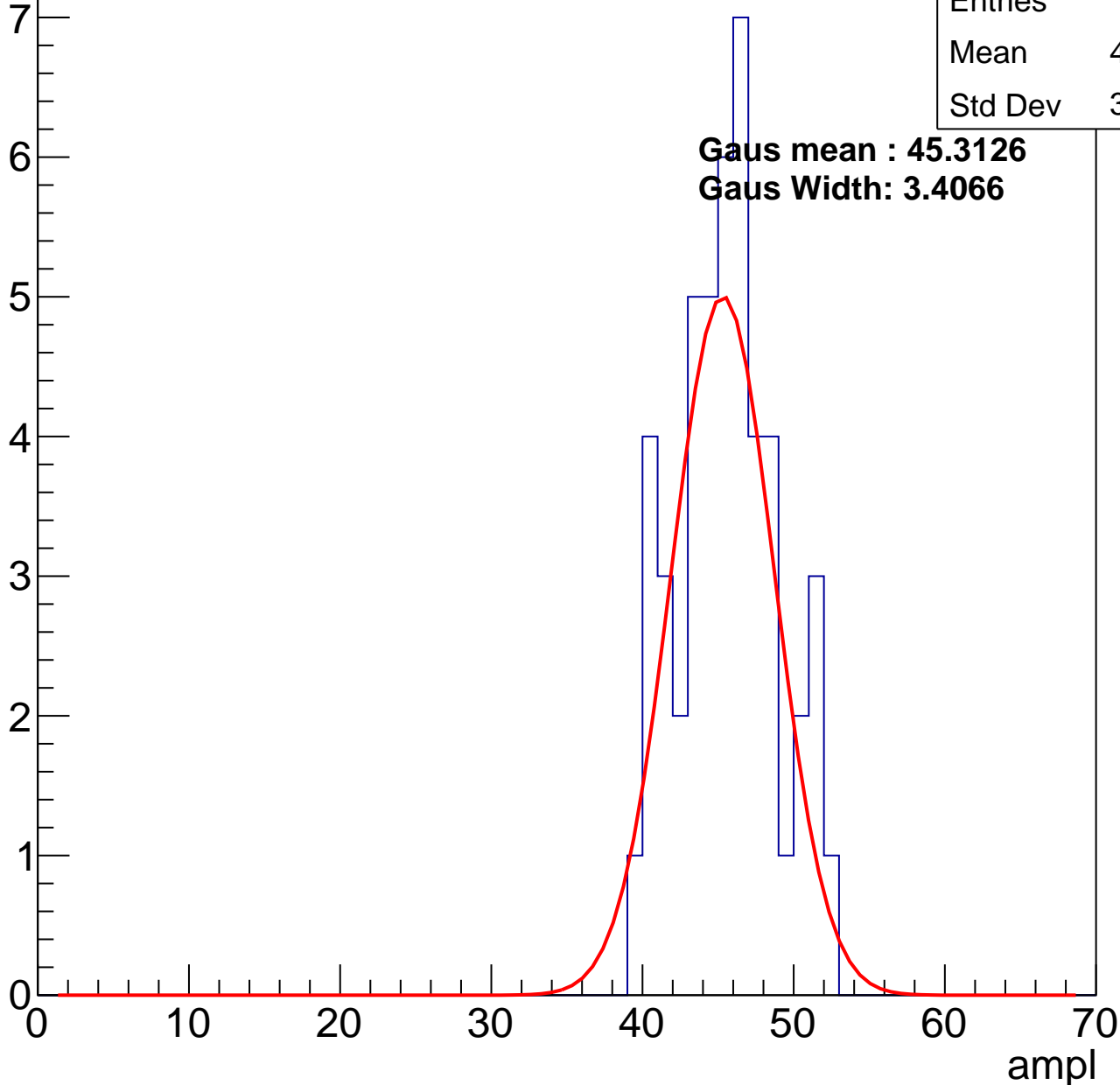
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	45.15
Std Dev	3.253

**Gaus mean : 45.3126**

**Gaus Width: 3.4066**

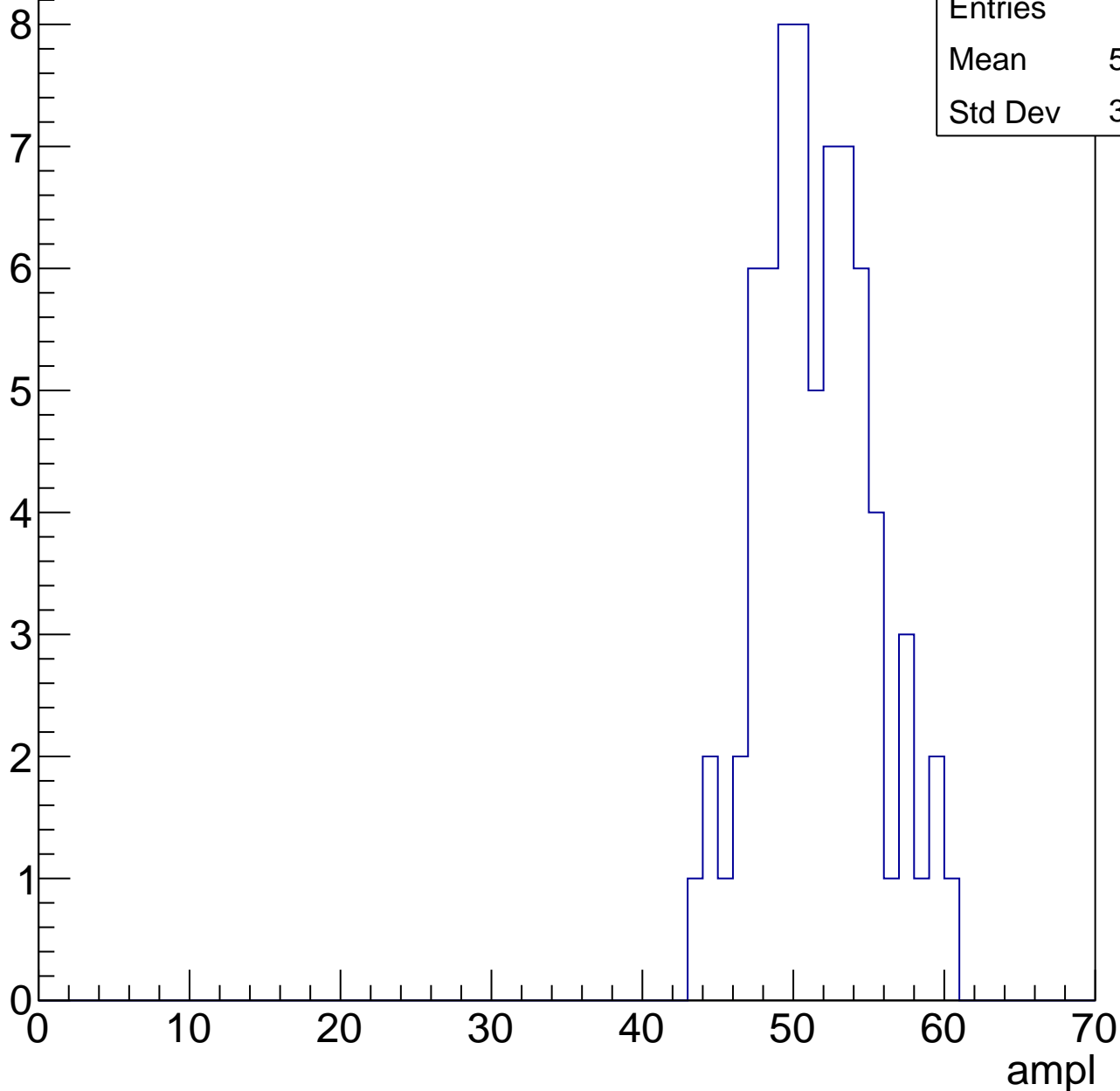


# B0L000S, U13-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	51.08
Std Dev	3.718

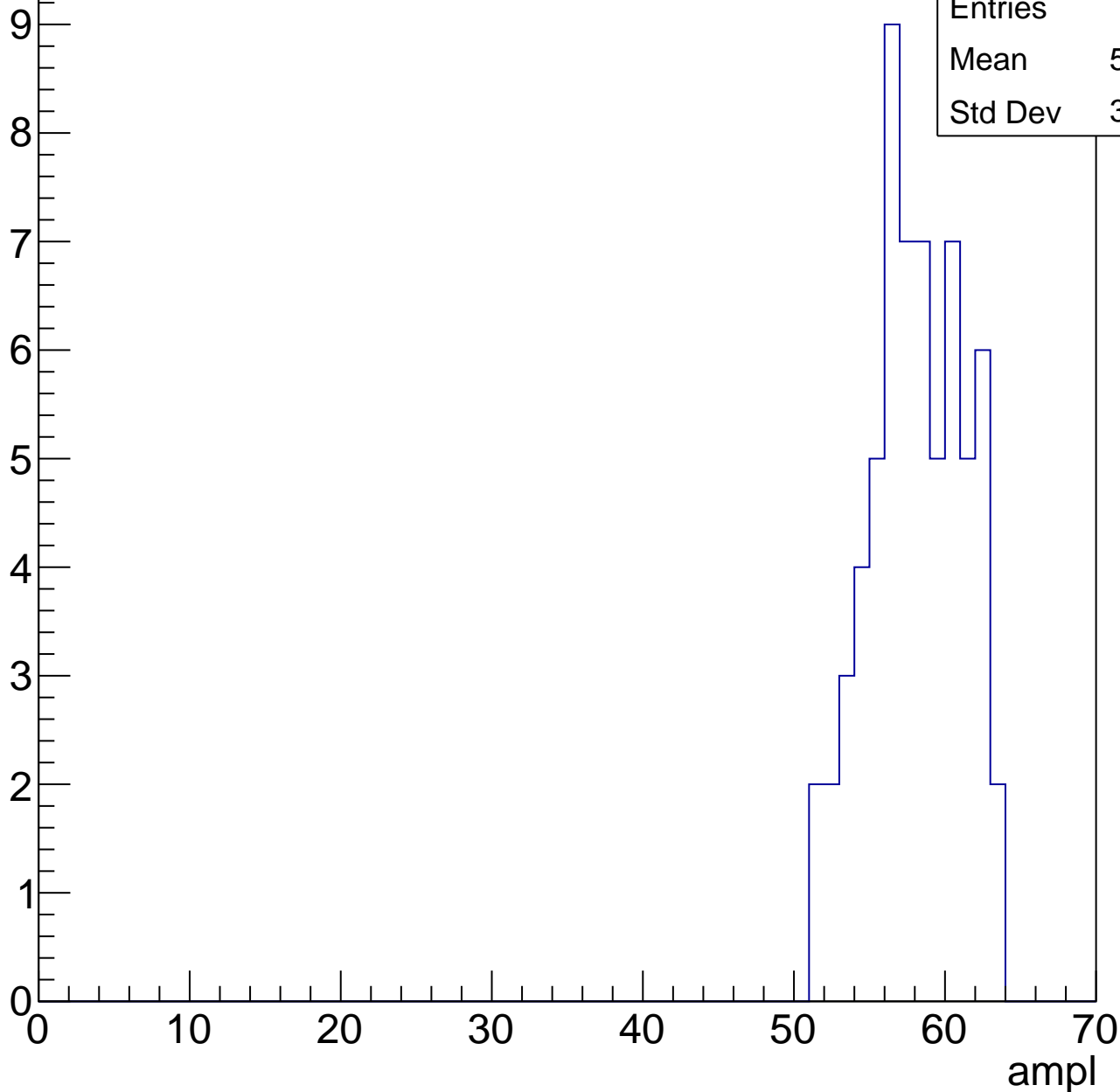


# B0L000S, U13-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	57.55
Std Dev	3.082

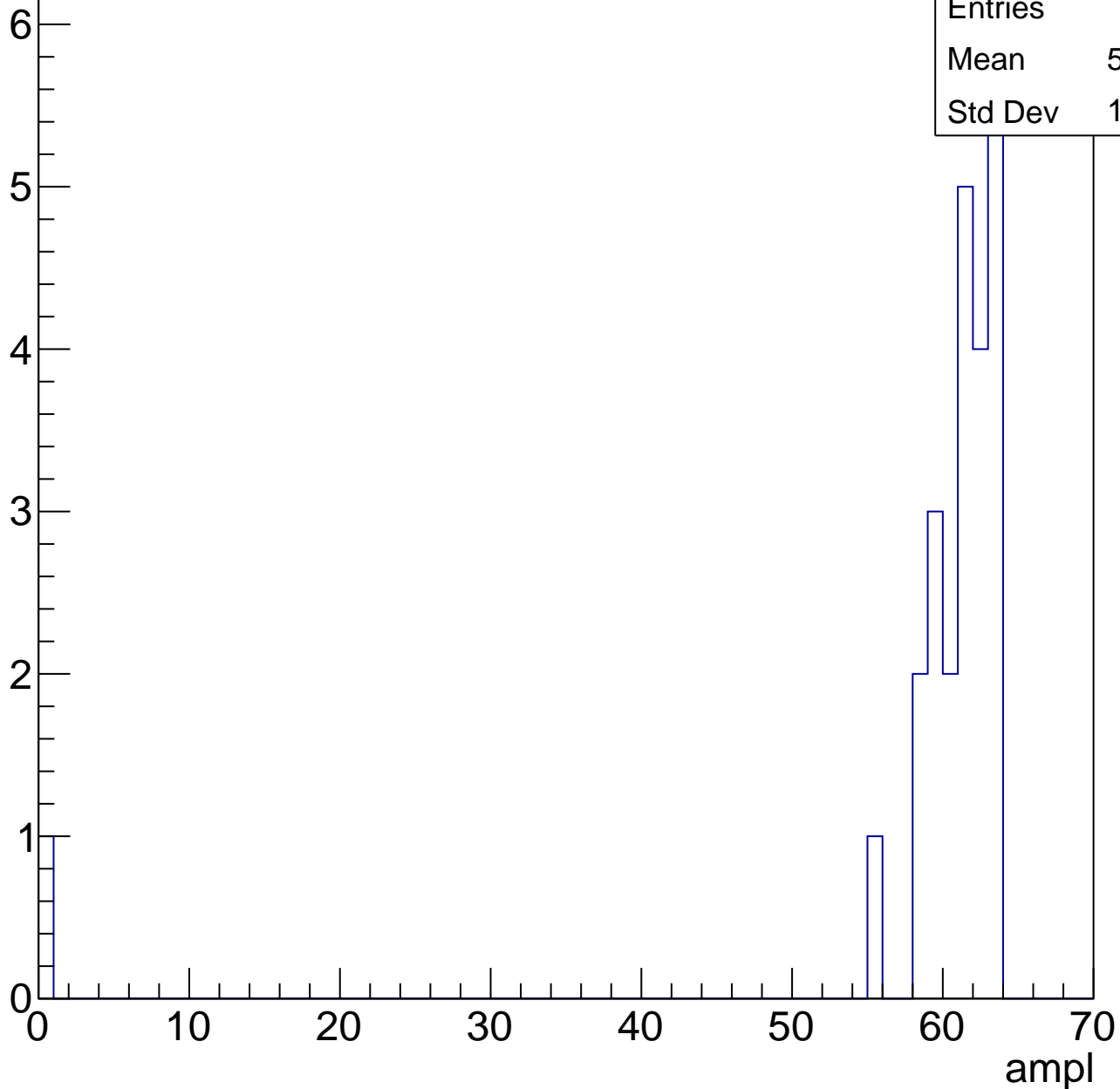


# B0L000S, U13-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	24
Mean	58.29
Std Dev	12.32



# B0L000S, U13-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

ampl



# B0L000S, U13-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch68, adc0

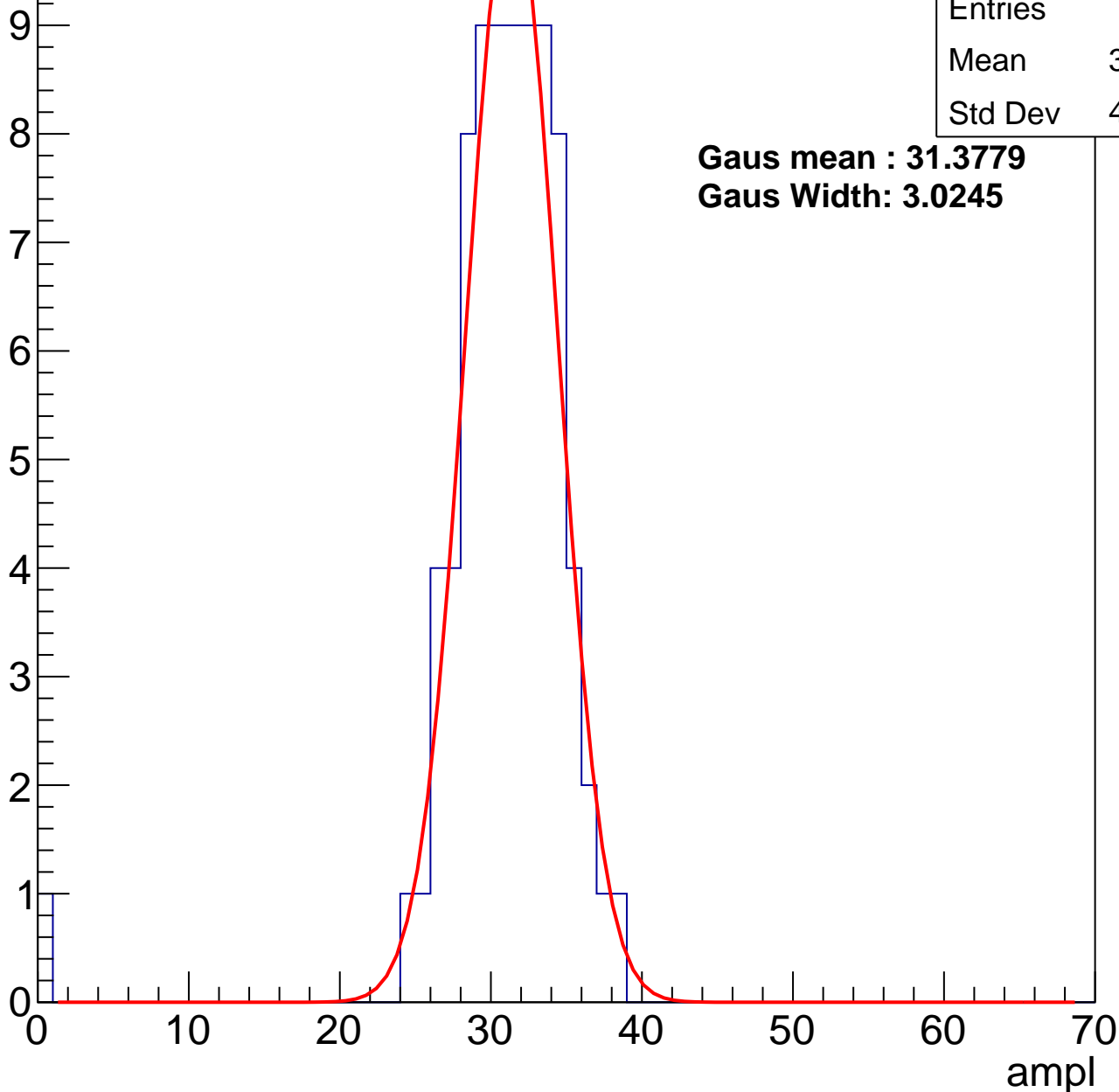
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	30.49
Std Dev	4.503

**Gaus mean : 31.3779**

**Gaus Width: 3.0245**



# B0L000S, U13-ch68, adc1

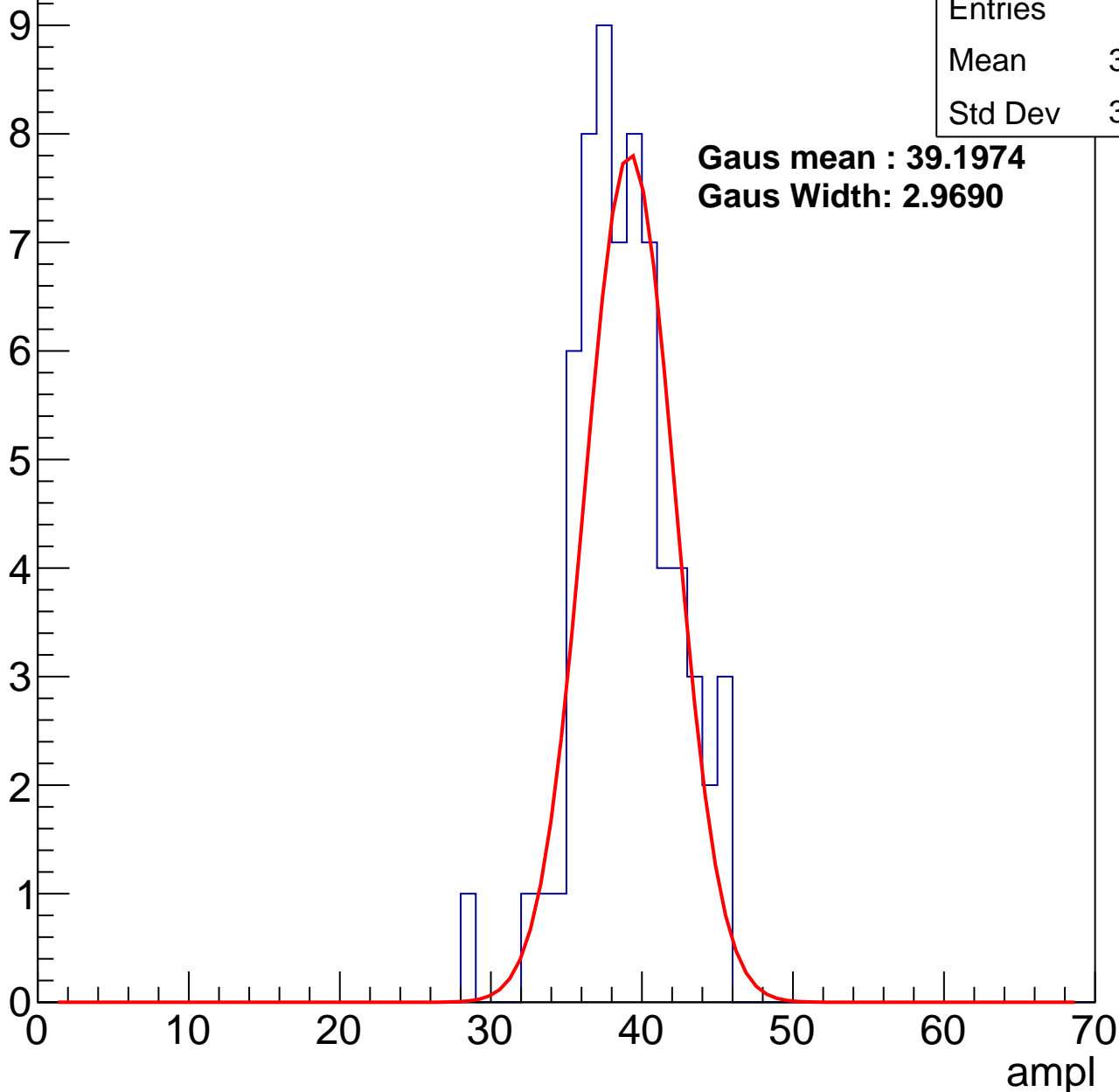
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	38.46
Std Dev	3.254

**Gaus mean : 39.1974**

**Gaus Width: 2.9690**

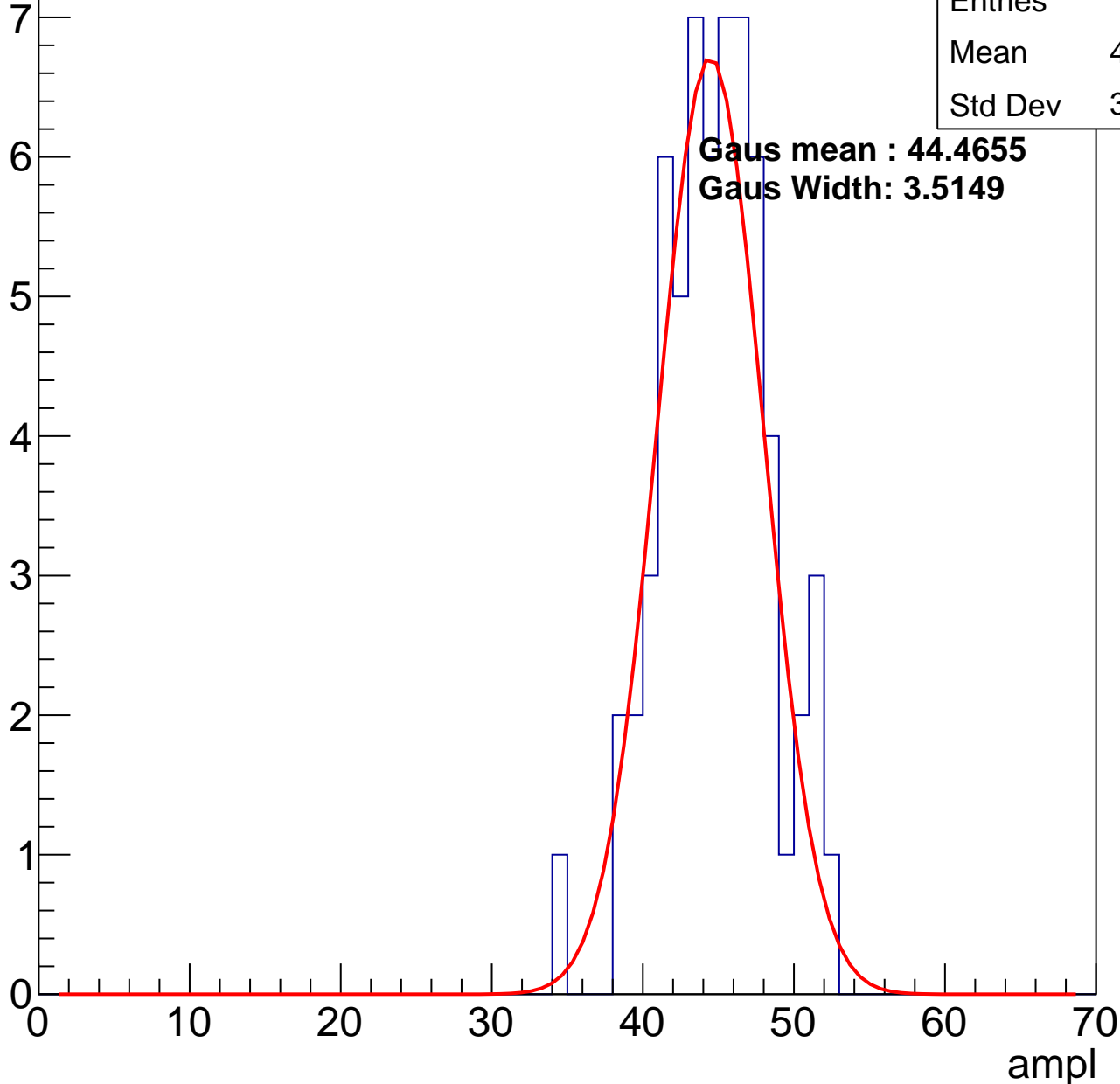


# B0L000S, U13-ch68, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.35
Std Dev	3.573

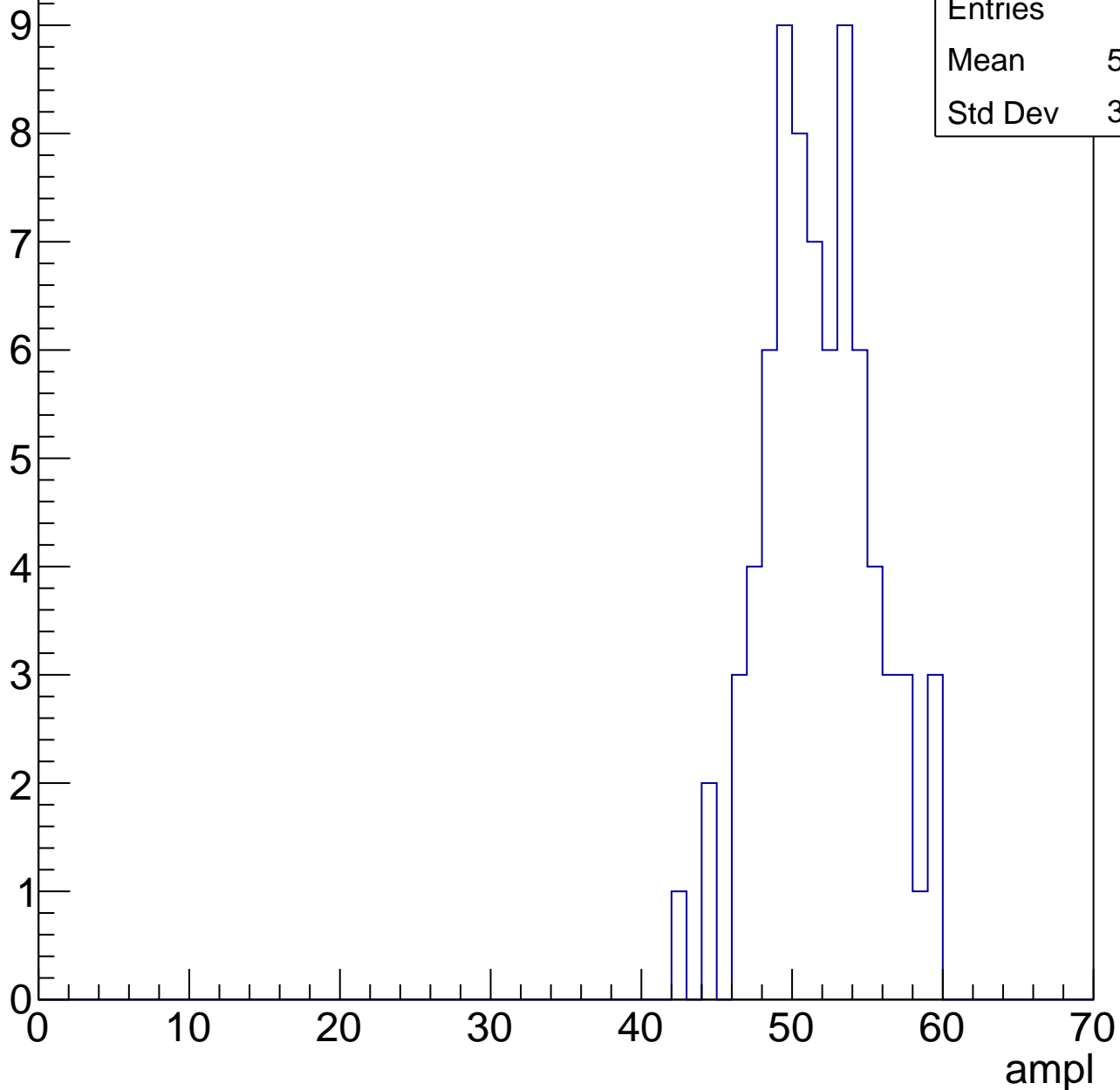


# B0L000S, U13-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	51.32
Std Dev	3.637

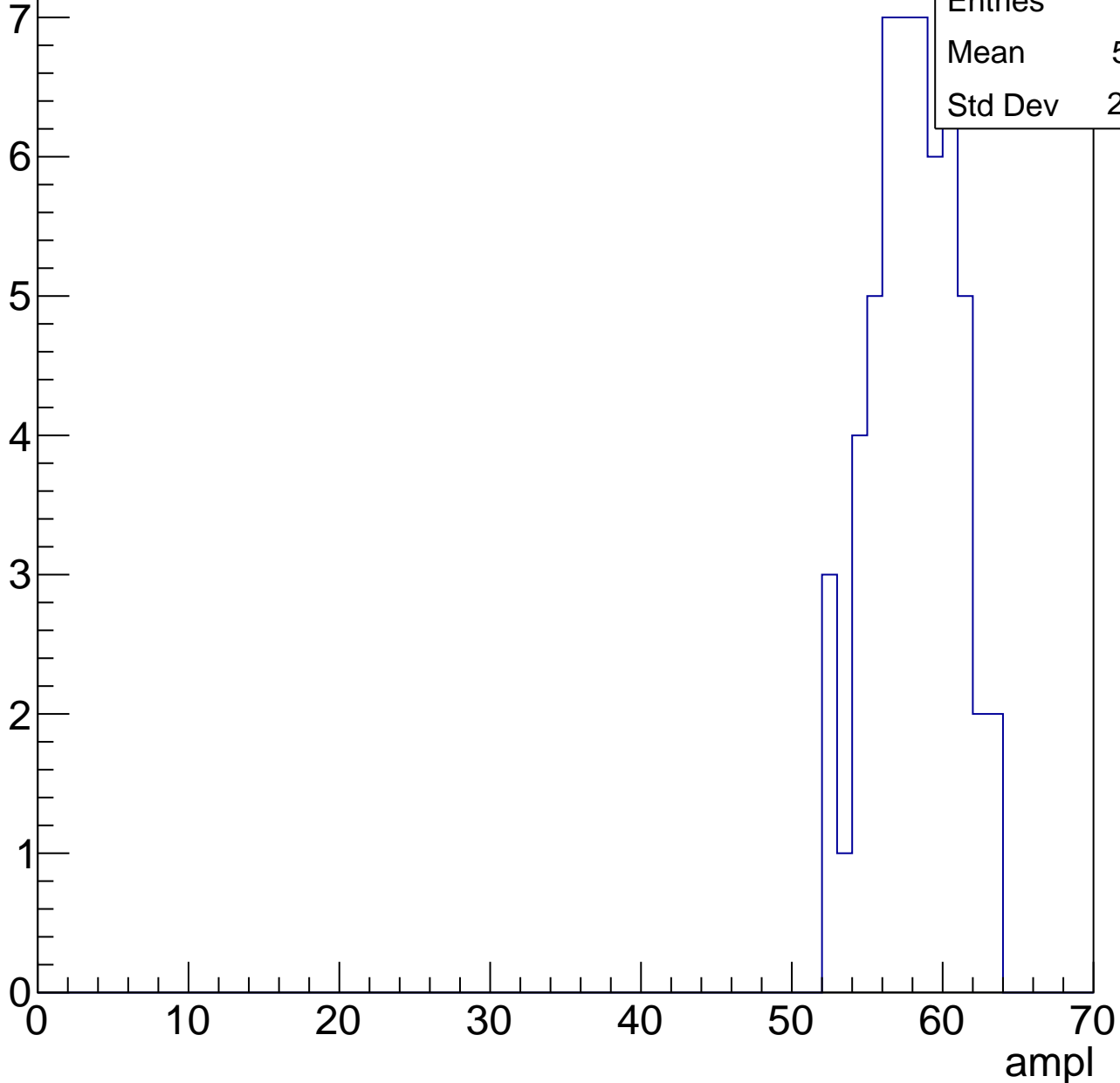


# B0L000S, U13-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	57.61
Std Dev	2.769

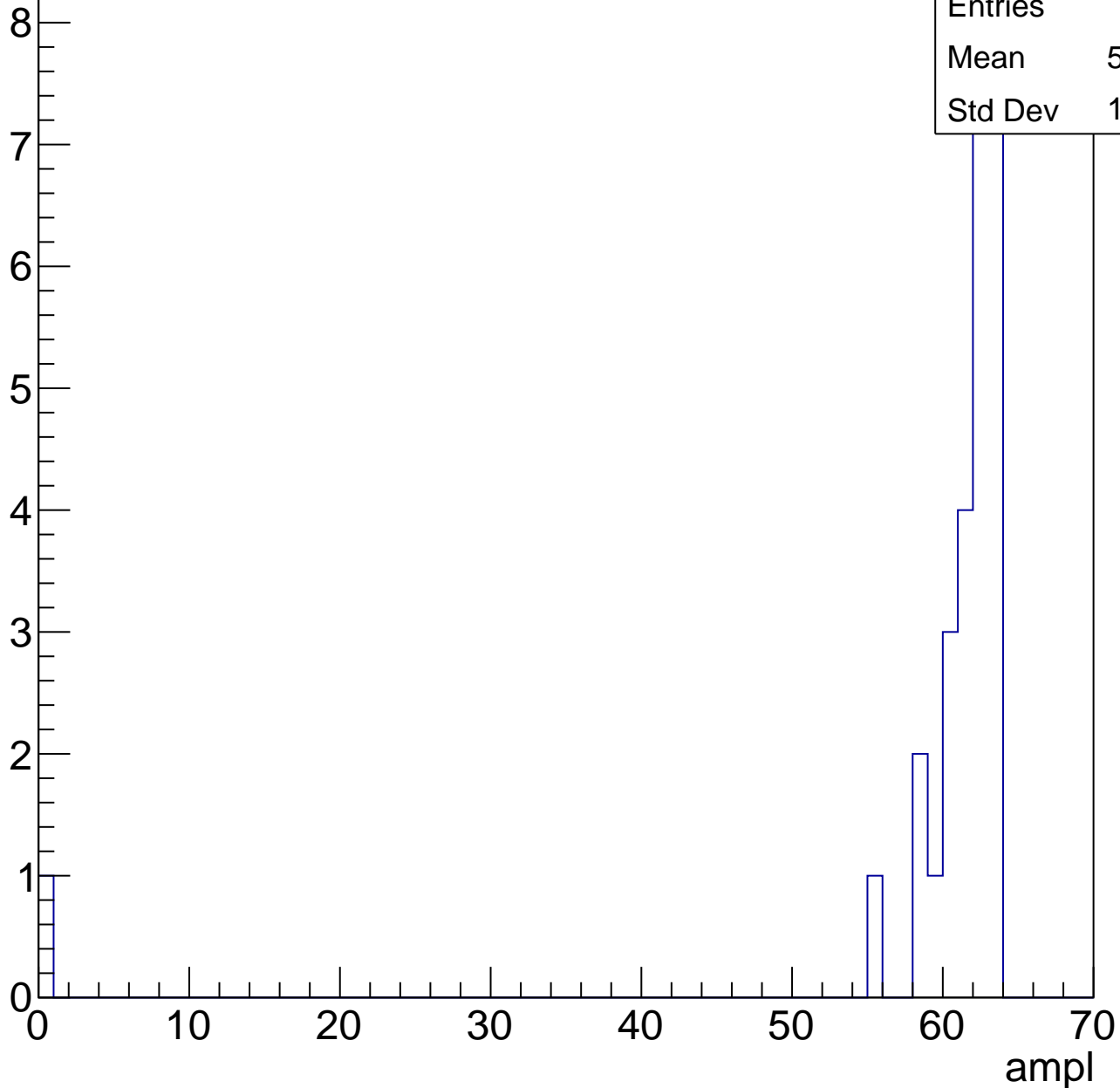


# B0L000S, U13-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	59.07
Std Dev	11.52



# B0L000S, U13-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	61
Std Dev	0



# B0L000S, U13-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U13-ch69, adc0

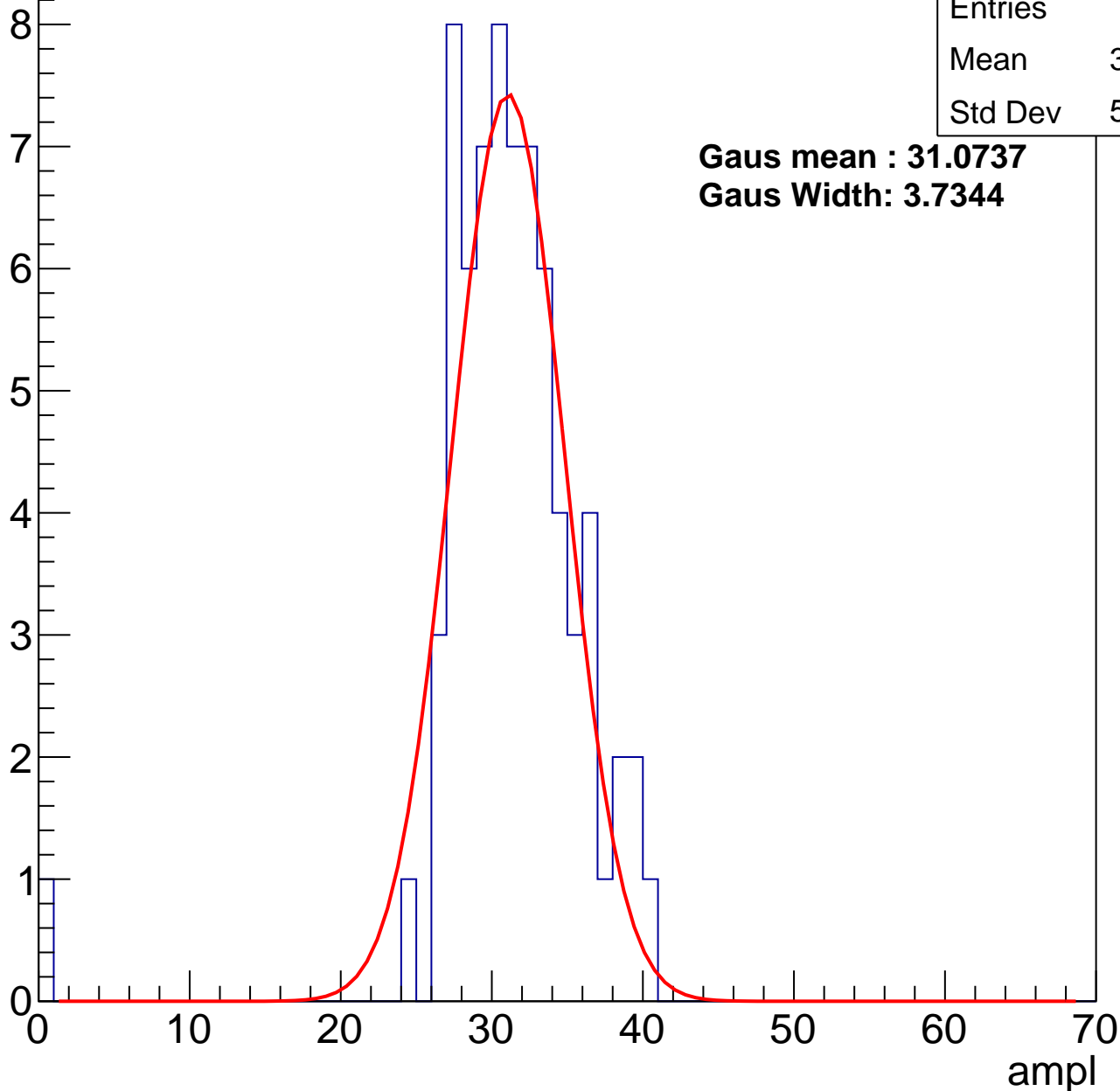
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.76
Std Dev	5.114

**Gaus mean : 31.0737**

**Gaus Width: 3.7344**



# B0L000S, U13-ch69, adc1

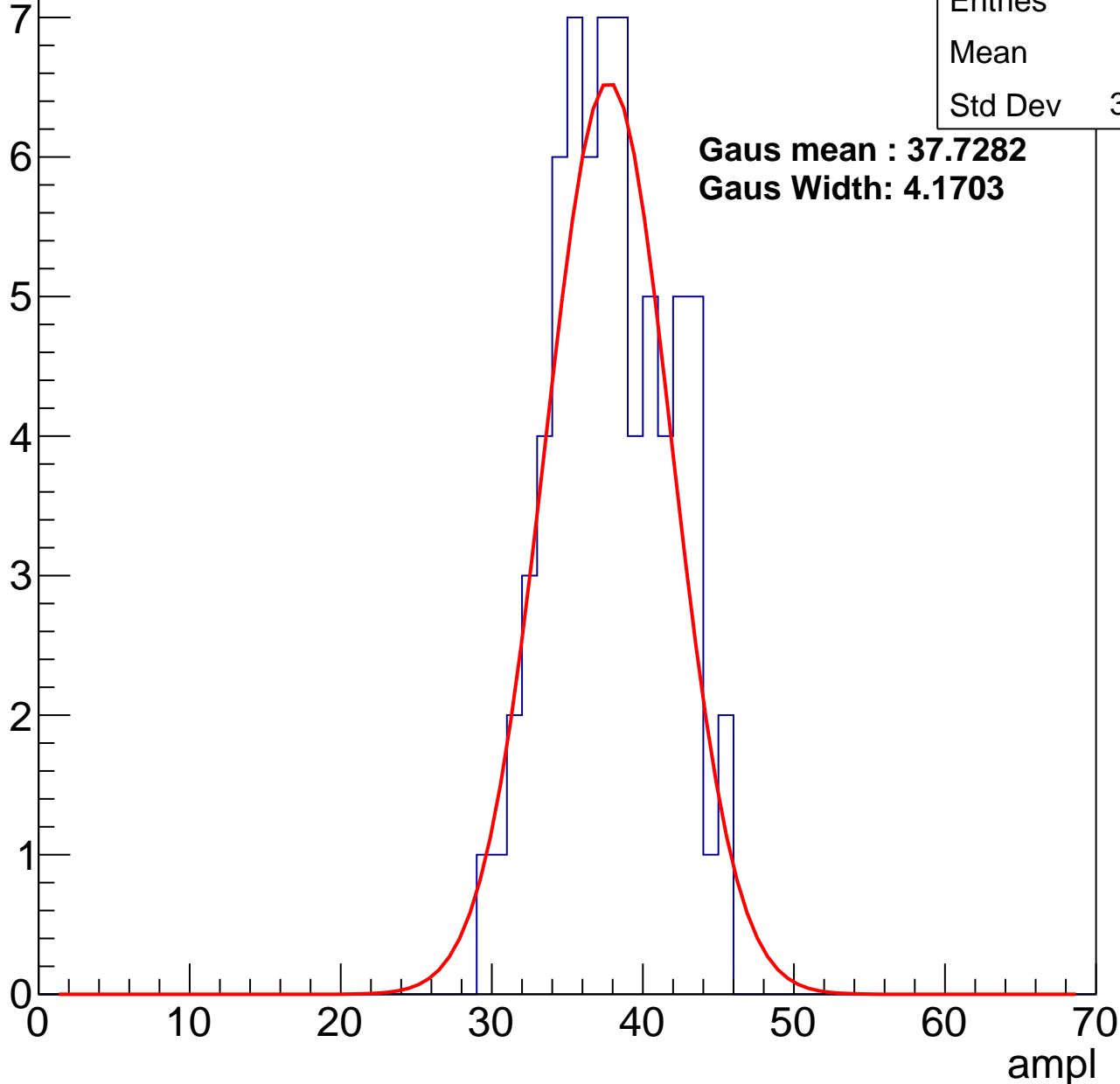
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.4
Std Dev	3.808

**Gaus mean : 37.7282**

**Gaus Width: 4.1703**



# B0L000S, U13-ch69, adc2

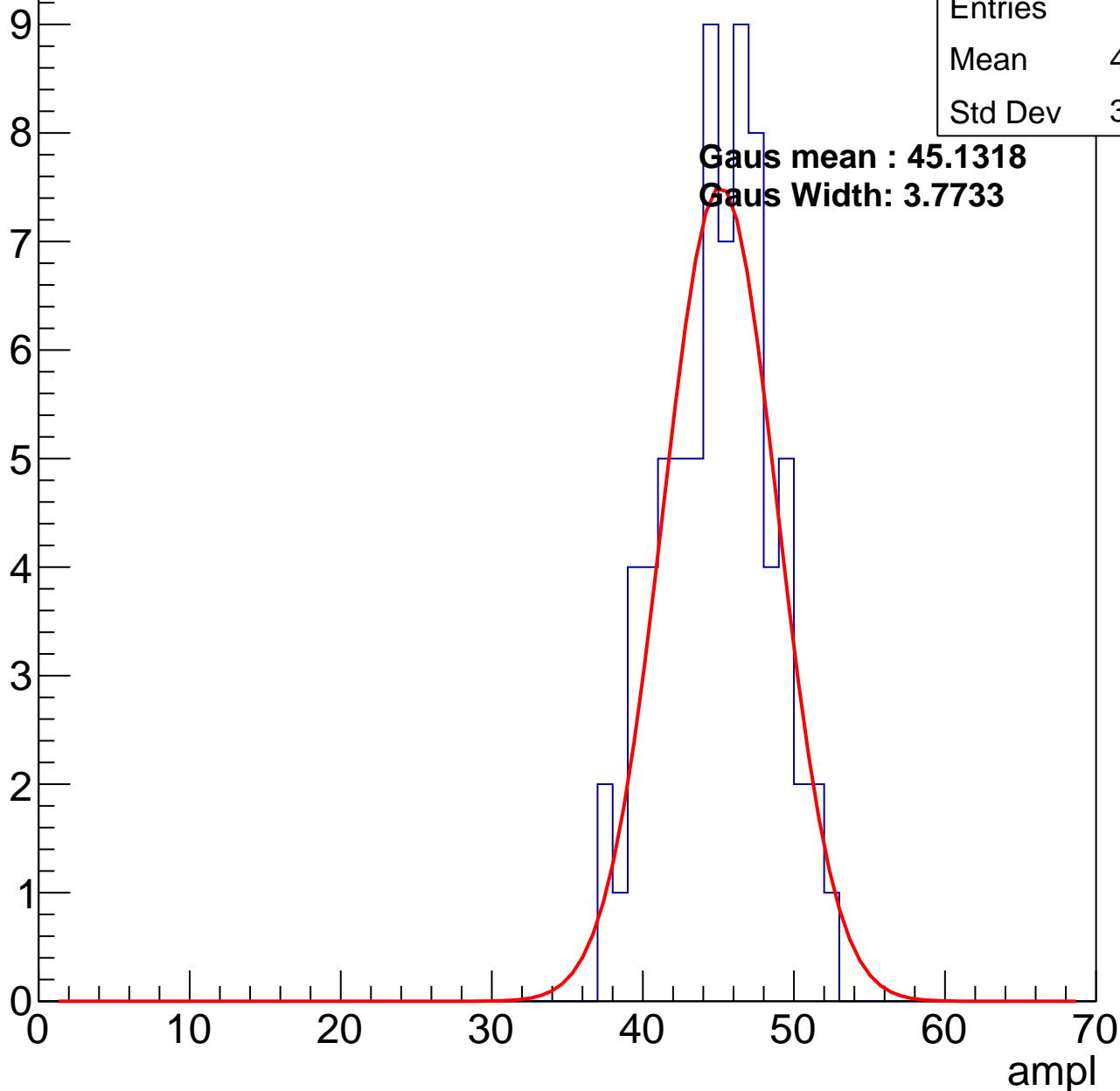
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	44.52
Std Dev	3.484

**Gaus mean : 45.1318**

**Gaus Width: 3.7733**

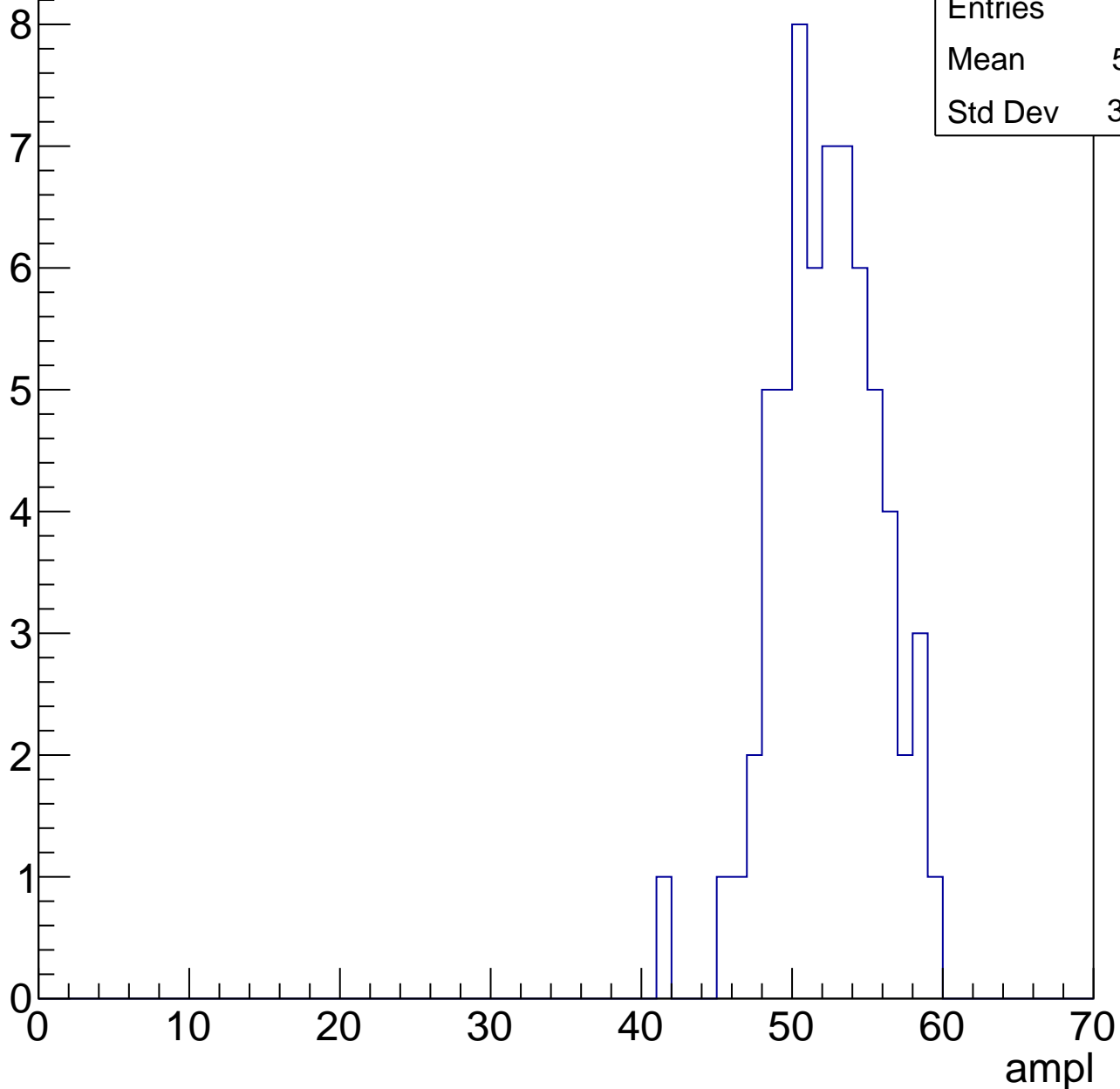


# B0L000S, U13-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.91
Std Dev	3.458

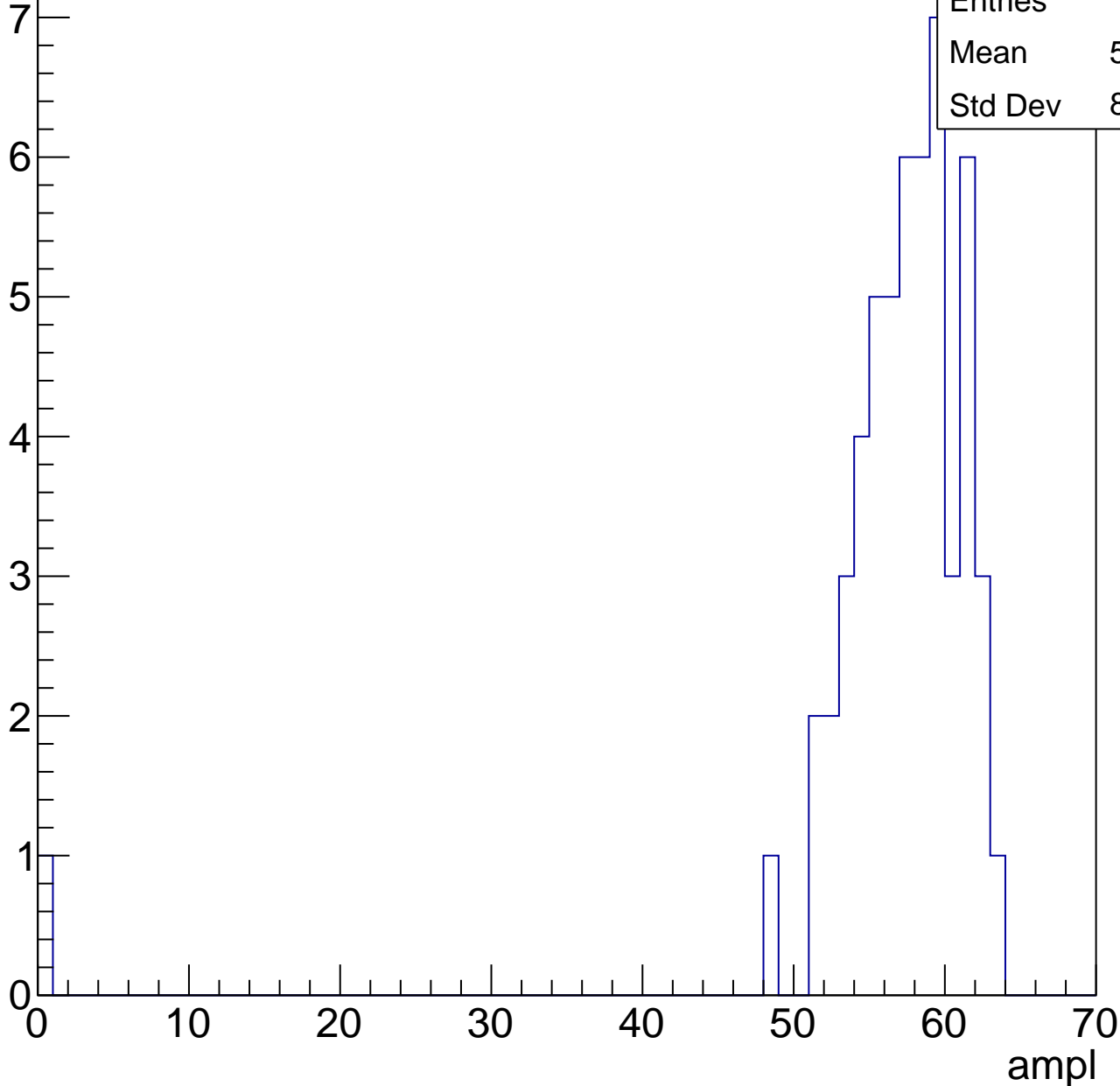


# B0L000S, U13-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.04
Std Dev	8.288



# B0L000S, U13-ch69, adc5

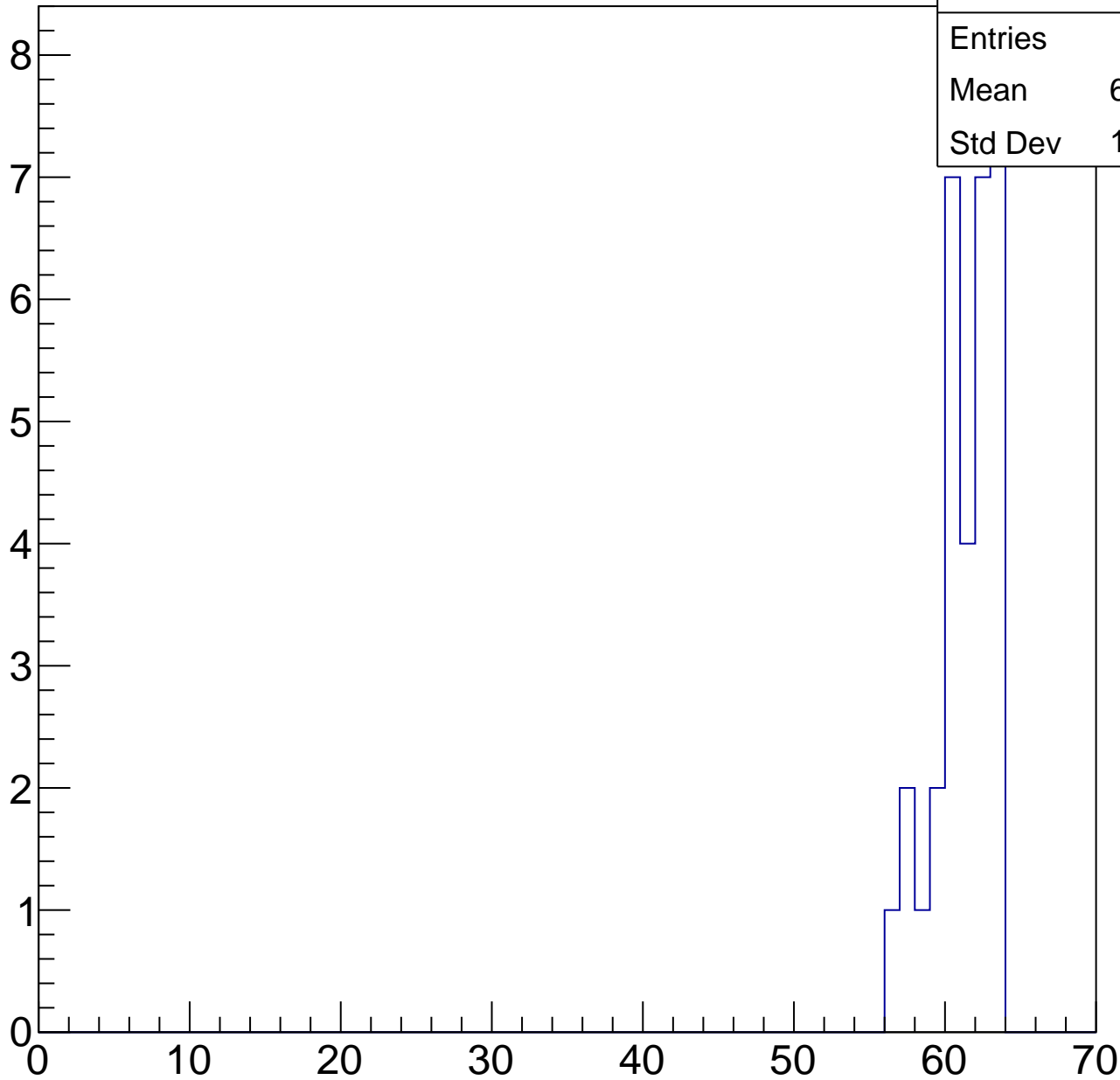
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	60.88
Std Dev	1.932

ampl



# B0L000S, U13-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



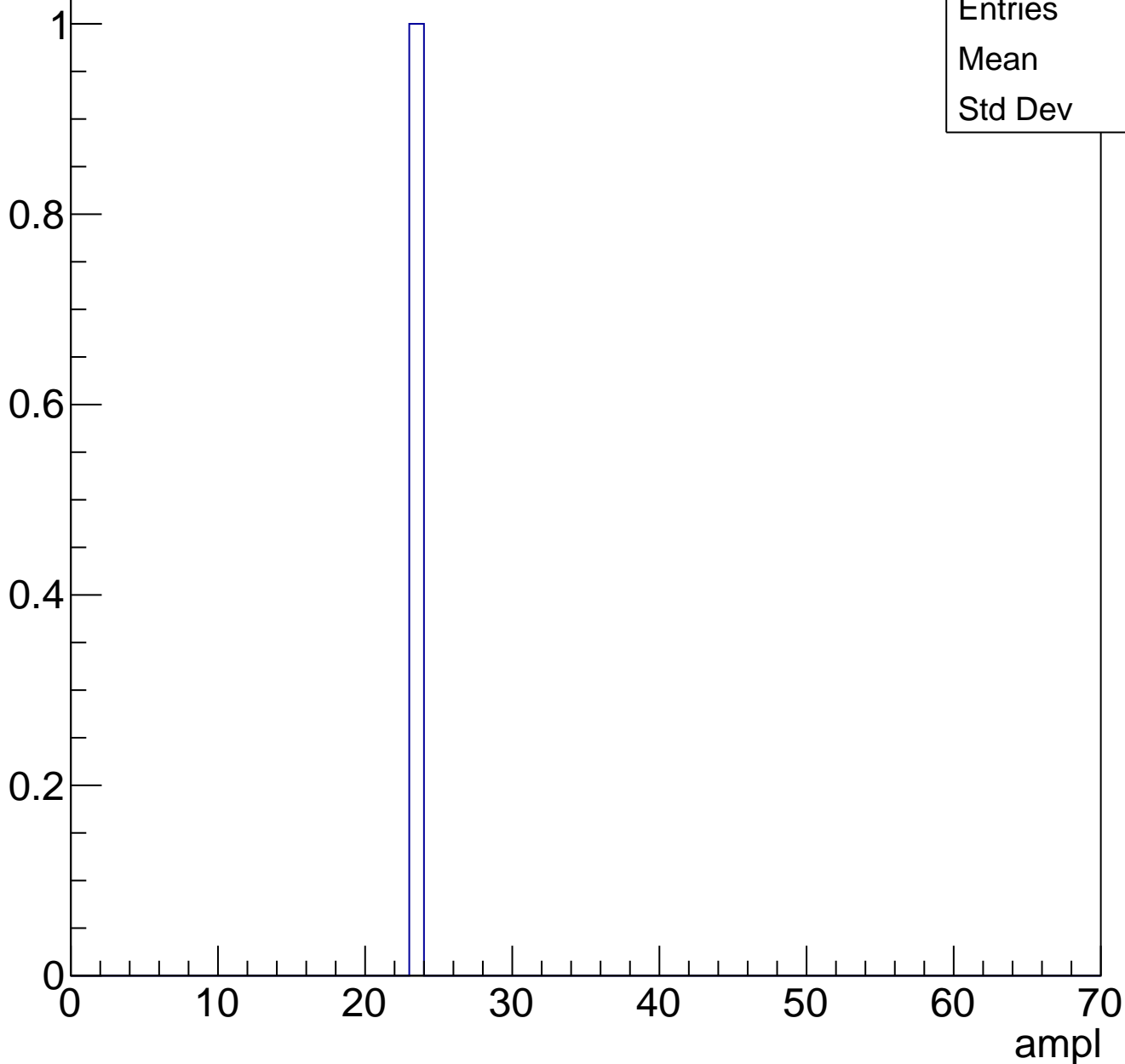
Entries	1
Mean	63
Std Dev	0



# B0L000S, U13-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch70, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	30.93
Std Dev	3.494

**Gaus mean : 31.3775**

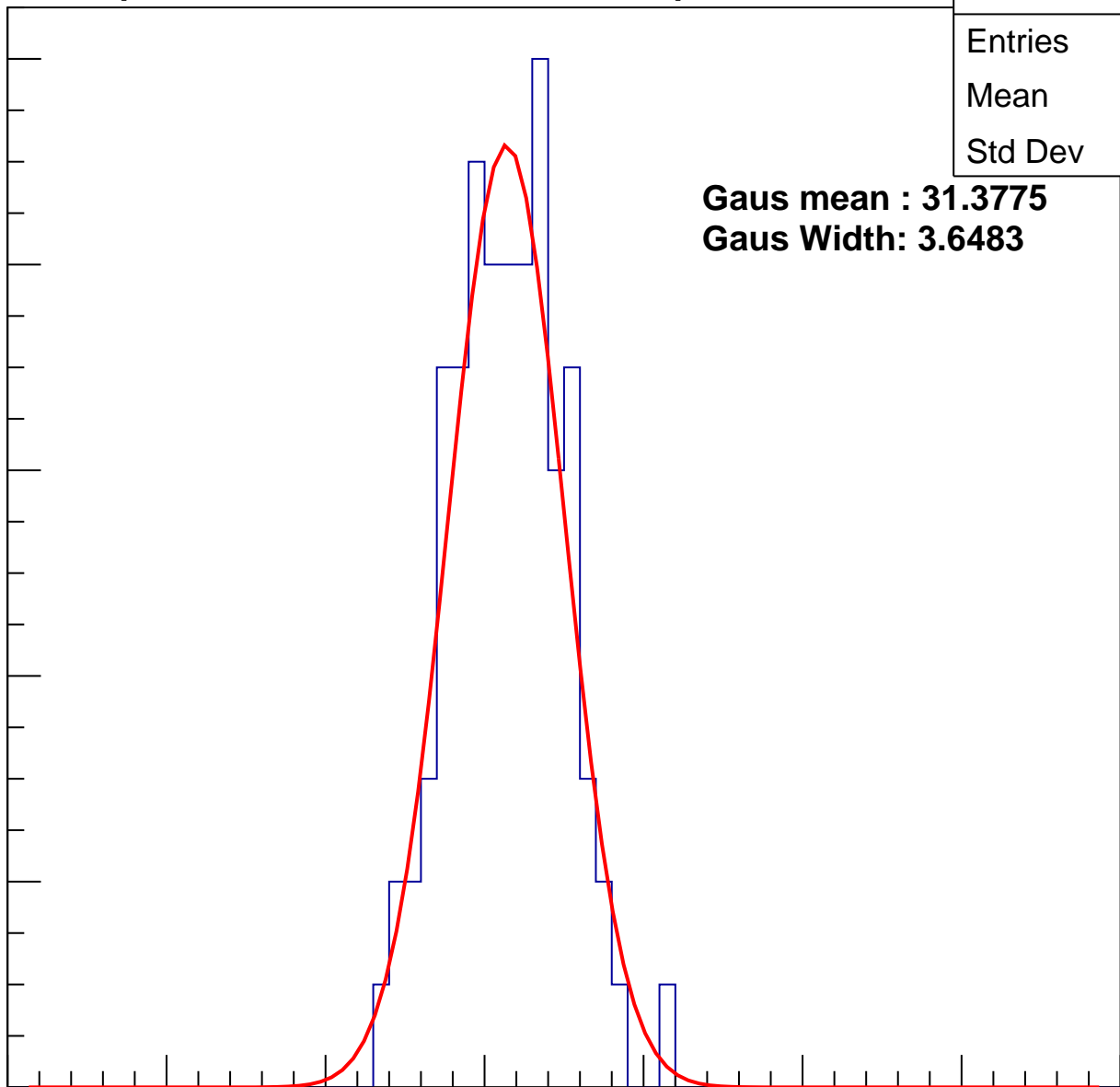
**Gaus Width: 3.6483**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch70, adc1

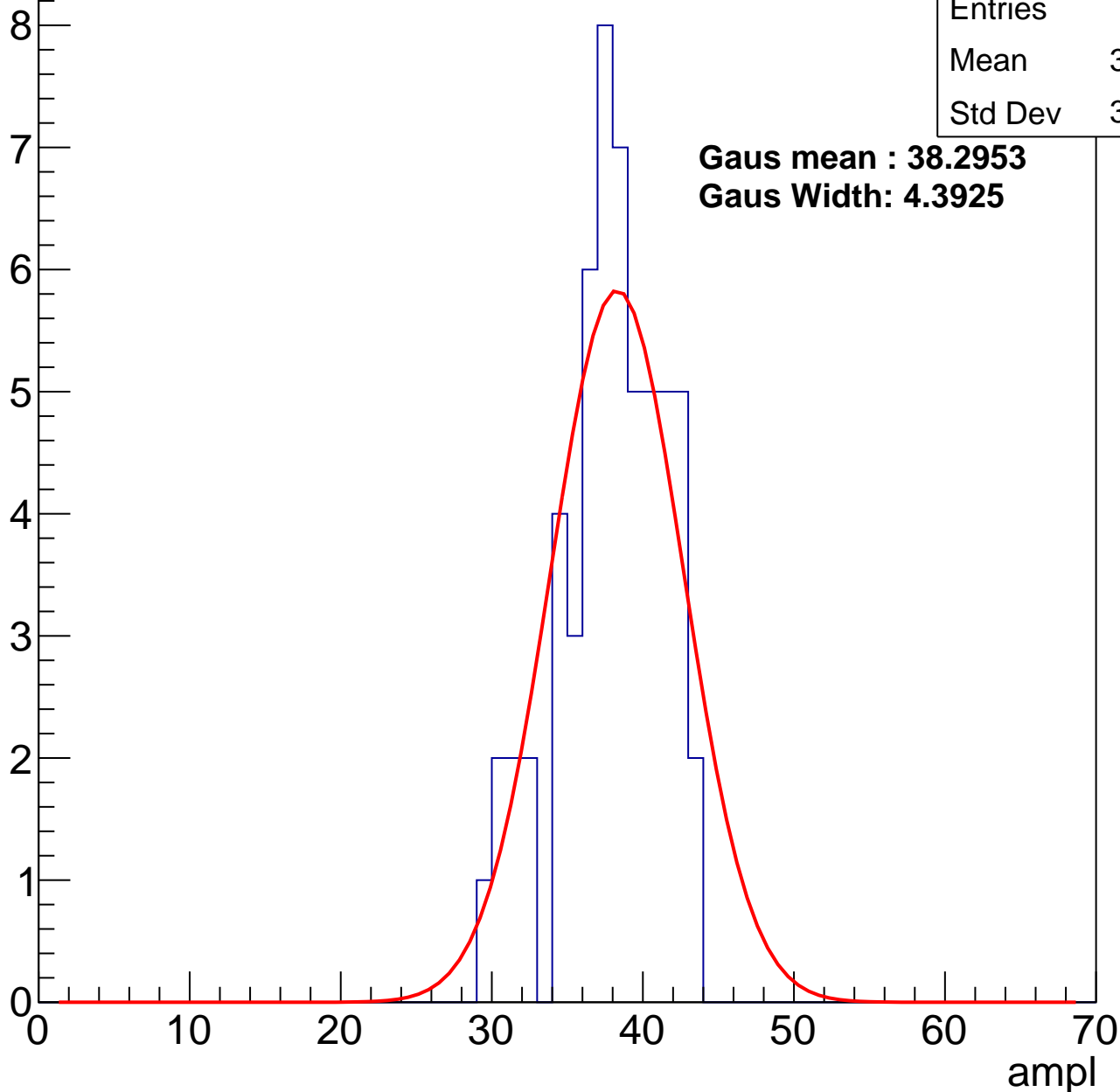
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	37.37
Std Dev	3.452

**Gaus mean : 38.2953**

**Gaus Width: 4.3925**



# B0L000S, U13-ch70, adc2

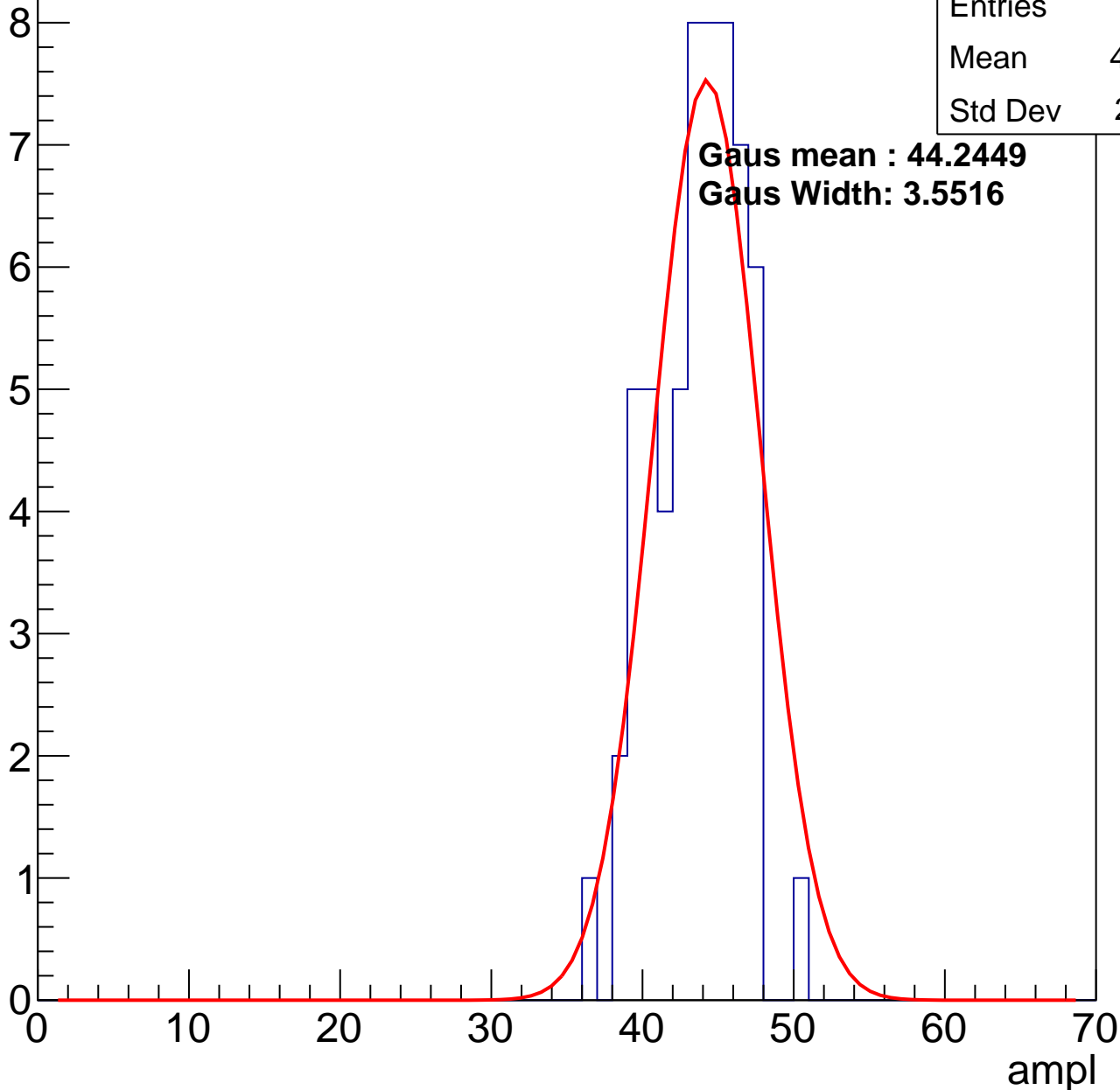
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.18
Std Dev	2.861

**Gaus mean : 44.2449**

**Gaus Width: 3.5516**



# B0L000S, U13-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	50.13
Std Dev	2.823

Entry

10

8

6

4

2

0

0

10

20

30

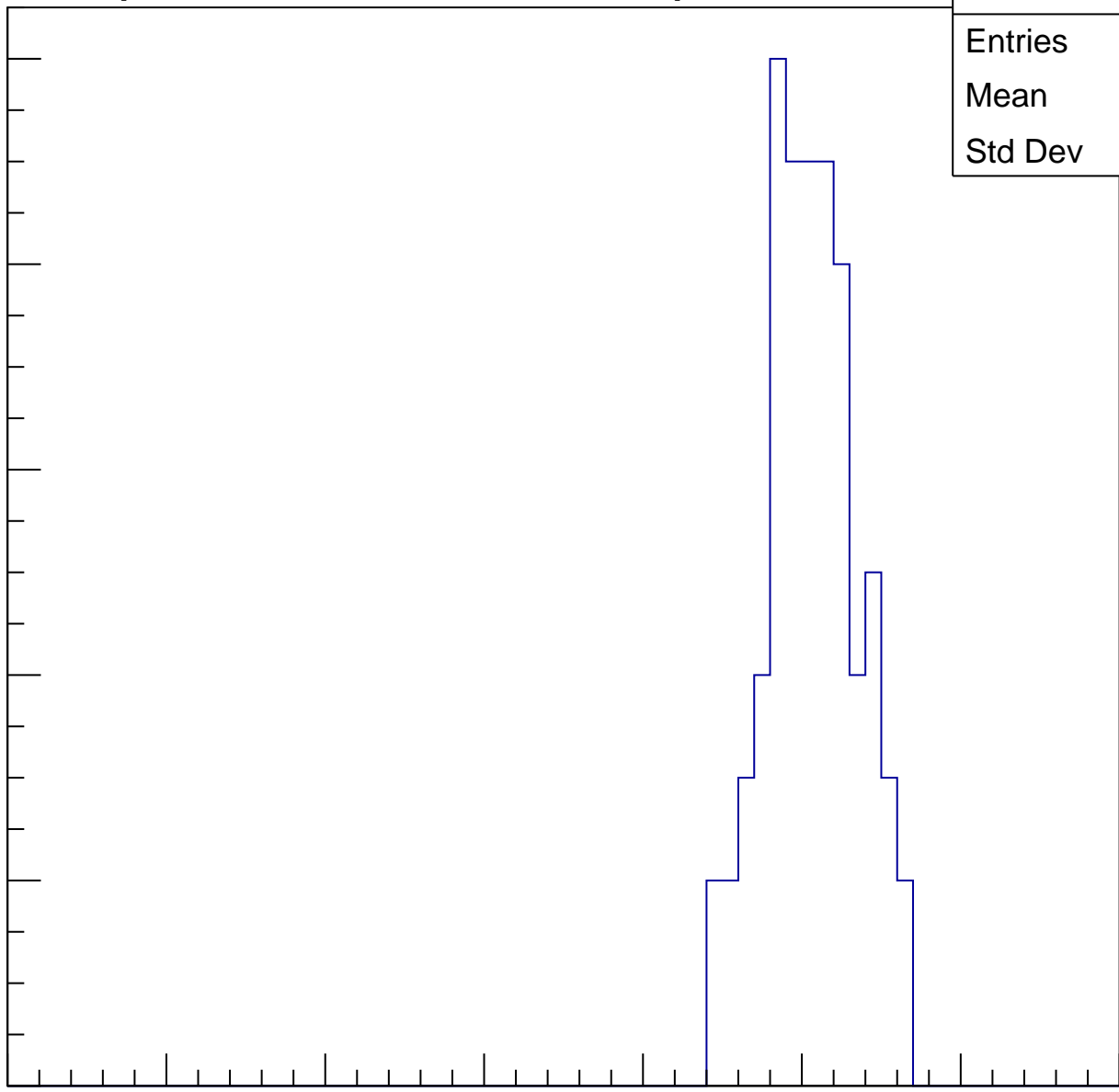
40

50

60

70

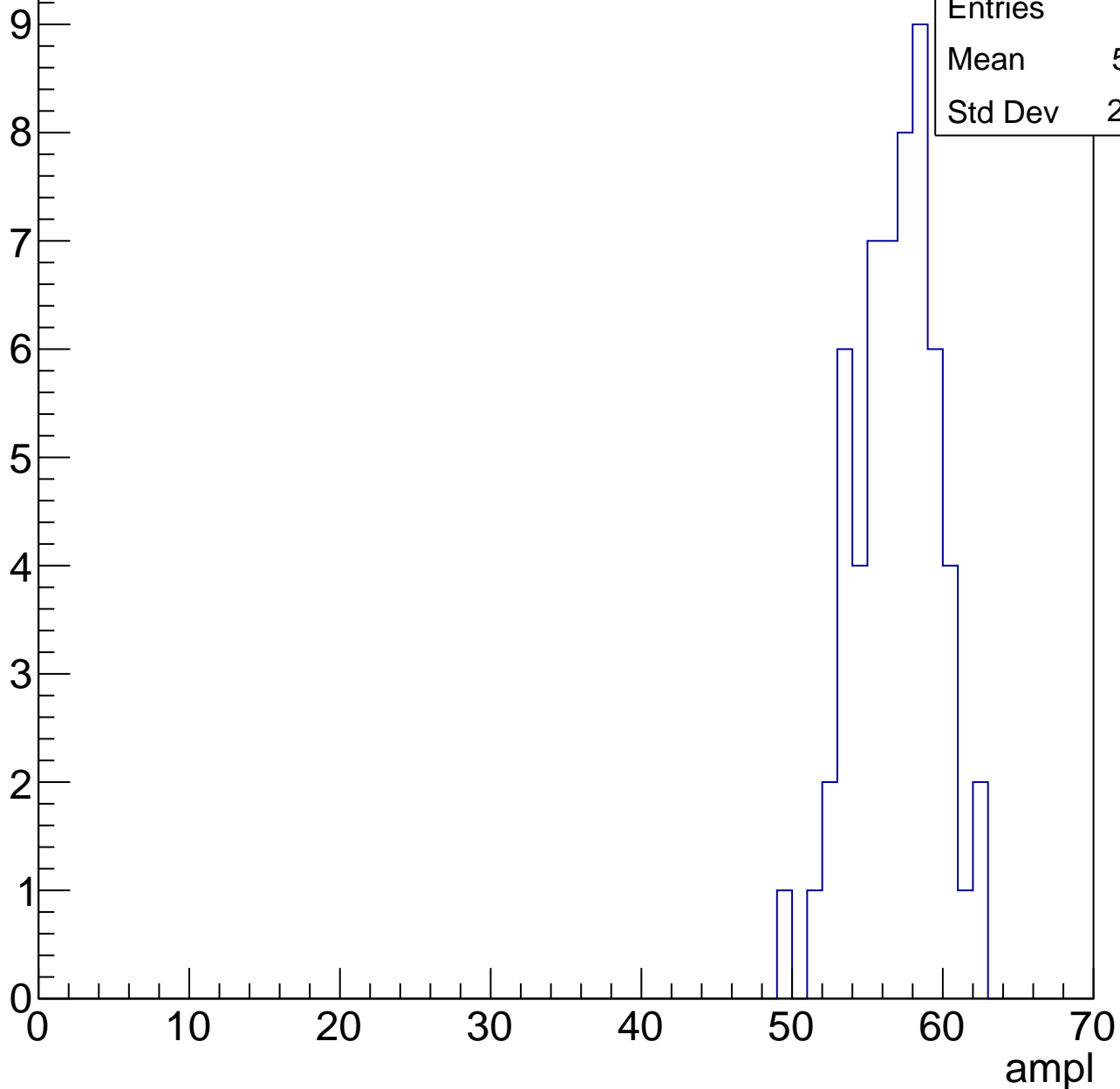
ampl



# B0L000S, U13-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

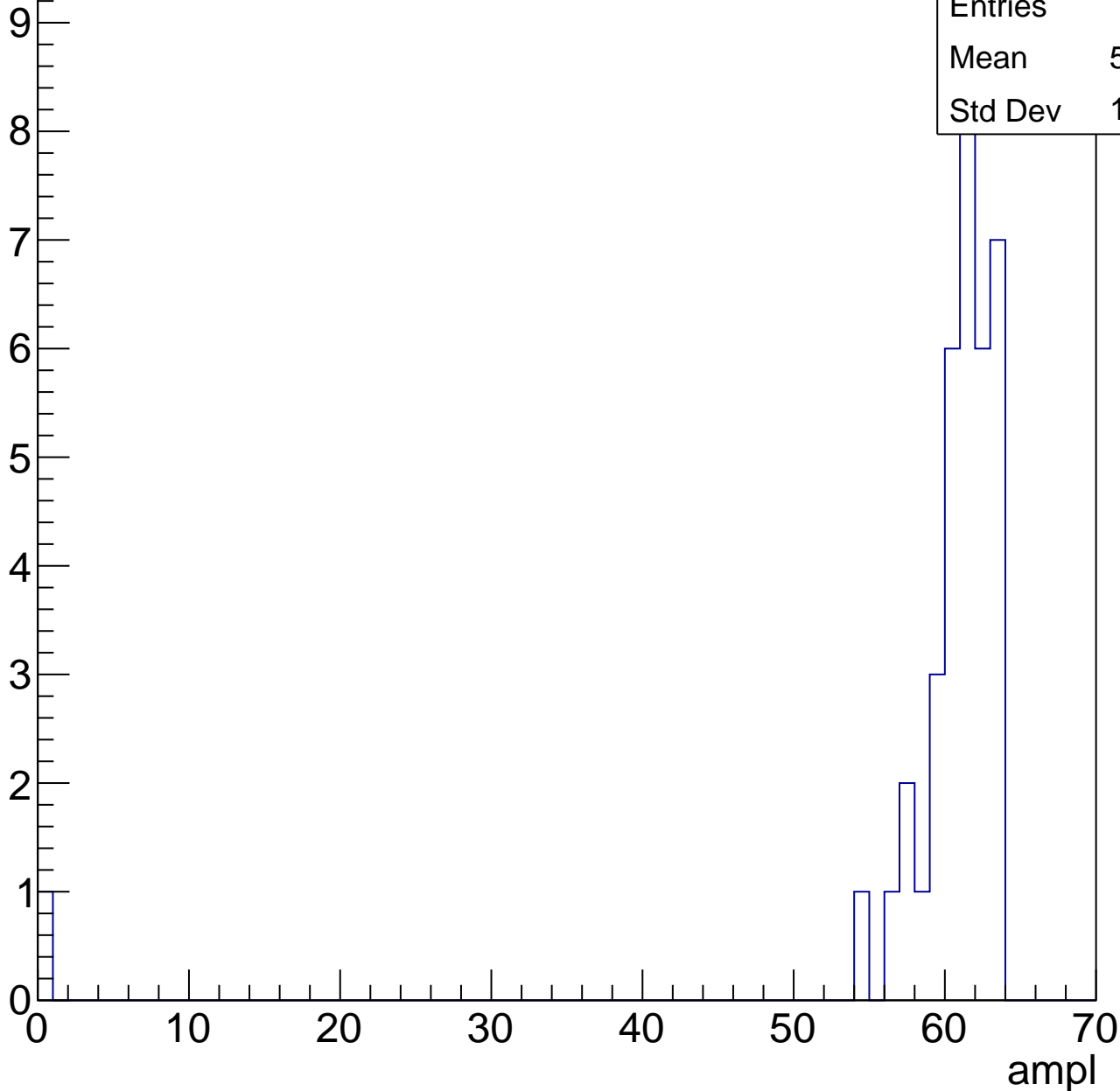


# B0L000S, U13-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.95
Std Dev	10.04



# B0L000S, U13-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch71, adc0

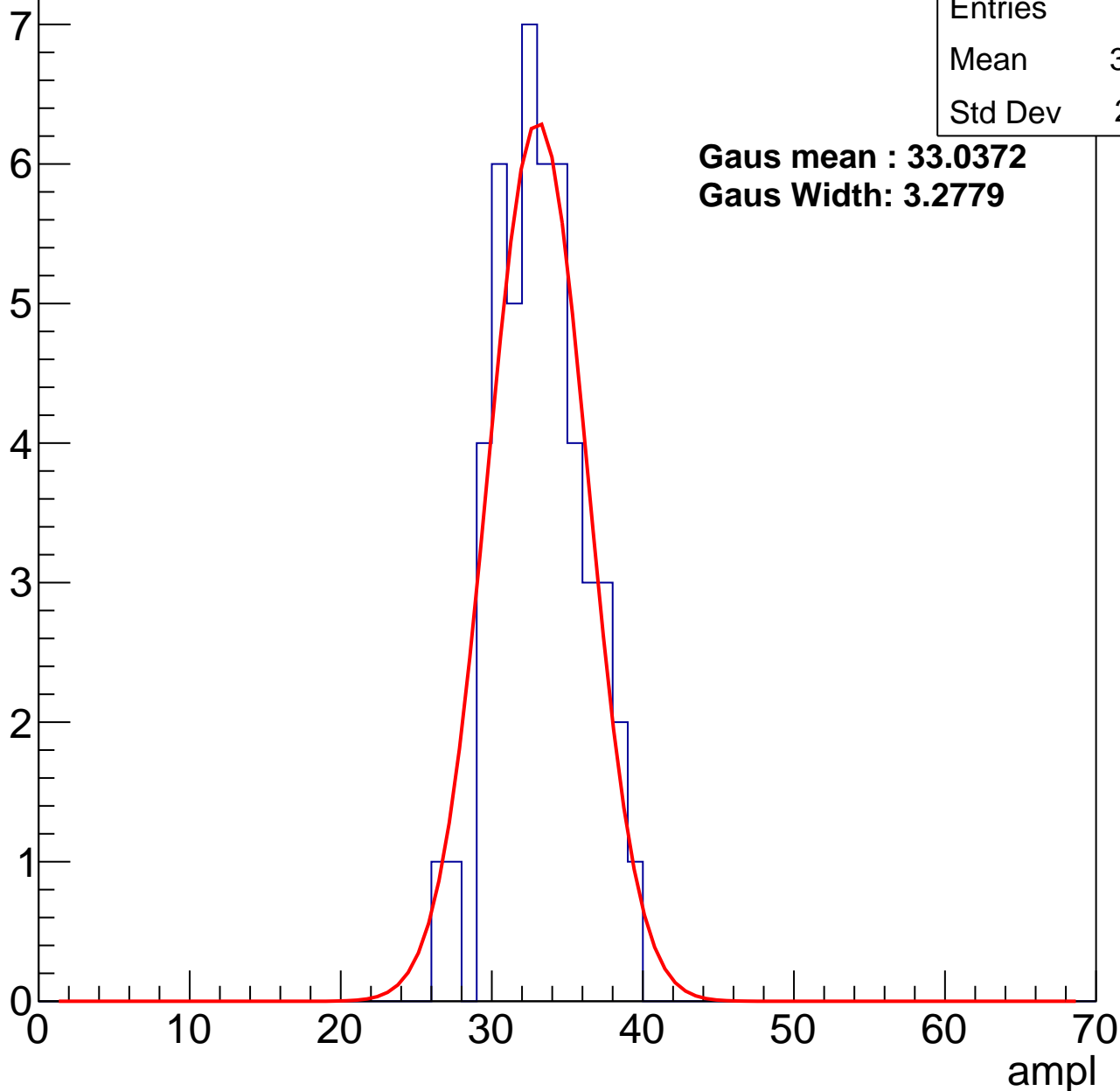
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	32.73
Std Dev	2.891

**Gaus mean : 33.0372**

**Gaus Width: 3.2779**



# B0L000S, U13-ch71, adc1

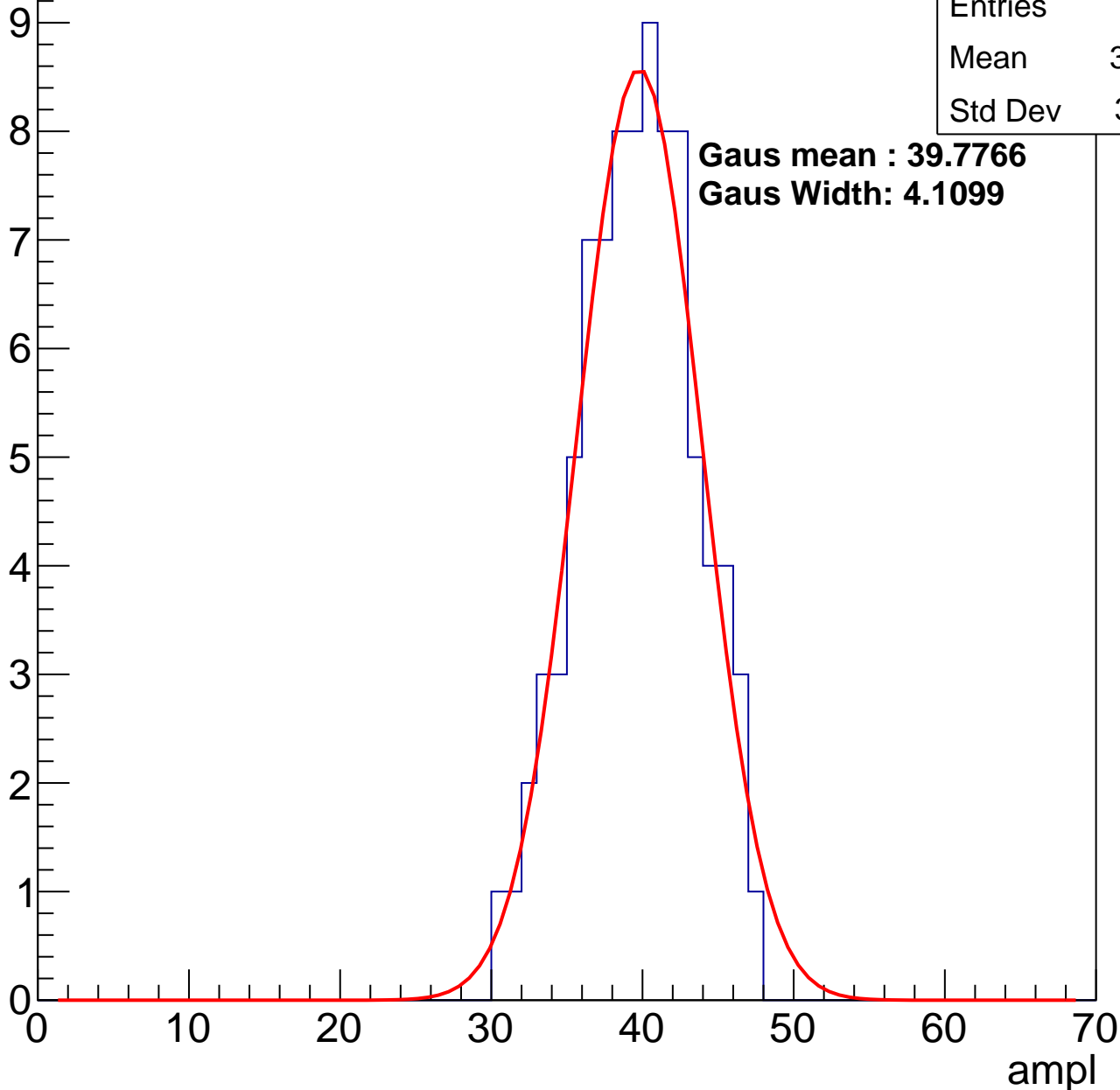
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	39.17
Std Dev	3.791

**Gaus mean : 39.7766**

**Gaus Width: 4.1099**



# B0L000S, U13-ch71, adc2

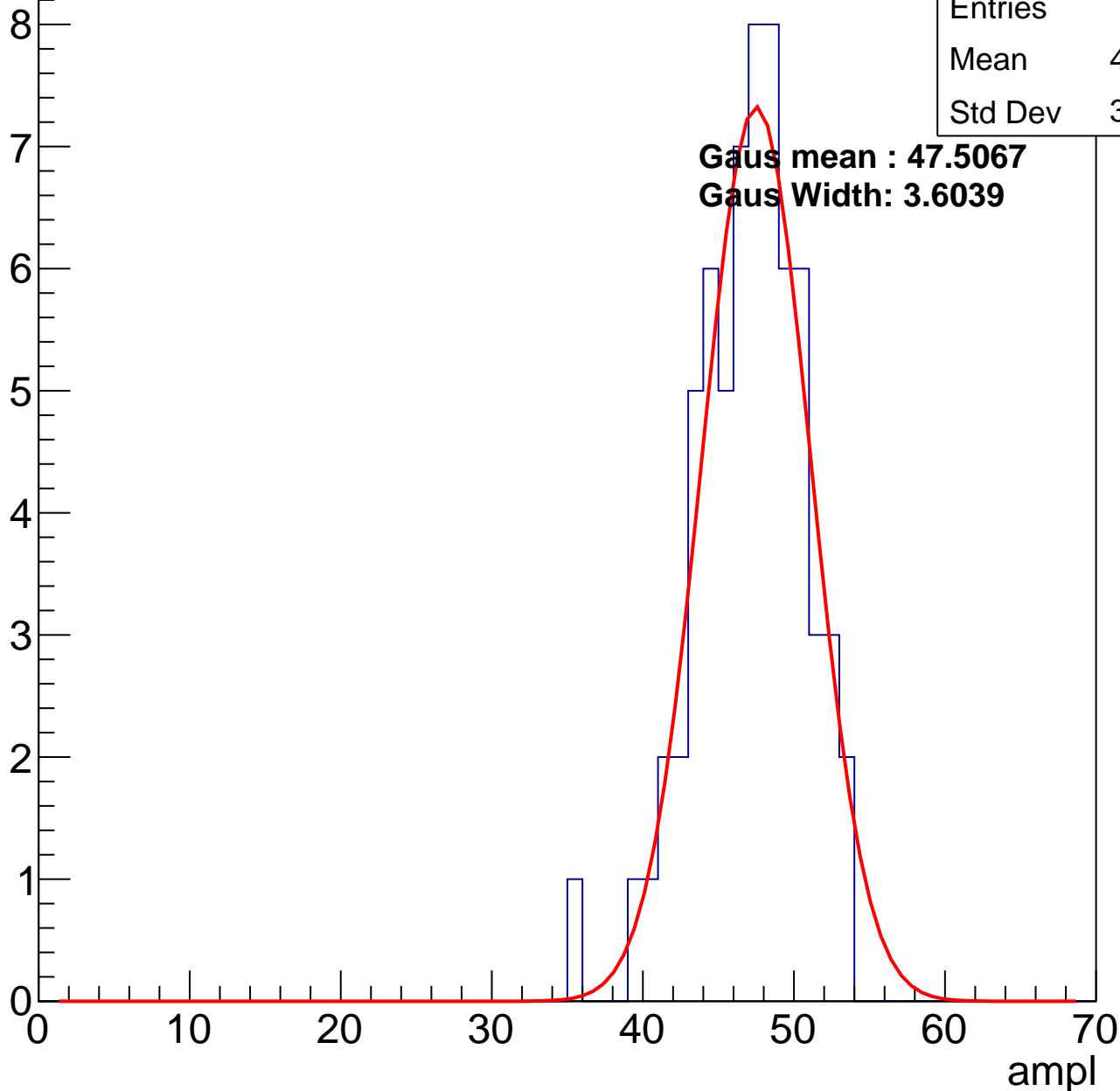
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	46.59
Std Dev	3.503

Gaus mean : 47.5067

Gaus Width: 3.6039

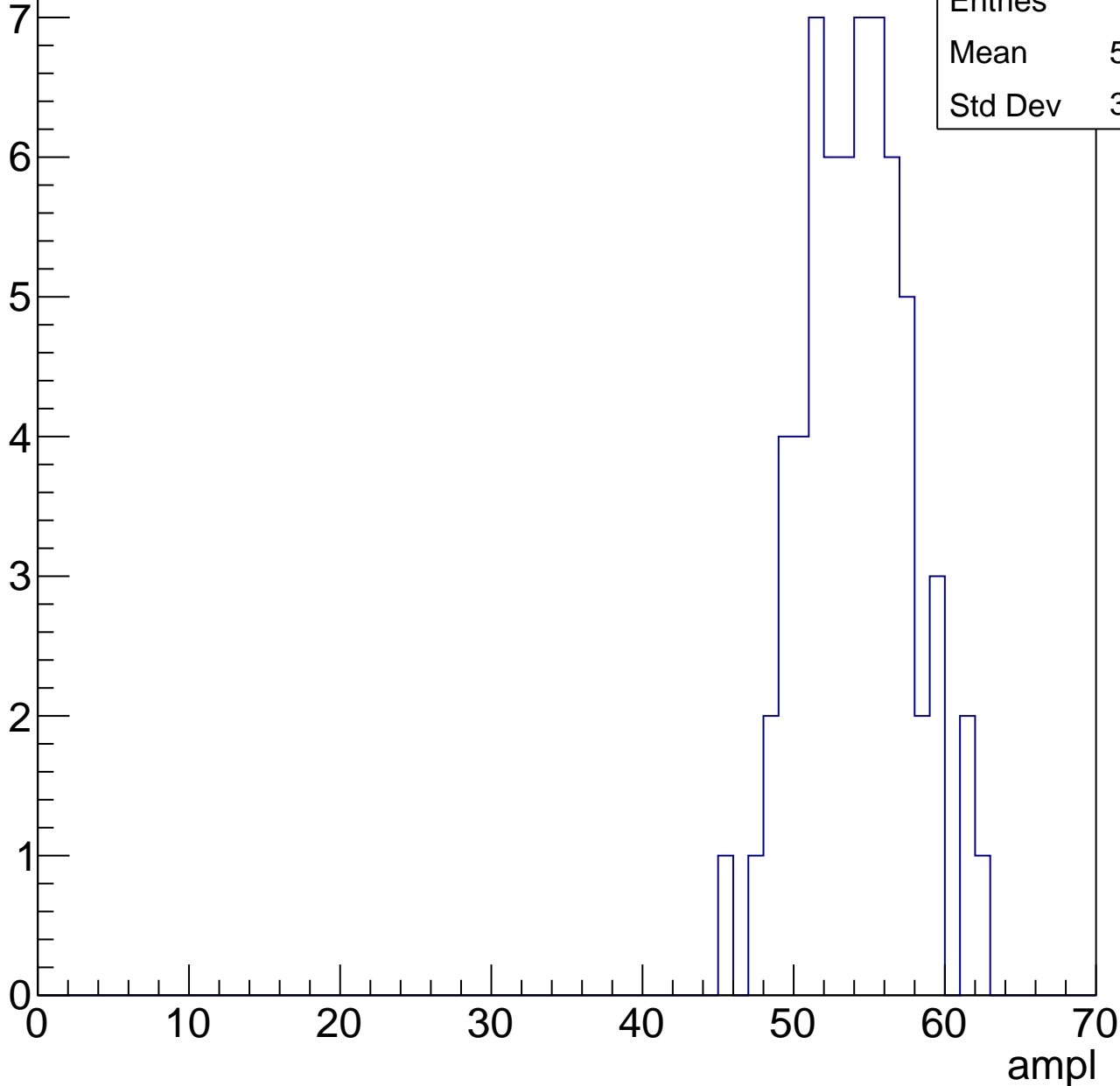


# B0L000S, U13-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	53.62
Std Dev	3.524

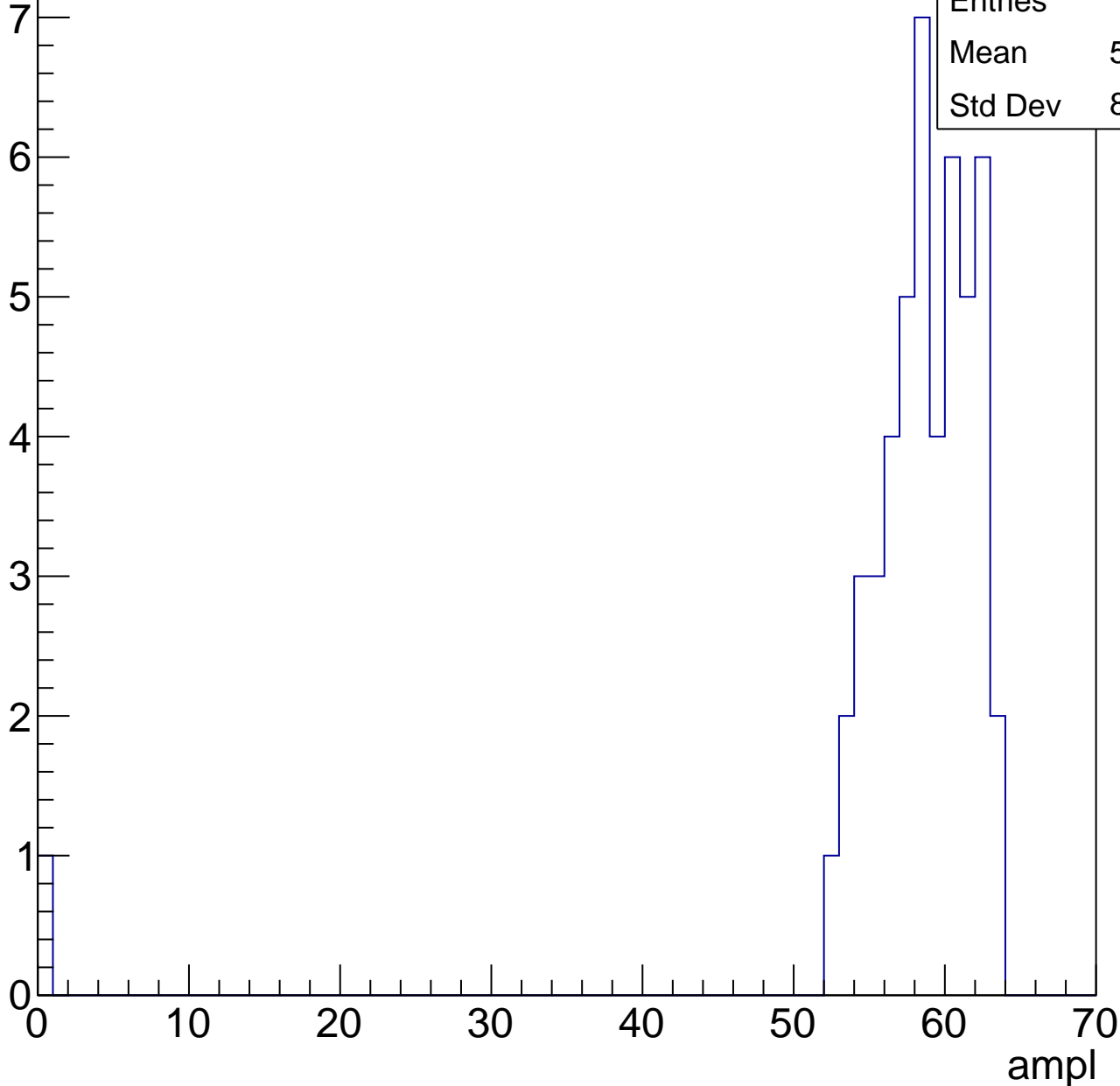


# B0L000S, U13-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	57.12
Std Dev	8.722

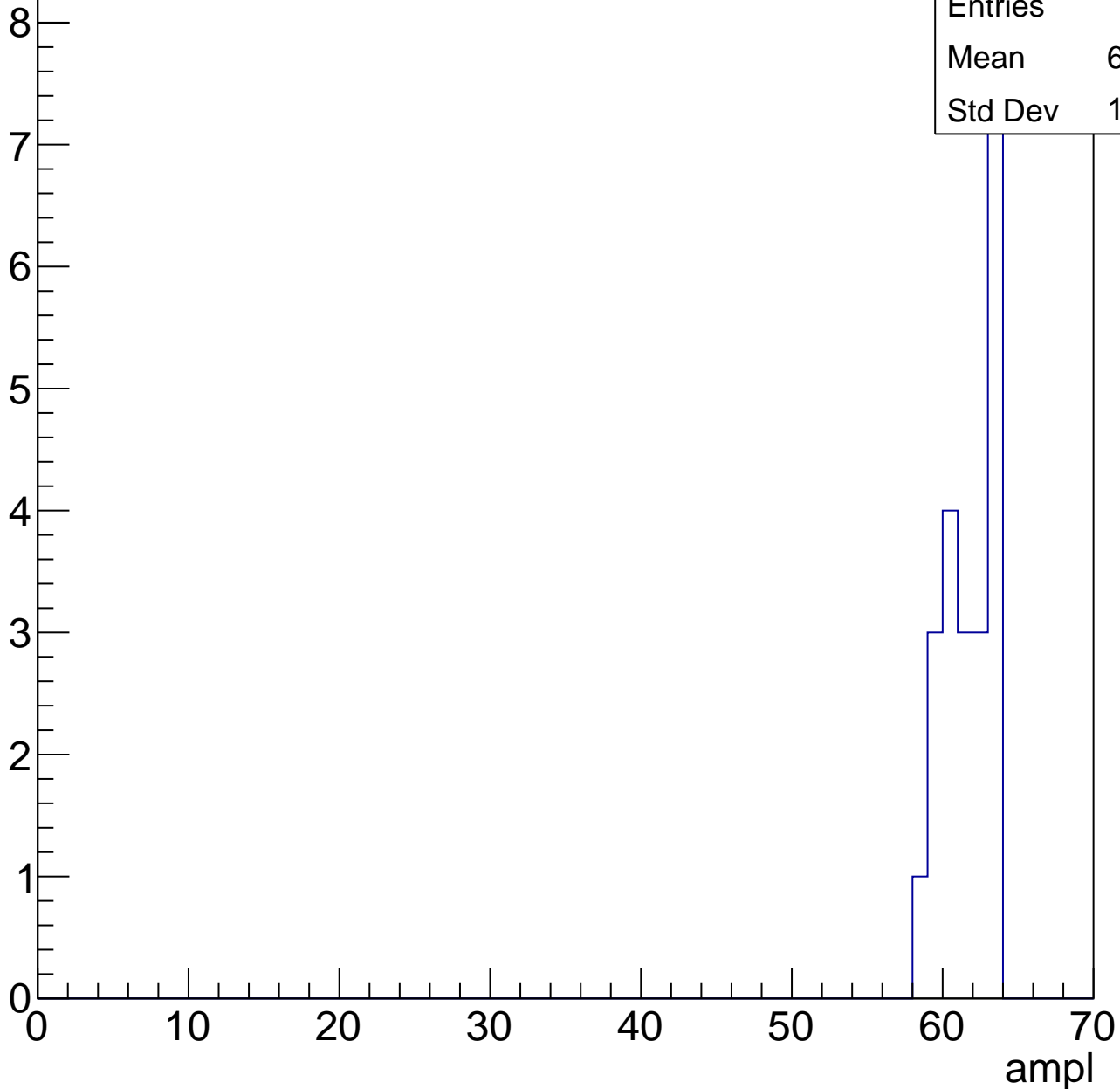


# B0L000S, U13-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	22
Mean	61.27
Std Dev	1.629



# B0L000S, U13-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



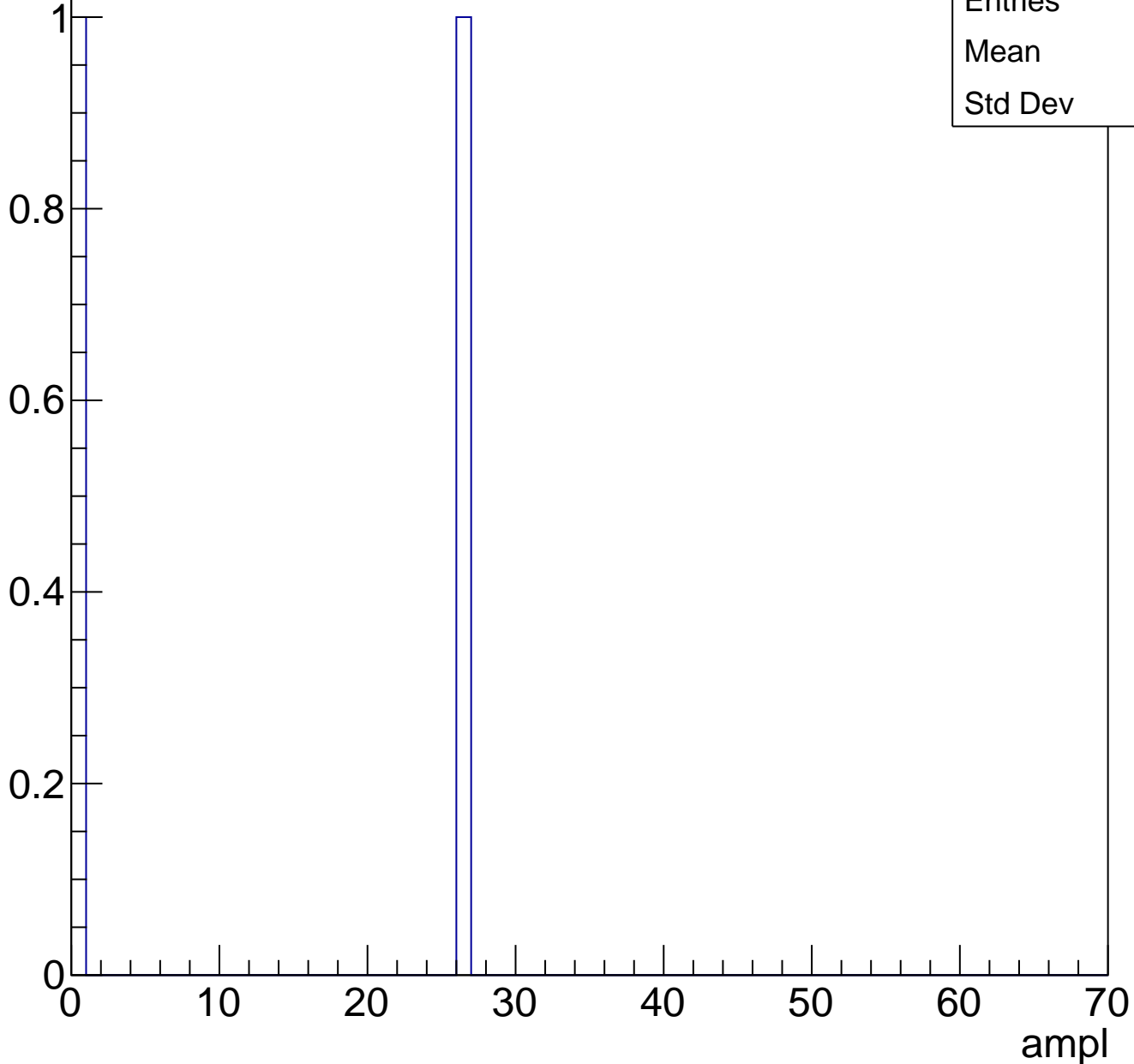
Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch72, adc0

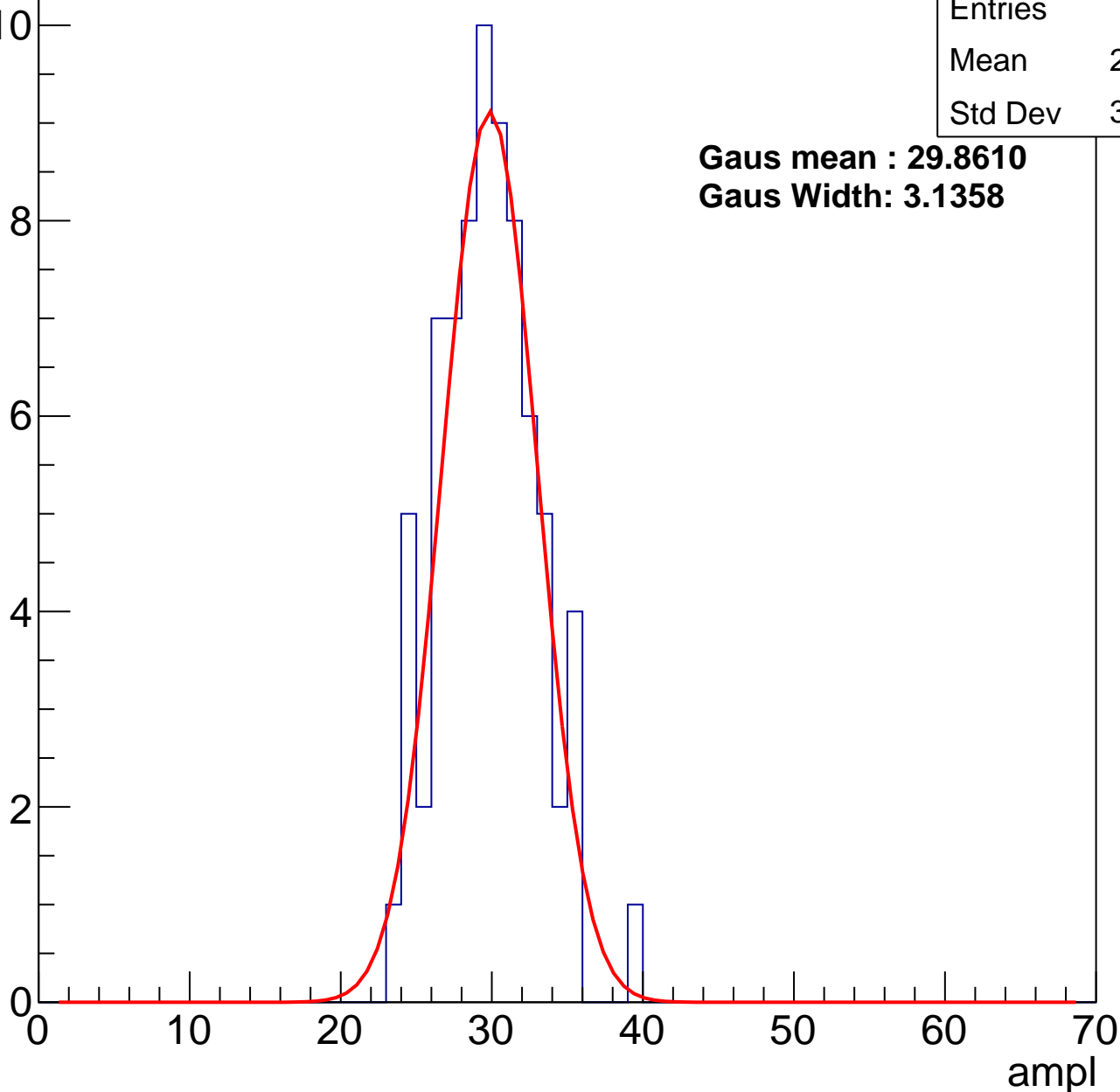
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	29.33
Std Dev	3.168

**Gaus mean : 29.8610**

**Gaus Width: 3.1358**



# B0L000S, U13-ch72, adc1

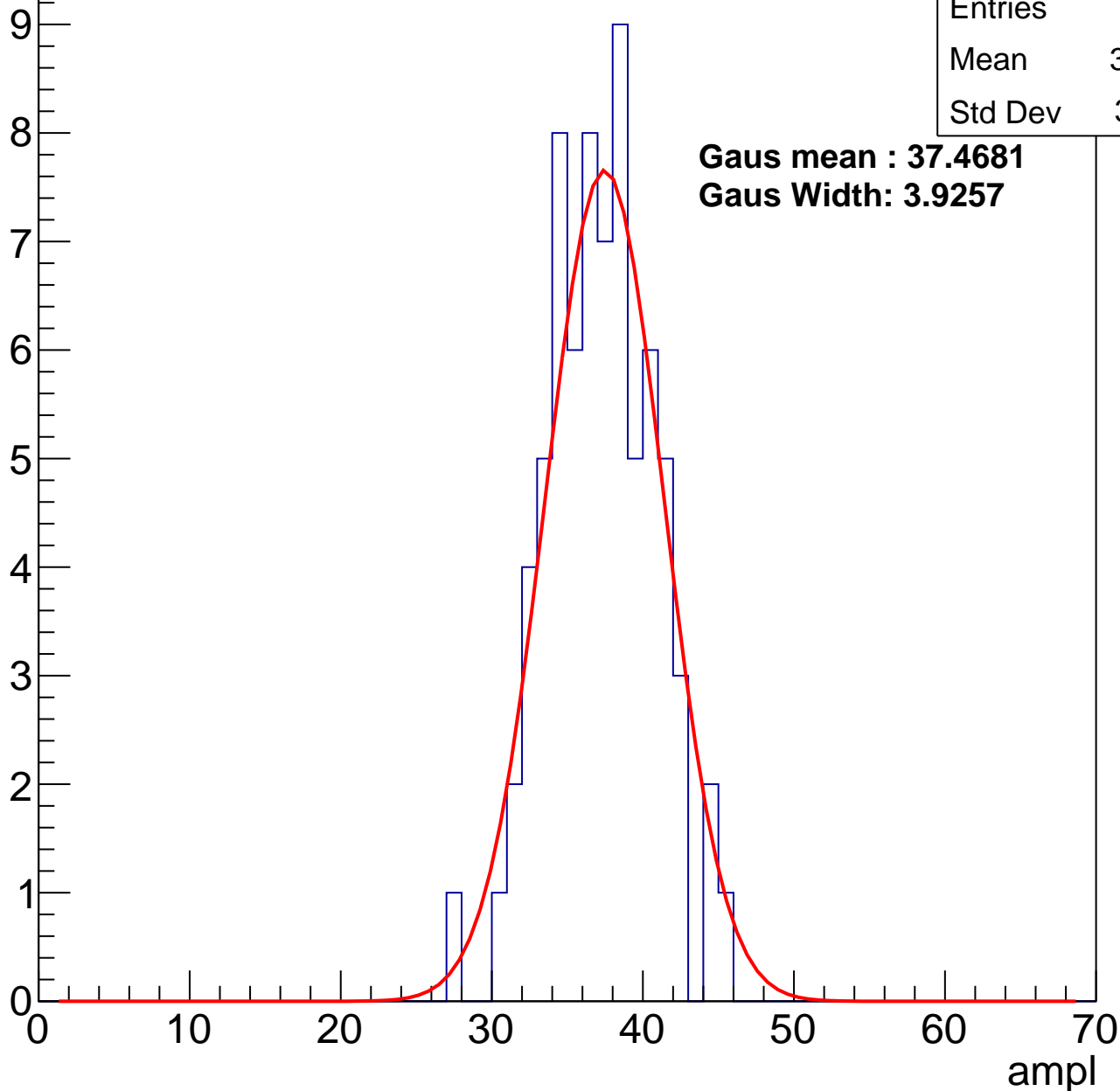
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.74
Std Dev	3.511

**Gaus mean : 37.4681**

**Gaus Width: 3.9257**



# B0L000S, U13-ch72, adc2

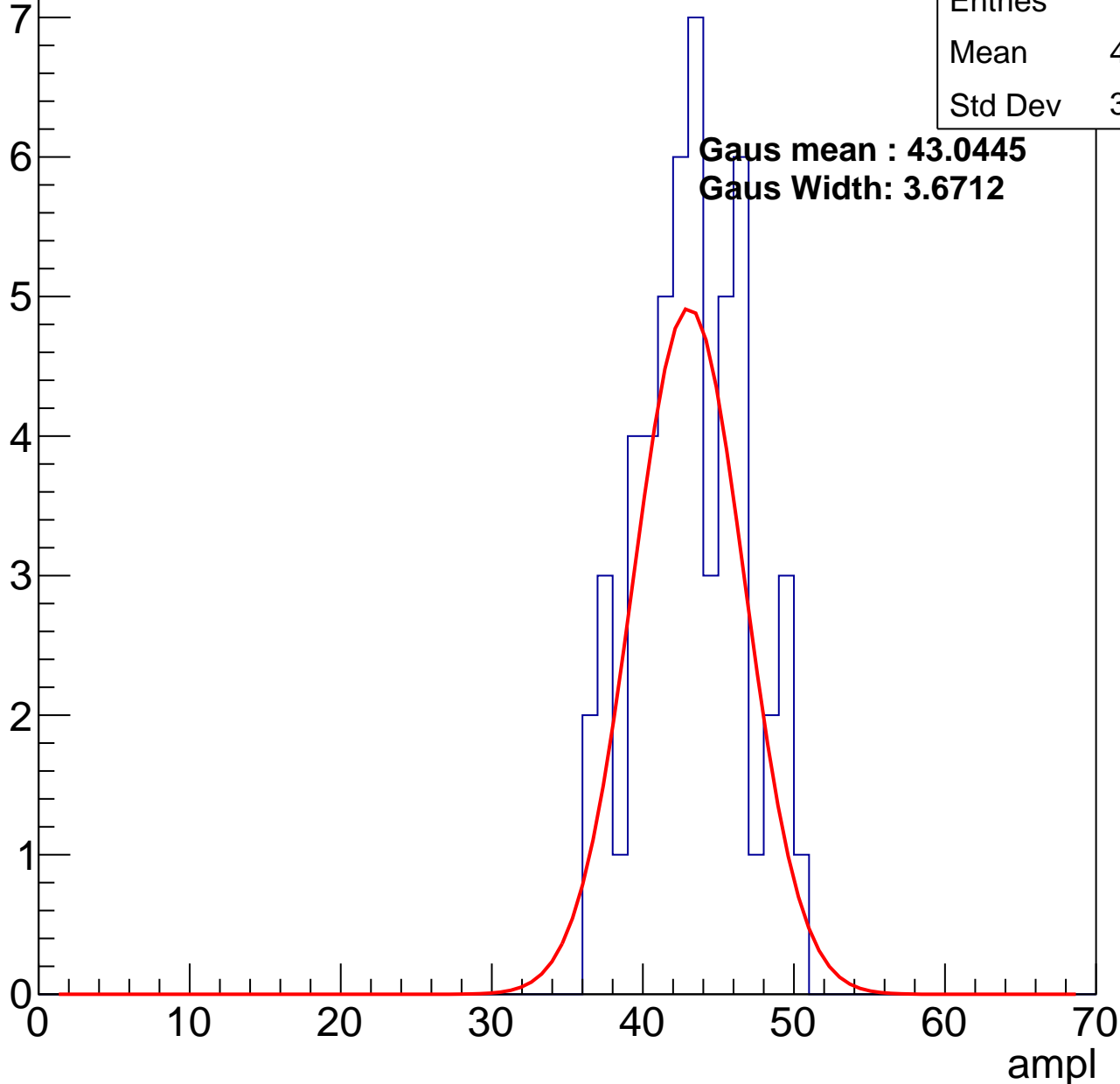
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	42.79
Std Dev	3.515

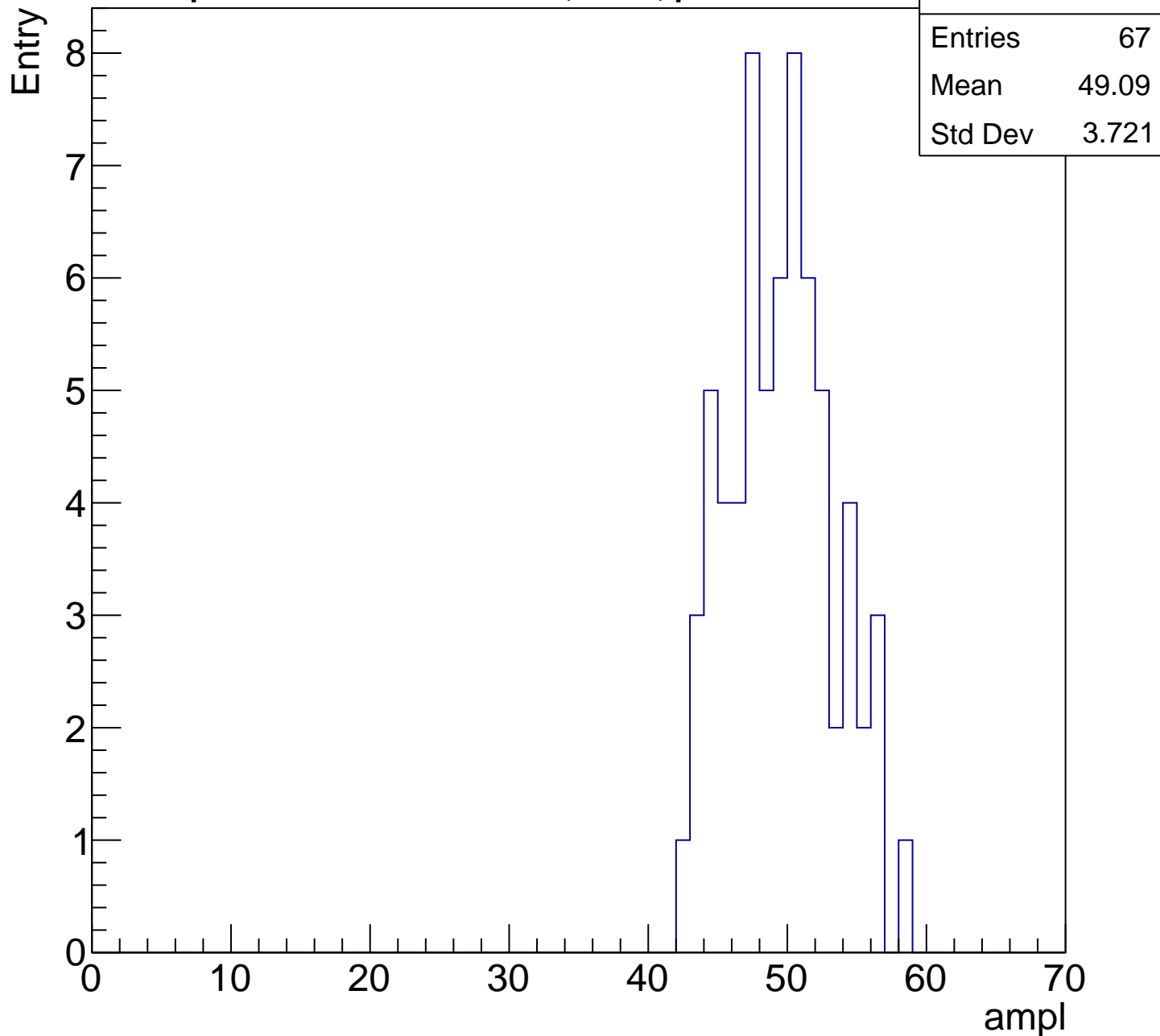
**Gaus mean : 43.0445**

**Gaus Width: 3.6712**



# B0L000S, U13-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

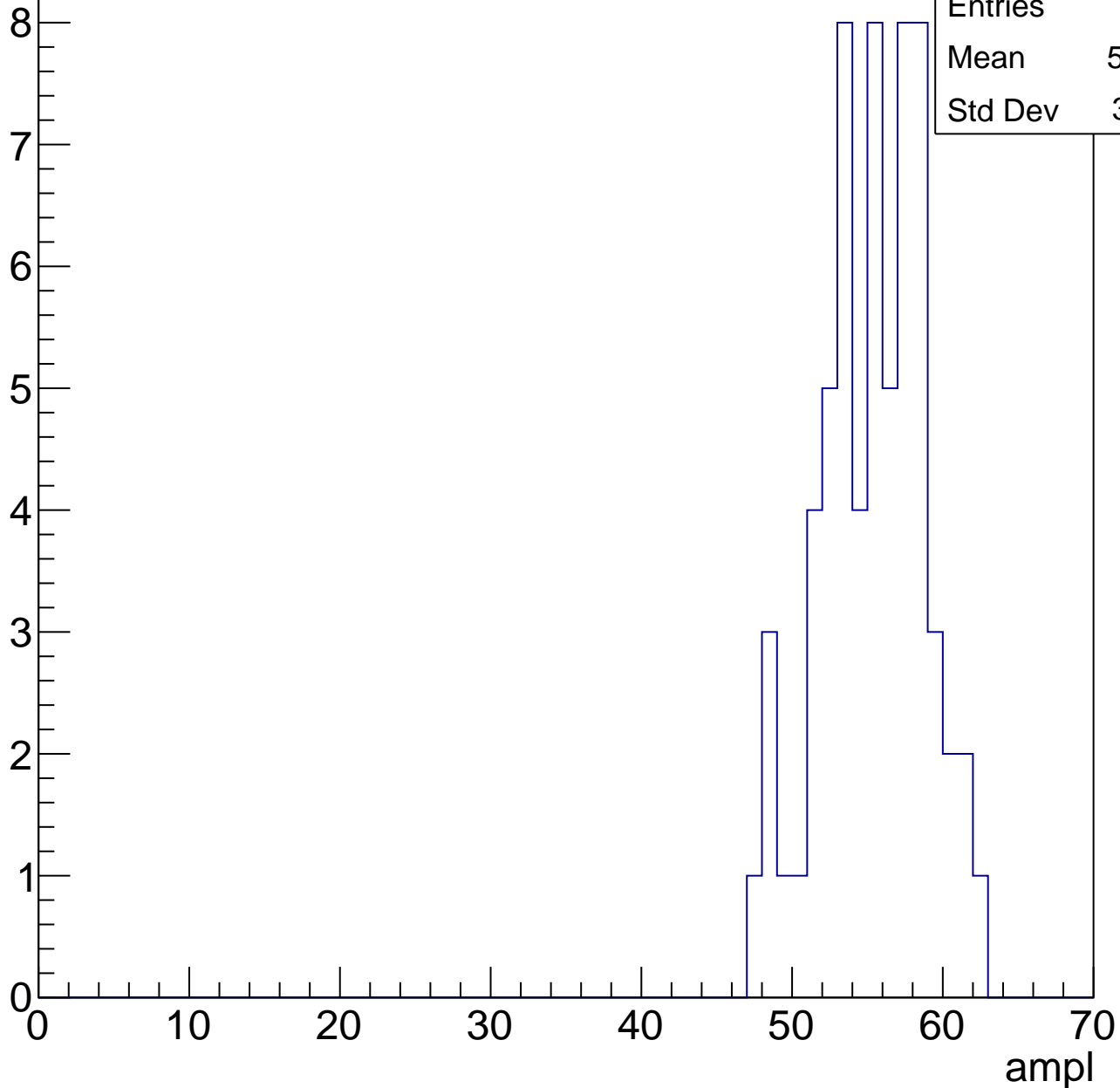


# B0L000S, U13-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.92
Std Dev	3.411

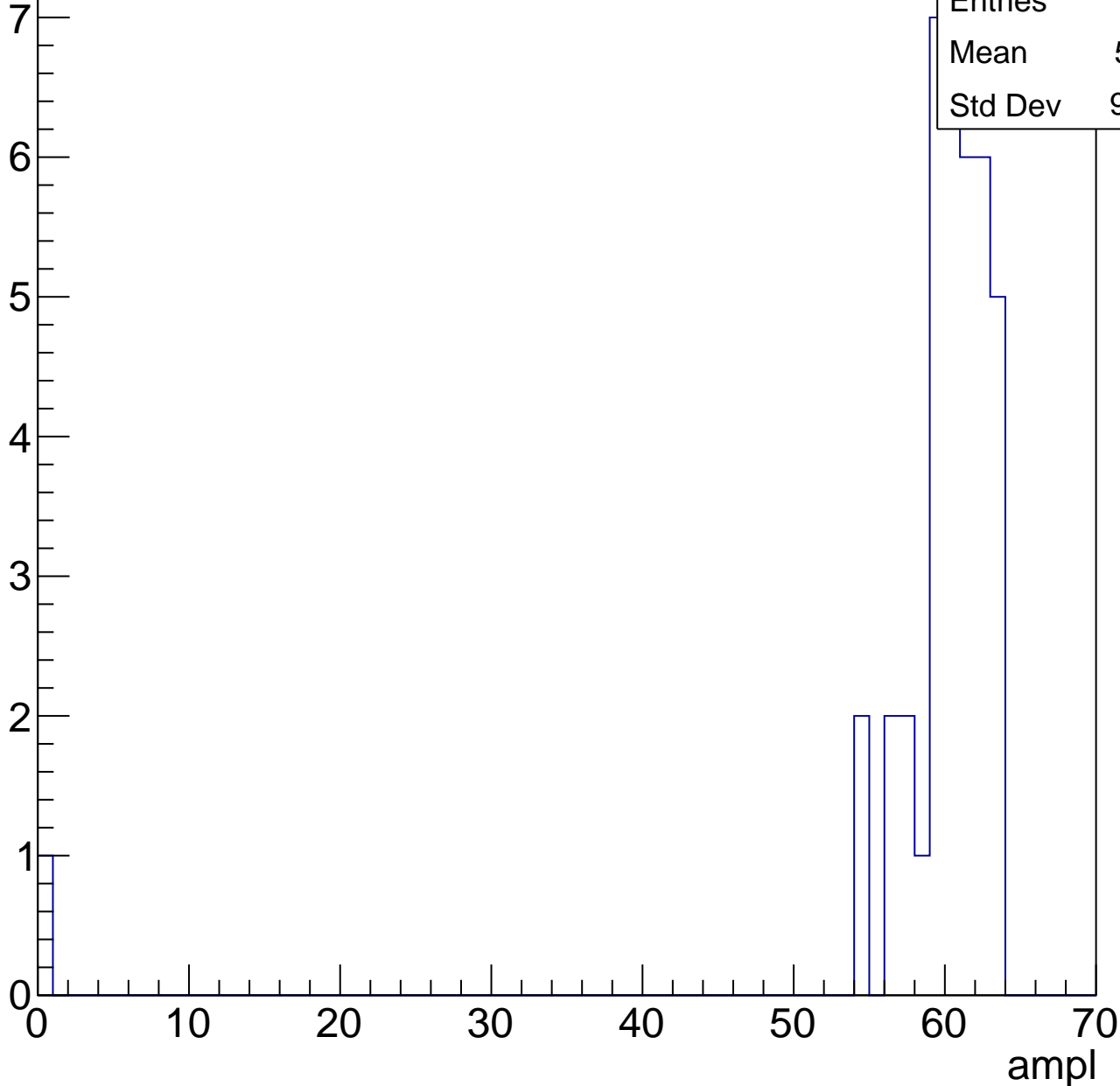


# B0L000S, U13-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	58.41
Std Dev	9.753

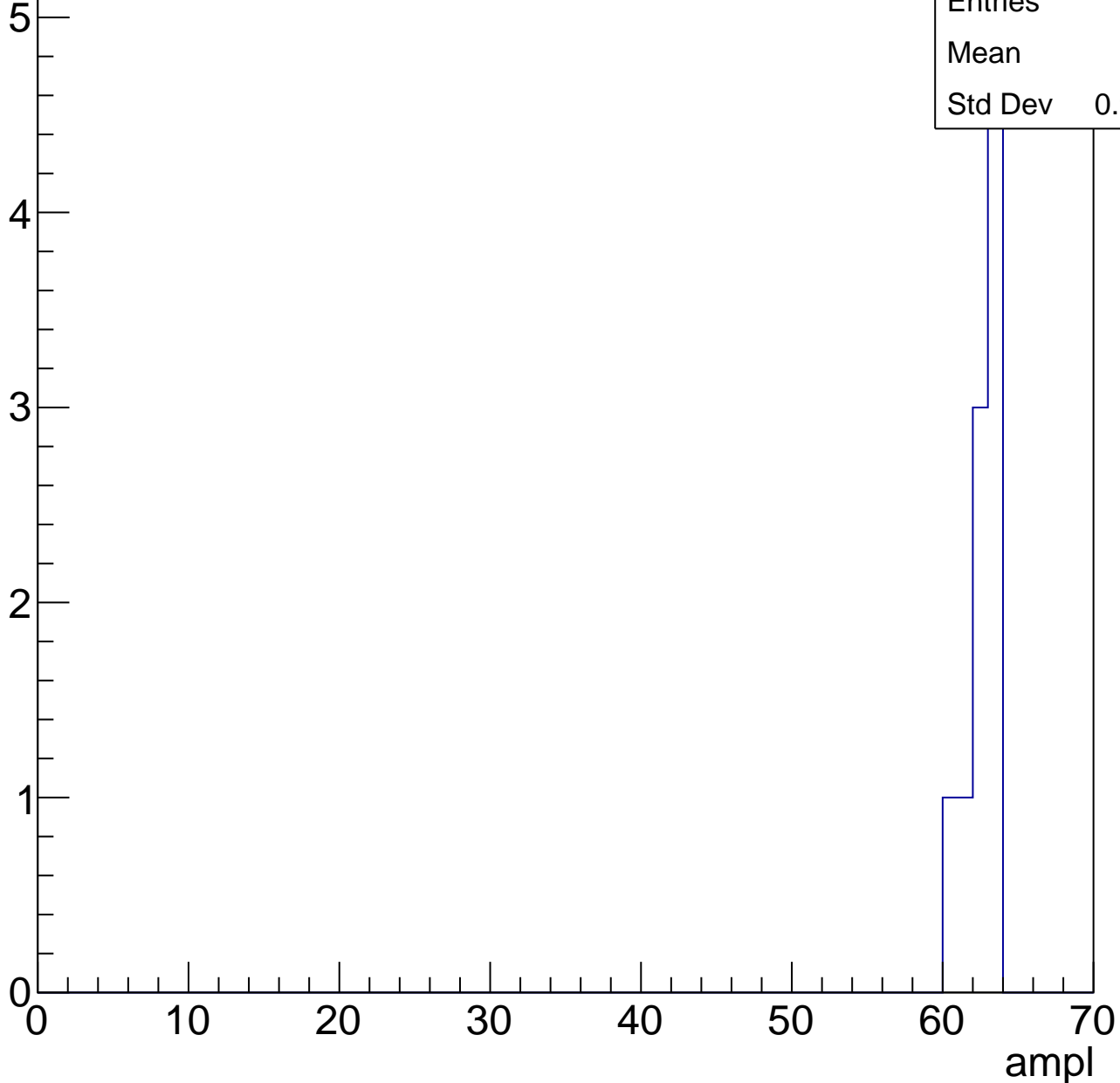


# B0L000S, U13-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.2
Std Dev	0.9798

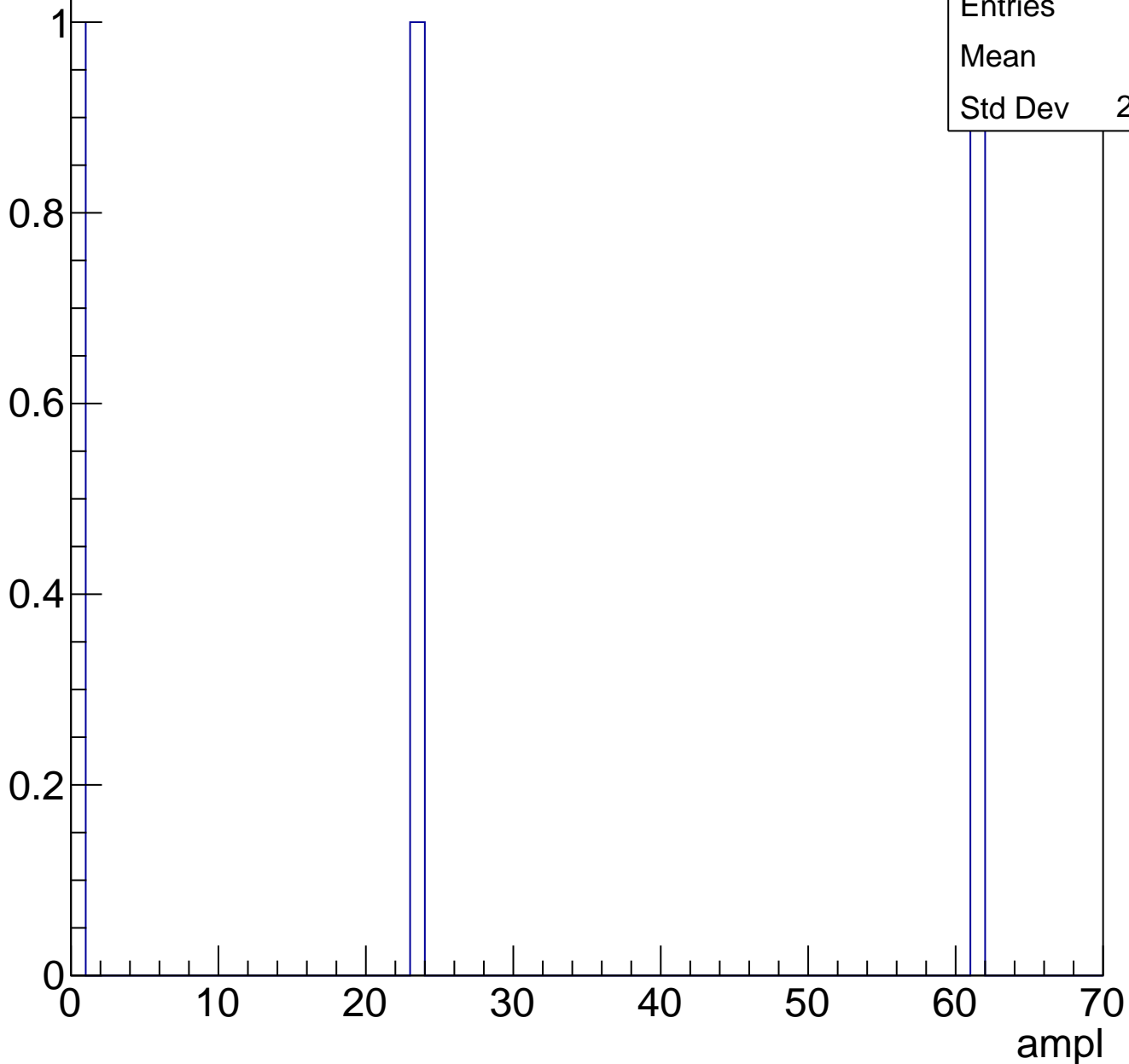




# B0L000S, U13-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28
Std Dev	25.15

# B0L000S, U13-ch73, adc0

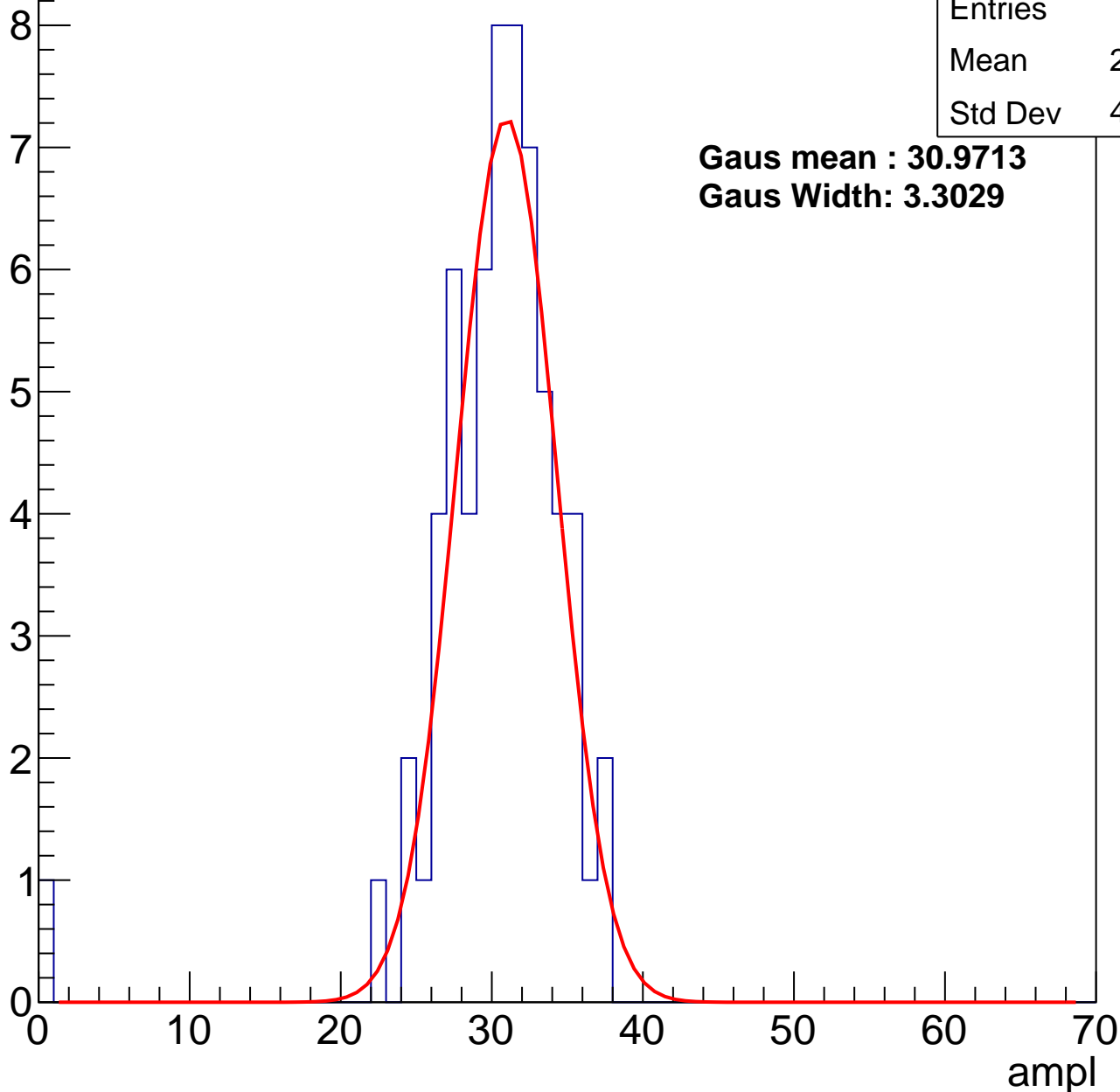
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.84
Std Dev	4.966

**Gaus mean : 30.9713**

**Gaus Width: 3.3029**



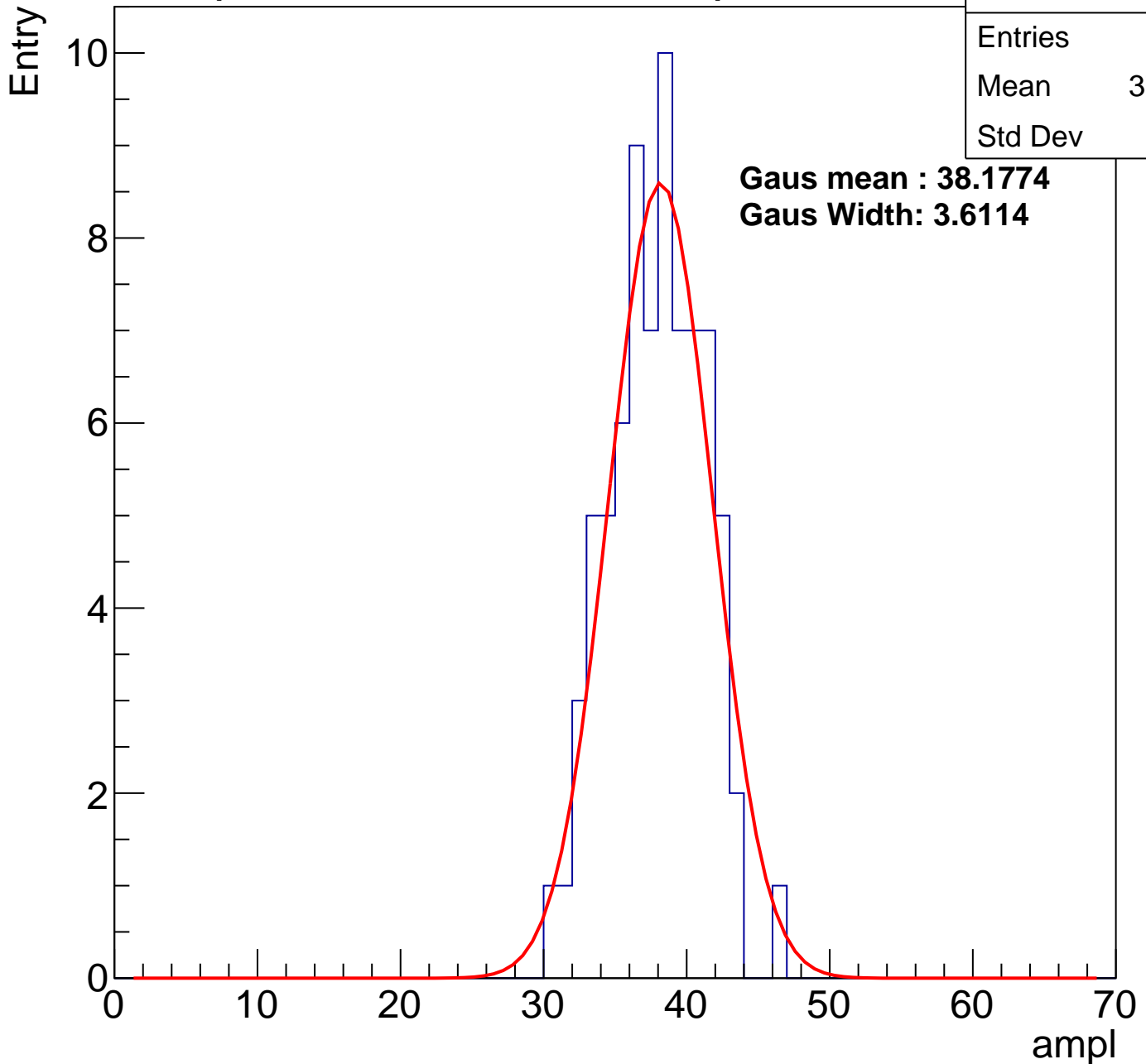
# B0L000S, U13-ch73, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	37.46
Std Dev	3.23

**Gaus mean : 38.1774**

**Gaus Width: 3.6114**



# B0L000S, U13-ch73, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	44.45
Std Dev	3.302

**Gaus mean : 44.9407**

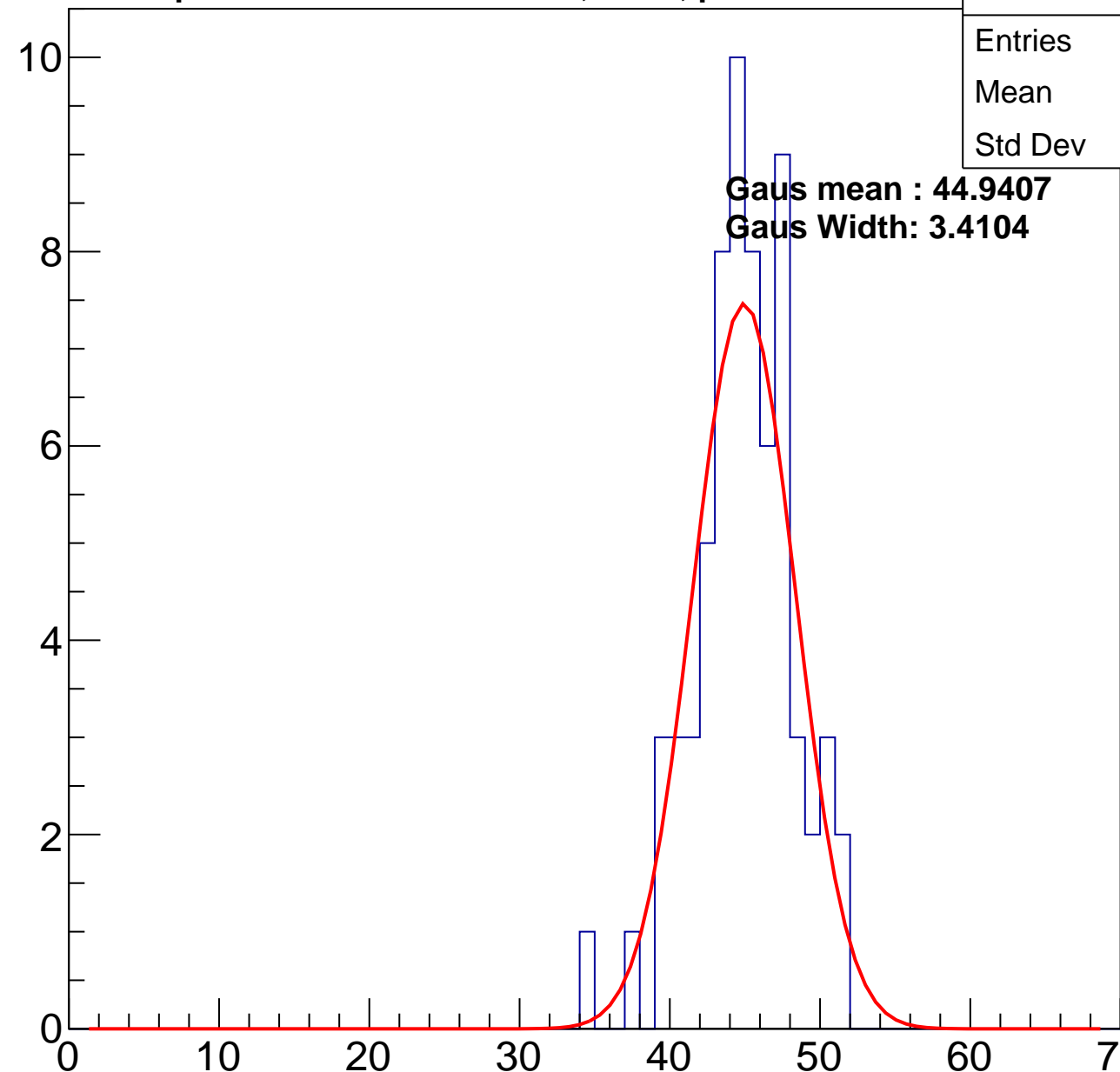
**Gaus Width: 3.4104**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

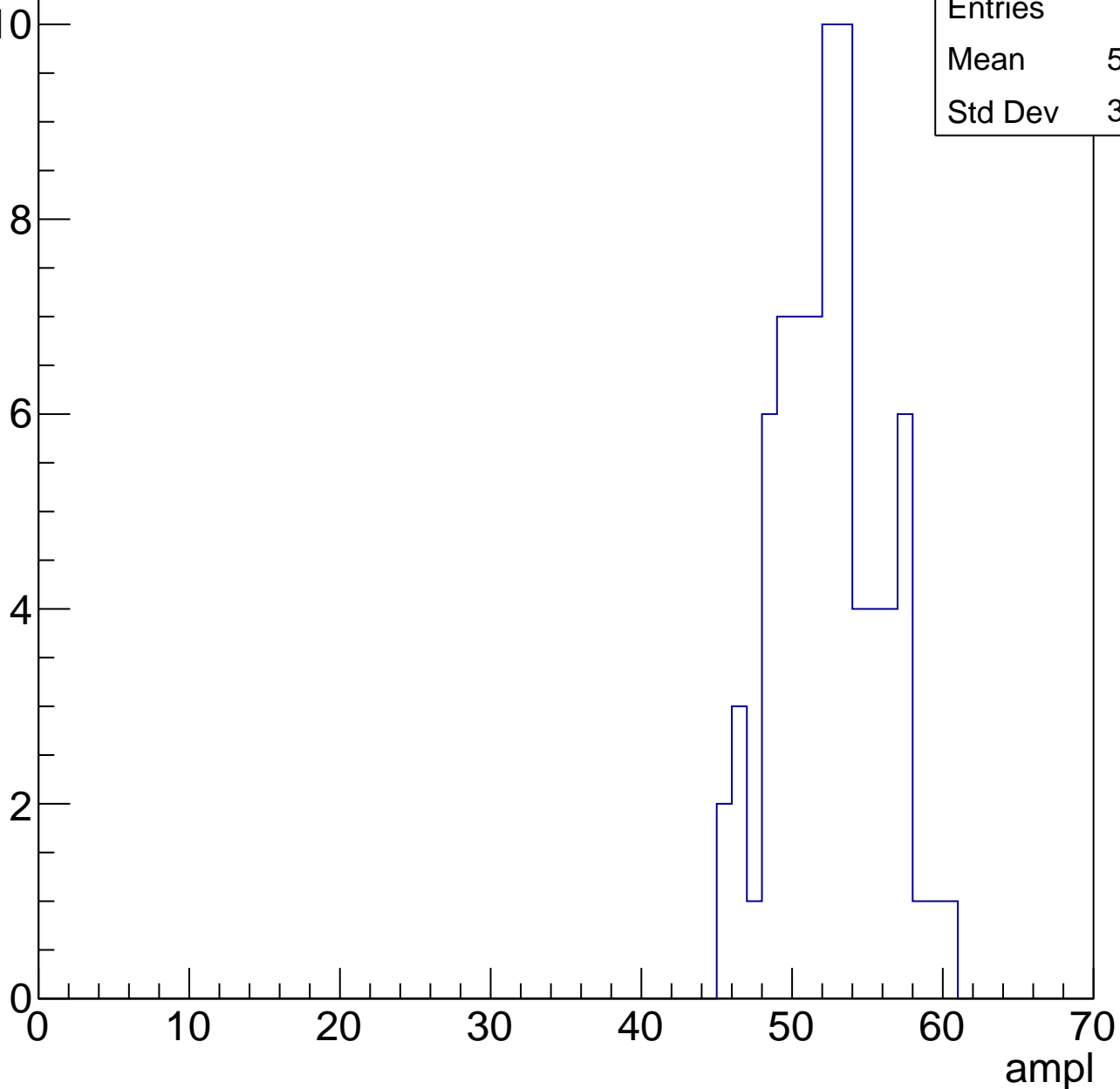


# B0L000S, U13-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	51.92
Std Dev	3.388

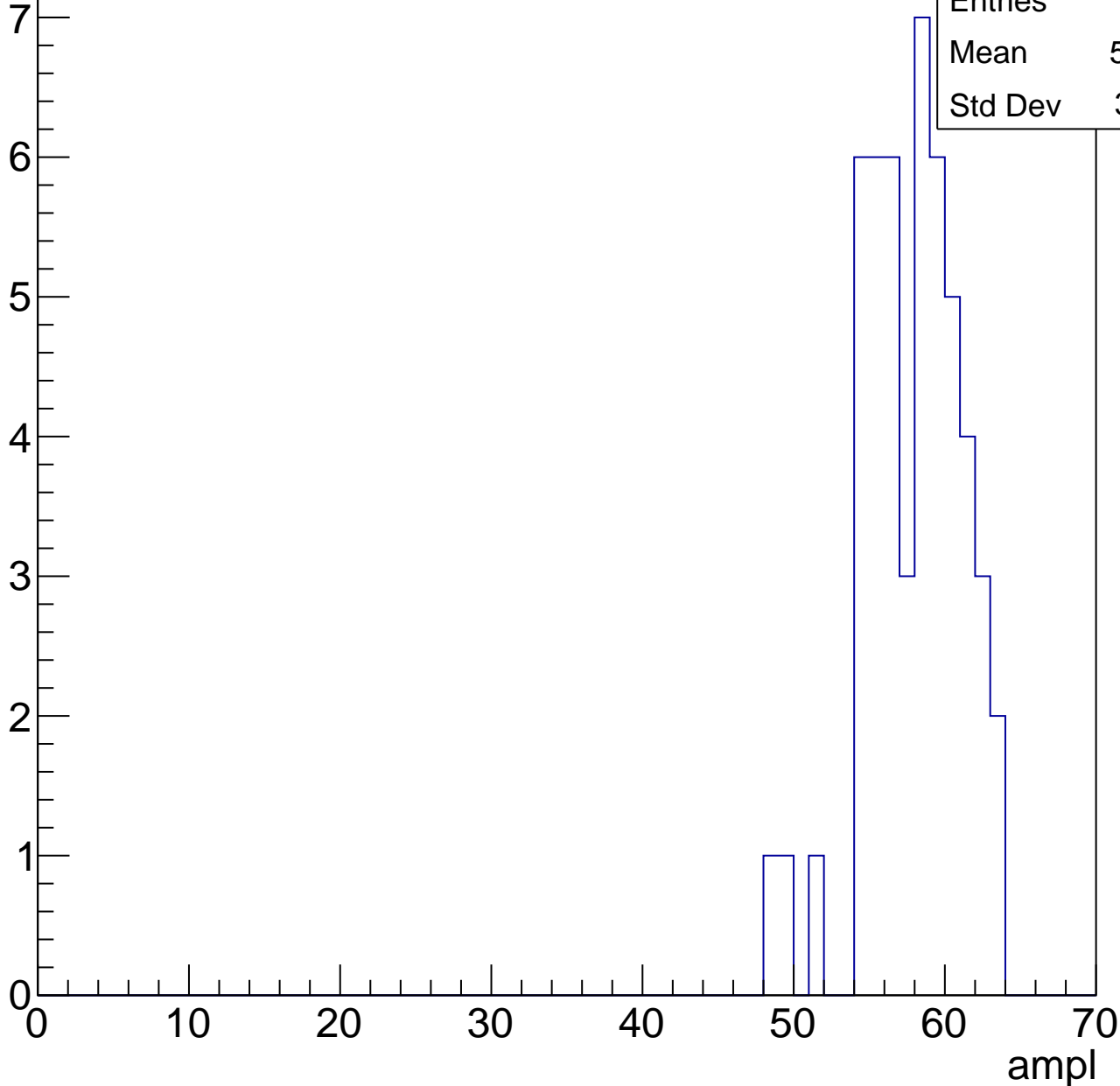


# B0L000S, U13-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	57.35
Std Dev	3.271

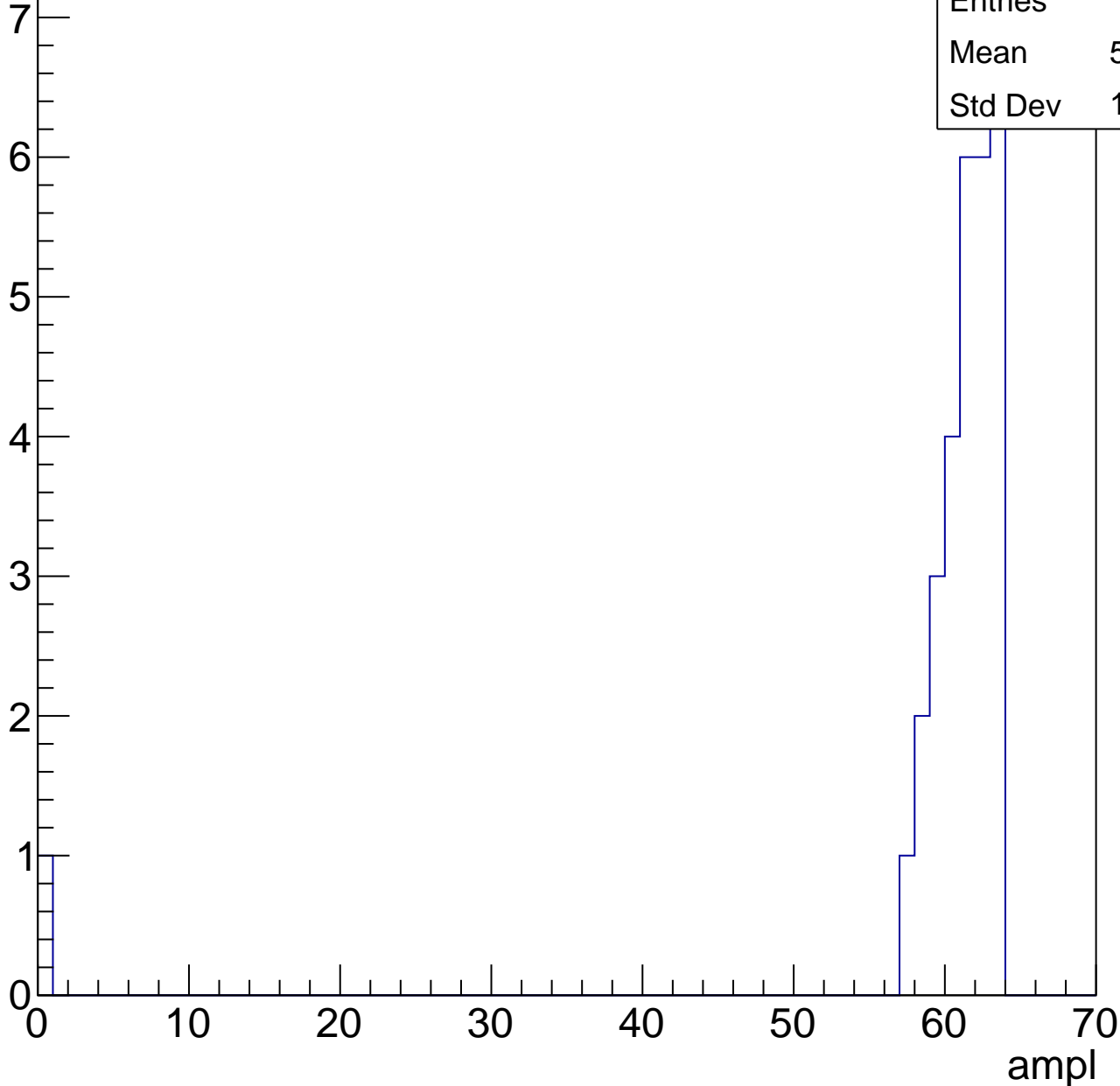


# B0L000S, U13-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	58.97
Std Dev	11.08



# B0L000S, U13-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch74, adc0

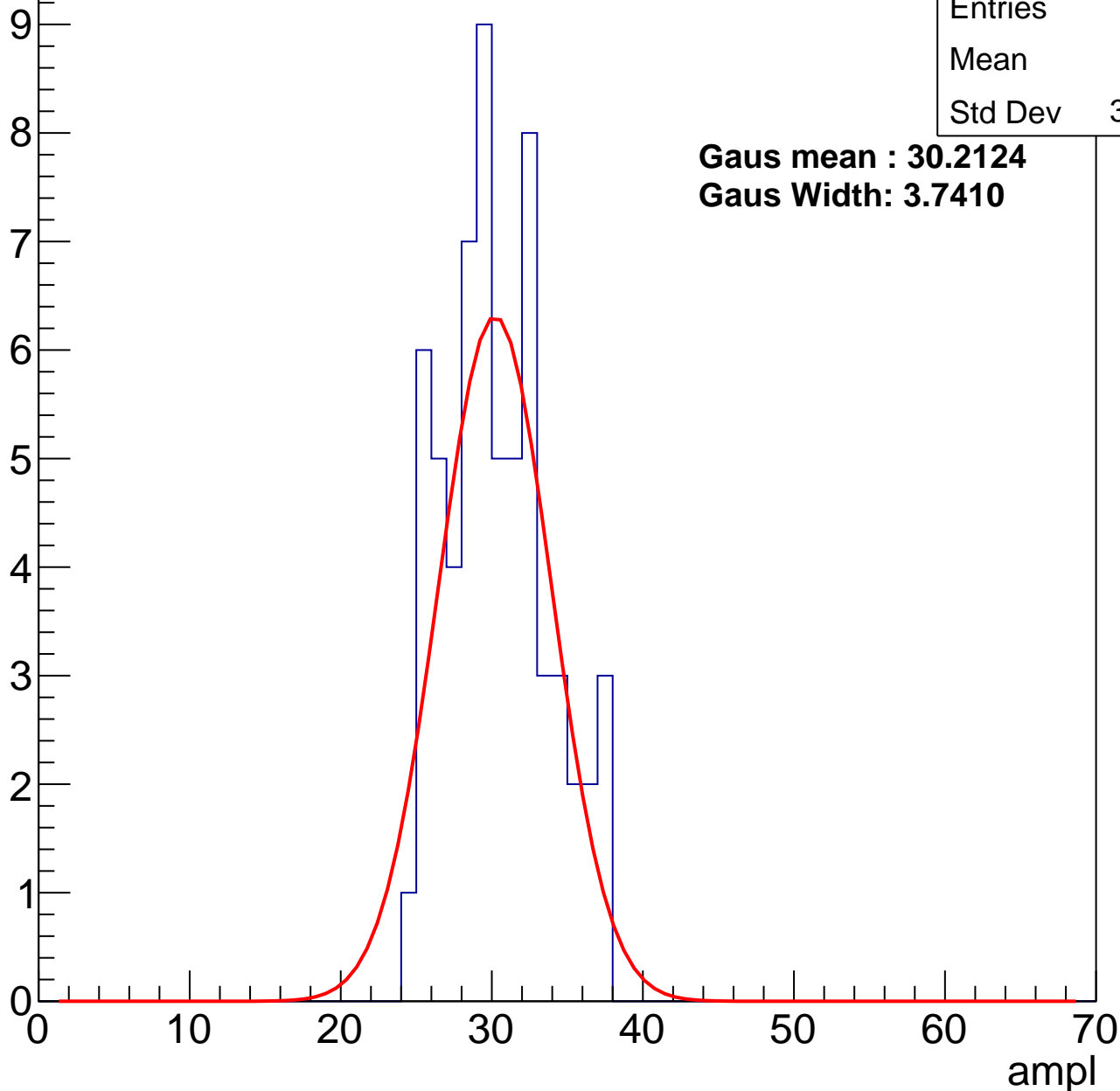
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.9
Std Dev	3.379

**Gaus mean : 30.2124**

**Gaus Width: 3.7410**



# B0L000S, U13-ch74, adc1

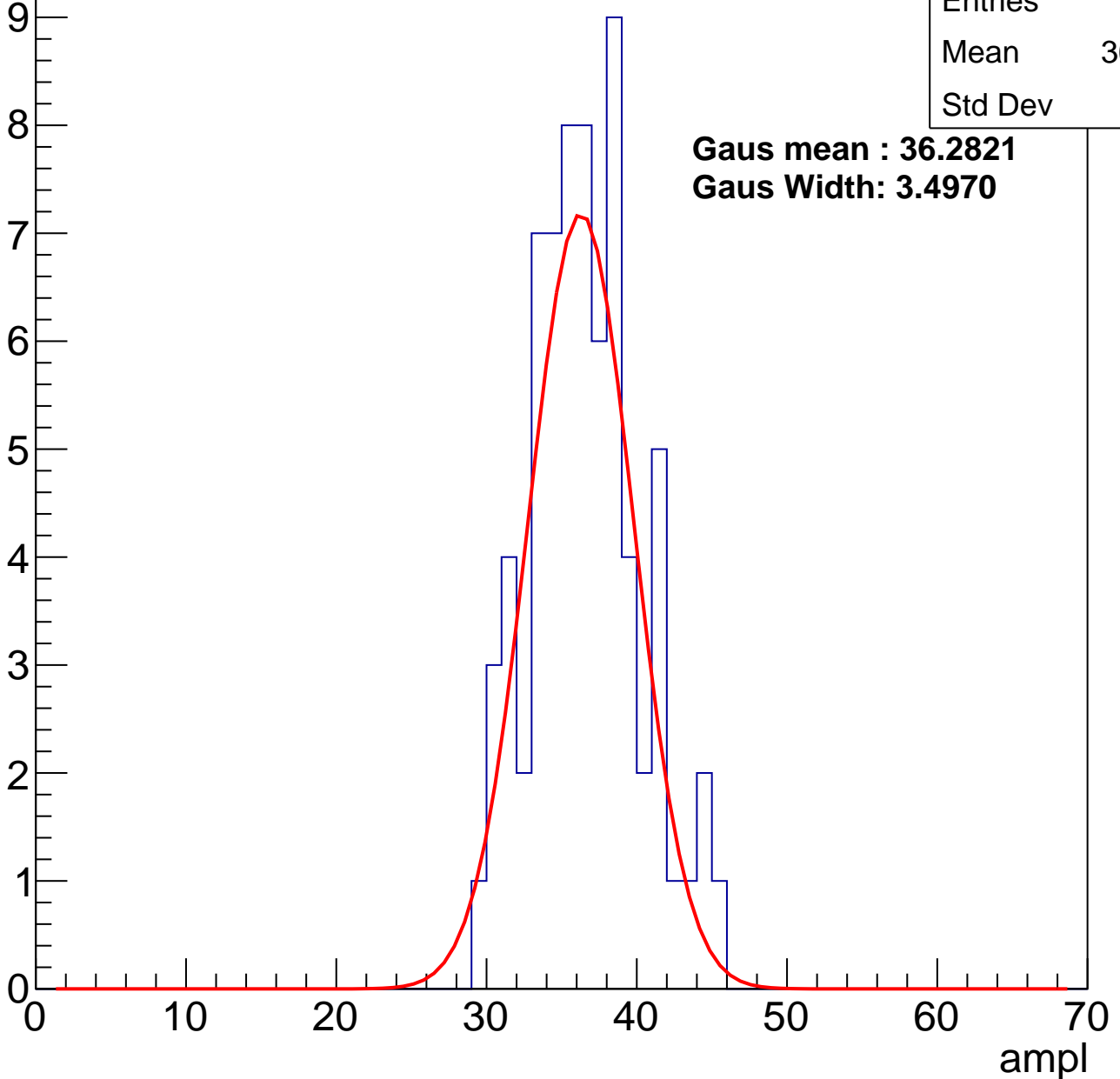
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.15
Std Dev	3.59

**Gaus mean : 36.2821**

**Gaus Width: 3.4970**



# B0L000S, U13-ch74, adc2

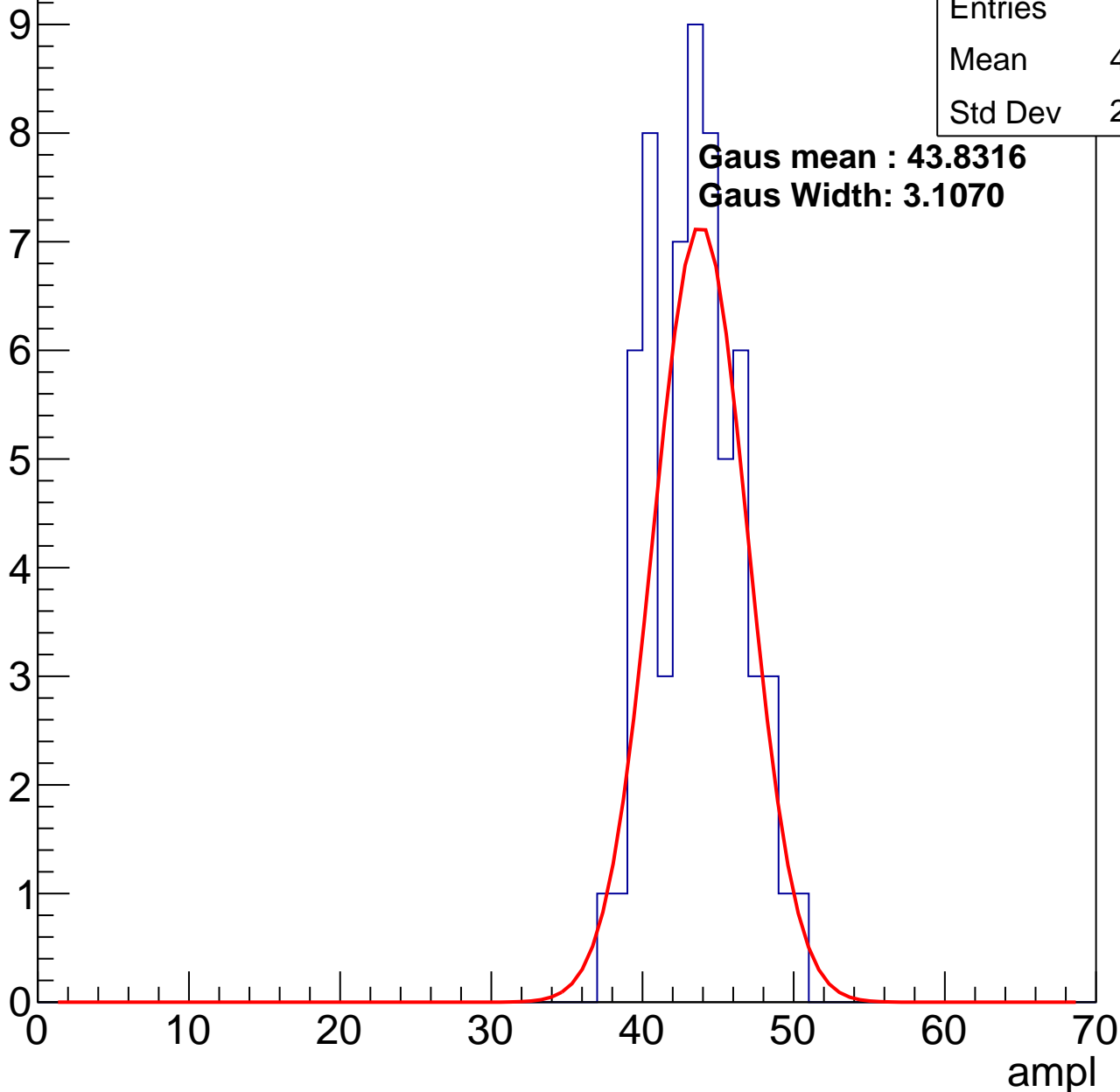
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.06
Std Dev	2.945

**Gaus mean : 43.8316**

**Gaus Width: 3.1070**

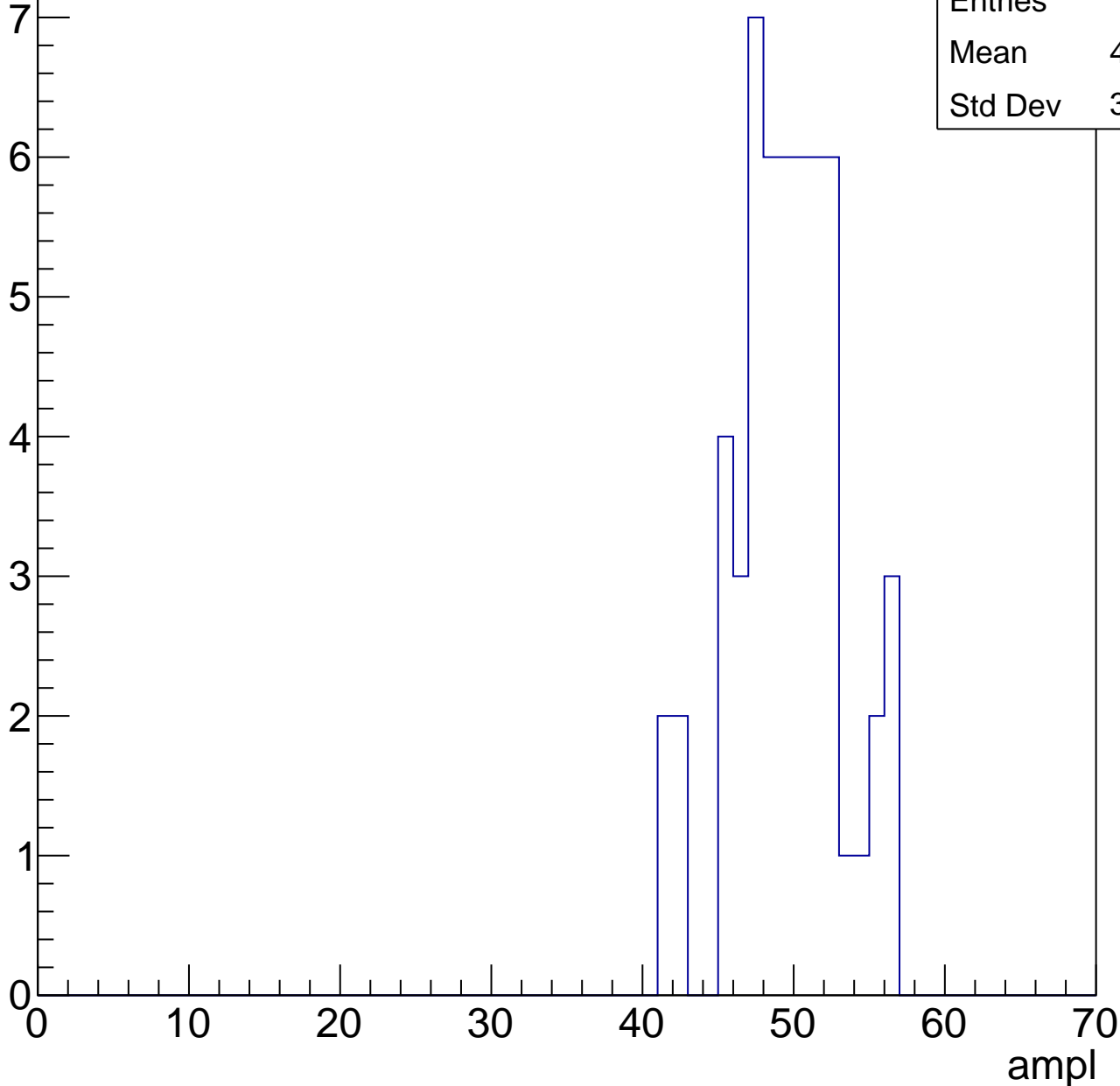


# B0L000S, U13-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.05
Std Dev	3.554

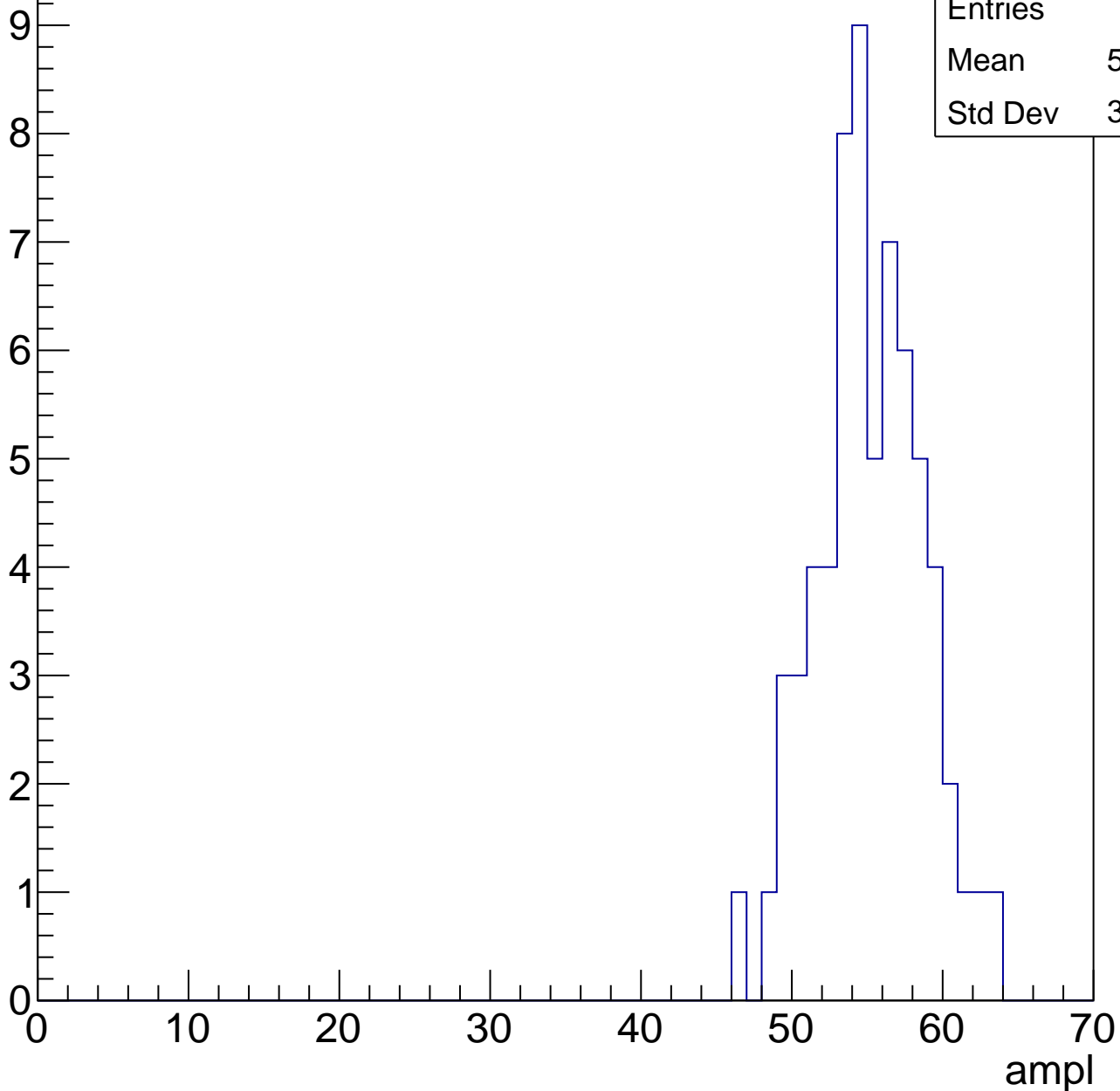


# B0L000S, U13-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	54.68
Std Dev	3.478

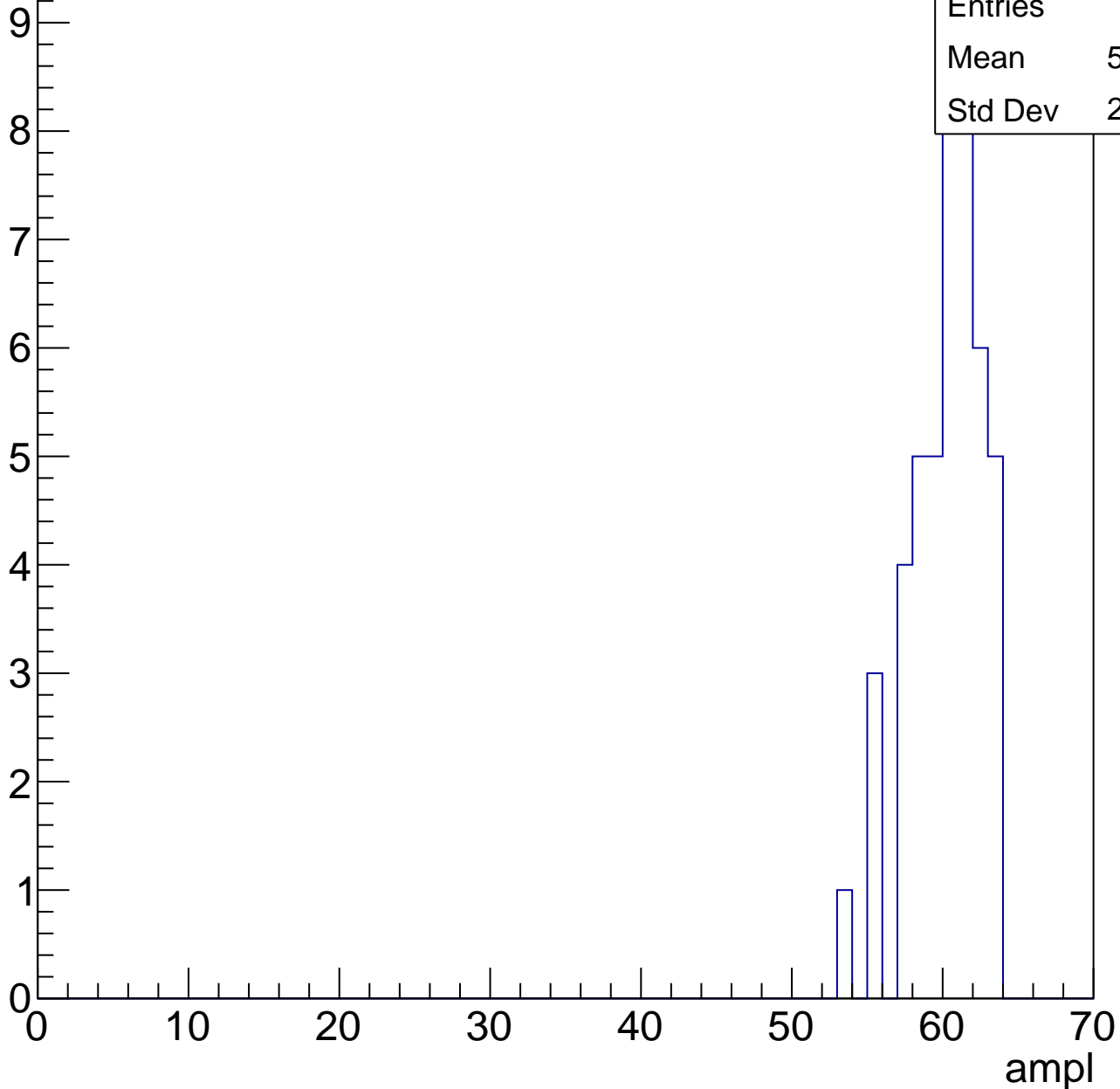


# B0L000S, U13-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

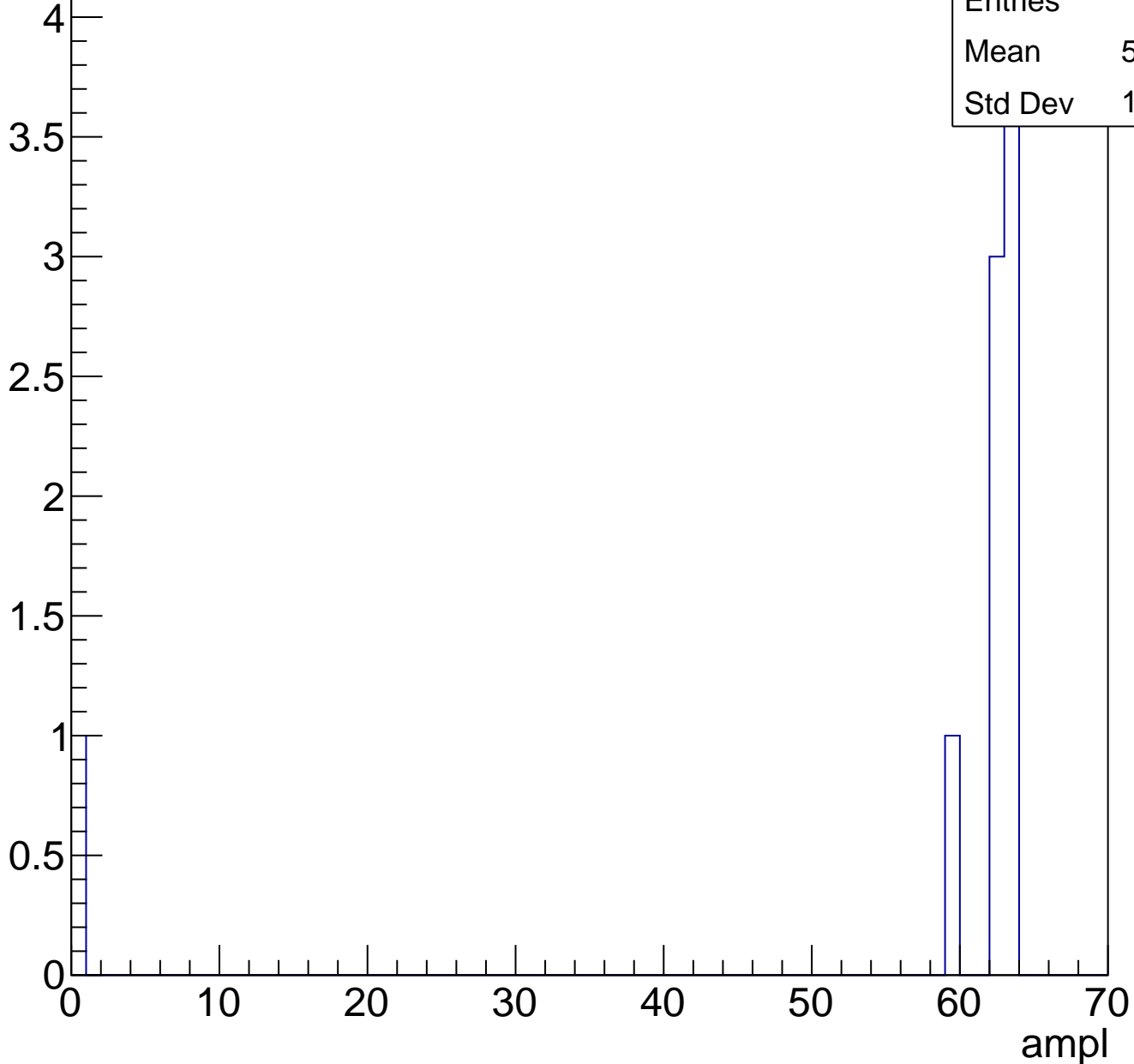
Entries	46
Mean	59.72
Std Dev	2.374



# B0L000S, U13-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch75, adc0

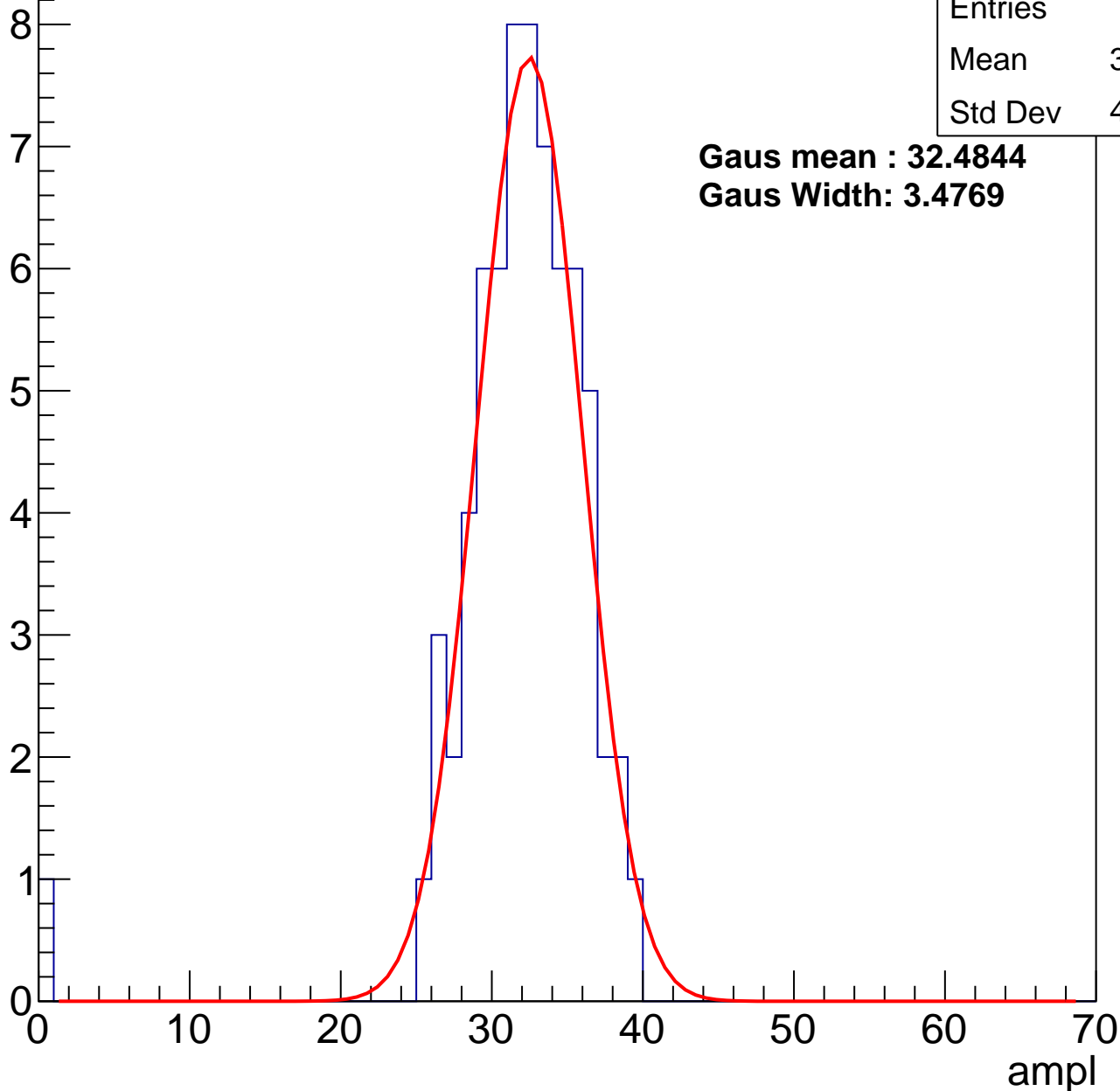
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	31.49
Std Dev	4.999

**Gaus mean : 32.4844**

**Gaus Width: 3.4769**



# B0L000S, U13-ch75, adc1

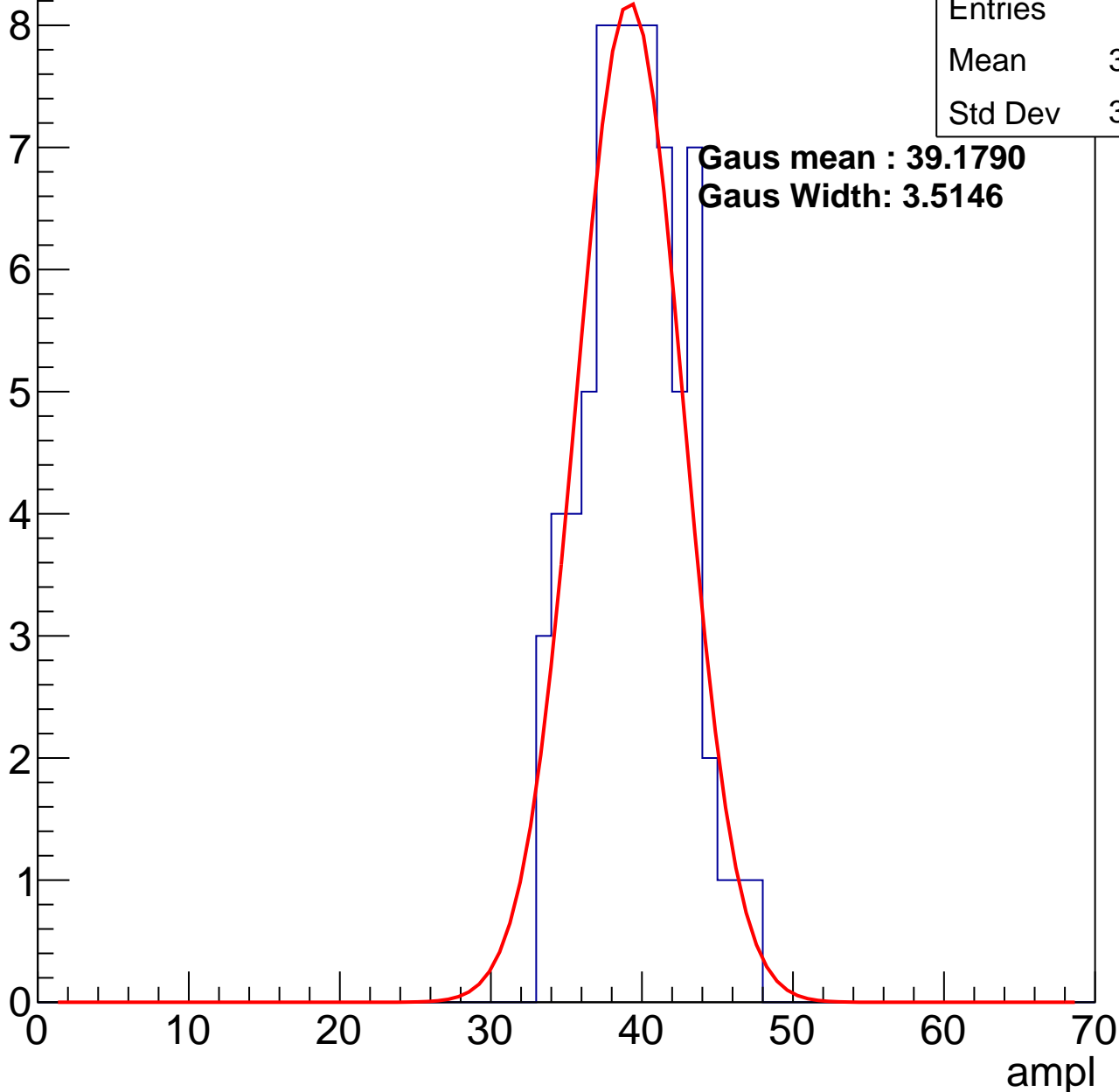
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	39.04
Std Dev	3.225

**Gaus mean : 39.1790**

**Gaus Width: 3.5146**



# B0L000S, U13-ch75, adc2

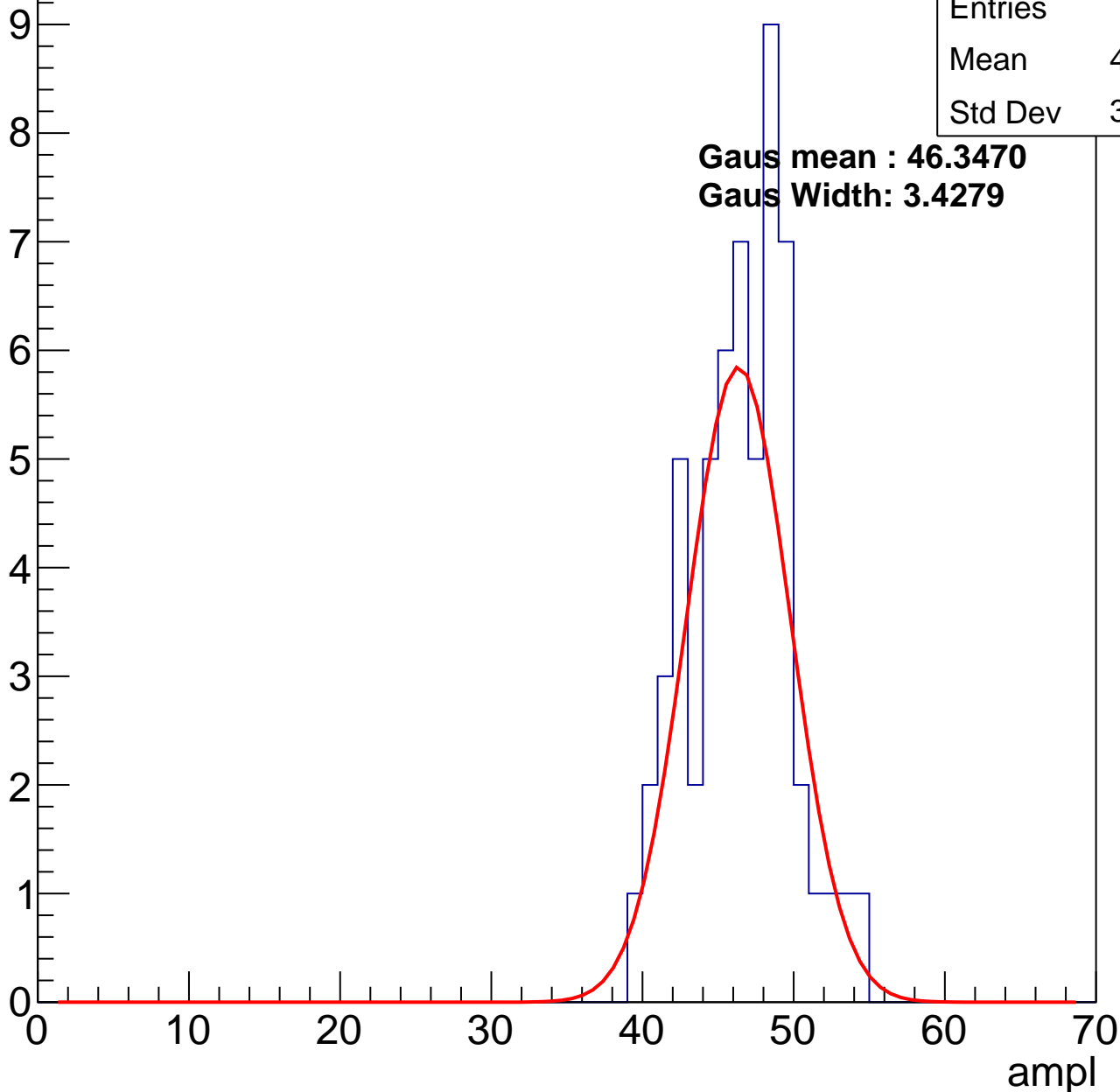
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	46.03
Std Dev	3.296

**Gaus mean : 46.3470**

**Gaus Width: 3.4279**

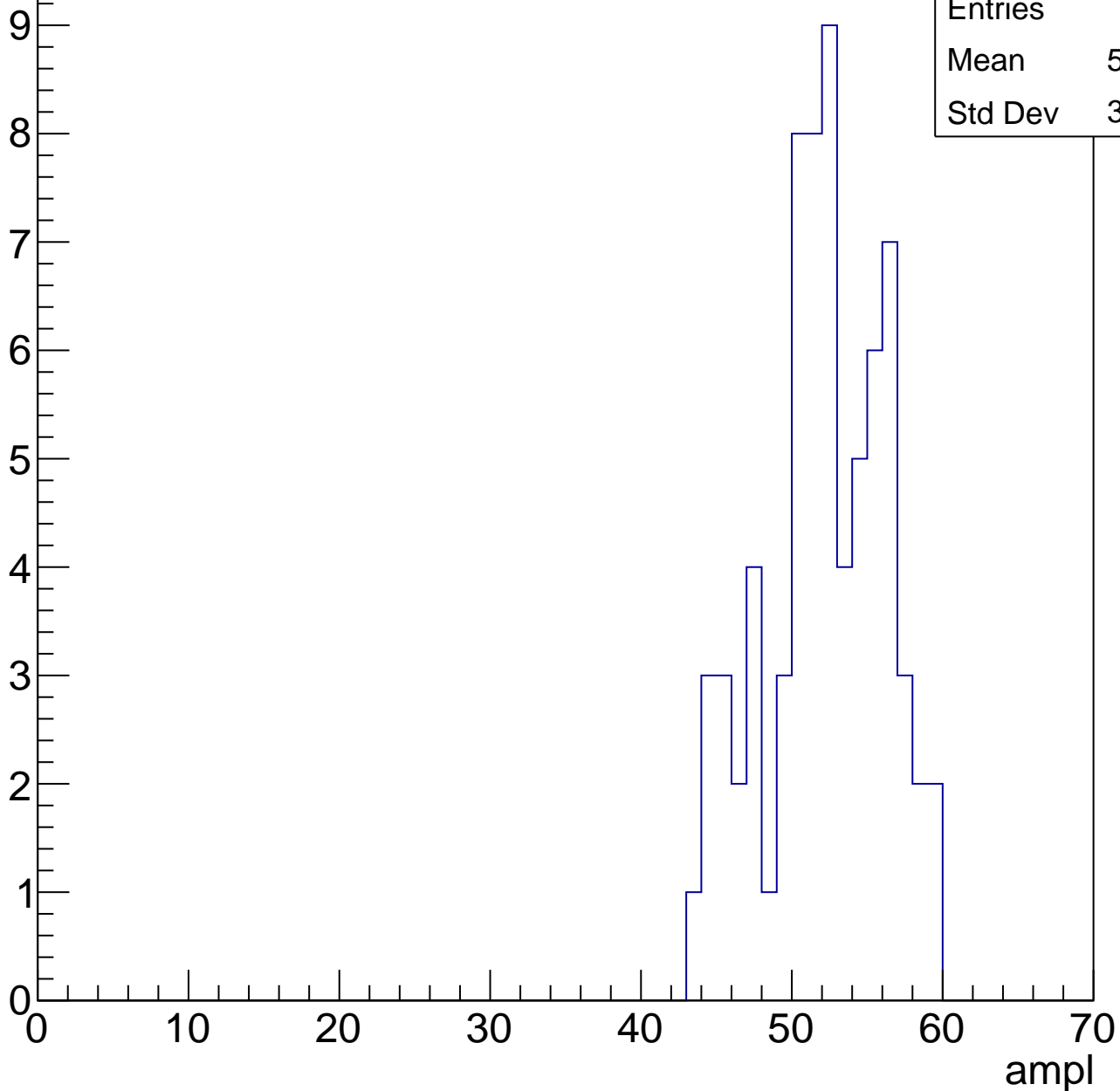


# B0L000S, U13-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	51.69
Std Dev	3.942

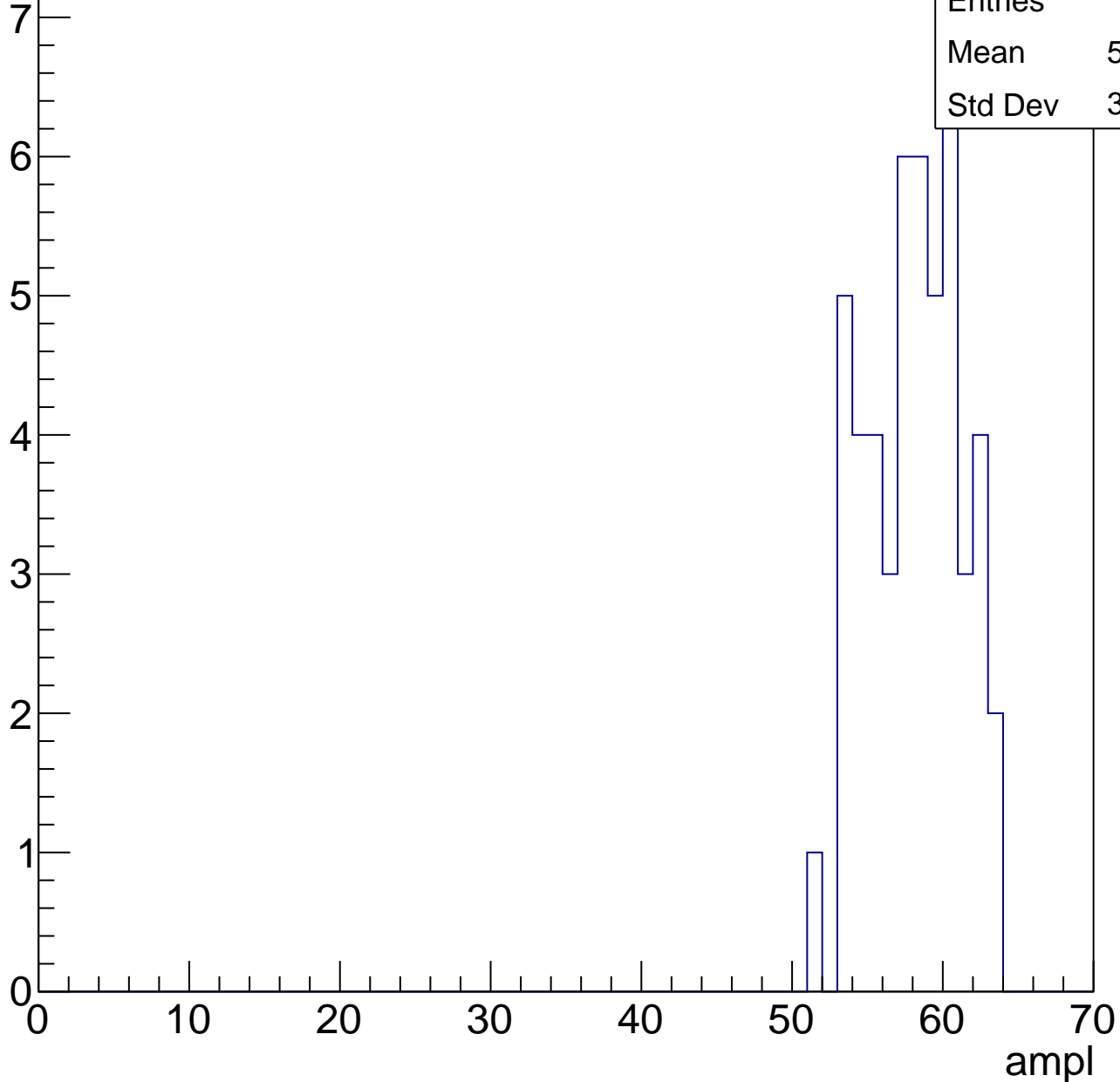


# B0L000S, U13-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	57.64
Std Dev	3.032

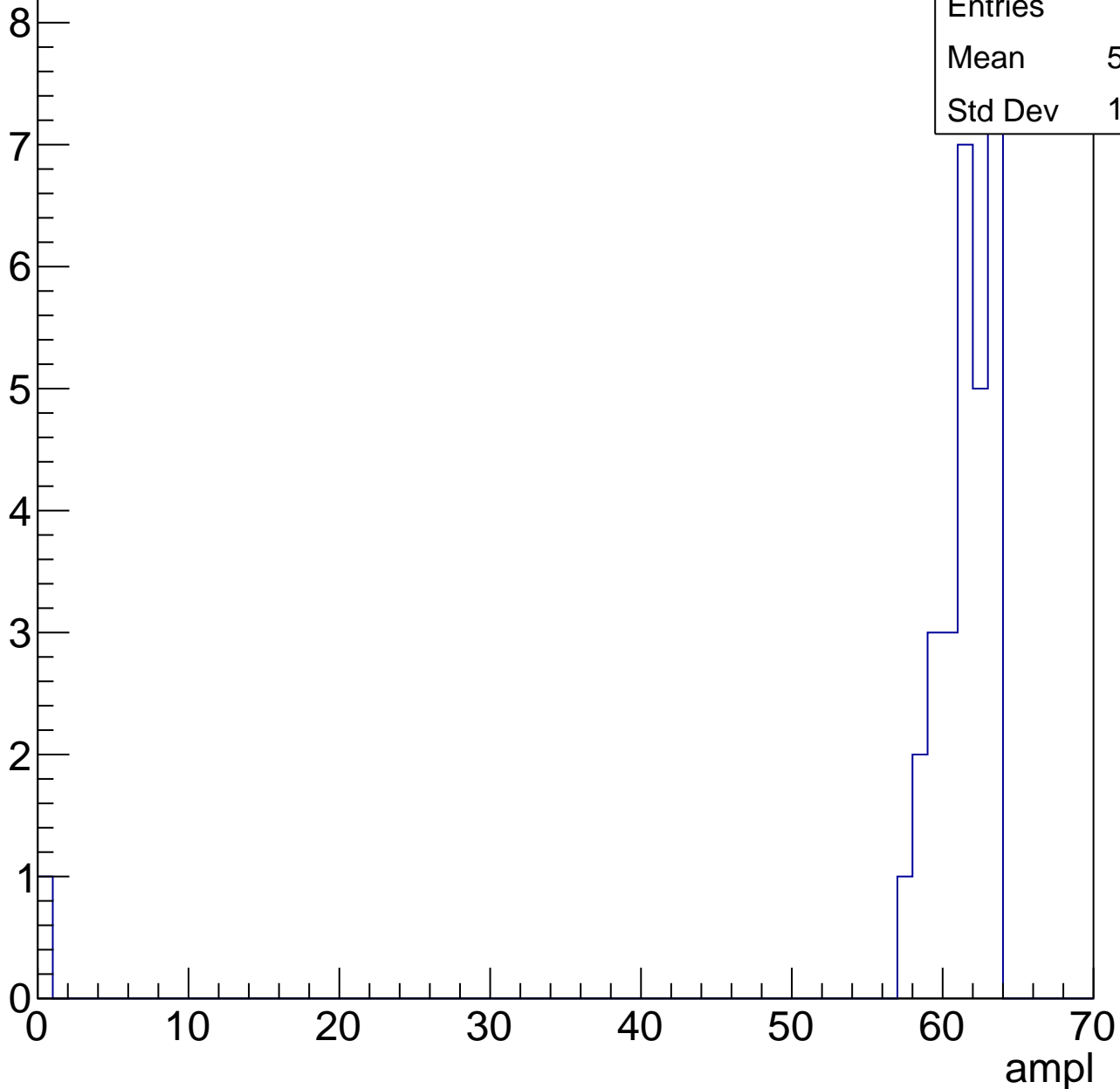


# B0L000S, U13-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	59.03
Std Dev	11.09



# B0L000S, U13-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	62
Std Dev	0

ampl



# B0L000S, U13-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch76, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	31.48
Std Dev	3.421

**Gaus mean : 32.0093**

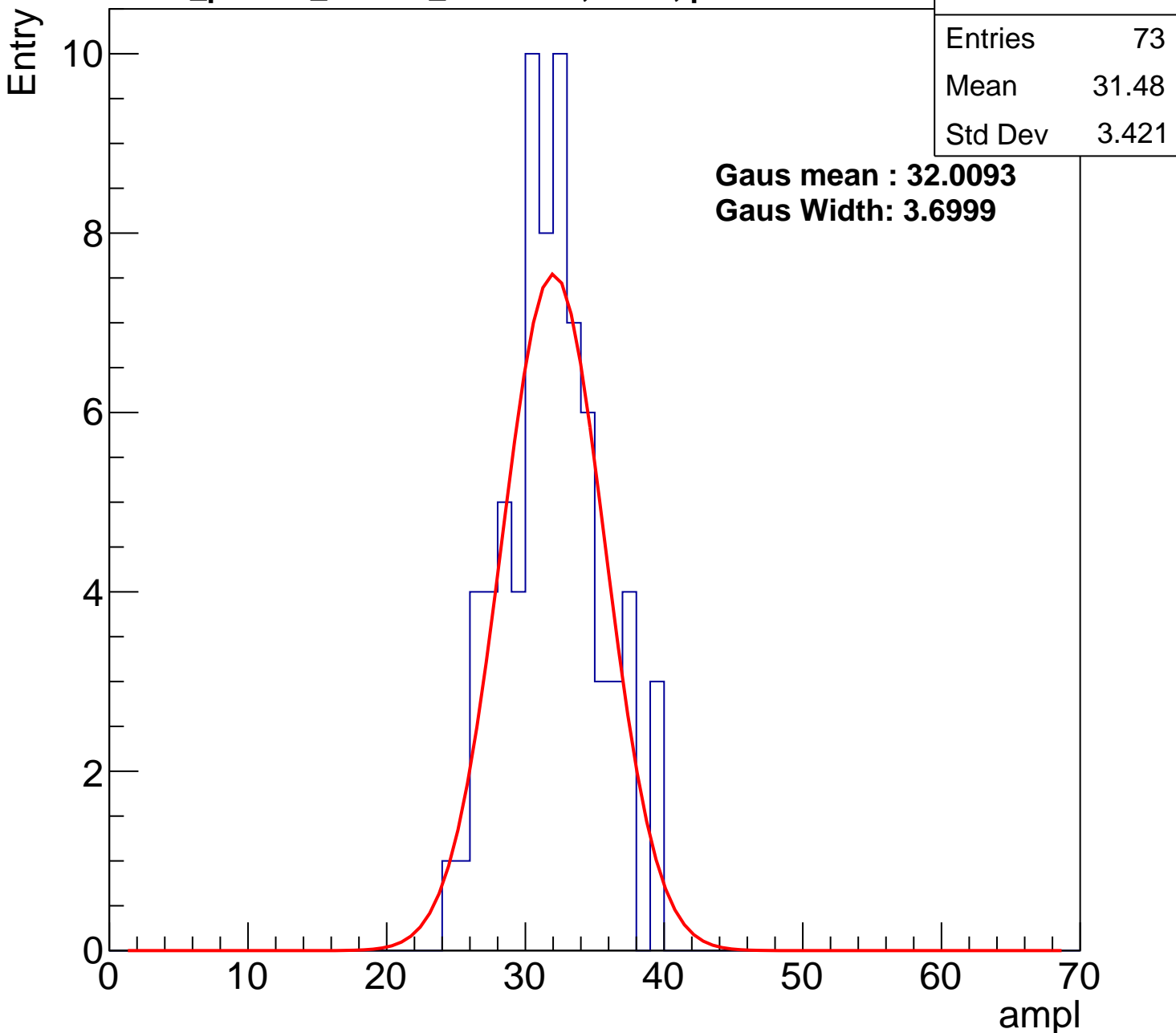
**Gaus Width: 3.6999**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch76, adc1

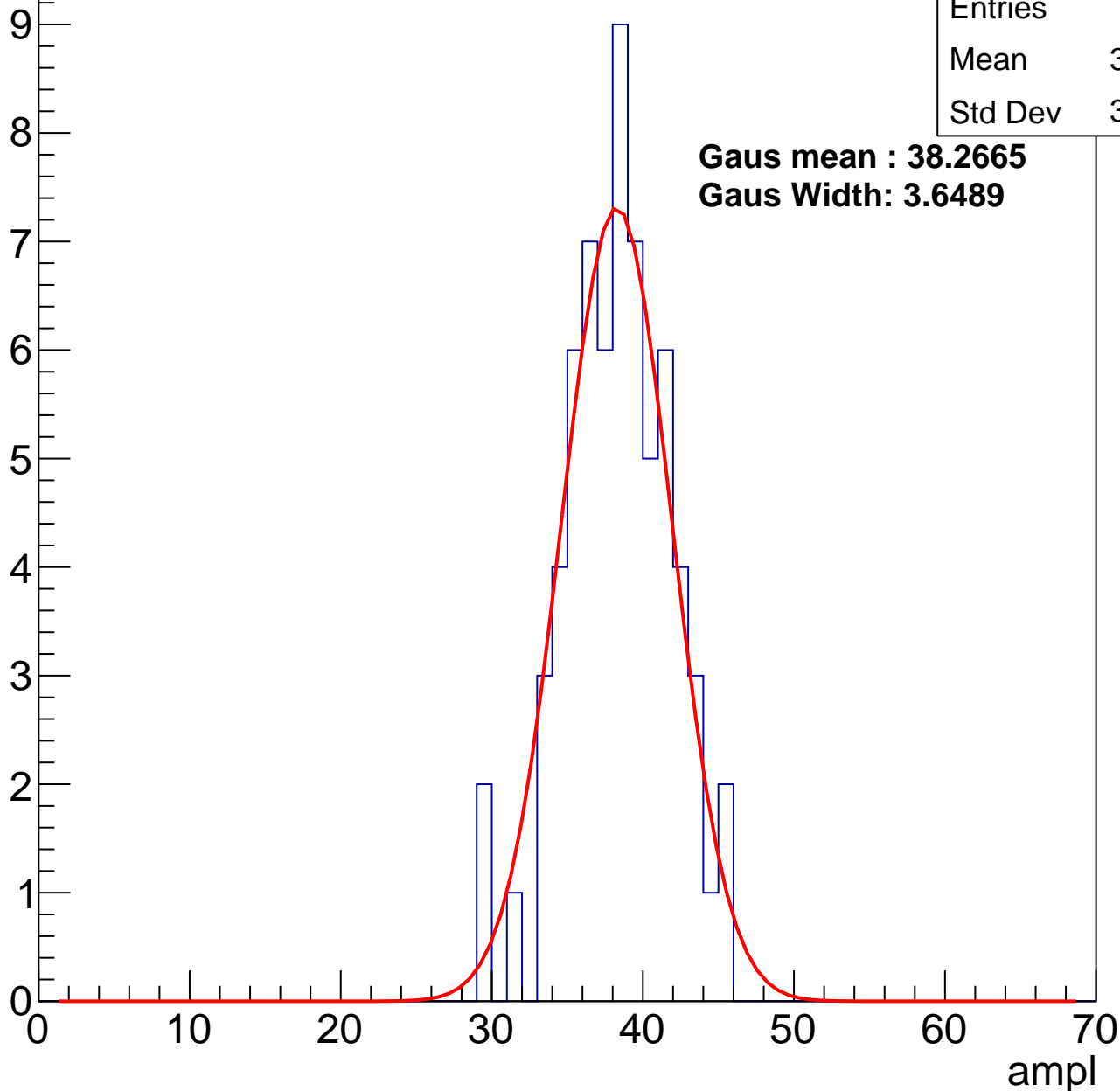
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.88
Std Dev	3.462

**Gaus mean : 38.2665**

**Gaus Width: 3.6489**



# B0L000S, U13-ch76, adc2

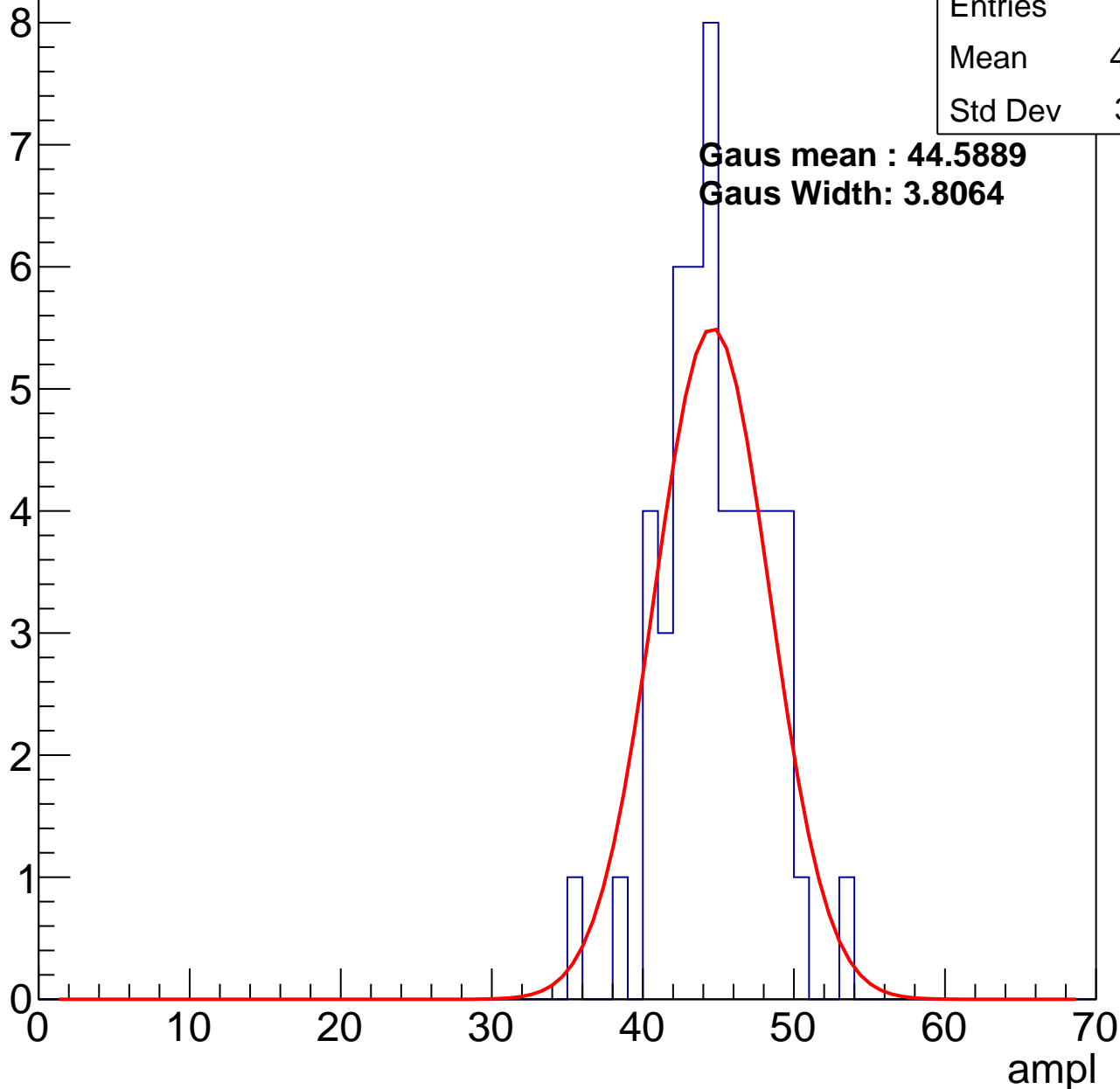
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	44.33
Std Dev	3.341

**Gaus mean : 44.5889**

**Gaus Width: 3.8064**

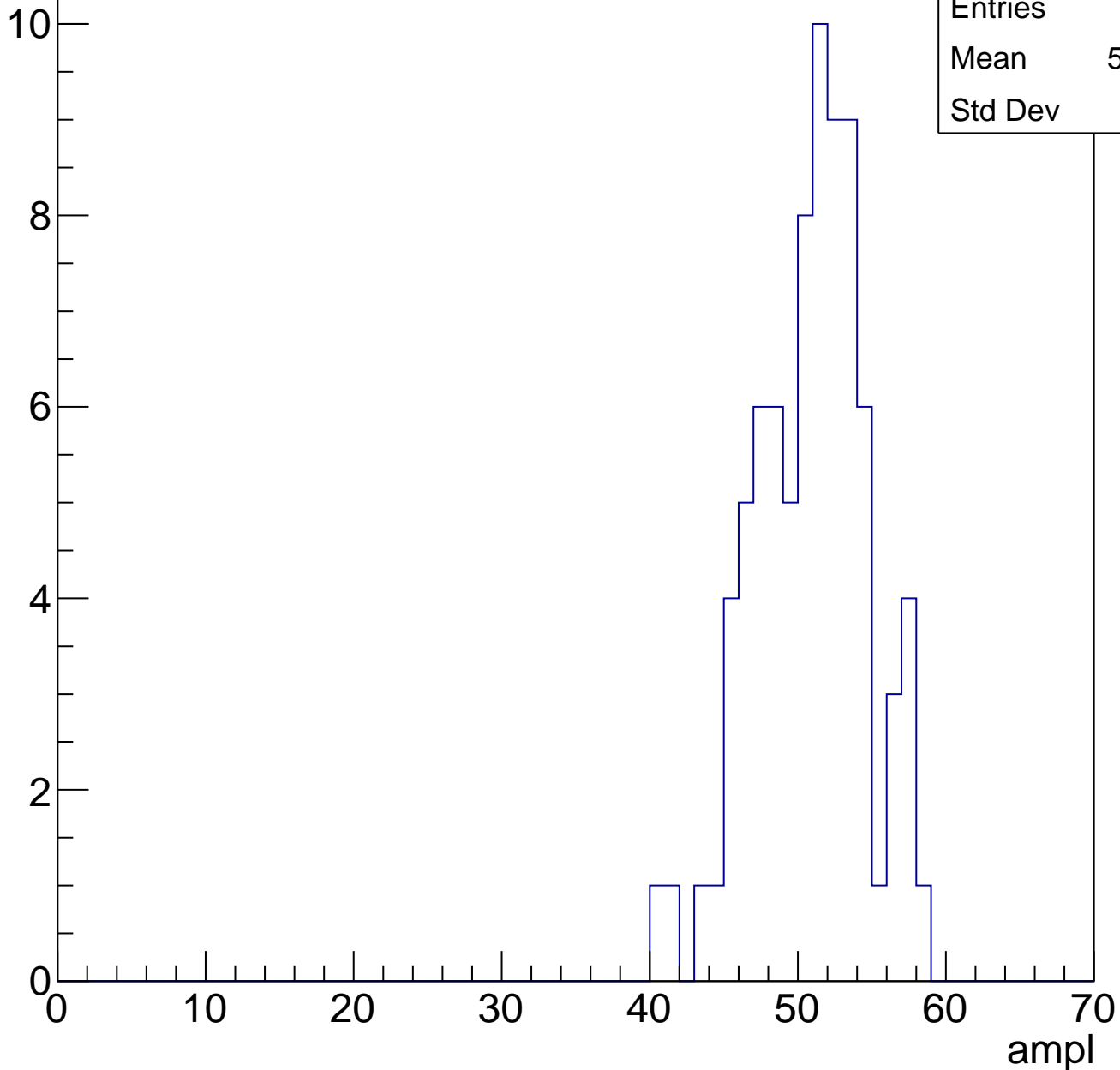


# B0L000S, U13-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	50.38
Std Dev	3.74

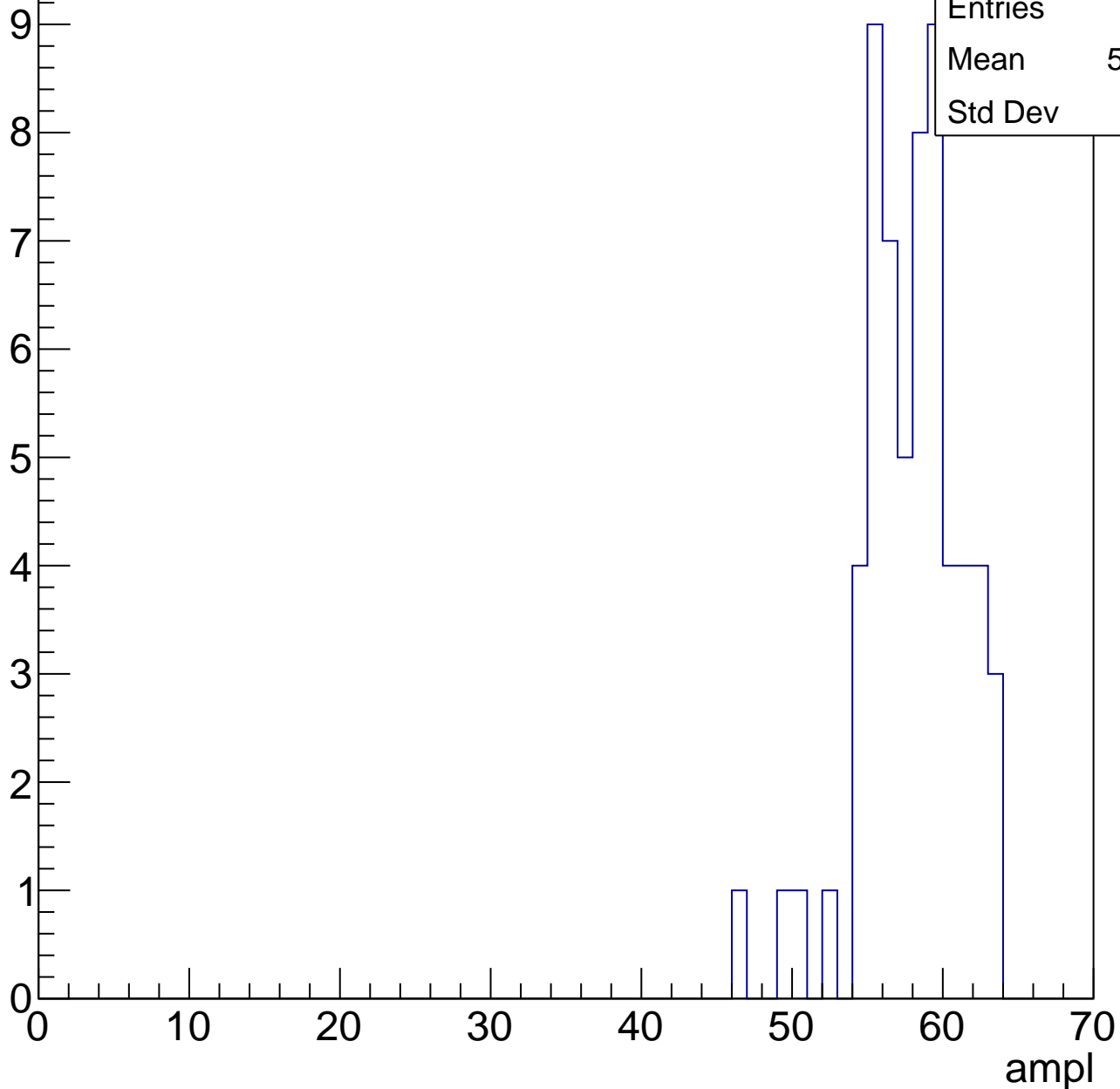
Entry



# B0L000S, U13-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

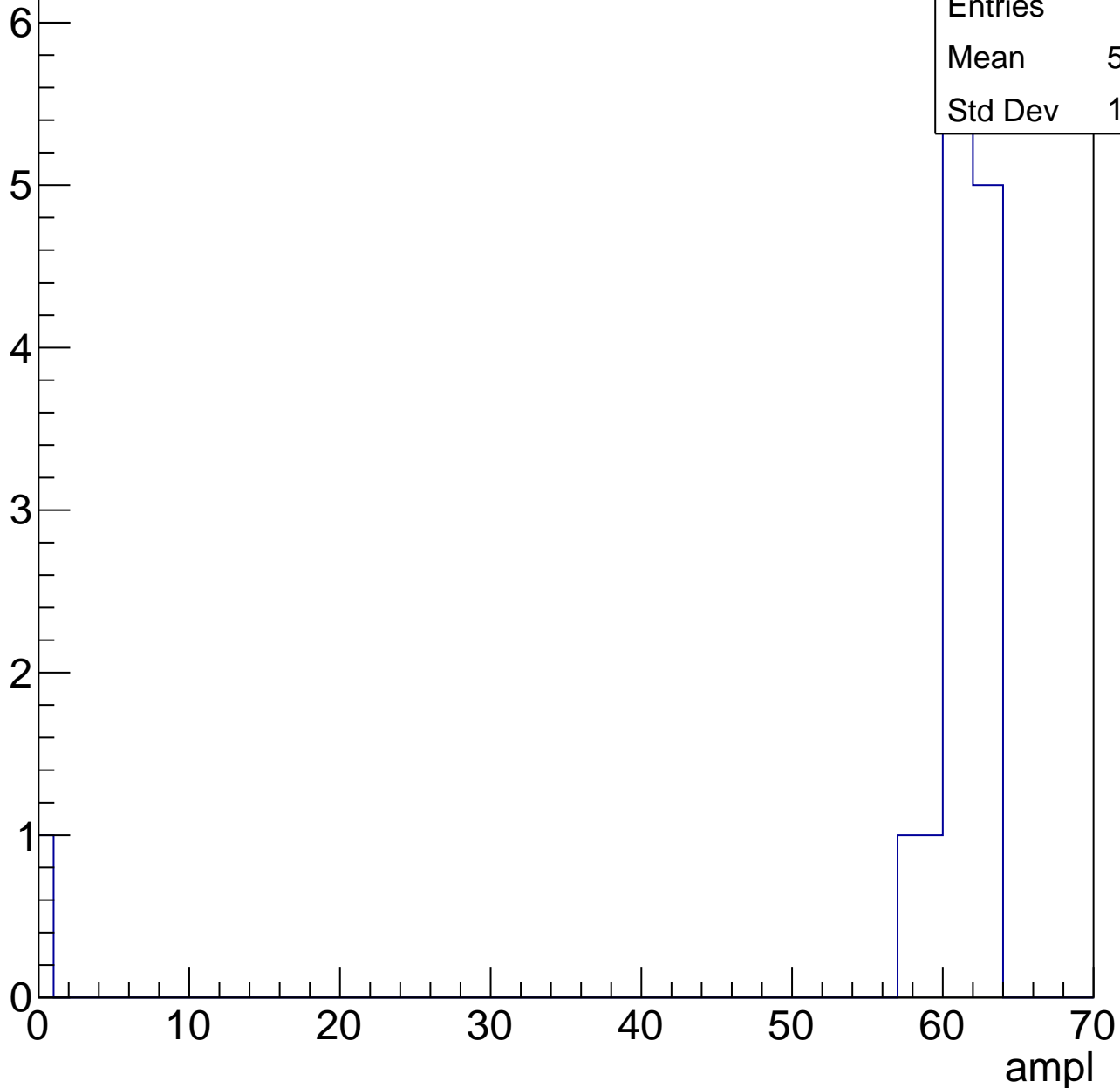
Entry



# B0L000S, U13-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	26
Mean	58.65
Std Dev	11.83

# B0L000S, U13-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch77, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	32.27
Std Dev	5.231

**Gaus mean : 32.8412**

**Gaus Width: 3.6073**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

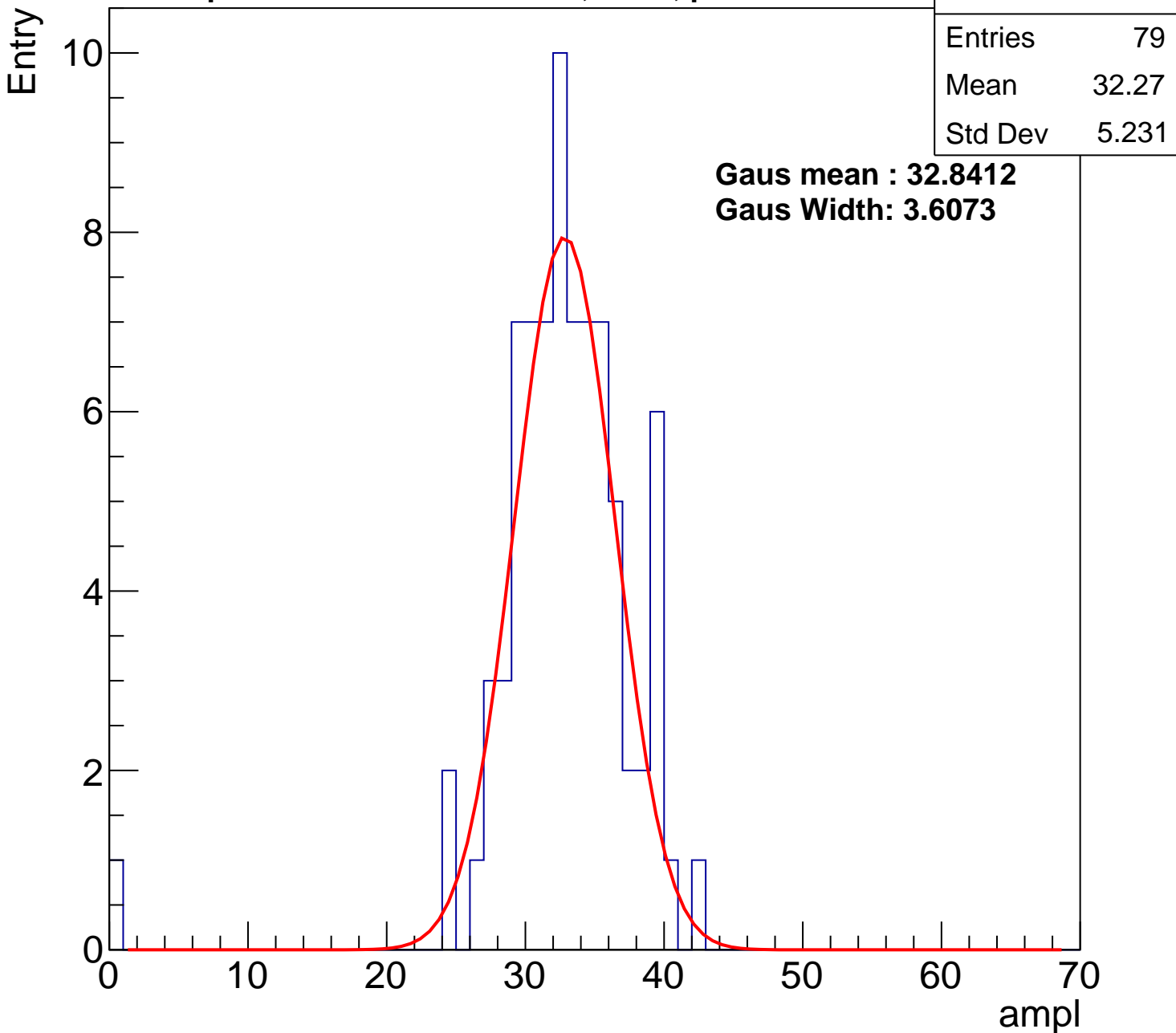
30

40

50

60

70



# B0L000S, U13-ch77, adc1

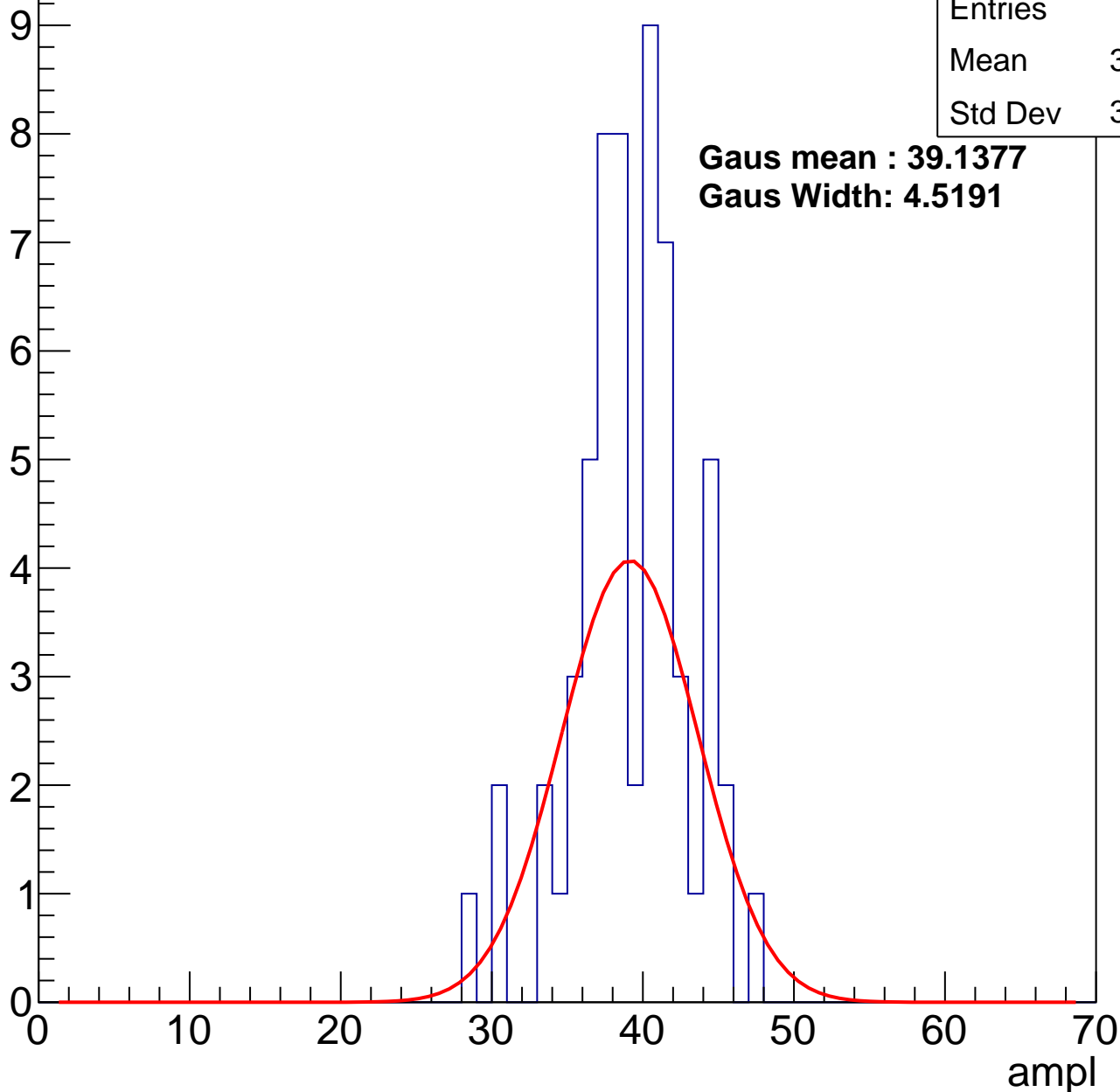
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	38.73
Std Dev	3.763

**Gaus mean : 39.1377**

**Gaus Width: 4.5191**



# B0L000S, U13-ch77, adc2

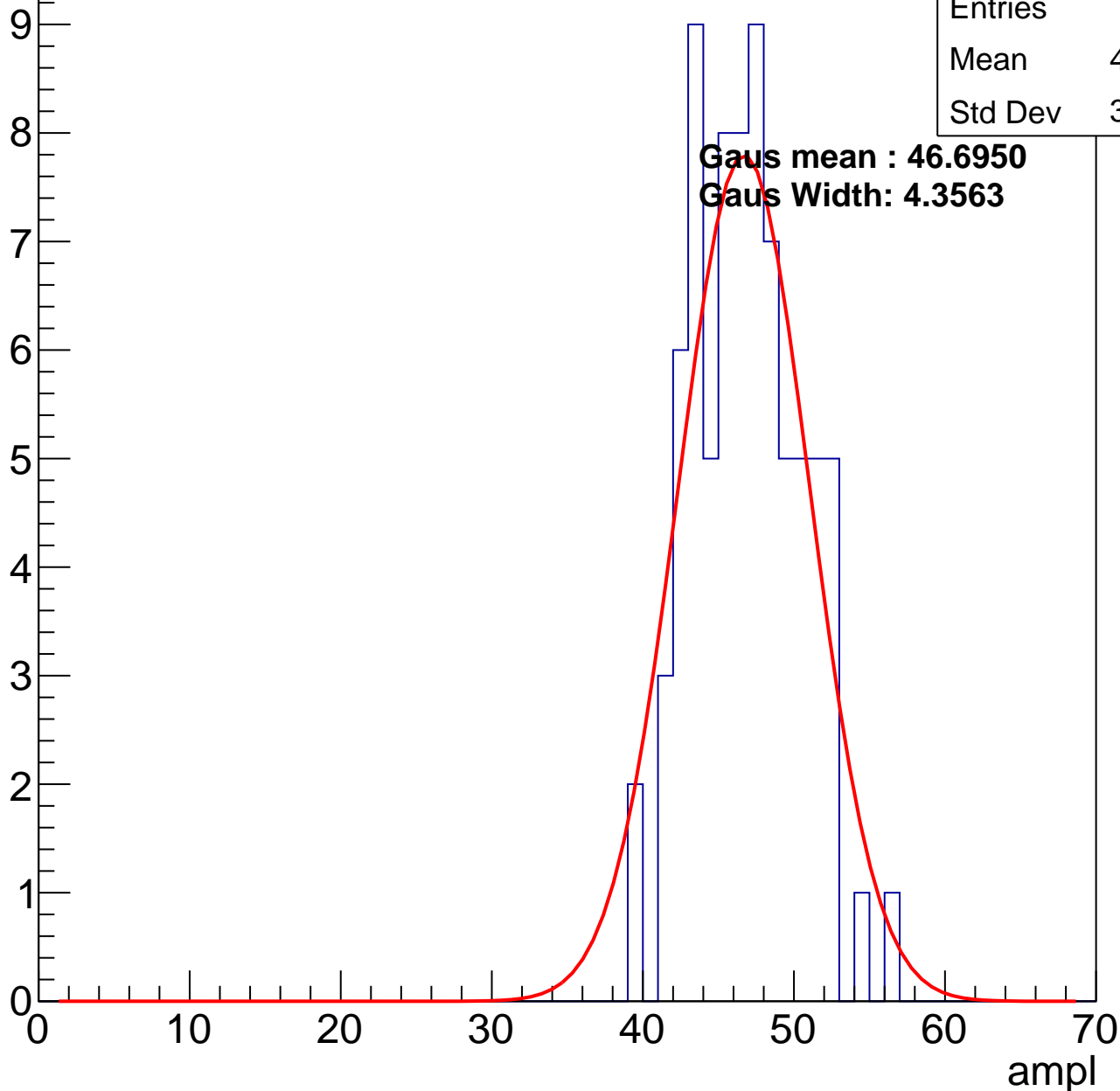
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	46.42
Std Dev	3.549

**Gaus mean : 46.6950**

**Gaus Width: 4.3563**

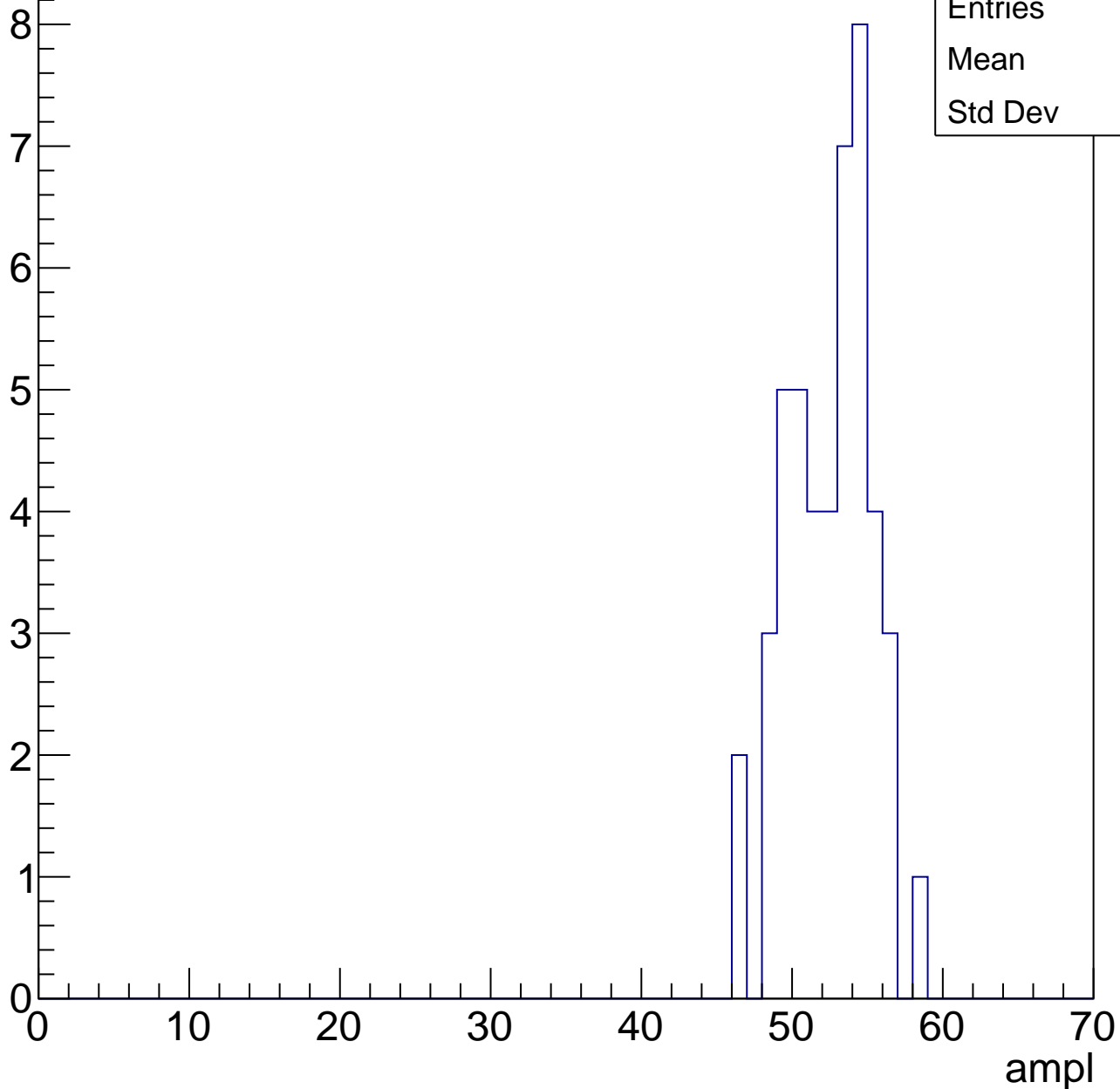


# B0L000S, U13-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	52
Std Dev	2.75

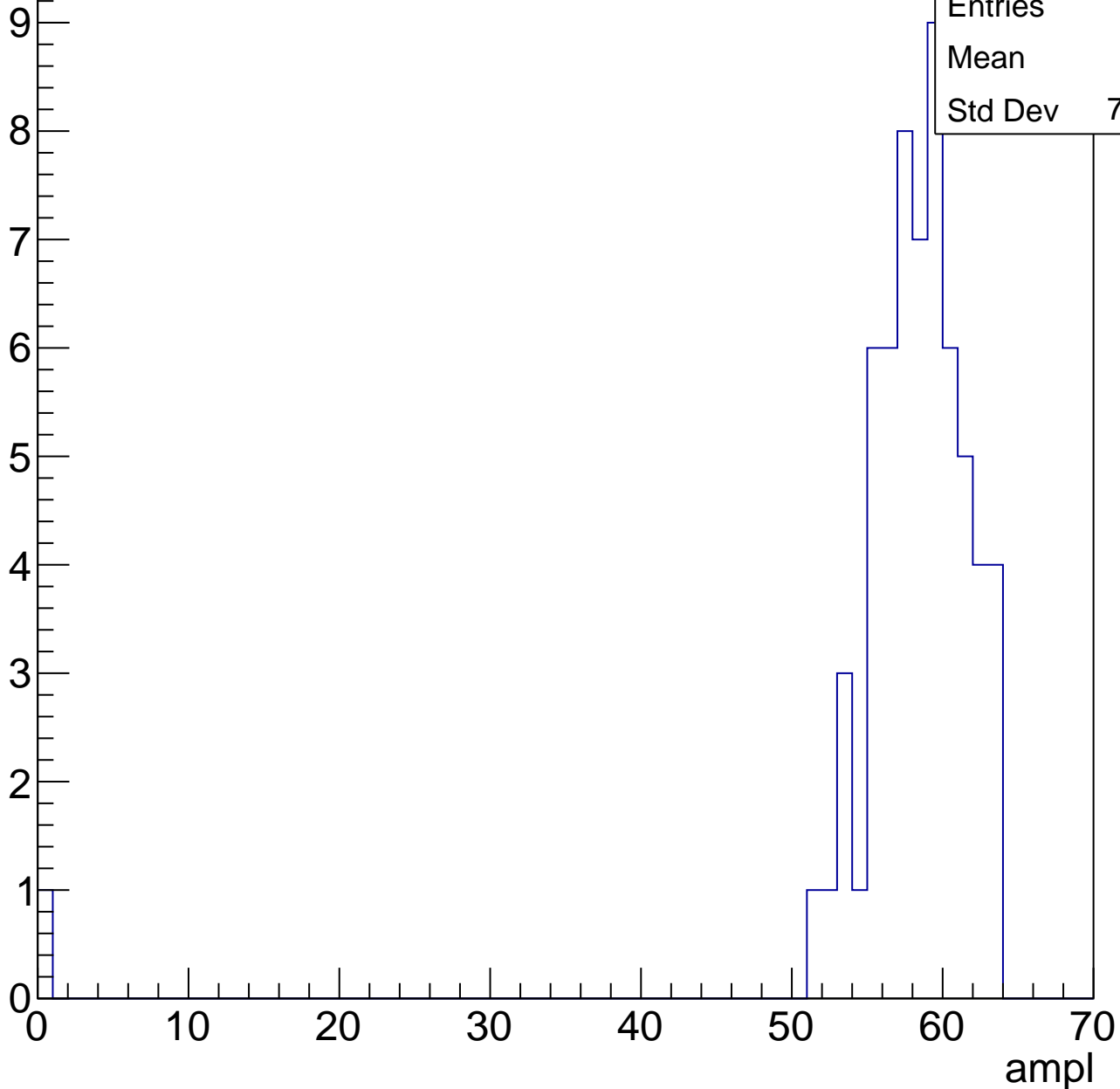


# B0L000S, U13-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

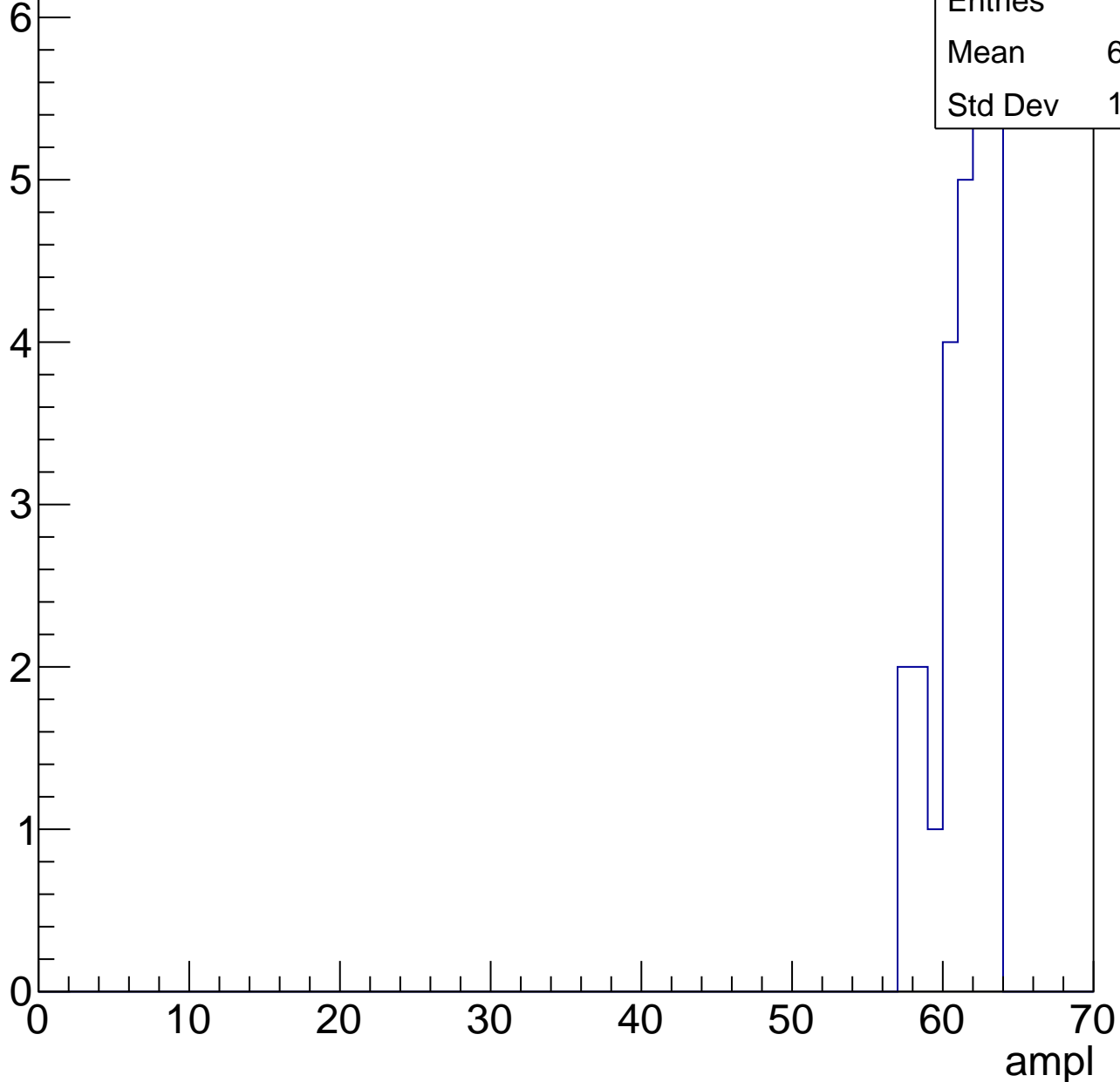
Entries	62
Mean	57.1
Std Dev	7.847



# B0L000S, U13-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch78, adc0

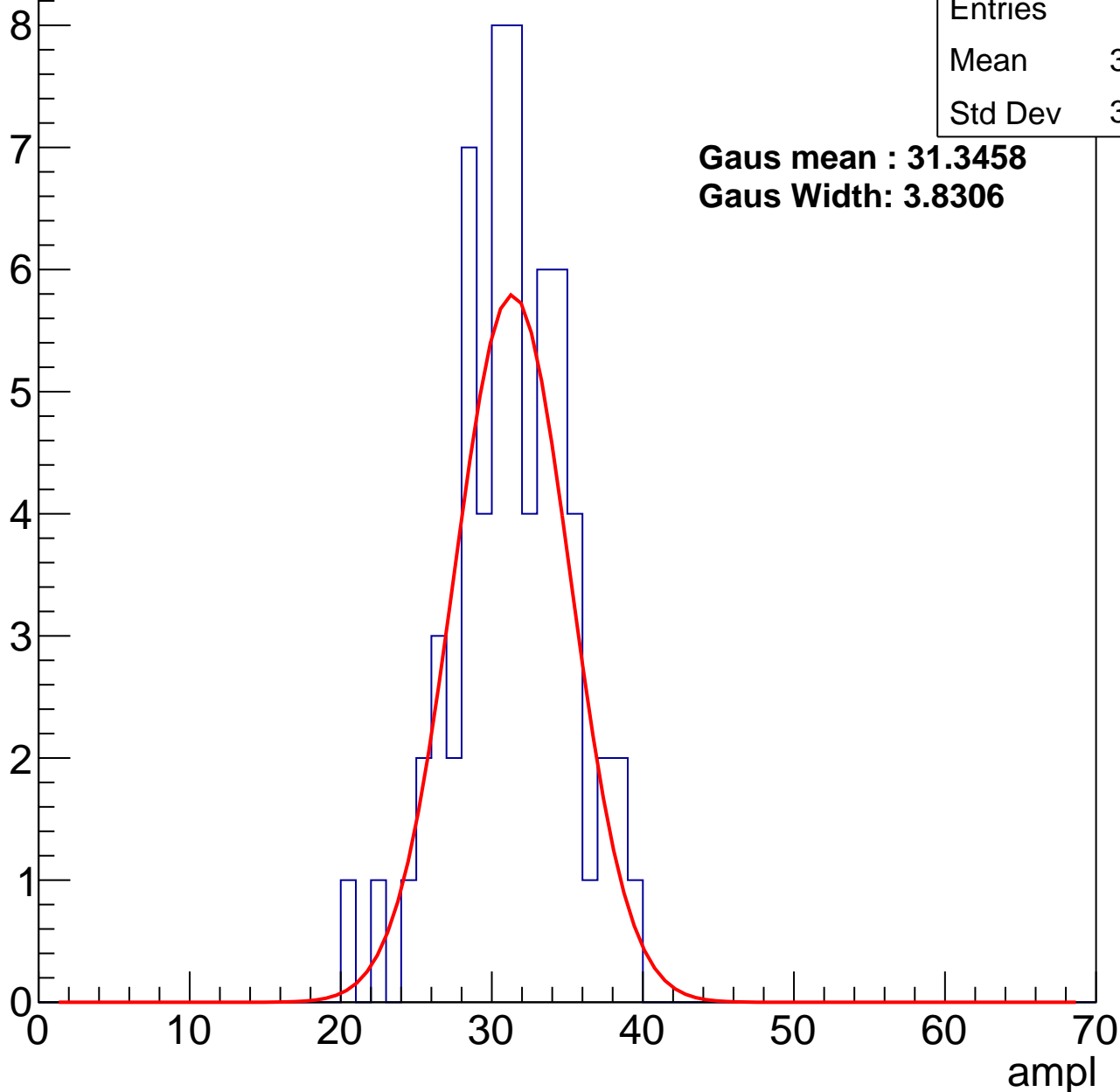
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.84
Std Dev	3.822

**Gaus mean : 31.3458**

**Gaus Width: 3.8306**



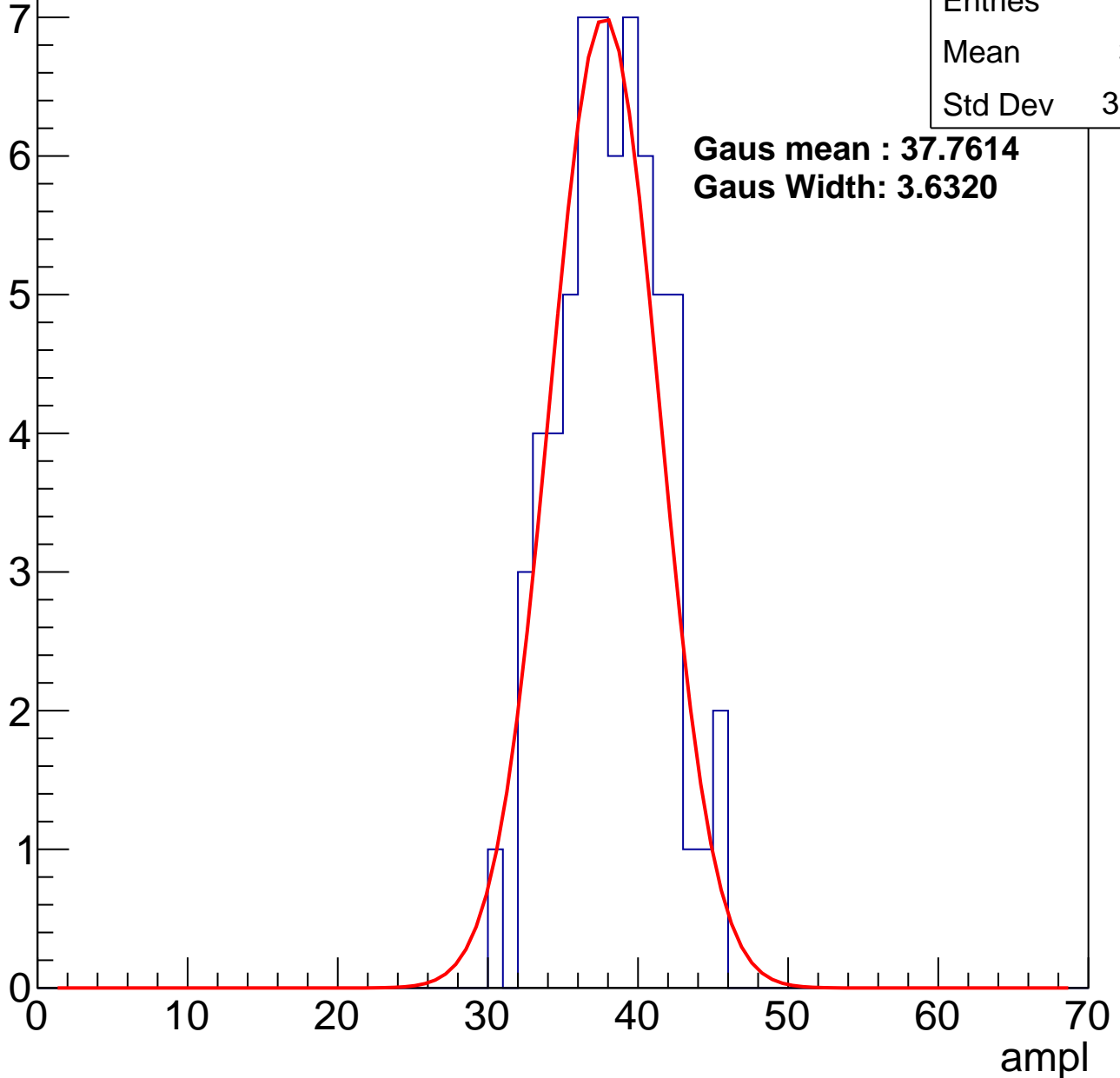
# B0L000S, U13-ch78, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.7
Std Dev	3.367

**Gaus mean : 37.7614**  
**Gaus Width: 3.6320**



# B0L000S, U13-ch78, adc2

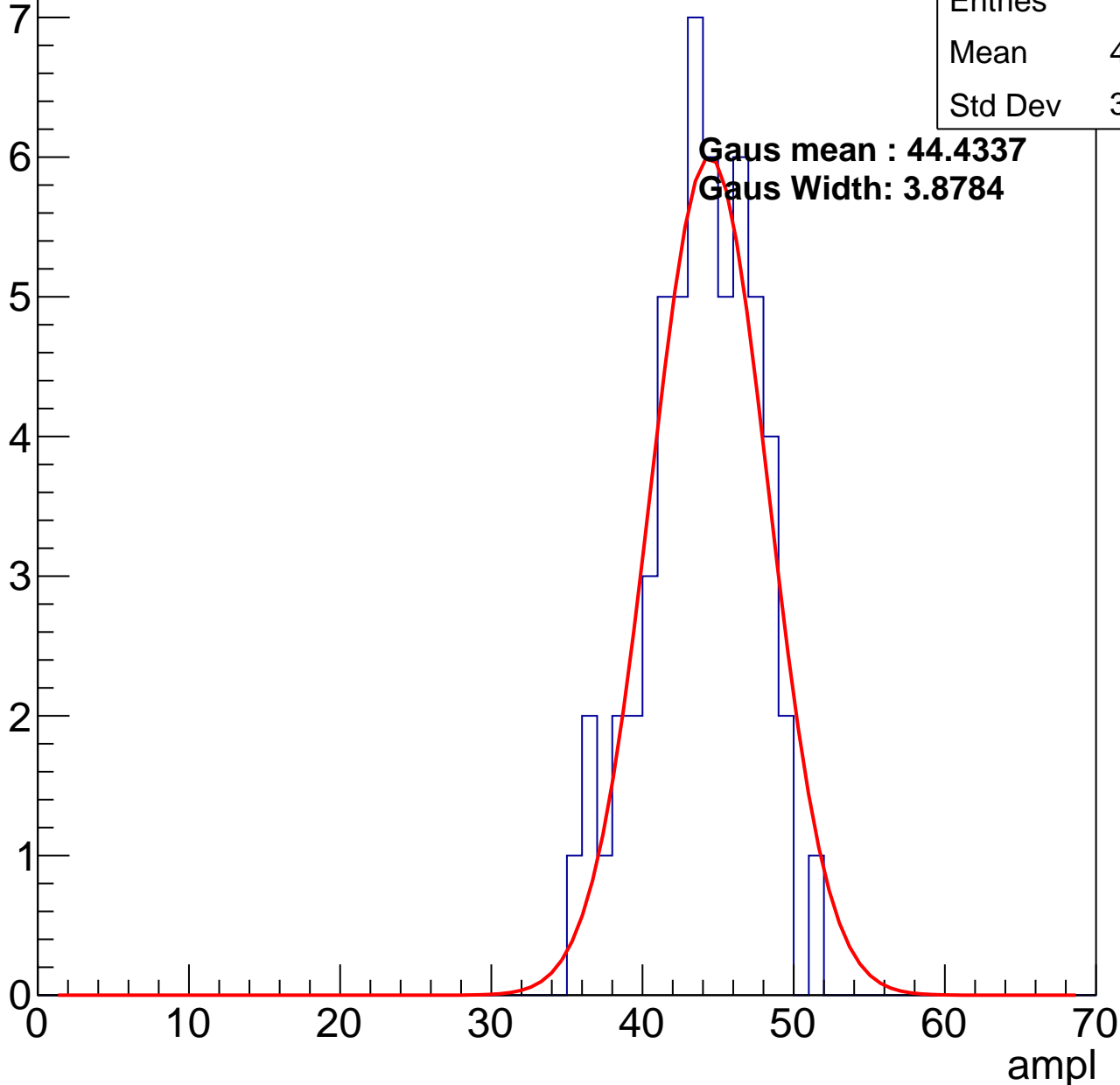
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.42
Std Dev	3.549

**Gaus mean : 44.4337**

**Gaus Width: 3.8784**

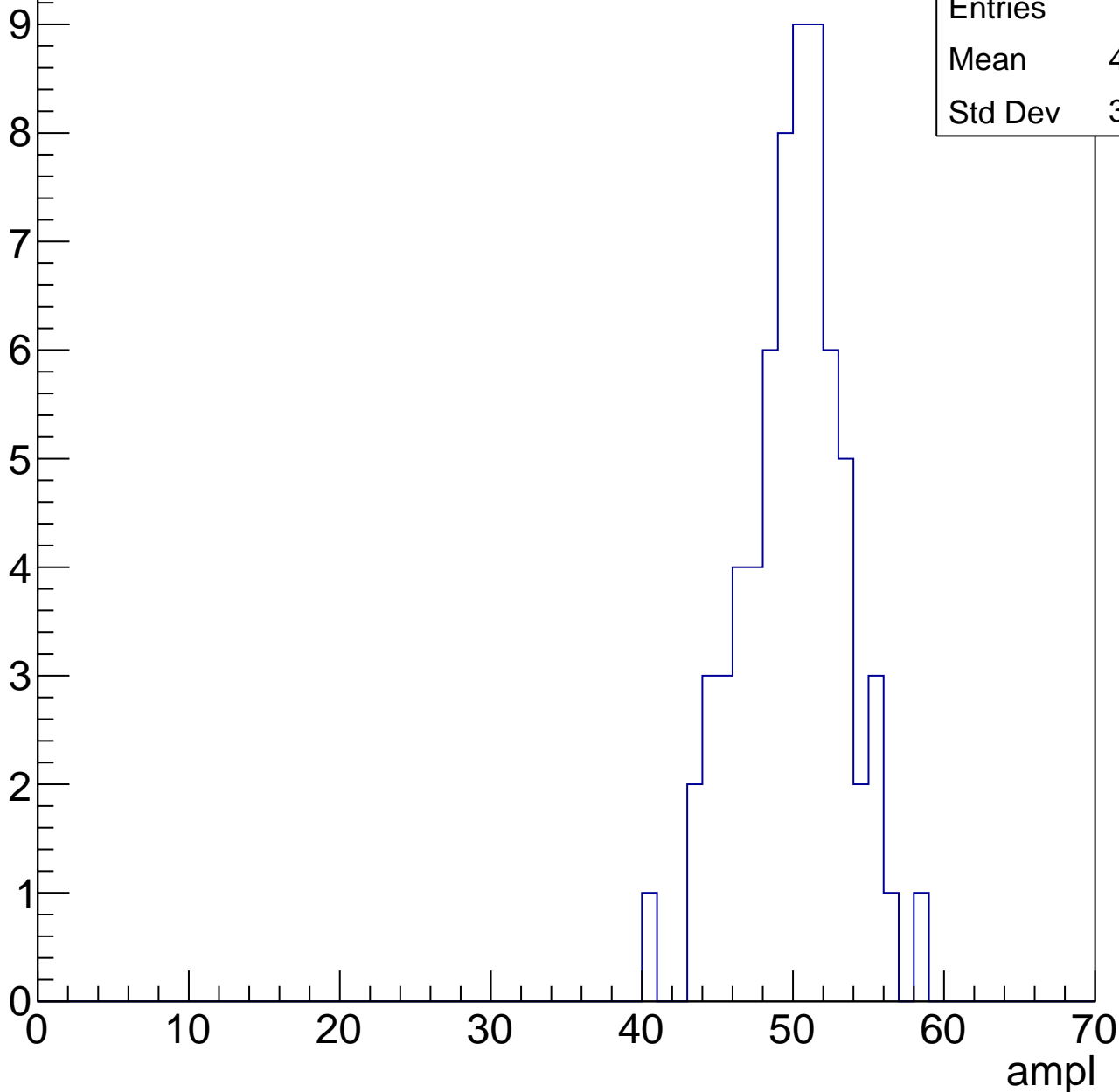


# B0L000S, U13-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.52
Std Dev	3.418

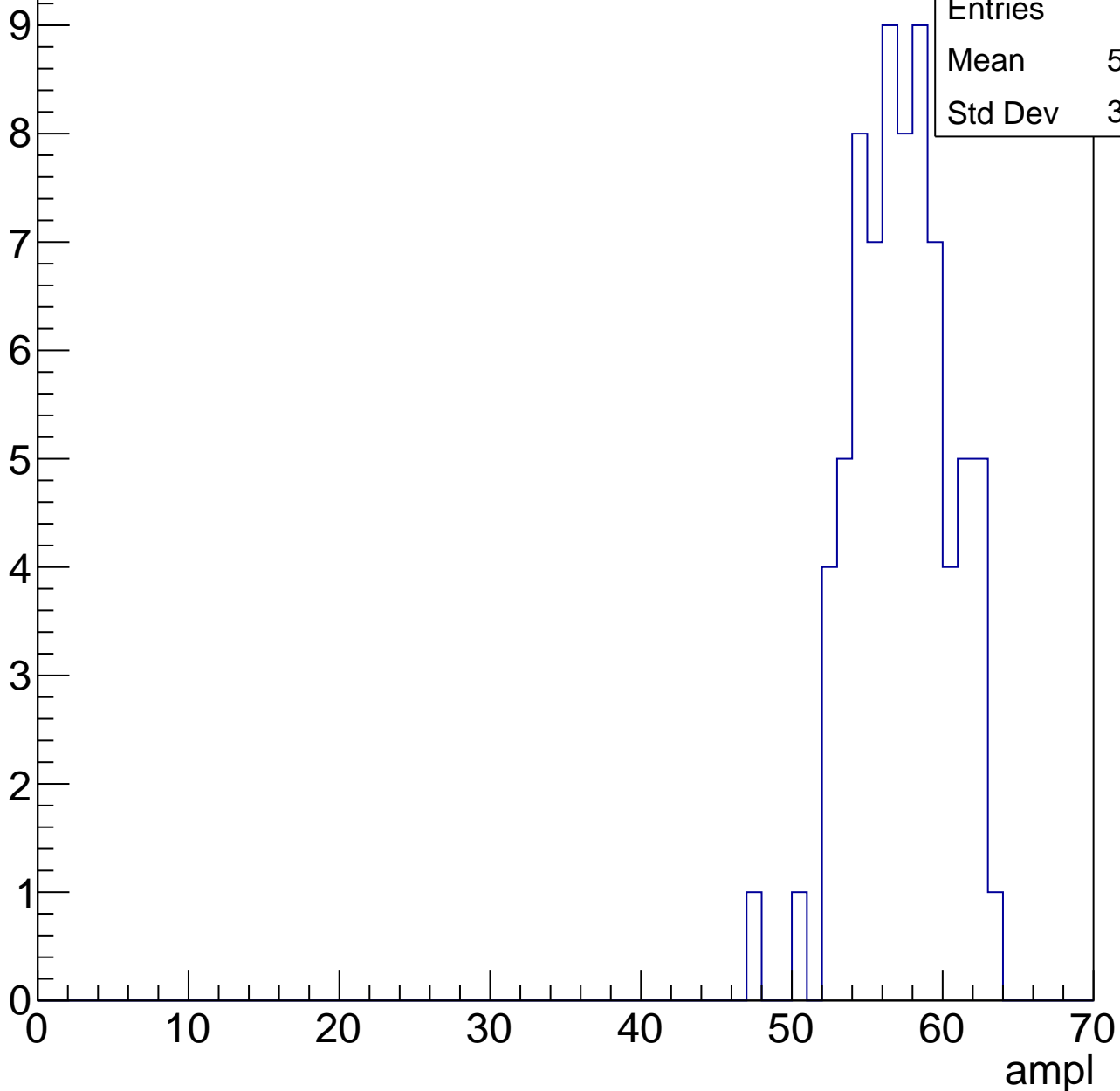


# B0L000S, U13-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

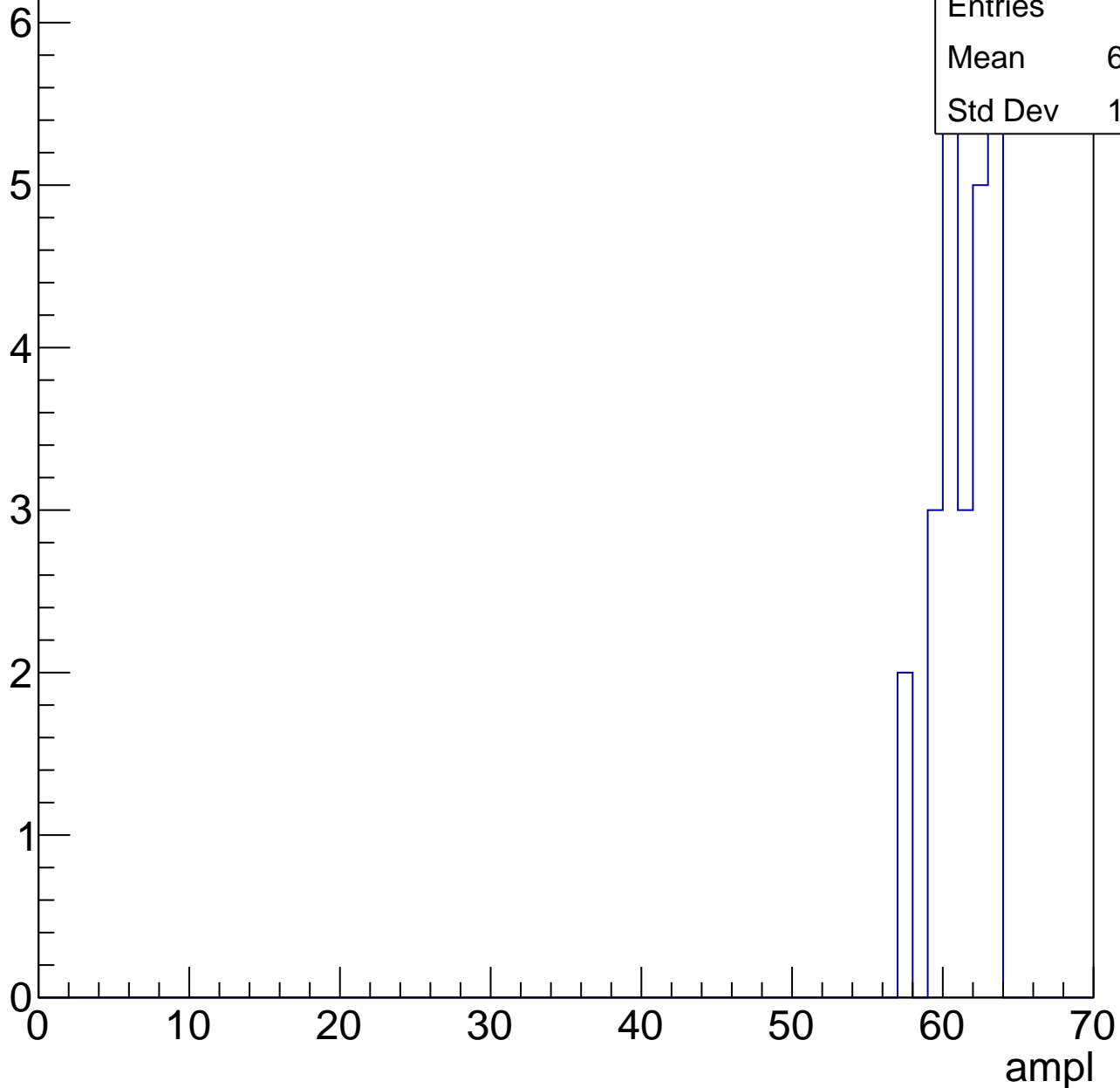
Entries	74
Mean	56.76
Std Dev	3.179



# B0L000S, U13-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

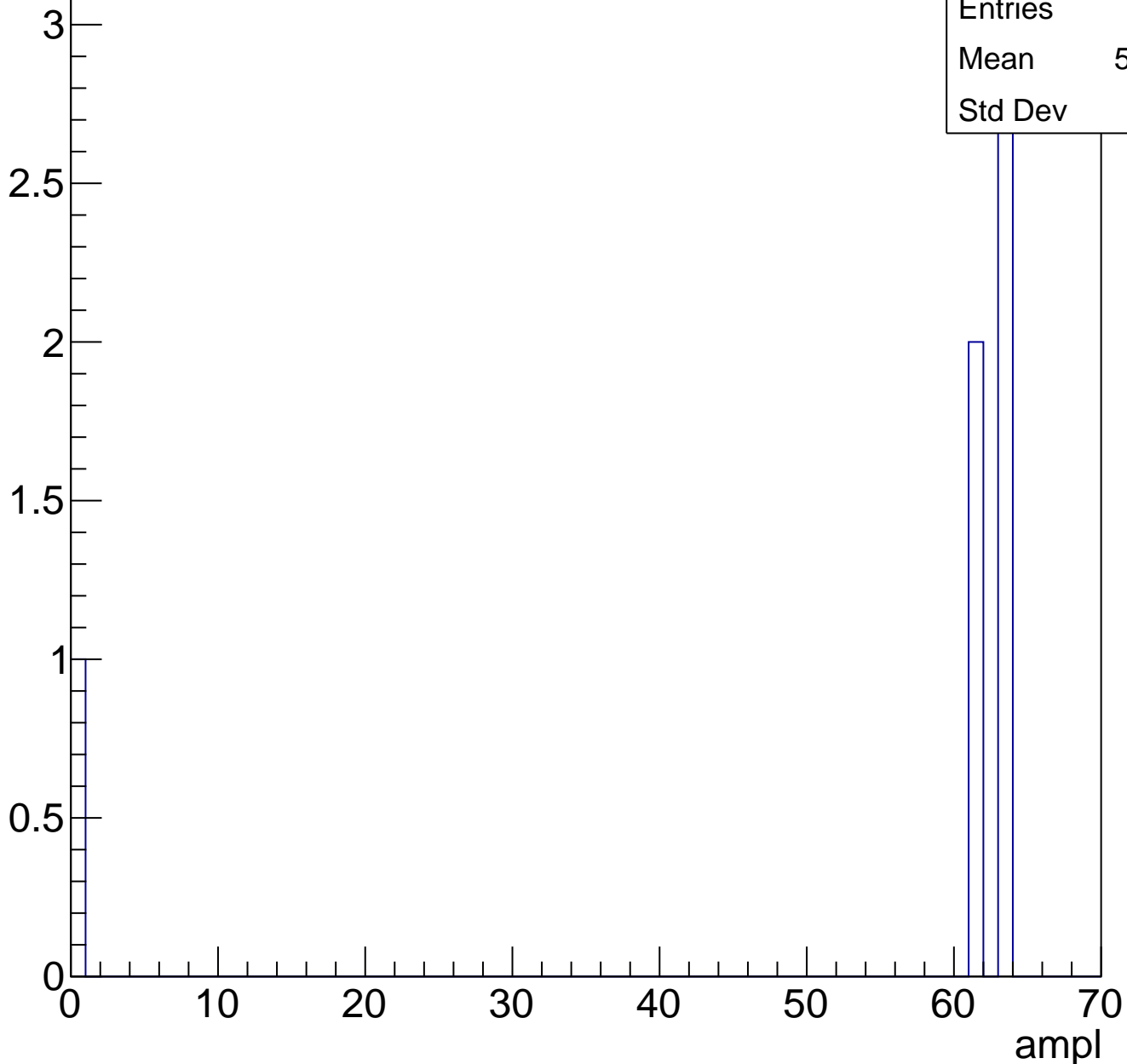
Entry



# B0L000S, U13-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	6
Mean	51.83
Std Dev	23.2



# B0L000S, U13-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch79, adc0

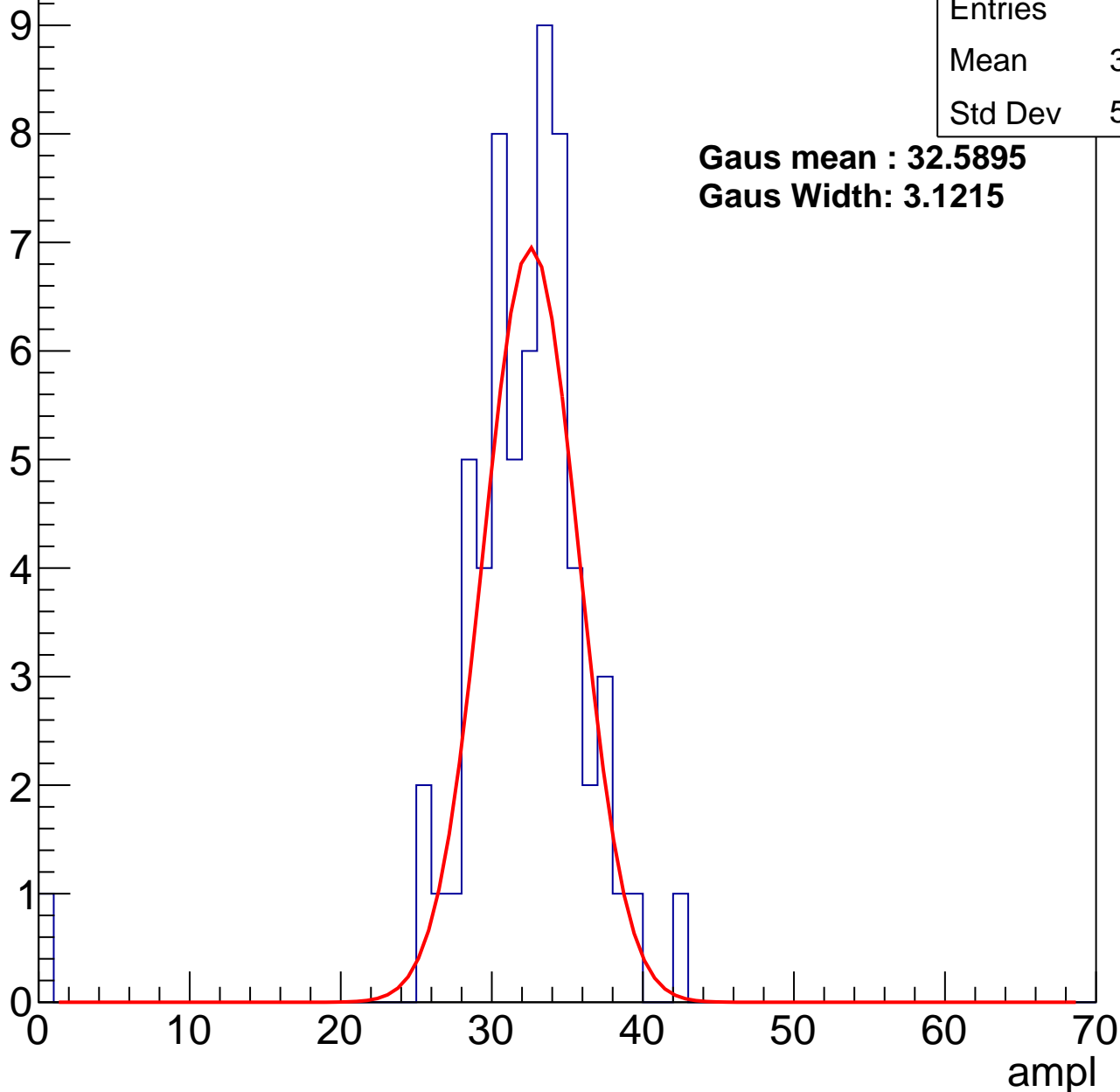
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	31.56
Std Dev	5.226

**Gaus mean : 32.5895**

**Gaus Width: 3.1215**



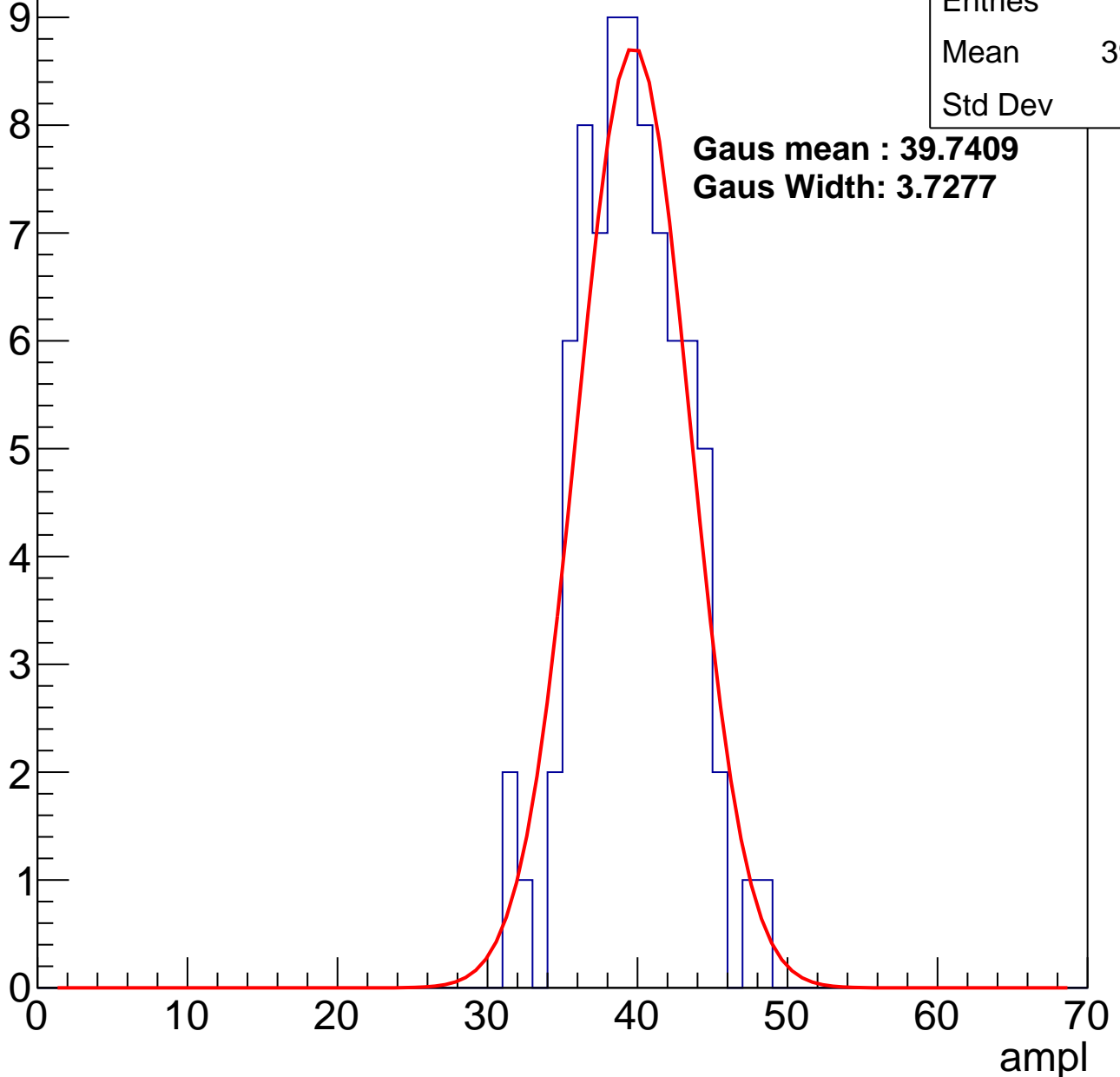
# B0L000S, U13-ch79, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	39.17
Std Dev	3.46

**Gaus mean : 39.7409**  
**Gaus Width: 3.7277**



# B0L000S, U13-ch79, adc2

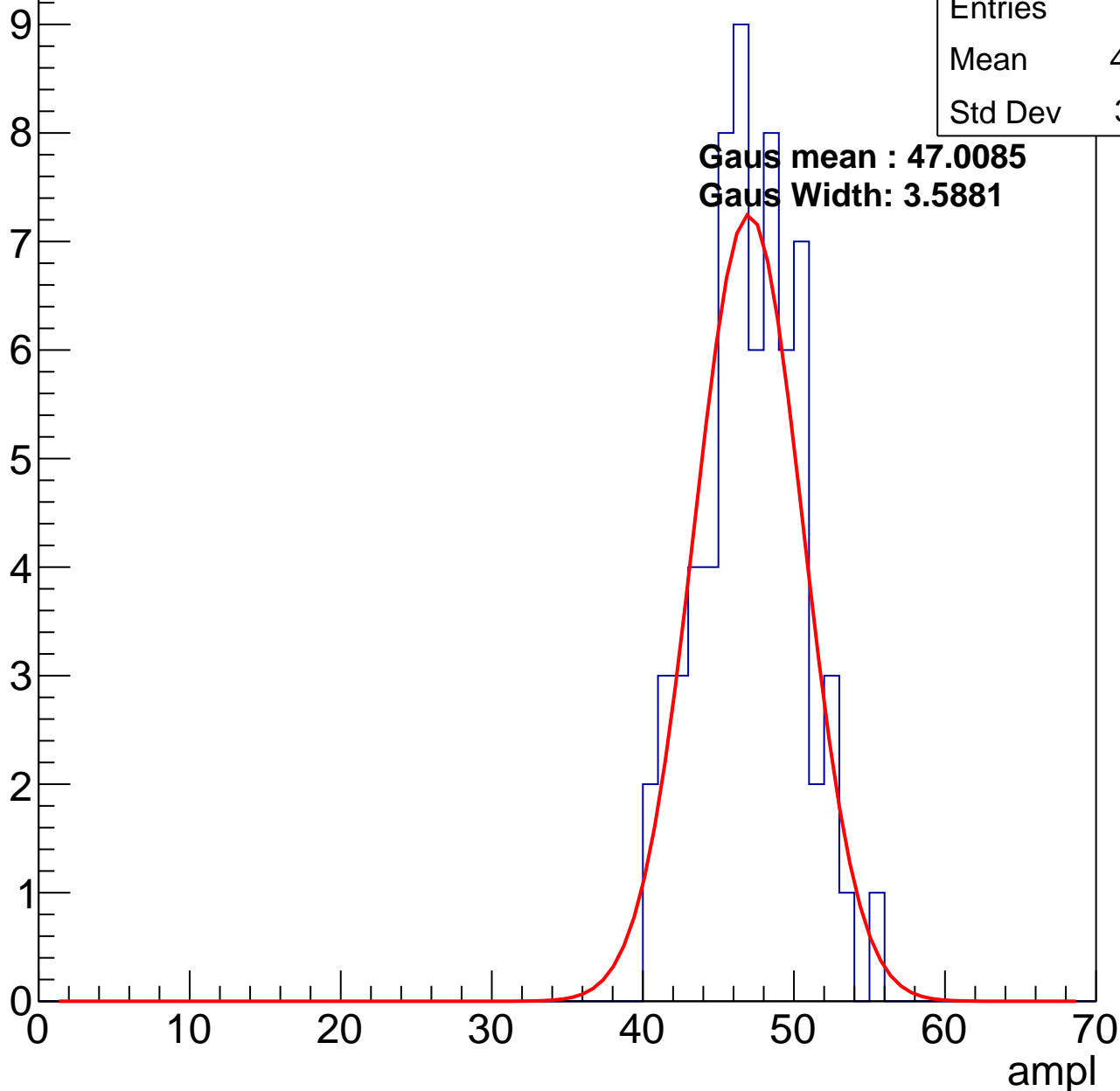
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	46.67
Std Dev	3.271

**Gaus mean : 47.0085**

**Gaus Width: 3.5881**

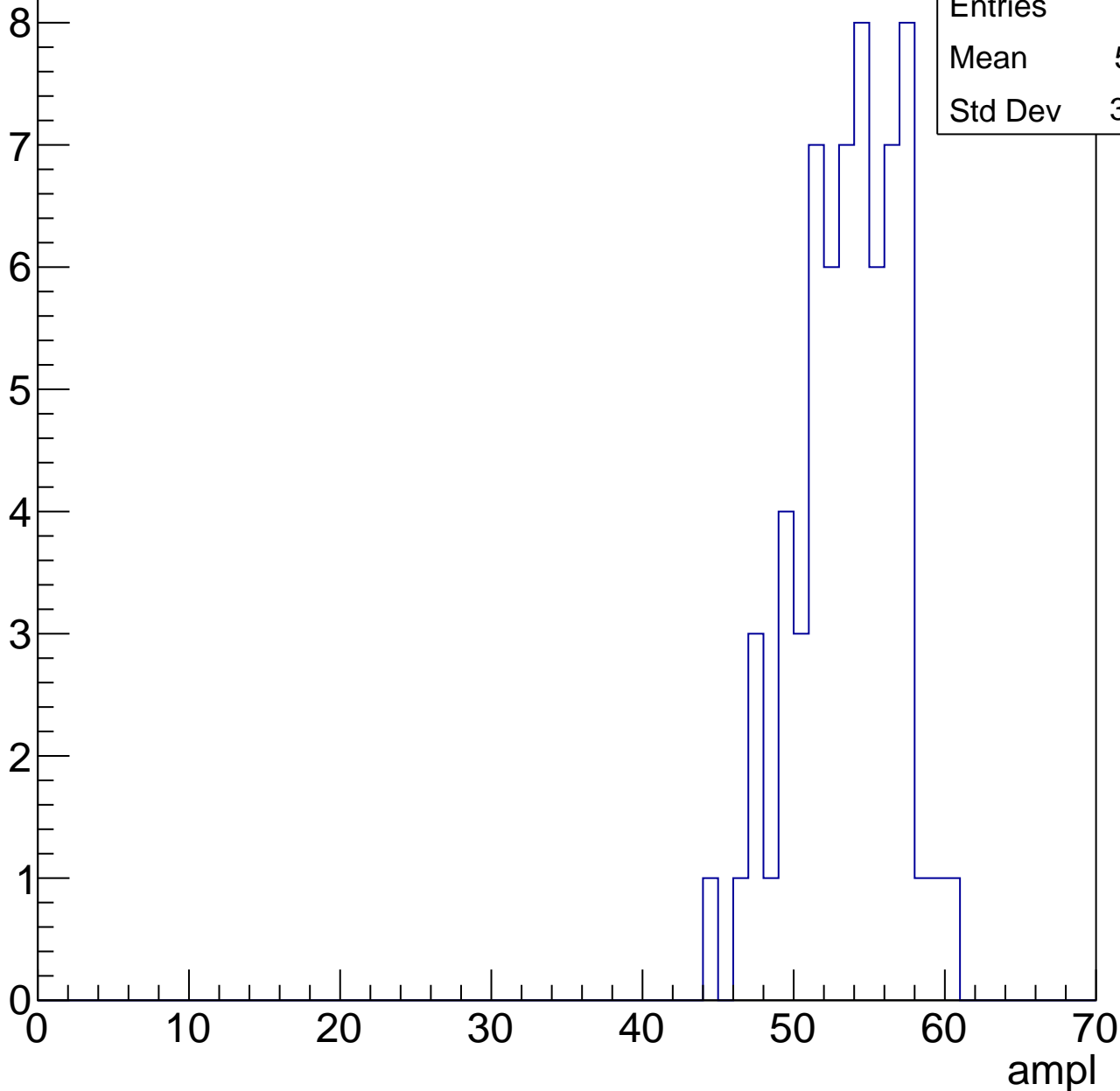


# B0L000S, U13-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

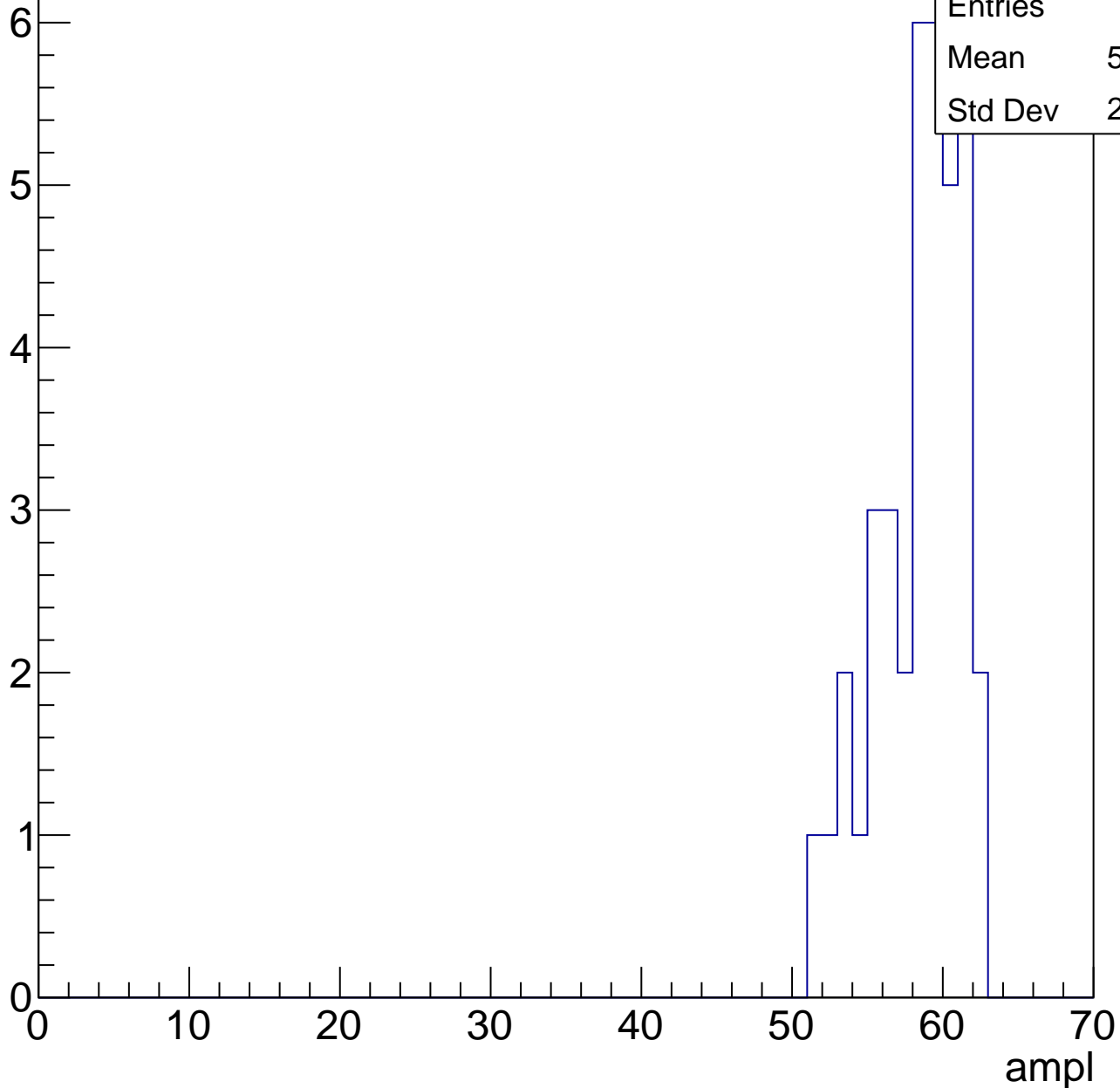
Entries	65
Mean	53.11
Std Dev	3.329



# B0L000S, U13-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

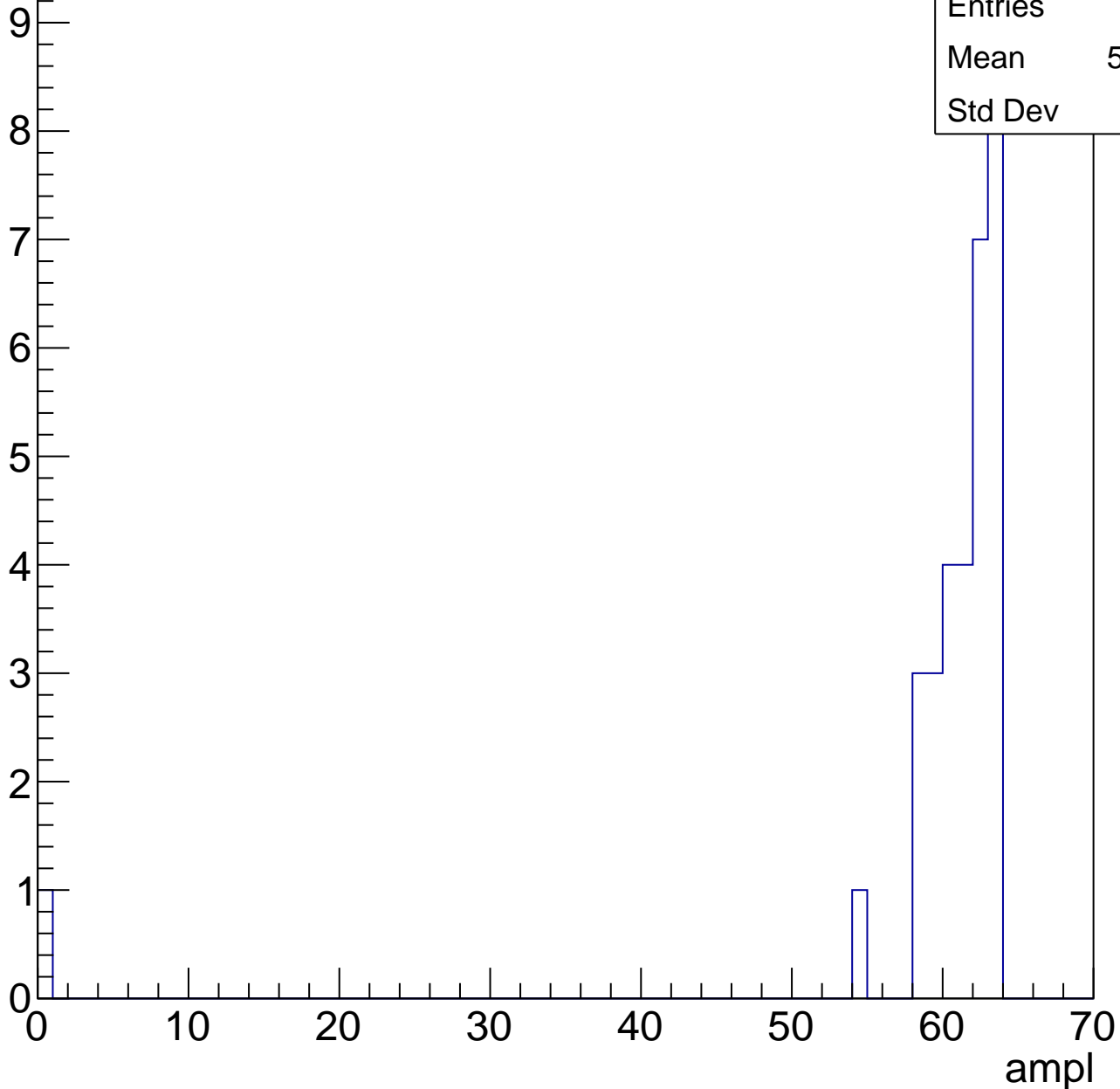


# B0L000S, U13-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	59.06
Std Dev	10.8



# B0L000S, U13-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch80, adc0

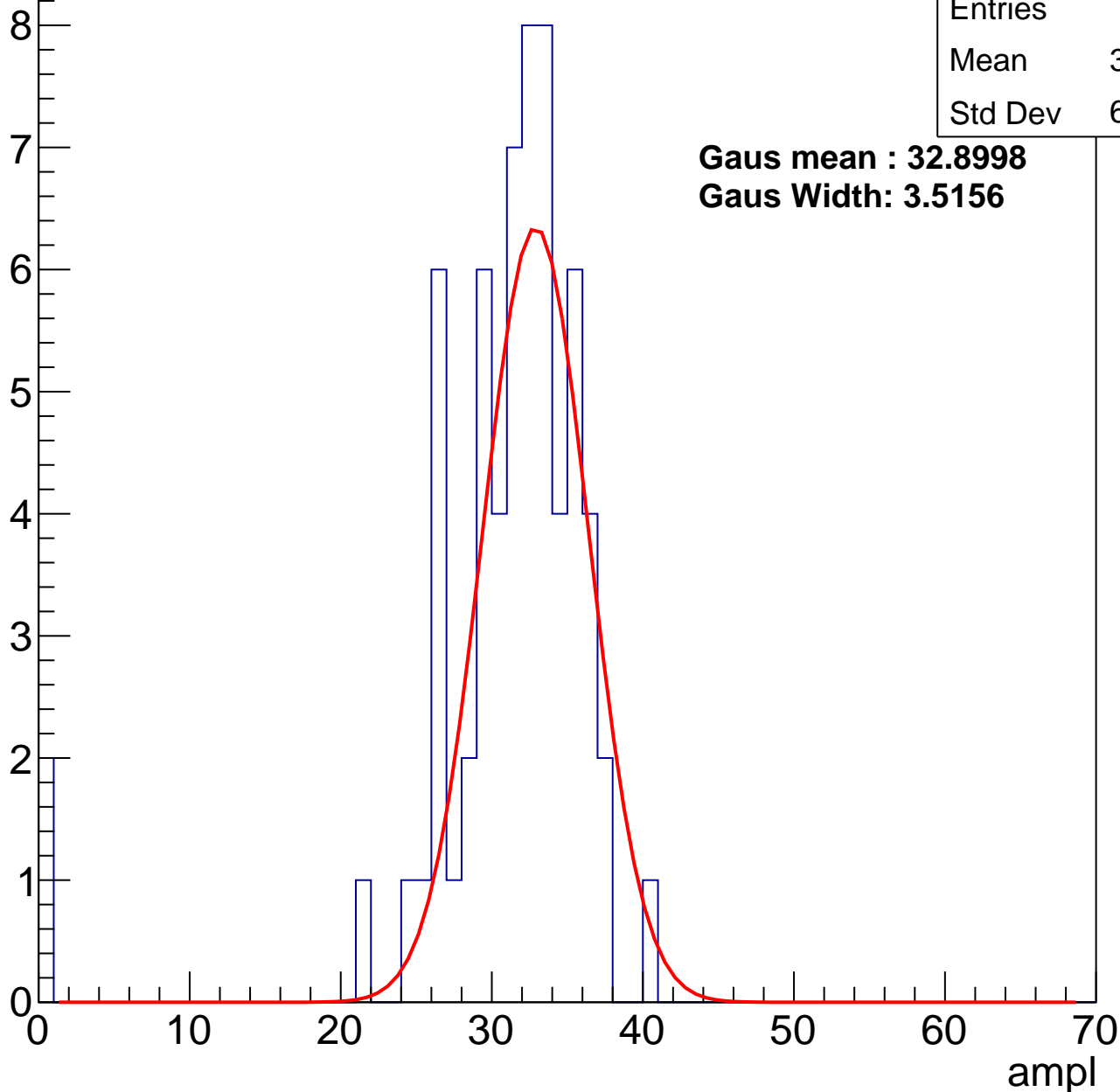
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.38
Std Dev	6.518

**Gaus mean : 32.8998**

**Gaus Width: 3.5156**



# B0L000S, U13-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	38.52
Std Dev	3.091

**Gaus mean : 39.5251**

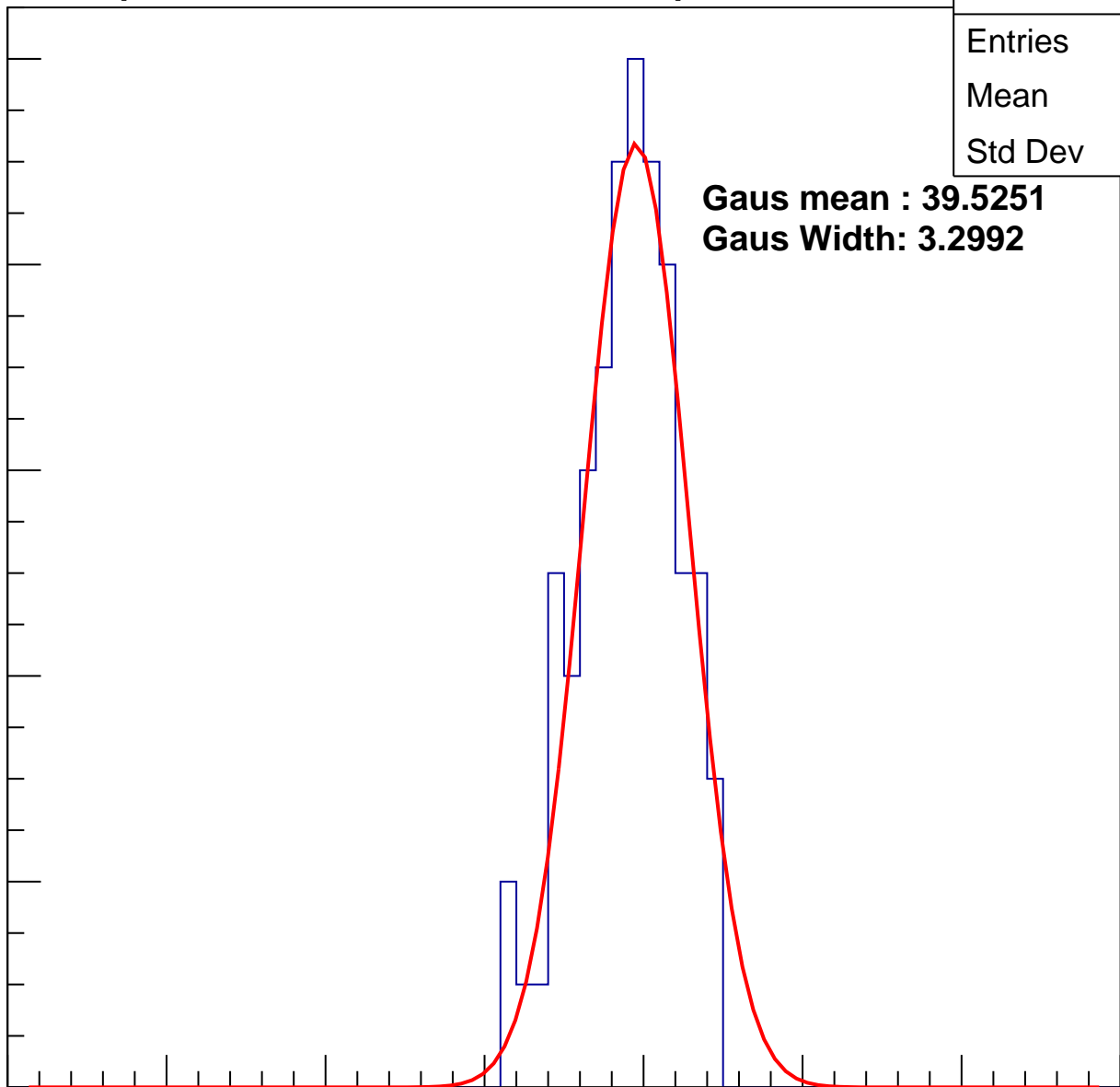
**Gaus Width: 3.2992**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch80, adc2

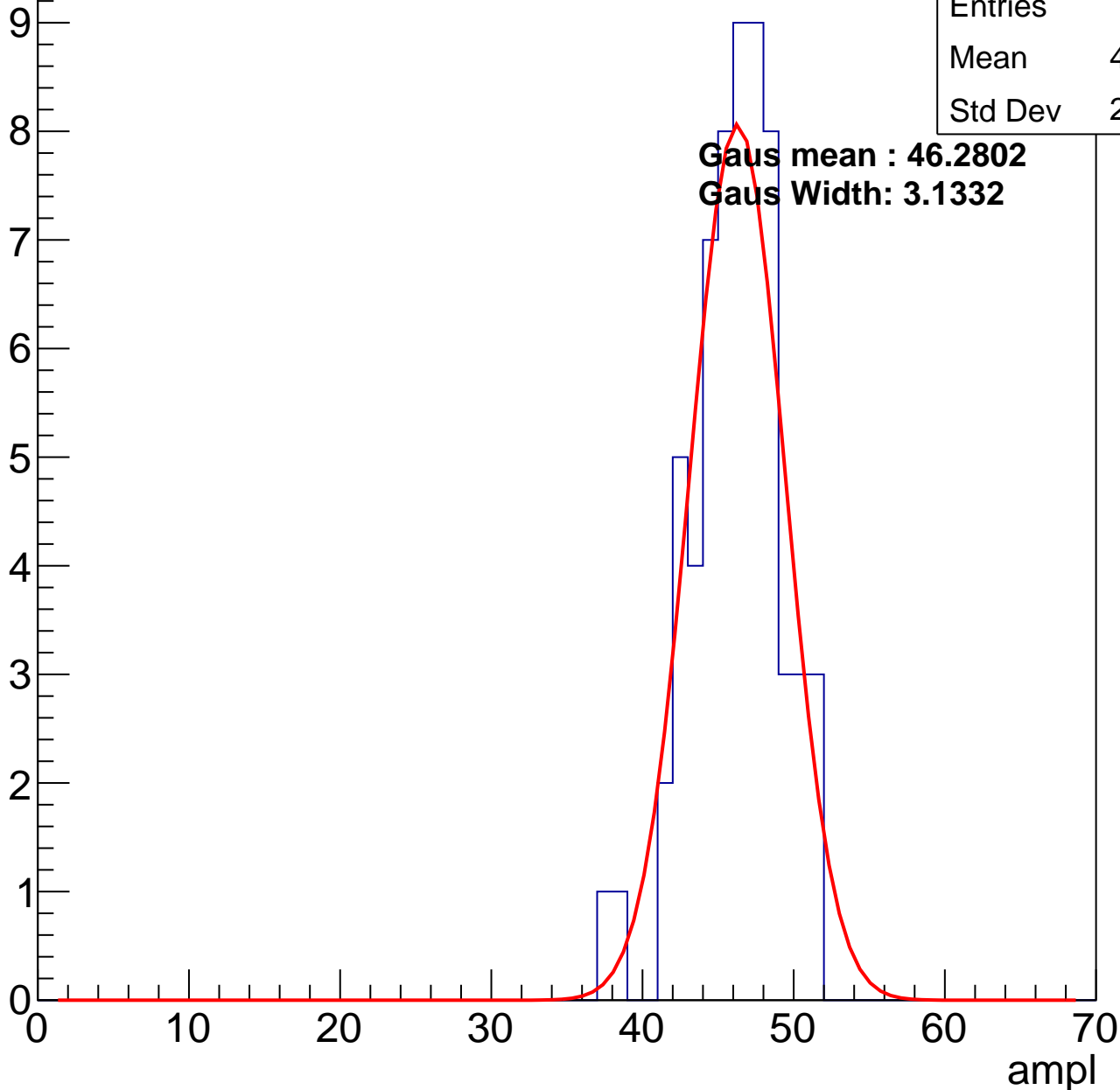
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	45.68
Std Dev	2.905

**Gaus mean : 46.2802**

**Gaus Width: 3.1332**

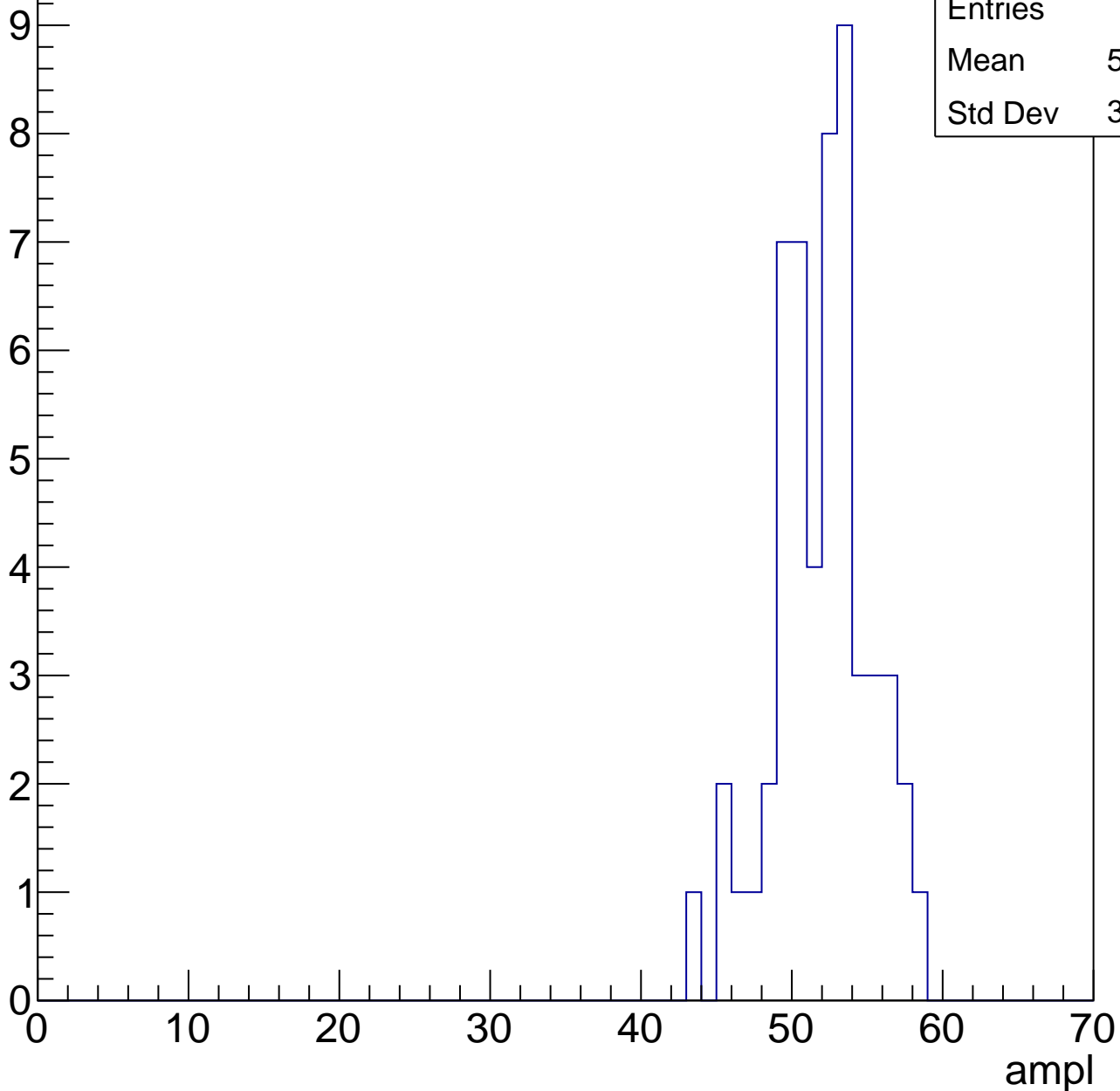


# B0L000S, U13-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

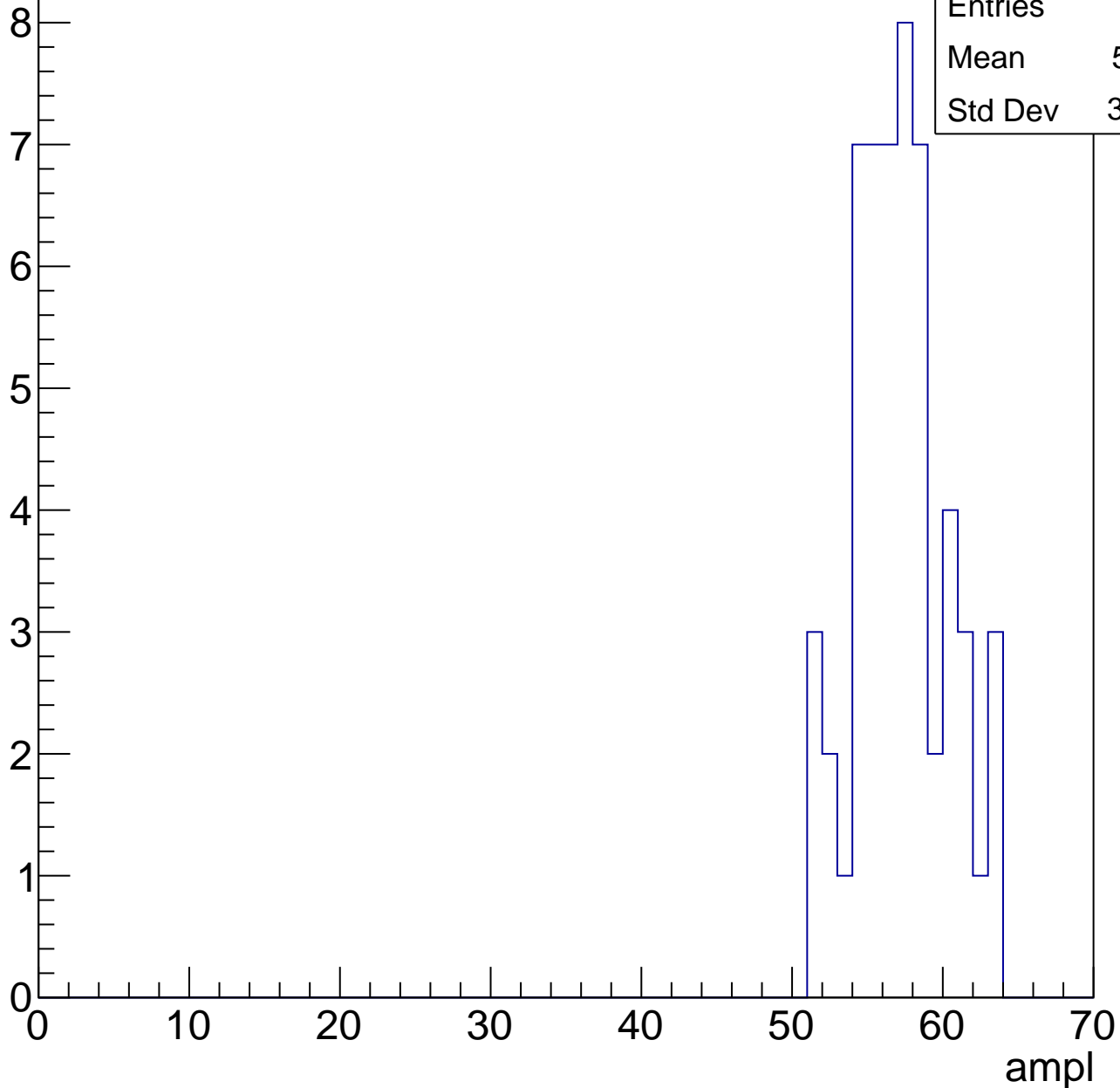
Entries	54
Mean	51.46
Std Dev	3.143



# B0L000S, U13-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

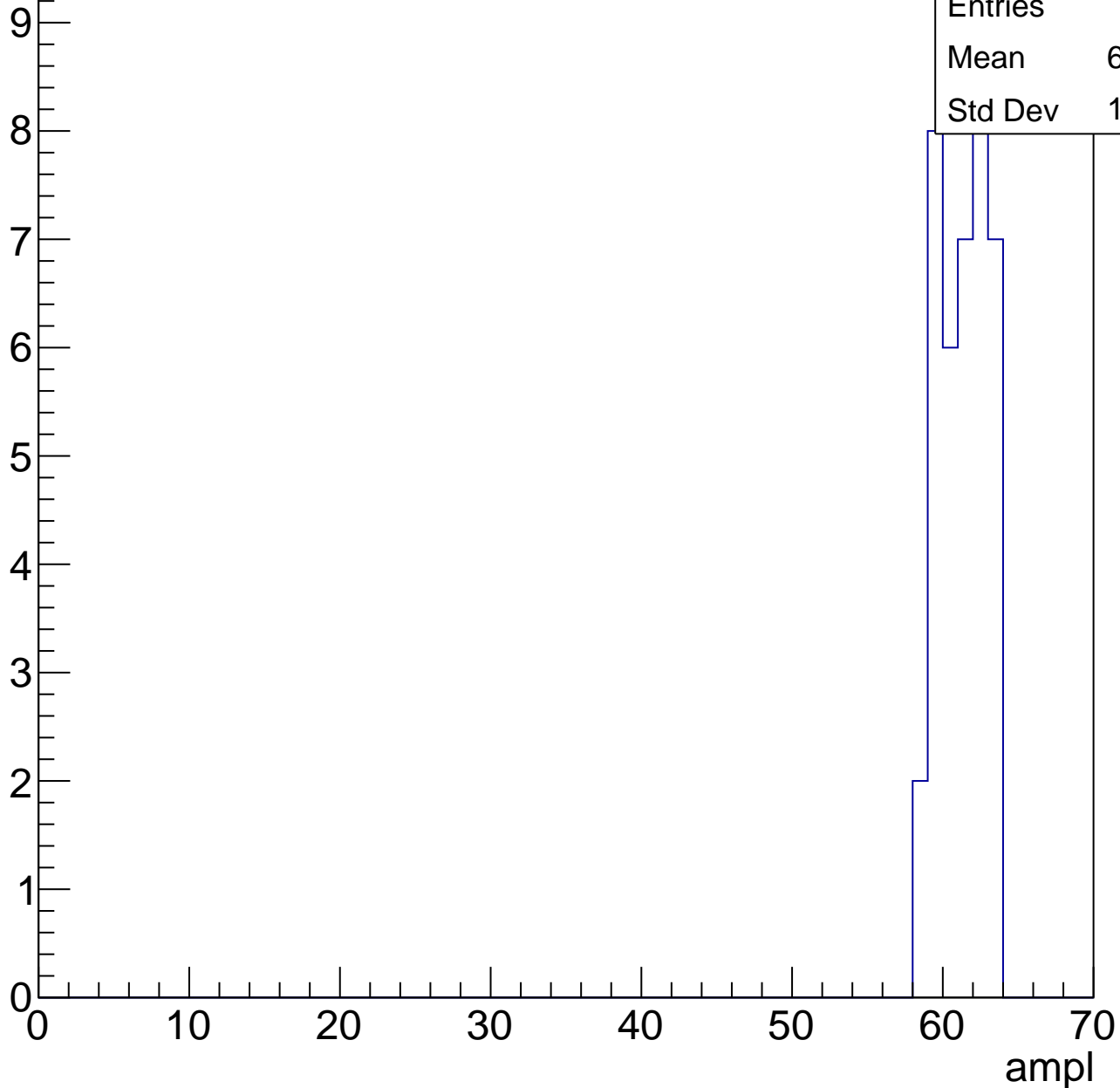
Entry



# B0L000S, U13-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

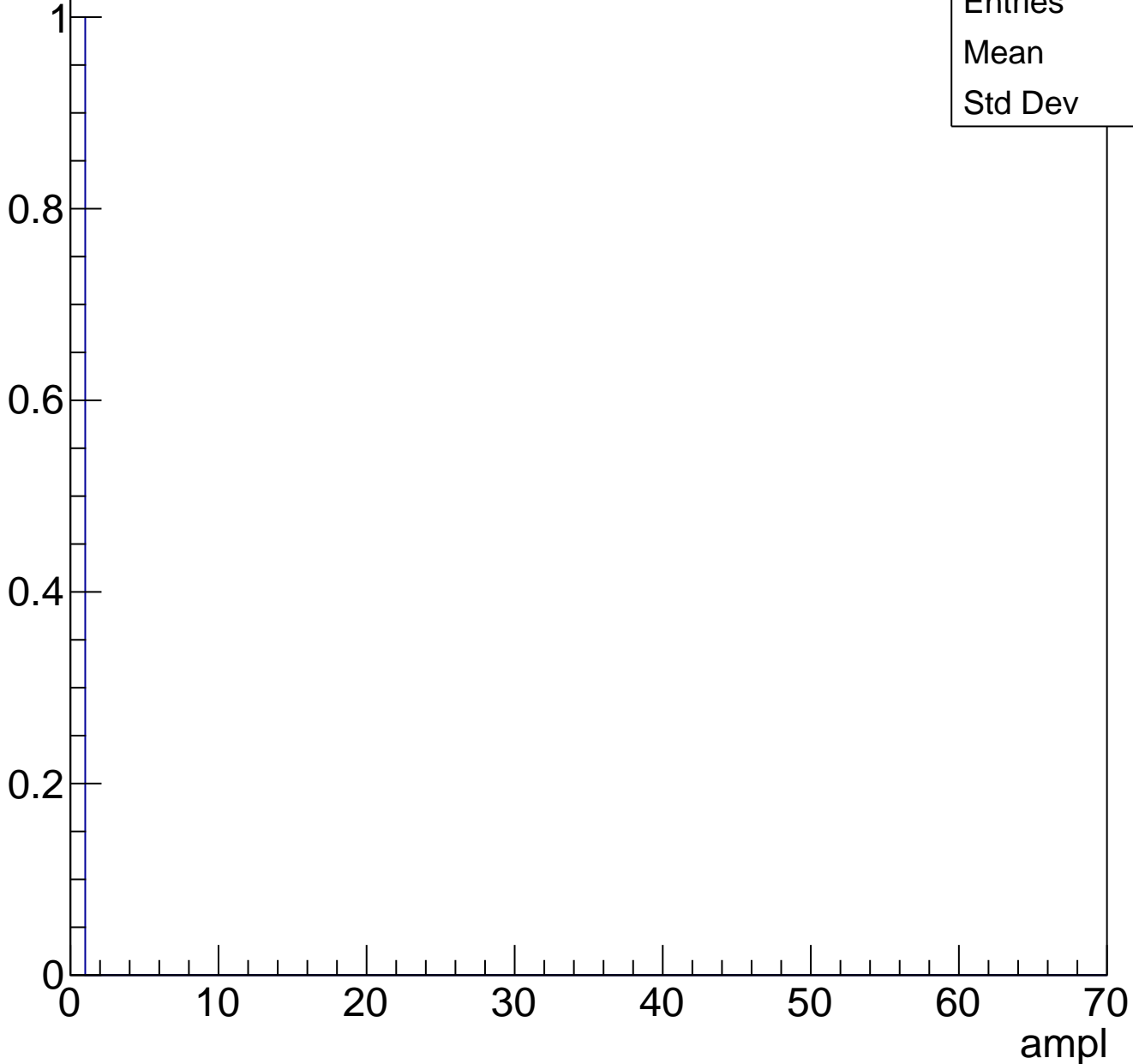
Entry



# B0L000S, U13-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch81, adc0

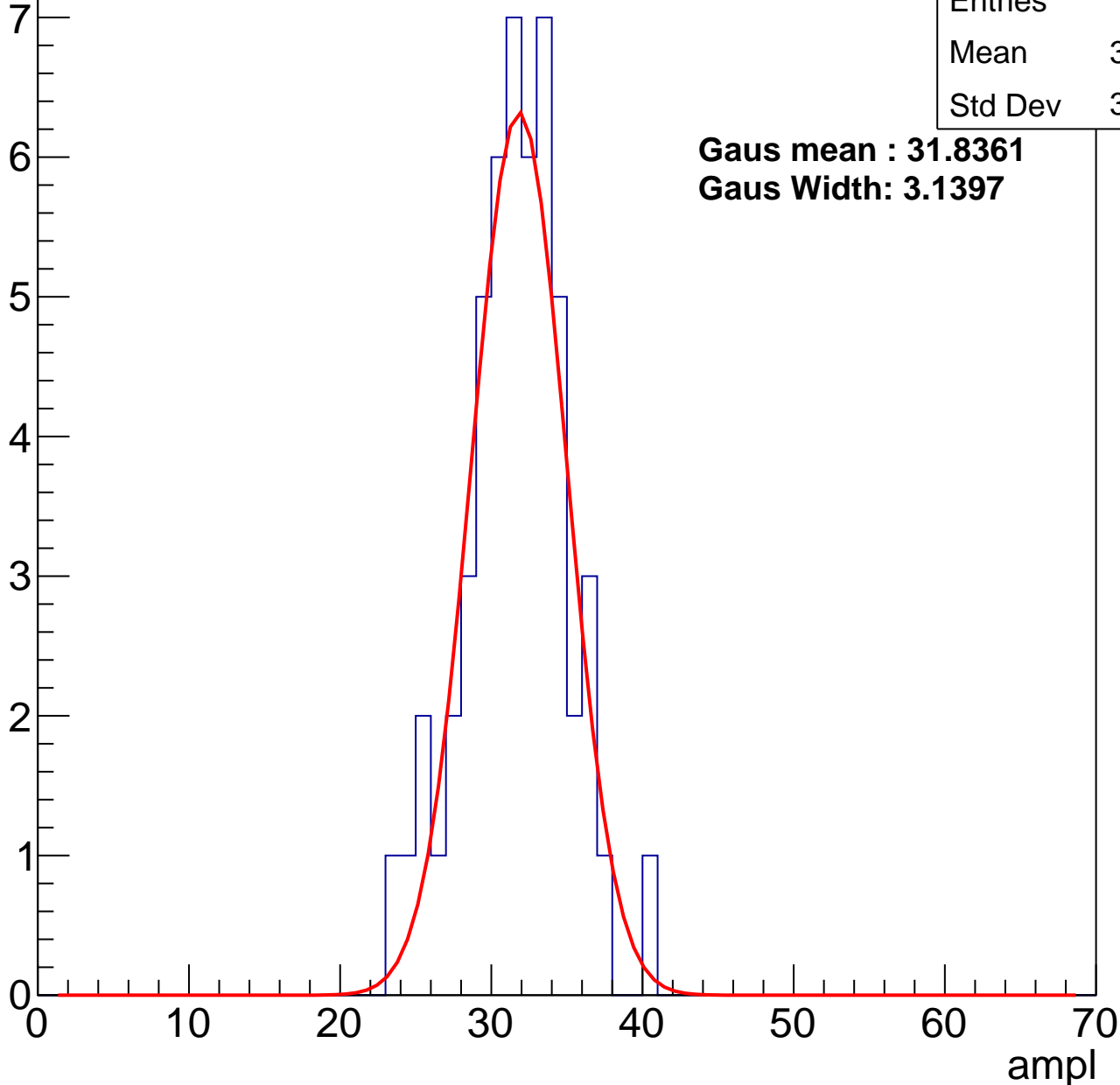
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	31.15
Std Dev	3.356

**Gaus mean : 31.8361**

**Gaus Width: 3.1397**

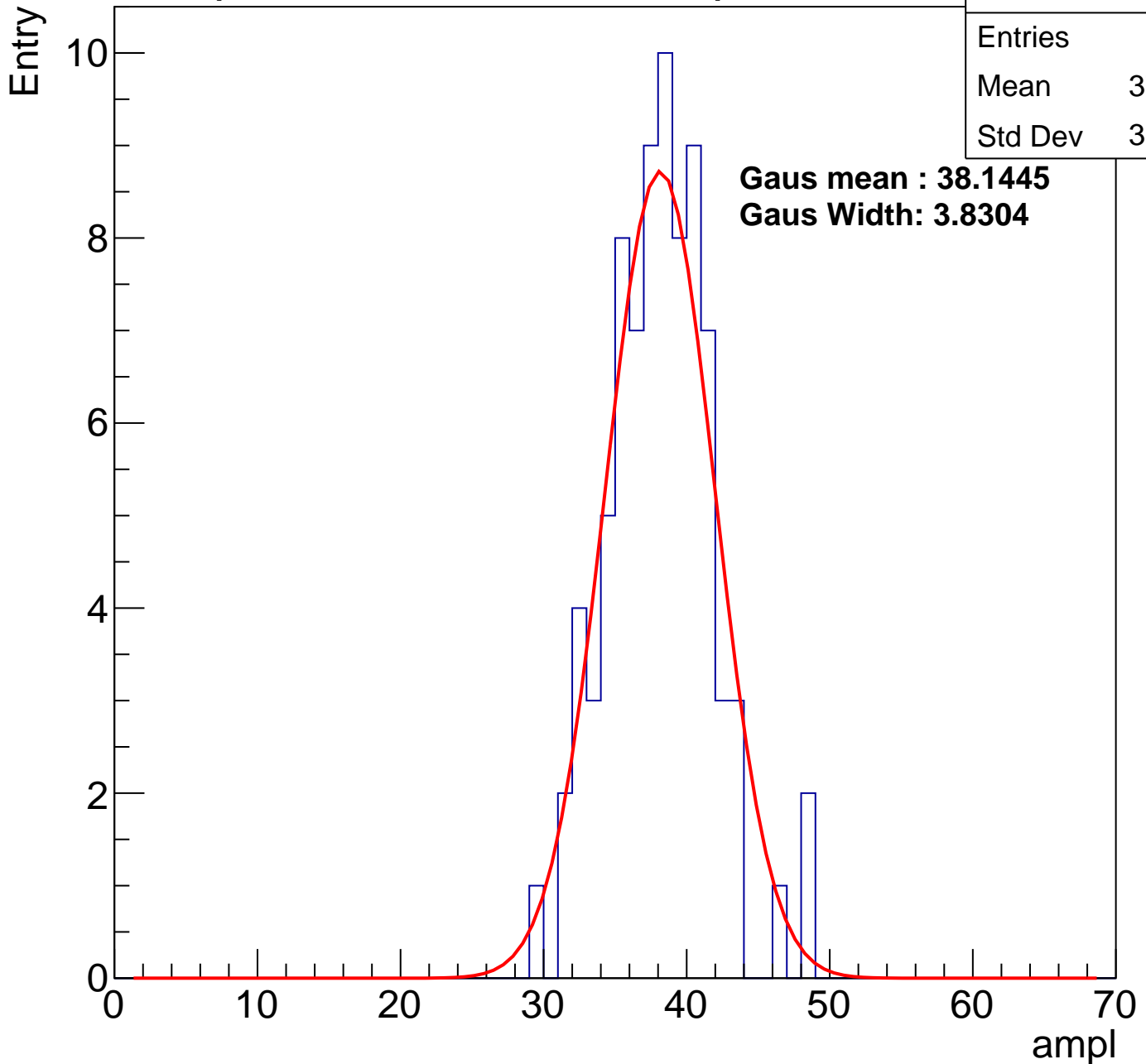


# B0L000S, U13-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	37.67
Std Dev	3.616

**Gaus mean : 38.1445**  
**Gaus Width: 3.8304**



# B0L000S, U13-ch81, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	44.74
Std Dev	2.629

**Gaus mean : 44.9914**

**Gaus Width: 2.3519**

10

8

6

4

2

0

0

10

20

30

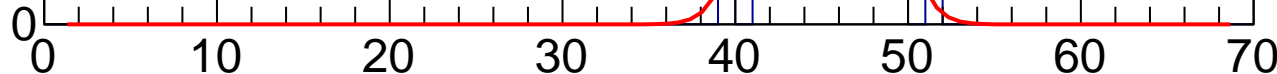
40

50

60

70

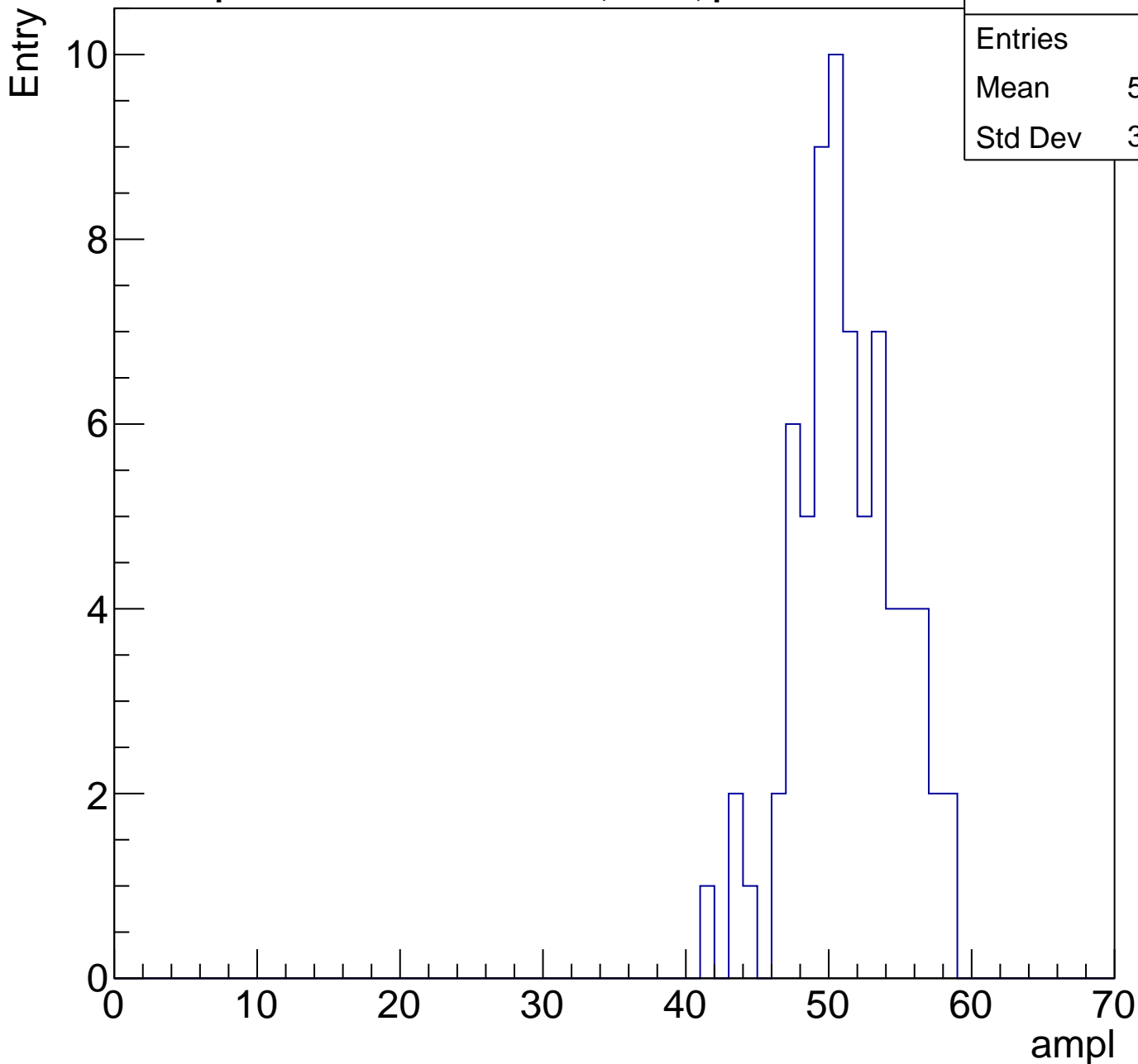
ampl



# B0L000S, U13-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	50.76
Std Dev	3.586

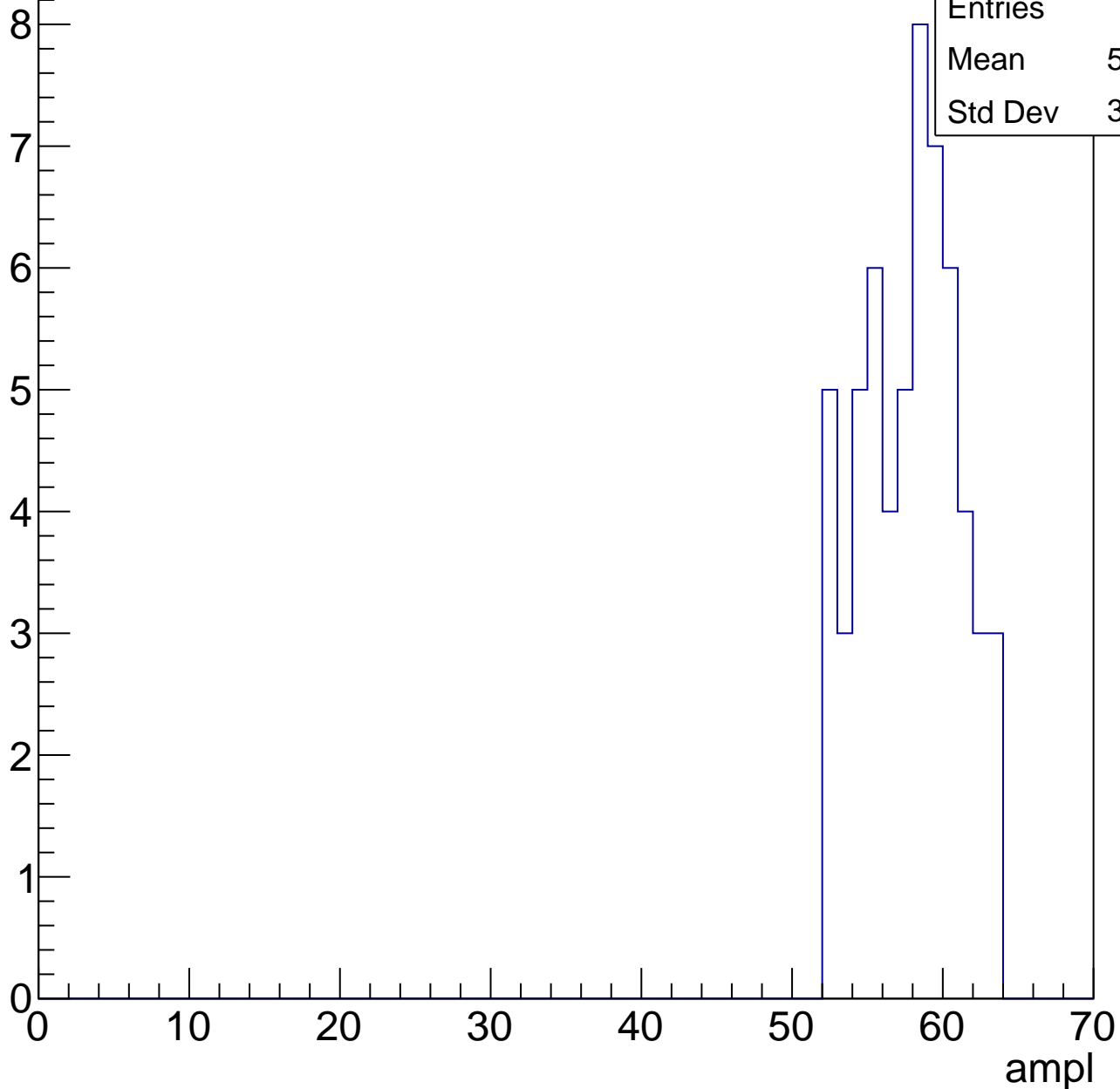


# B0L000S, U13-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	57.36
Std Dev	3.123

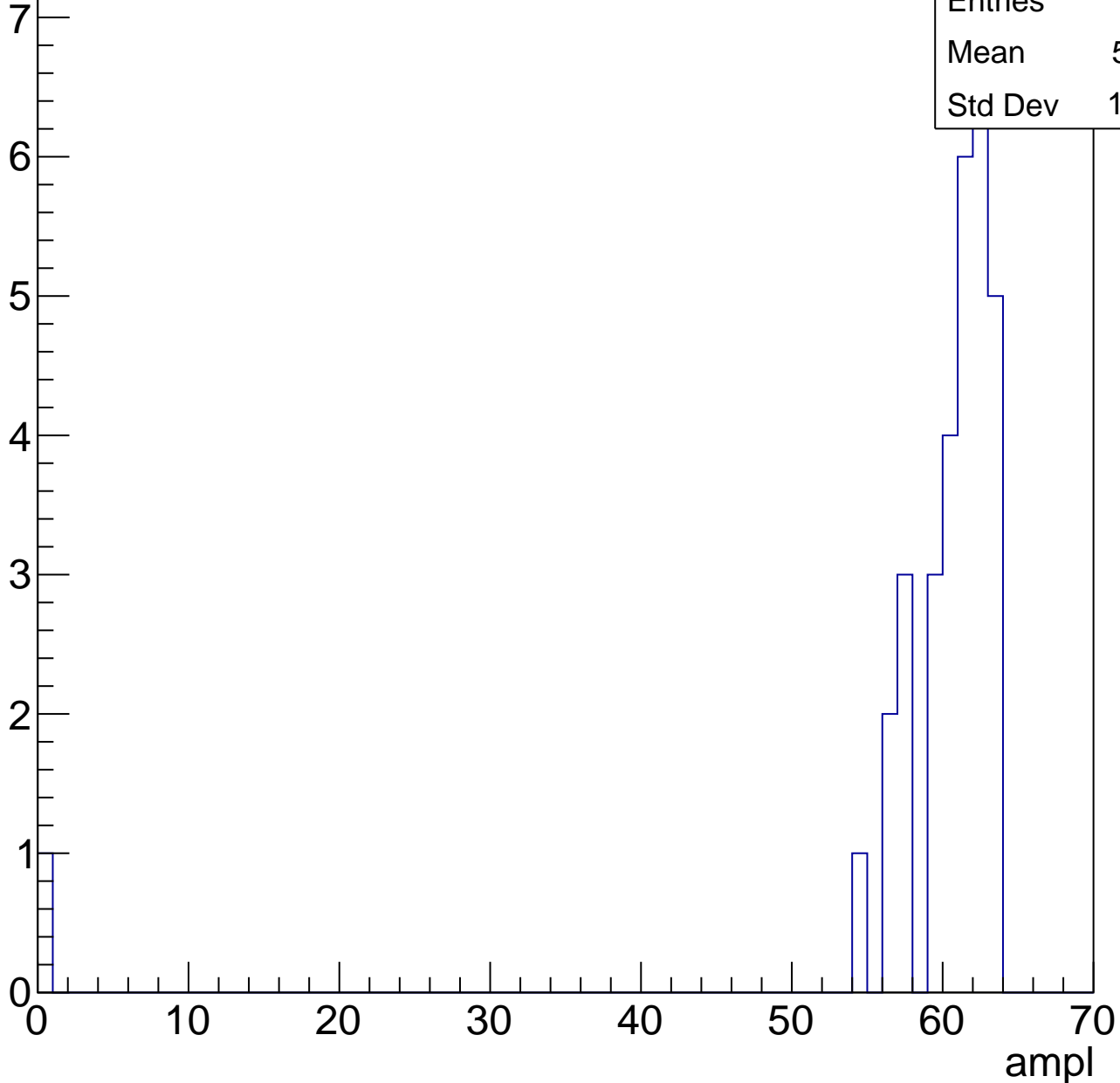


# B0L000S, U13-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.41
Std Dev	10.75



# B0L000S, U13-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	63
Std Dev	0

ampl



# B0L000S, U13-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch82, adc0

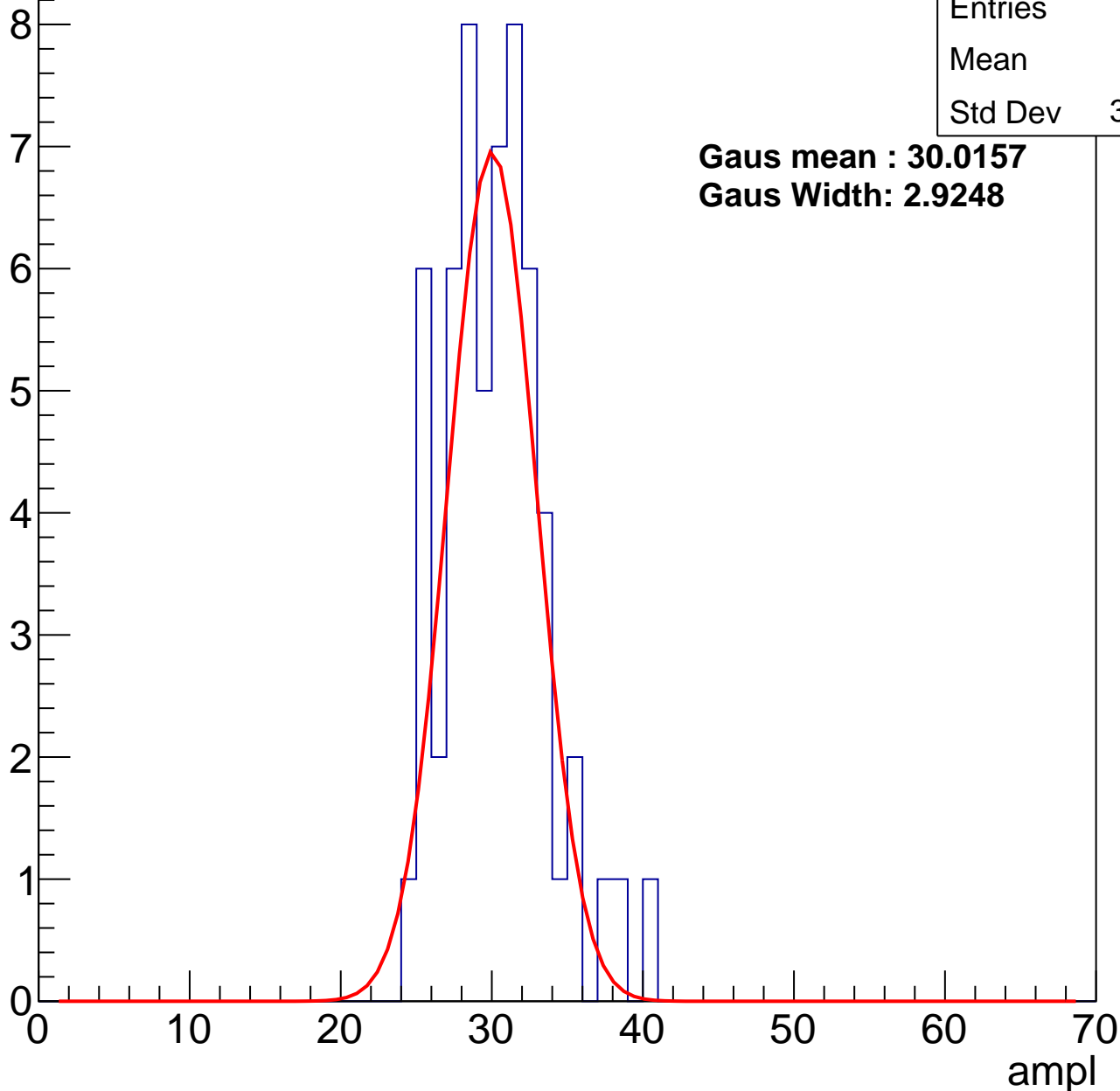
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.8
Std Dev	3.328

**Gaus mean : 30.0157**

**Gaus Width: 2.9248**



# B0L000S, U13-ch82, adc1

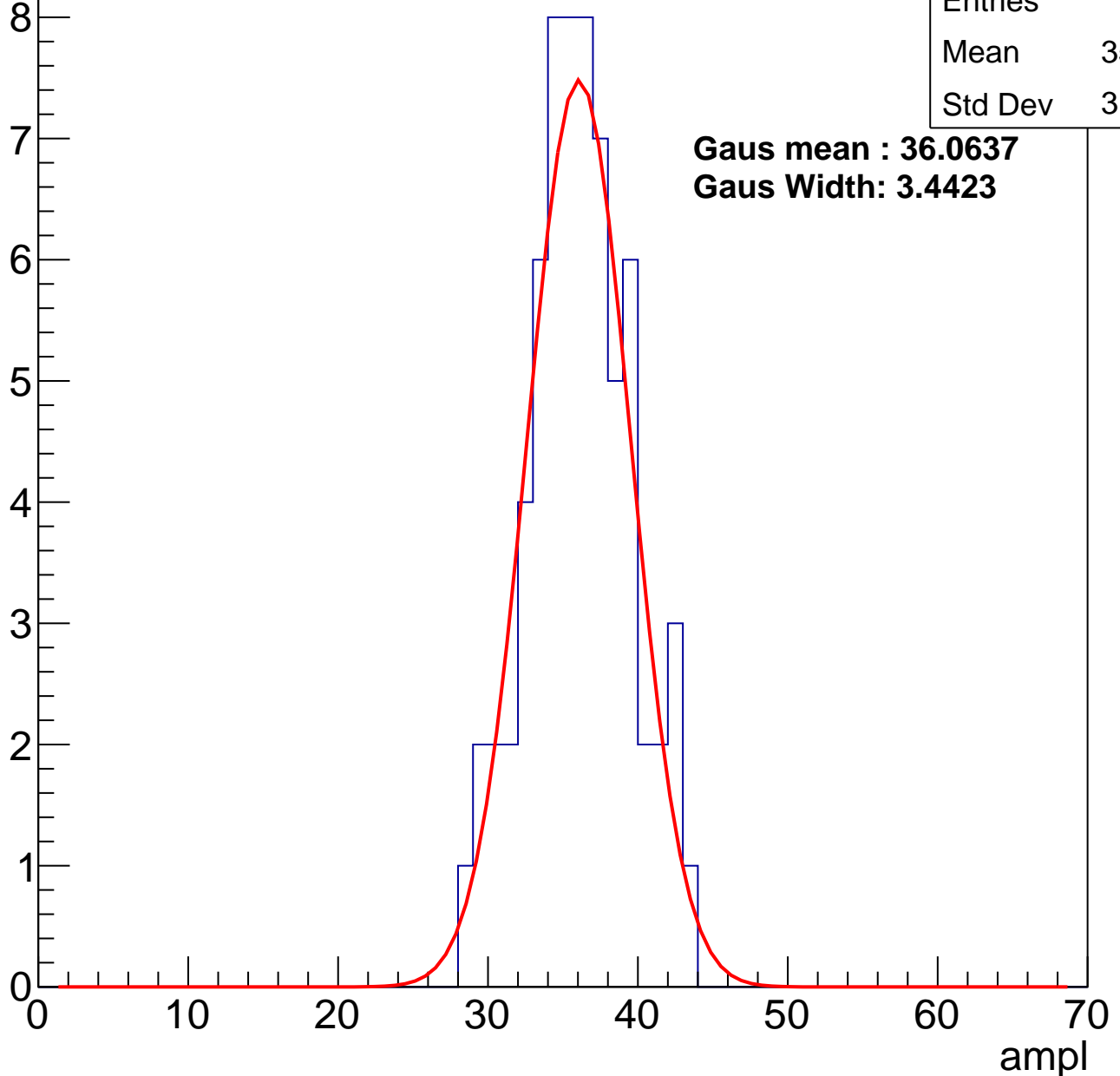
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.64
Std Dev	3.353

**Gaus mean : 36.0637**

**Gaus Width: 3.4423**



# B0L000S, U13-ch82, adc2

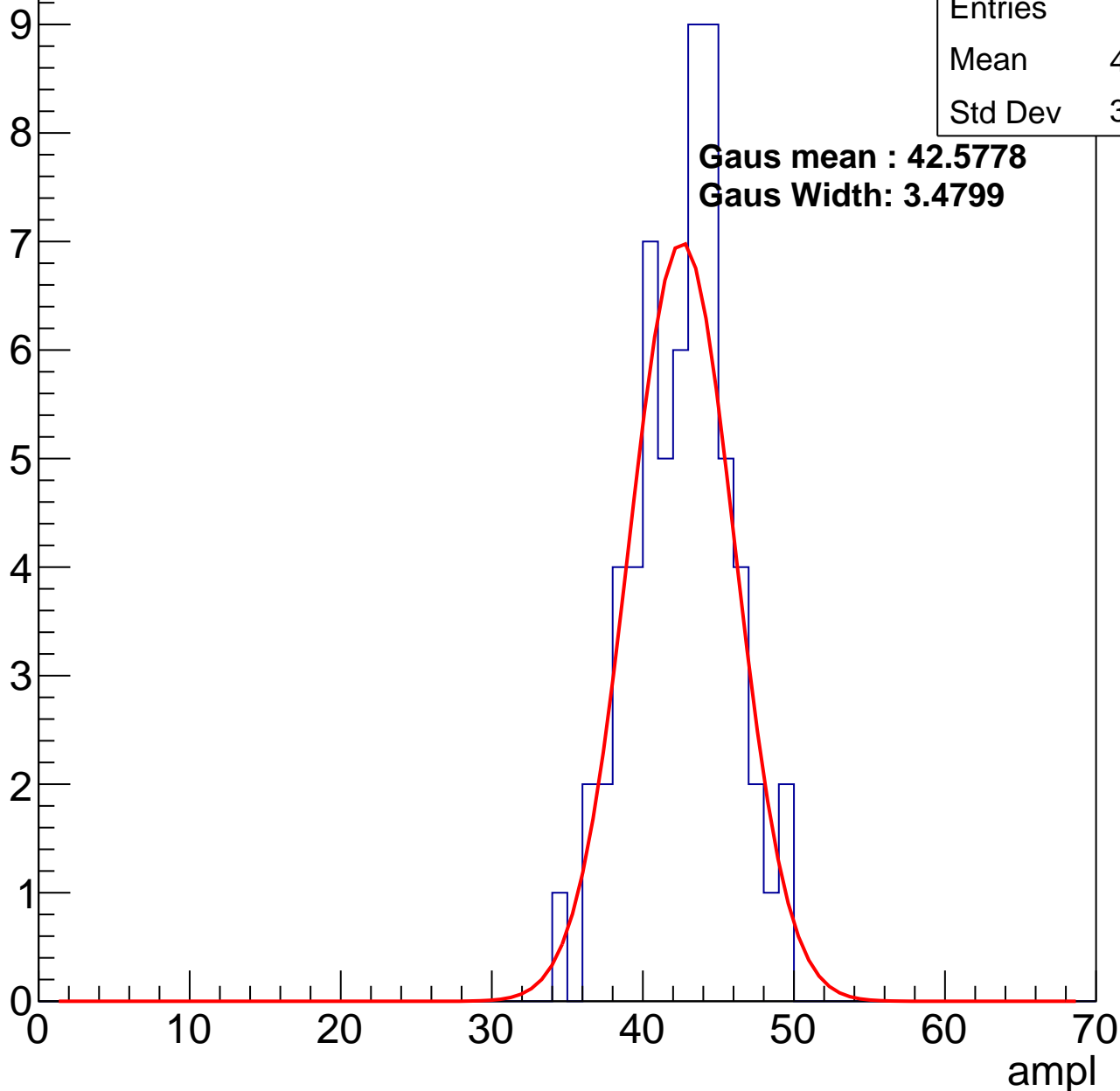
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.17
Std Dev	3.225

**Gaus mean : 42.5778**

**Gaus Width: 3.4799**

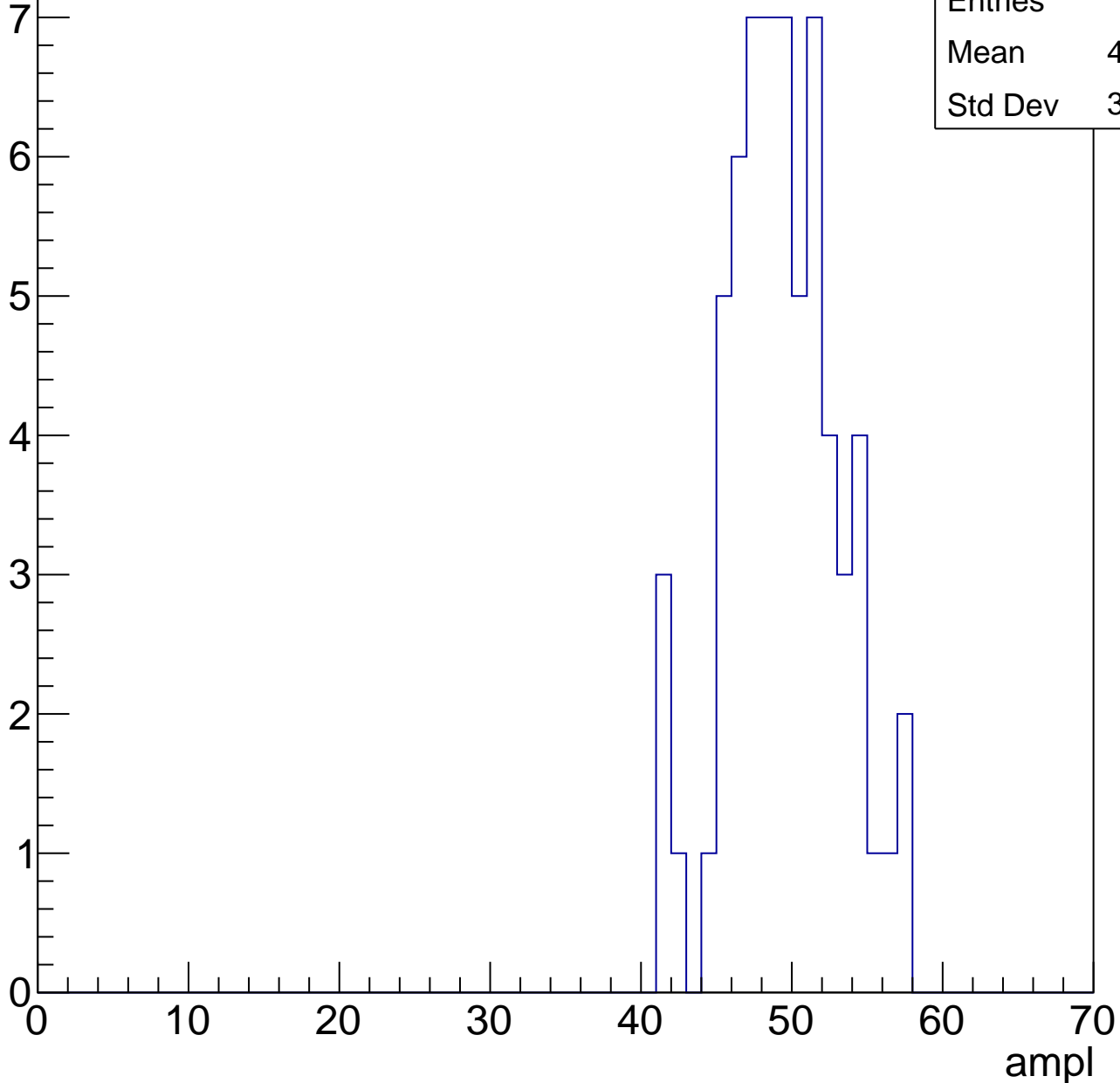


# B0L000S, U13-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	48.95
Std Dev	3.676

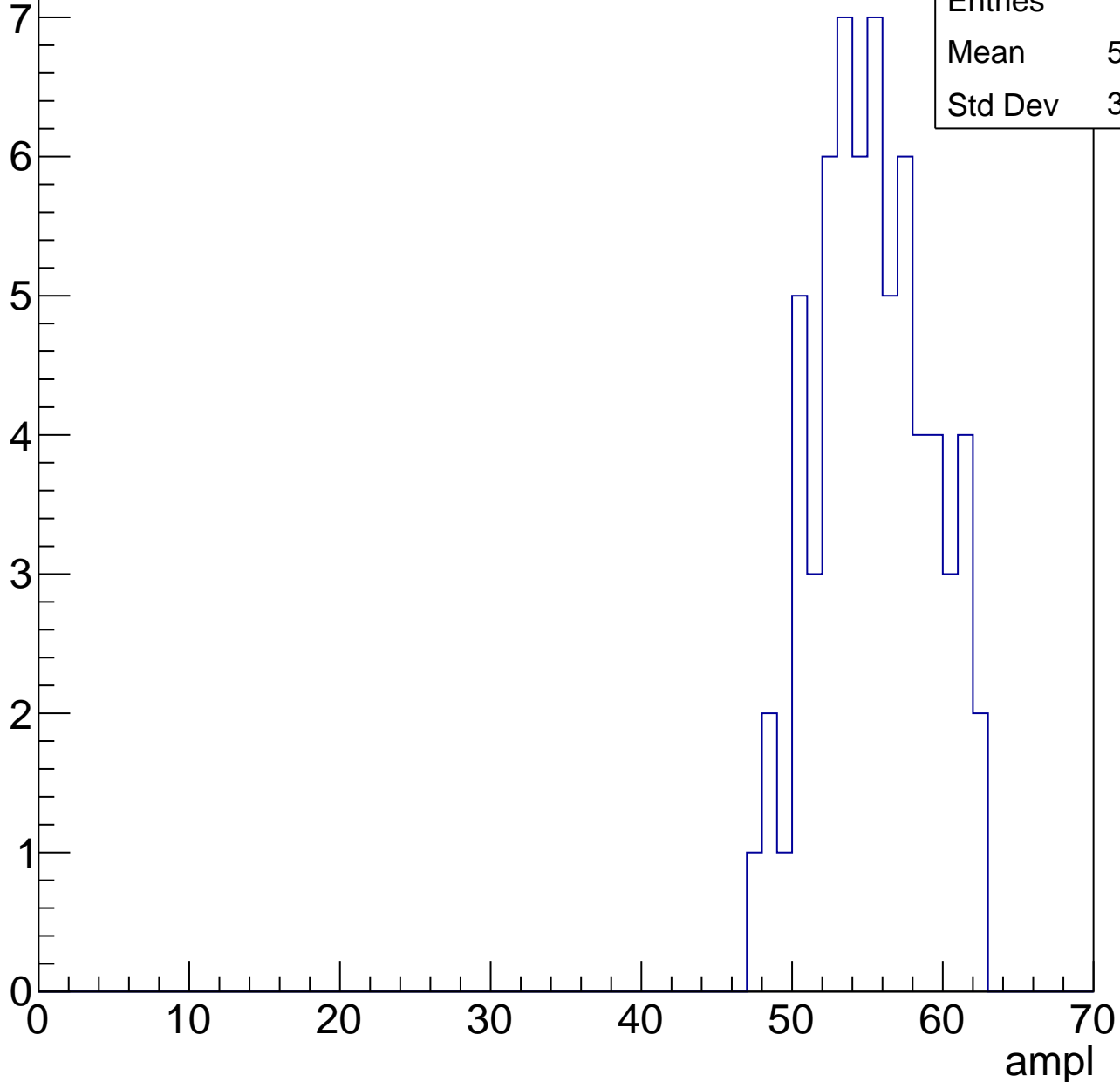


# B0L000S, U13-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	54.92
Std Dev	3.702

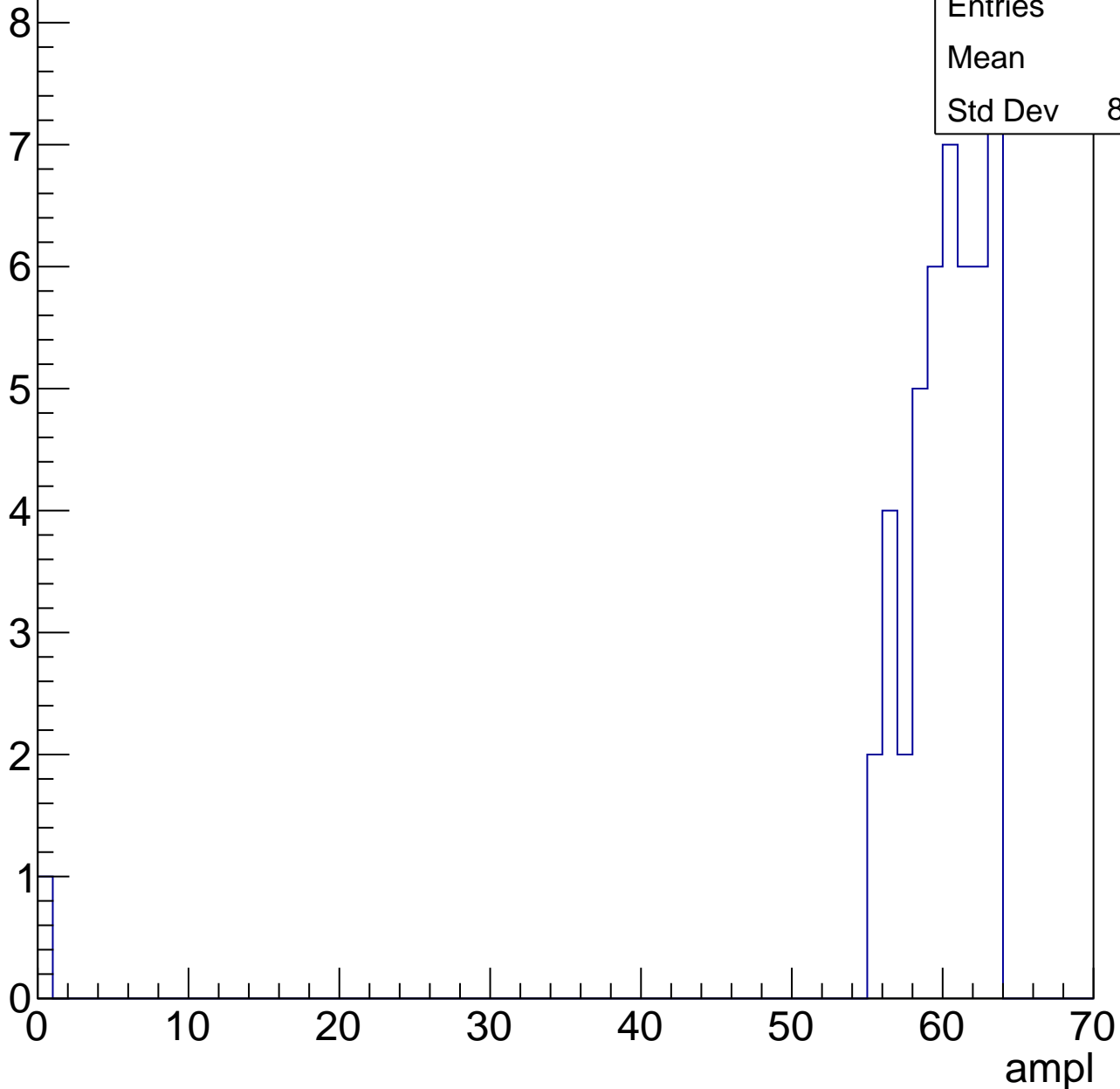


# B0L000S, U13-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

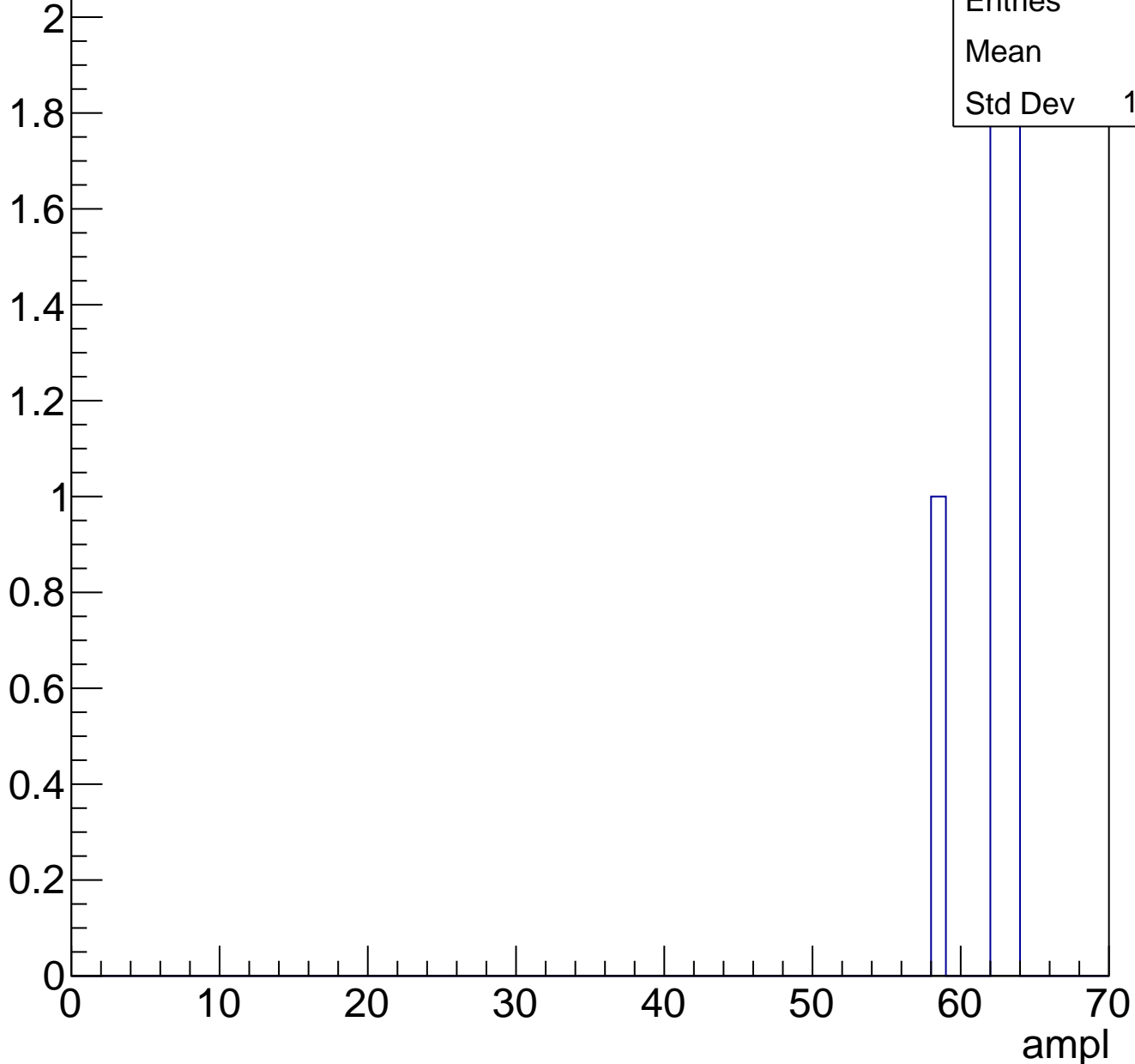
Entries	47
Mean	58.6
Std Dev	8.953



# B0L000S, U13-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch83, adc0

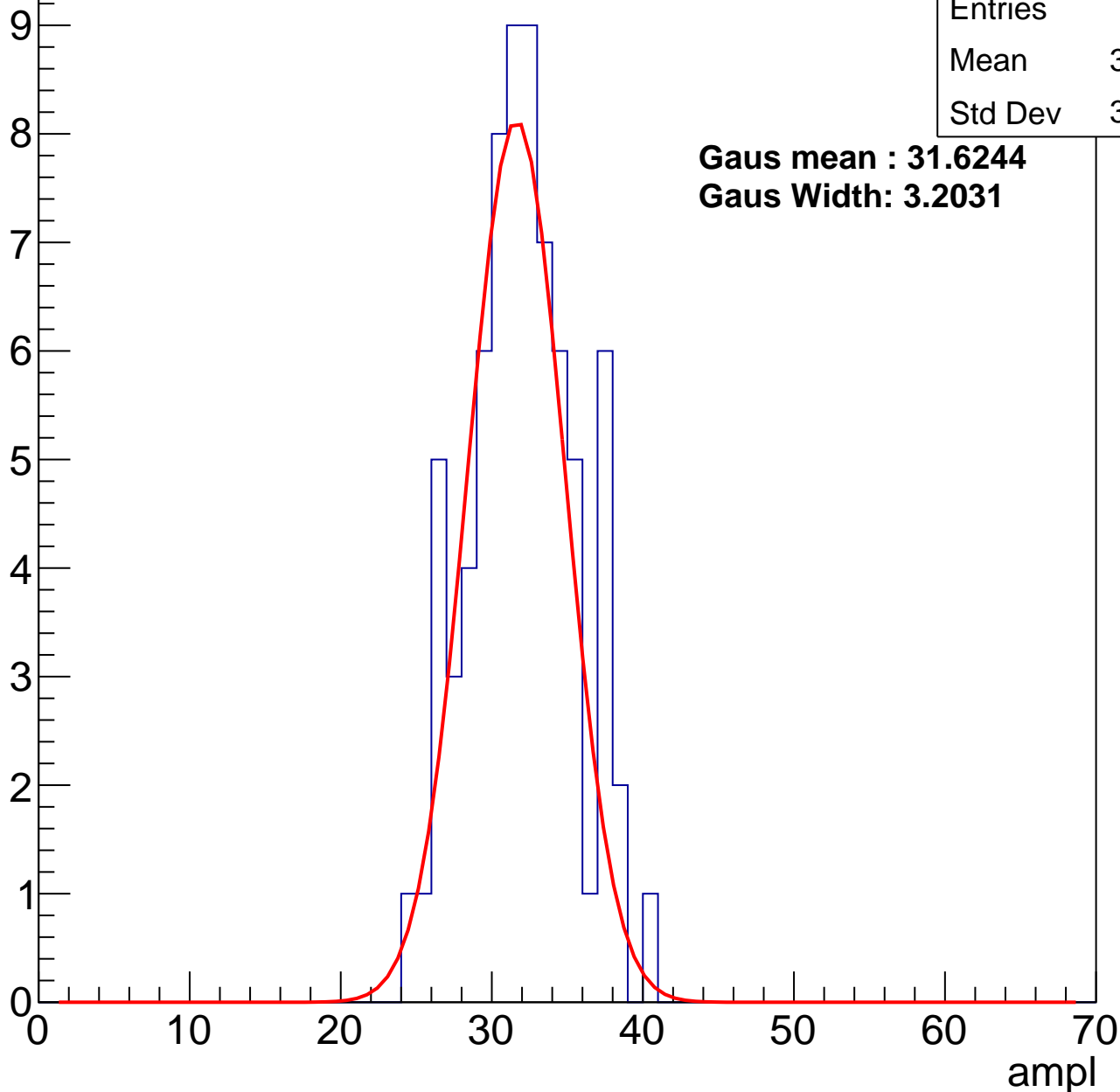
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	31.58
Std Dev	3.472

**Gaus mean : 31.6244**

**Gaus Width: 3.2031**



# B0L000S, U13-ch83, adc1

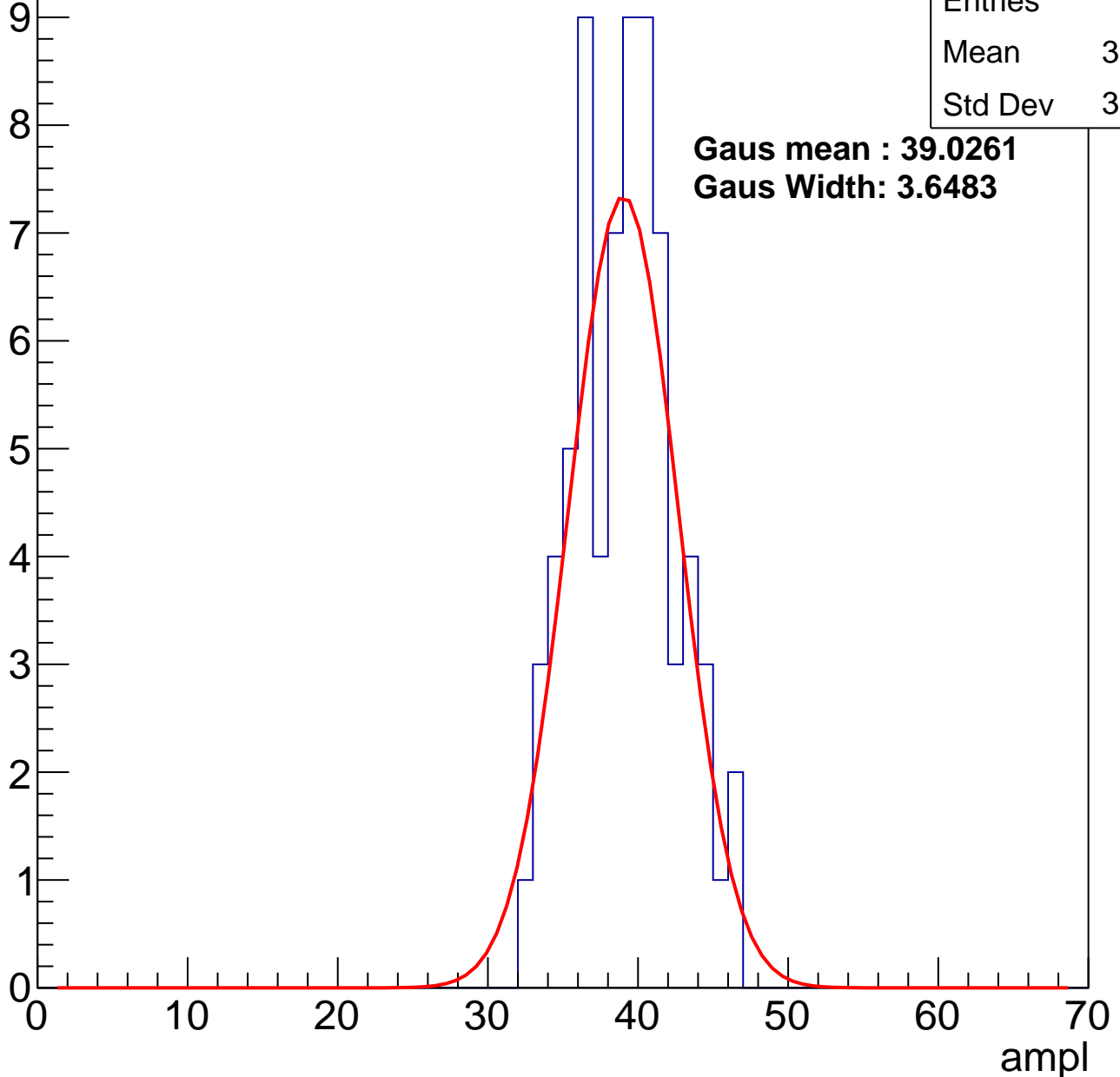
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	38.66
Std Dev	3.293

**Gaus mean : 39.0261**

**Gaus Width: 3.6483**



# B0L000S, U13-ch83, adc2

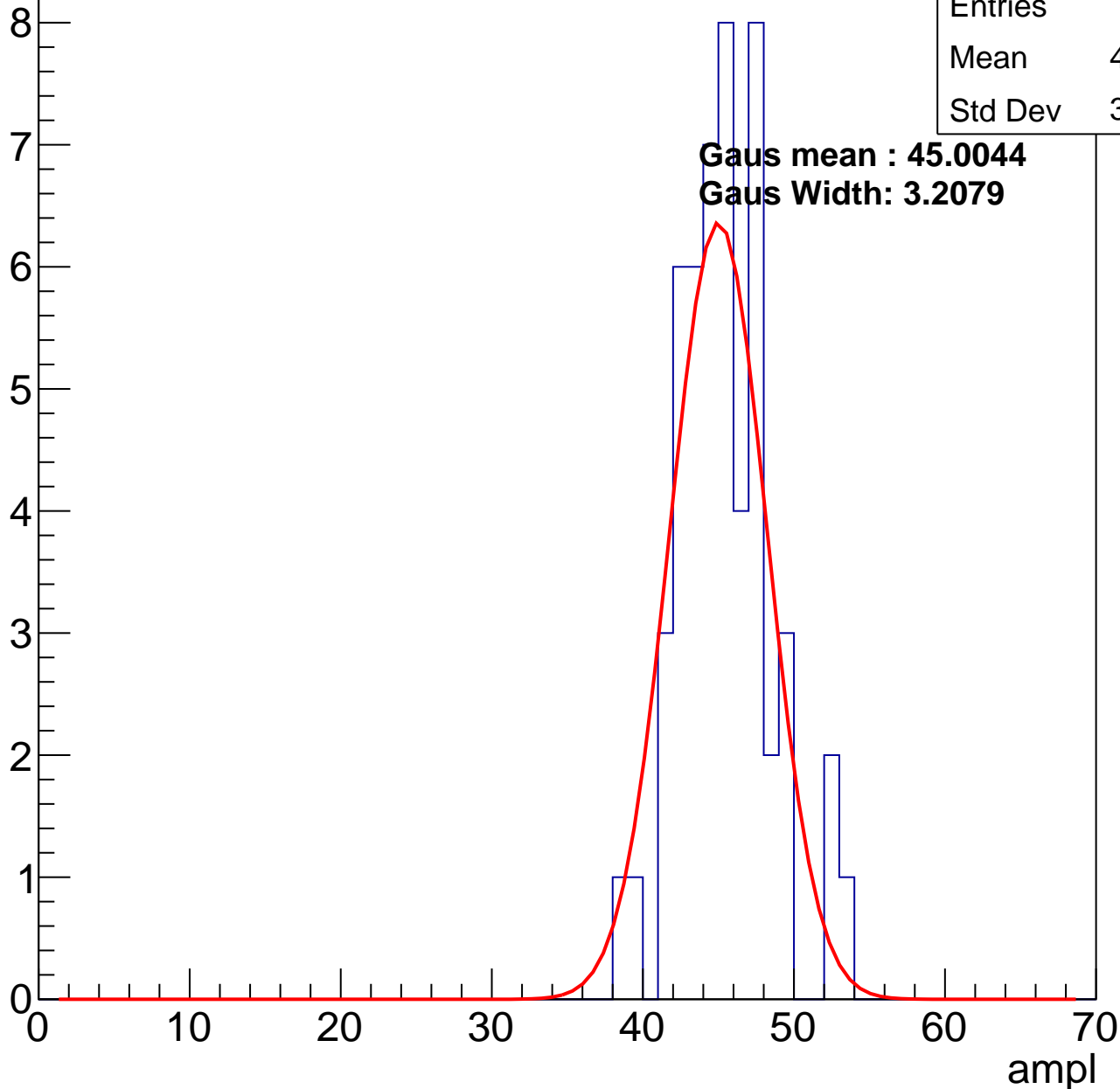
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.96
Std Dev	3.044

**Gaus mean : 45.0044**

**Gaus Width: 3.2079**

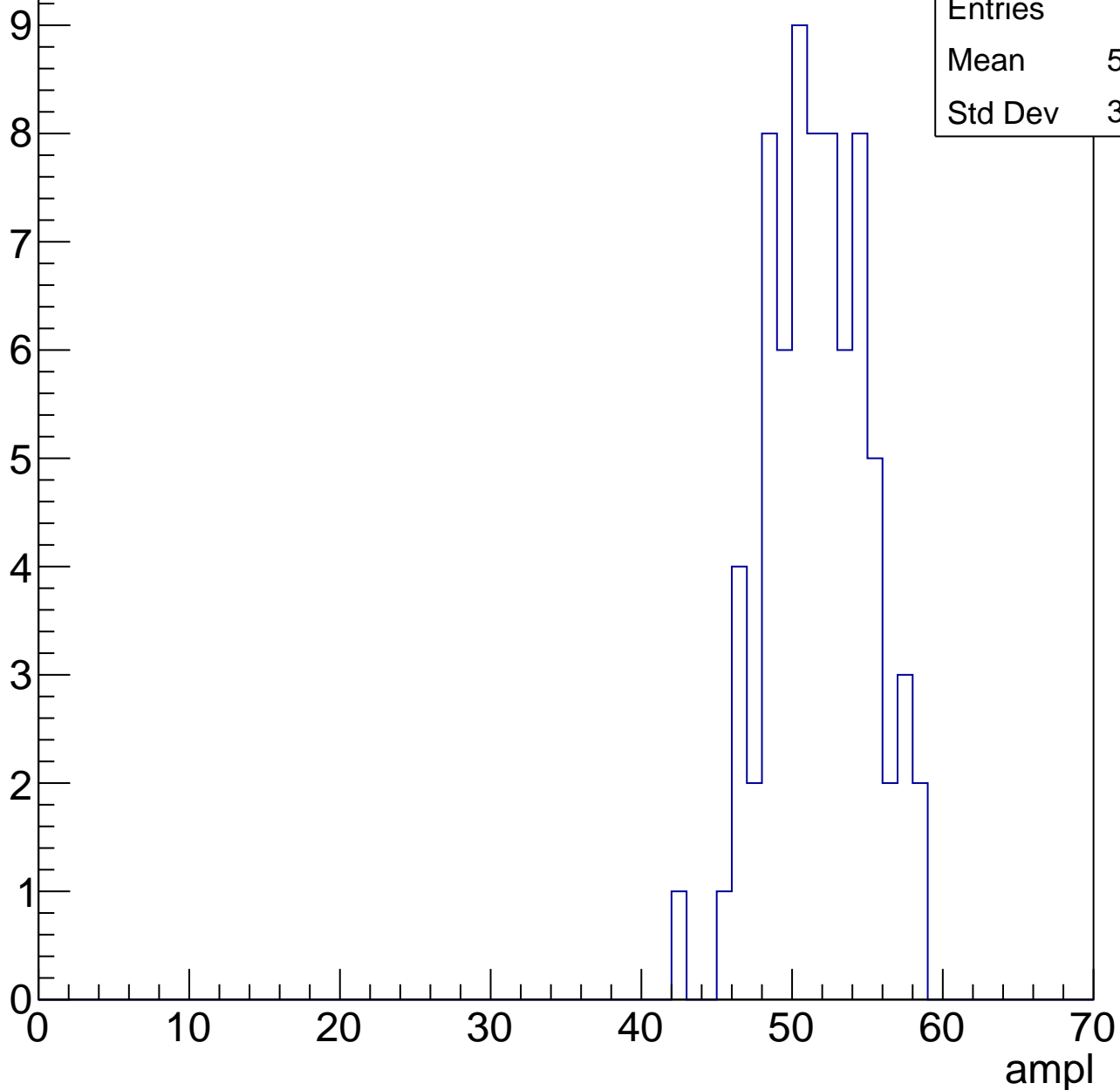


# B0L000S, U13-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

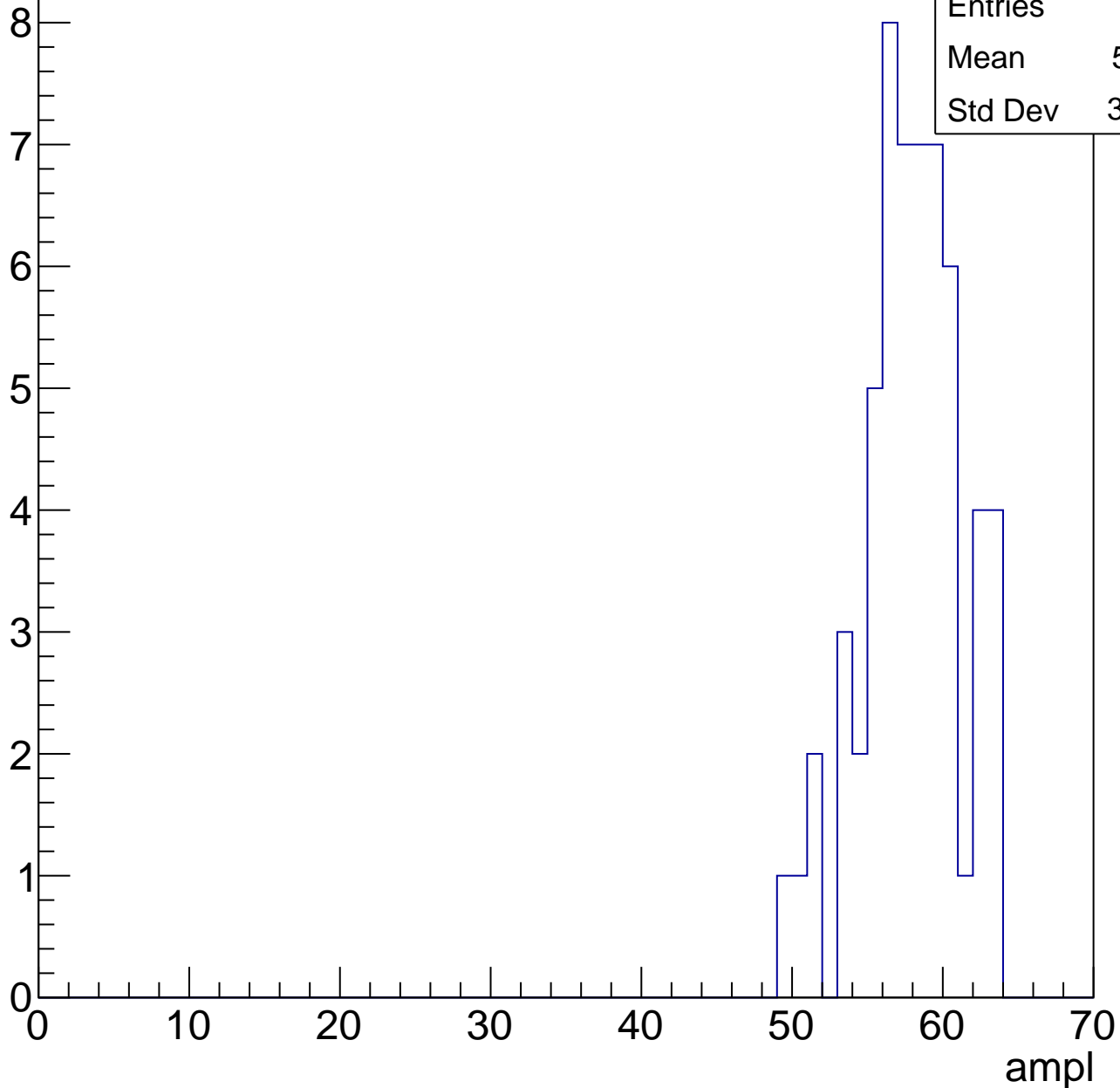
Entries	73
Mean	51.25
Std Dev	3.289



# B0L000S, U13-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

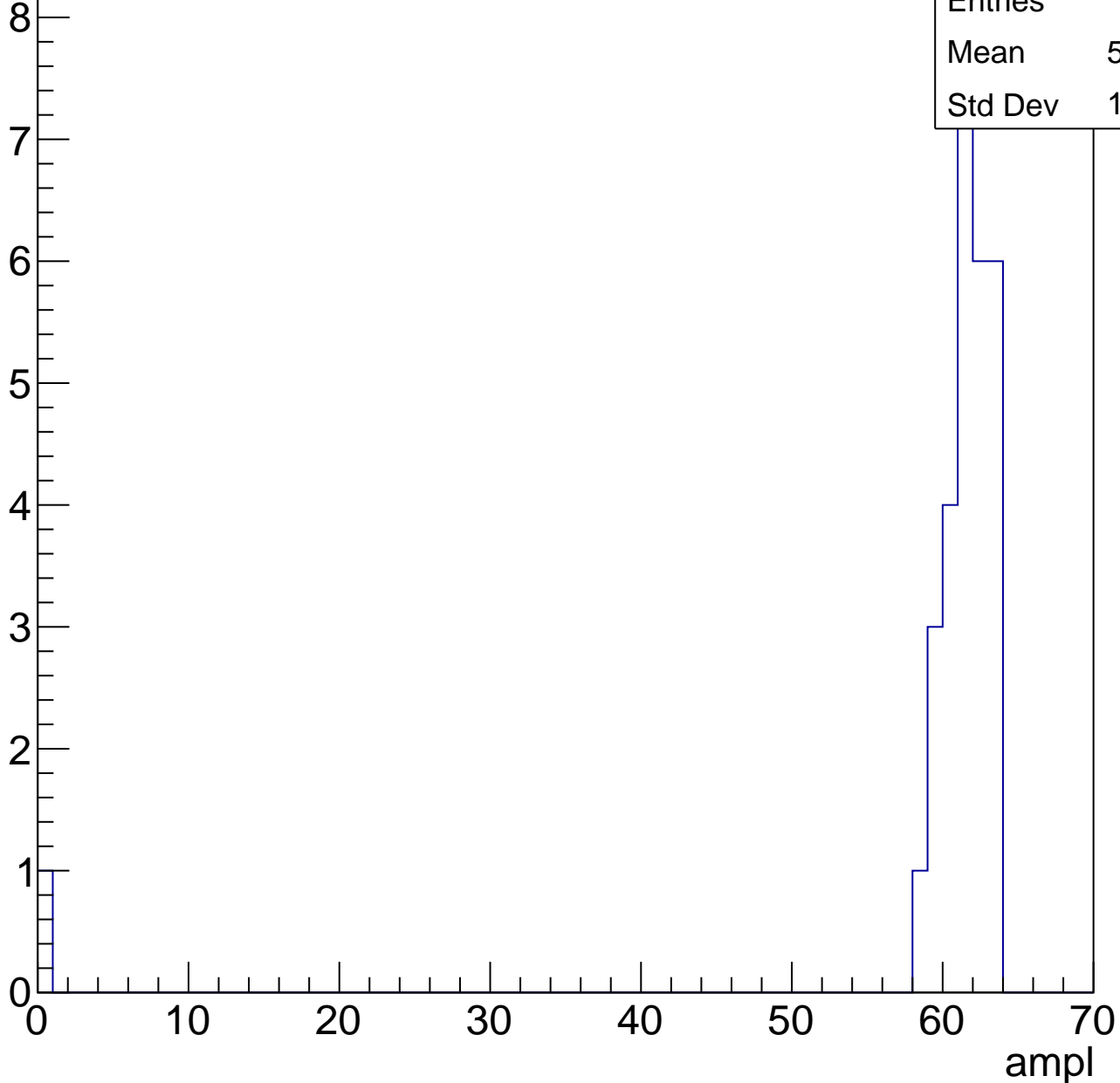


# B0L000S, U13-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	29
Mean	59.07
Std Dev	11.25



# B0L000S, U13-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch84, adc0

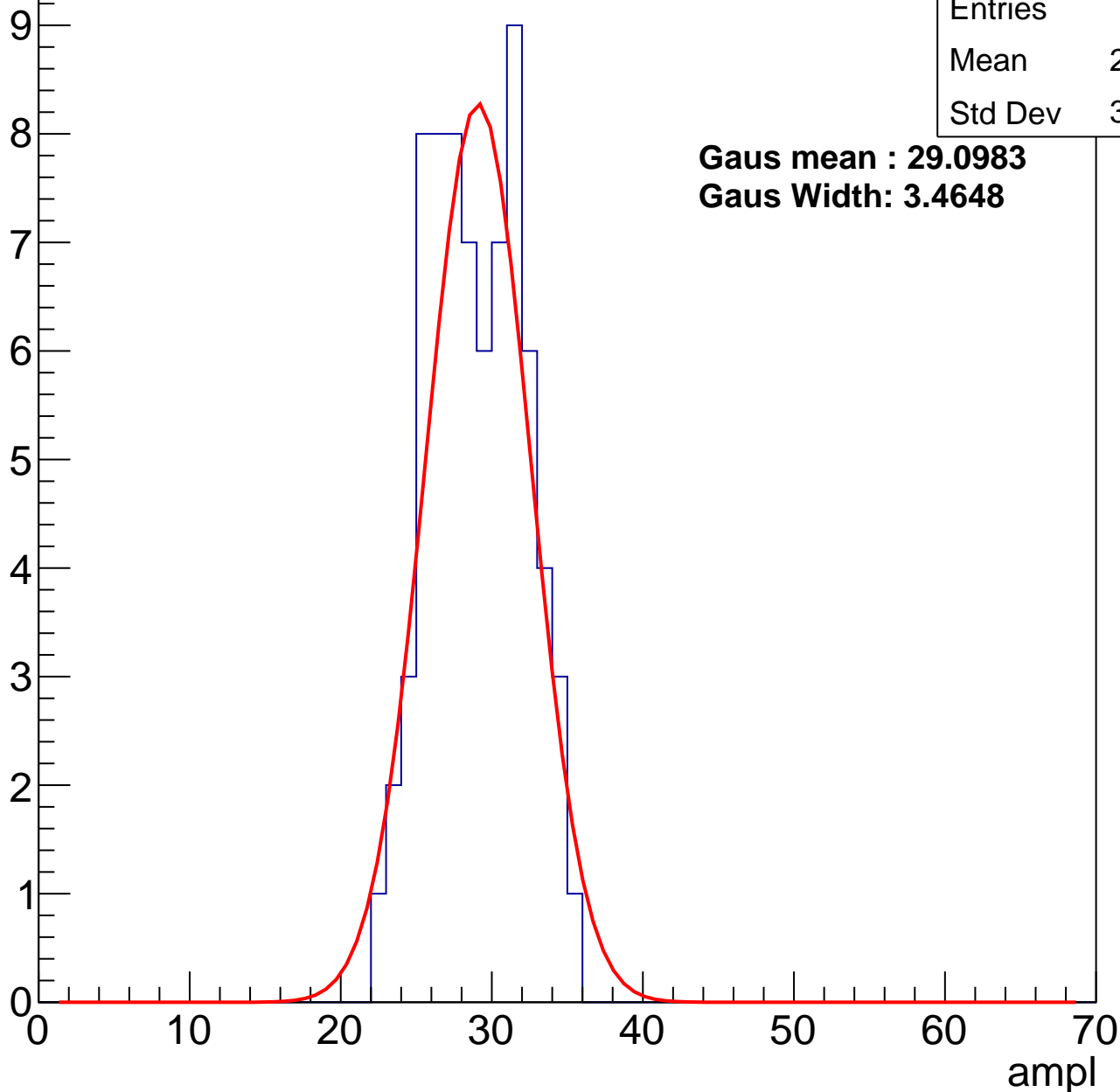
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	28.55
Std Dev	3.079

**Gaus mean : 29.0983**

**Gaus Width: 3.4648**



# B0L000S, U13-ch84, adc1

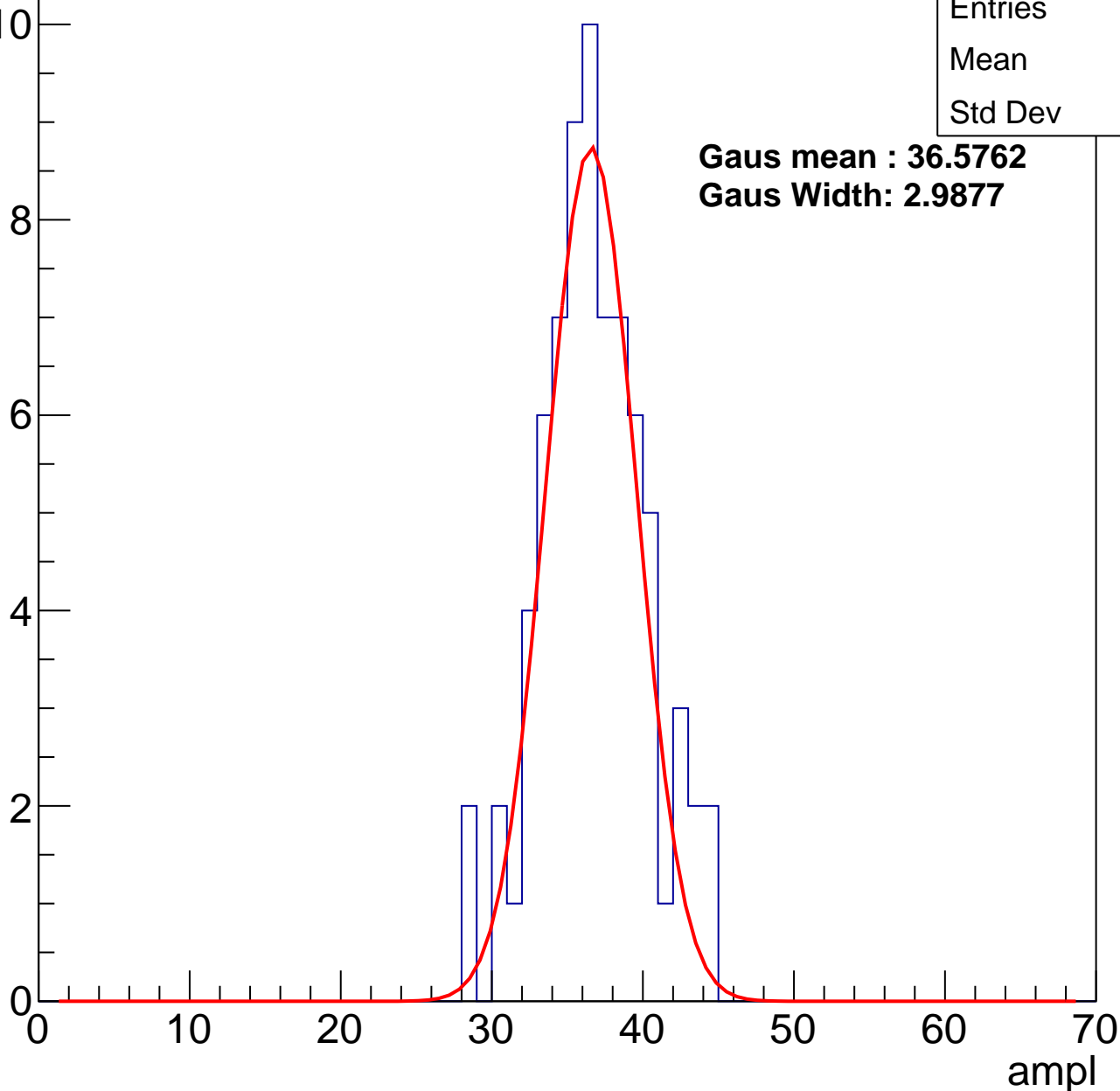
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.3
Std Dev	3.49

**Gaus mean : 36.5762**

**Gaus Width: 2.9877**



# B0L000S, U13-ch84, adc2

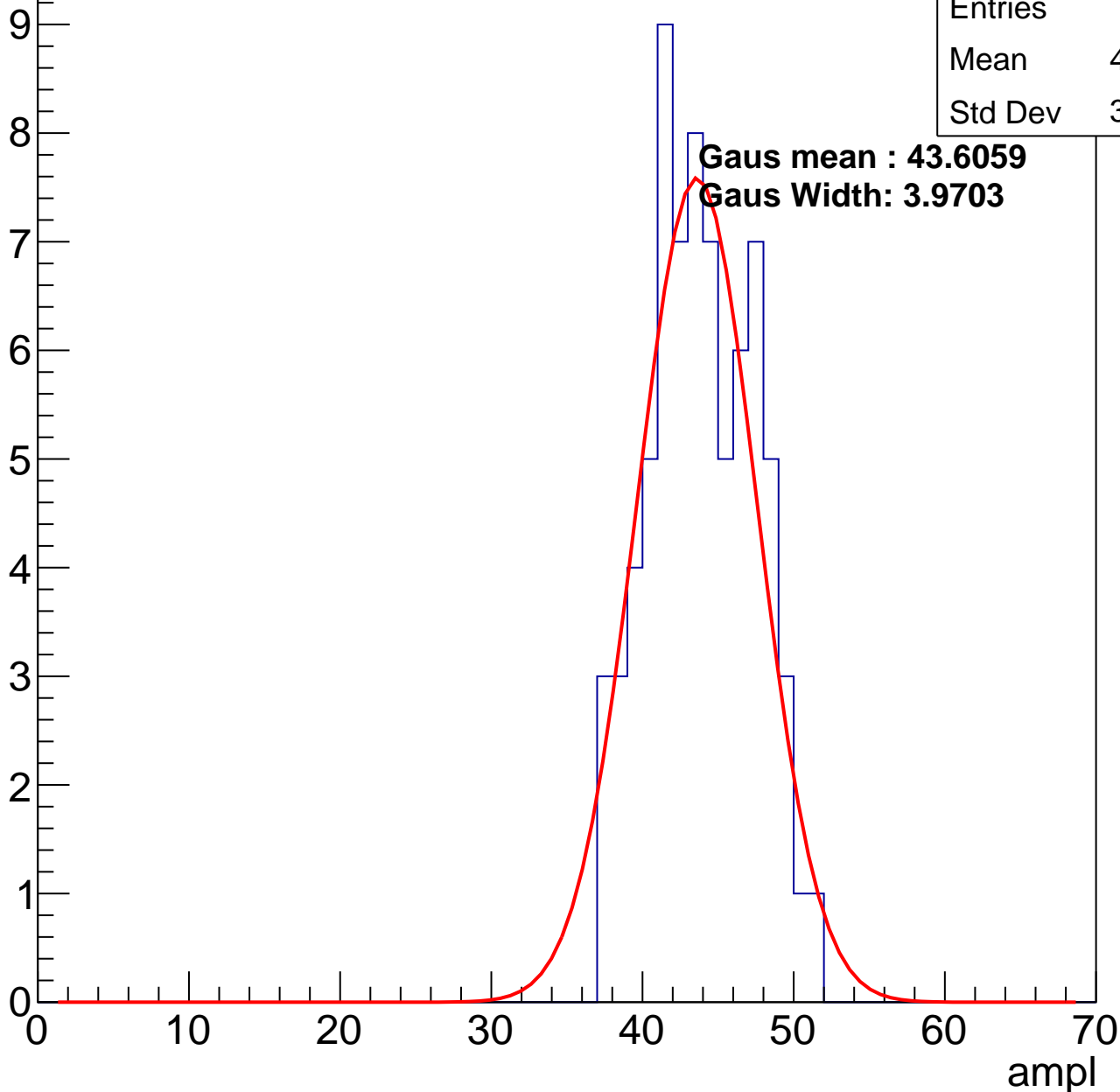
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.43
Std Dev	3.409

**Gaus mean : 43.6059**

**Gaus Width: 3.9703**

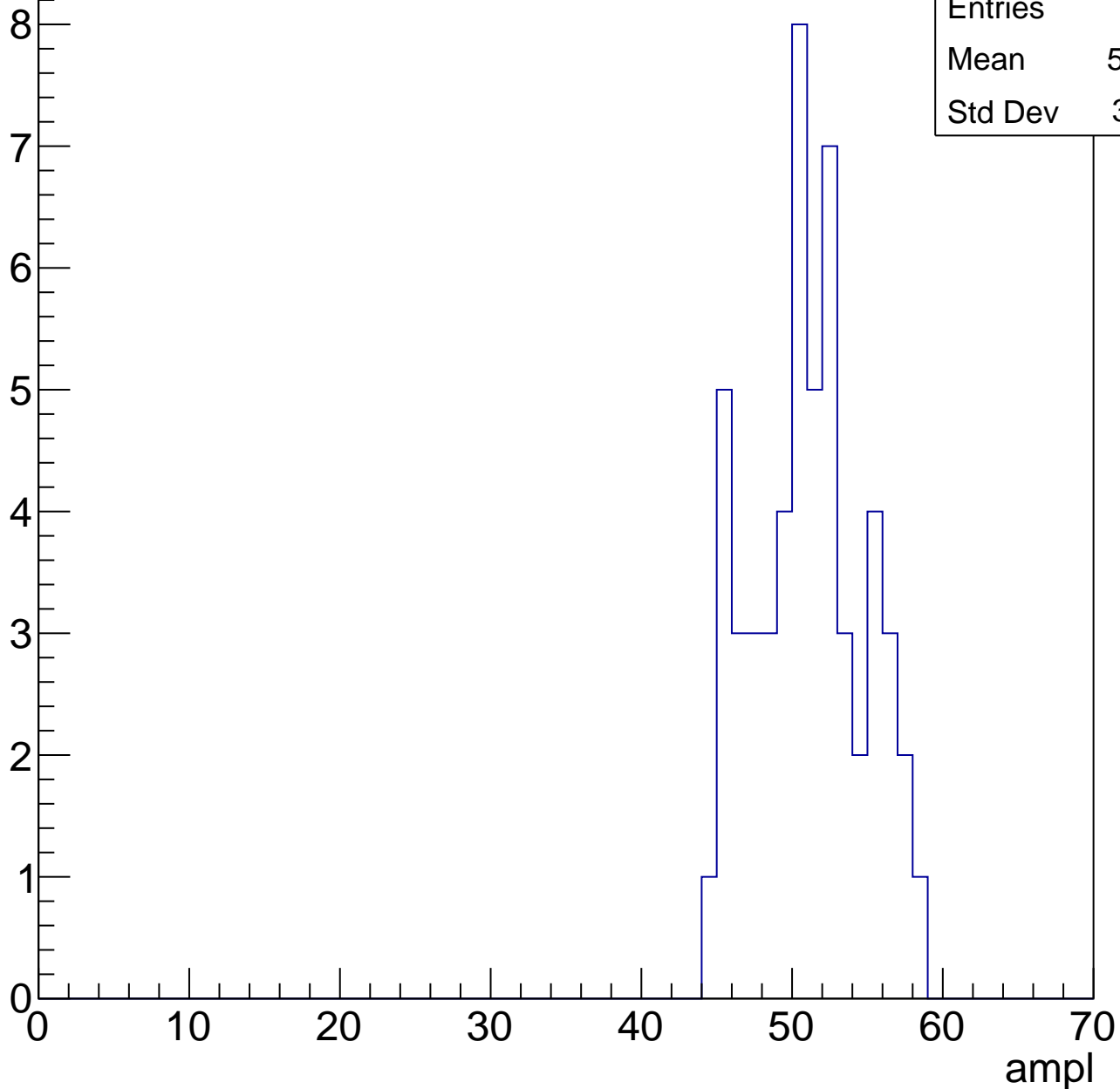


# B0L000S, U13-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	50.63
Std Dev	3.581

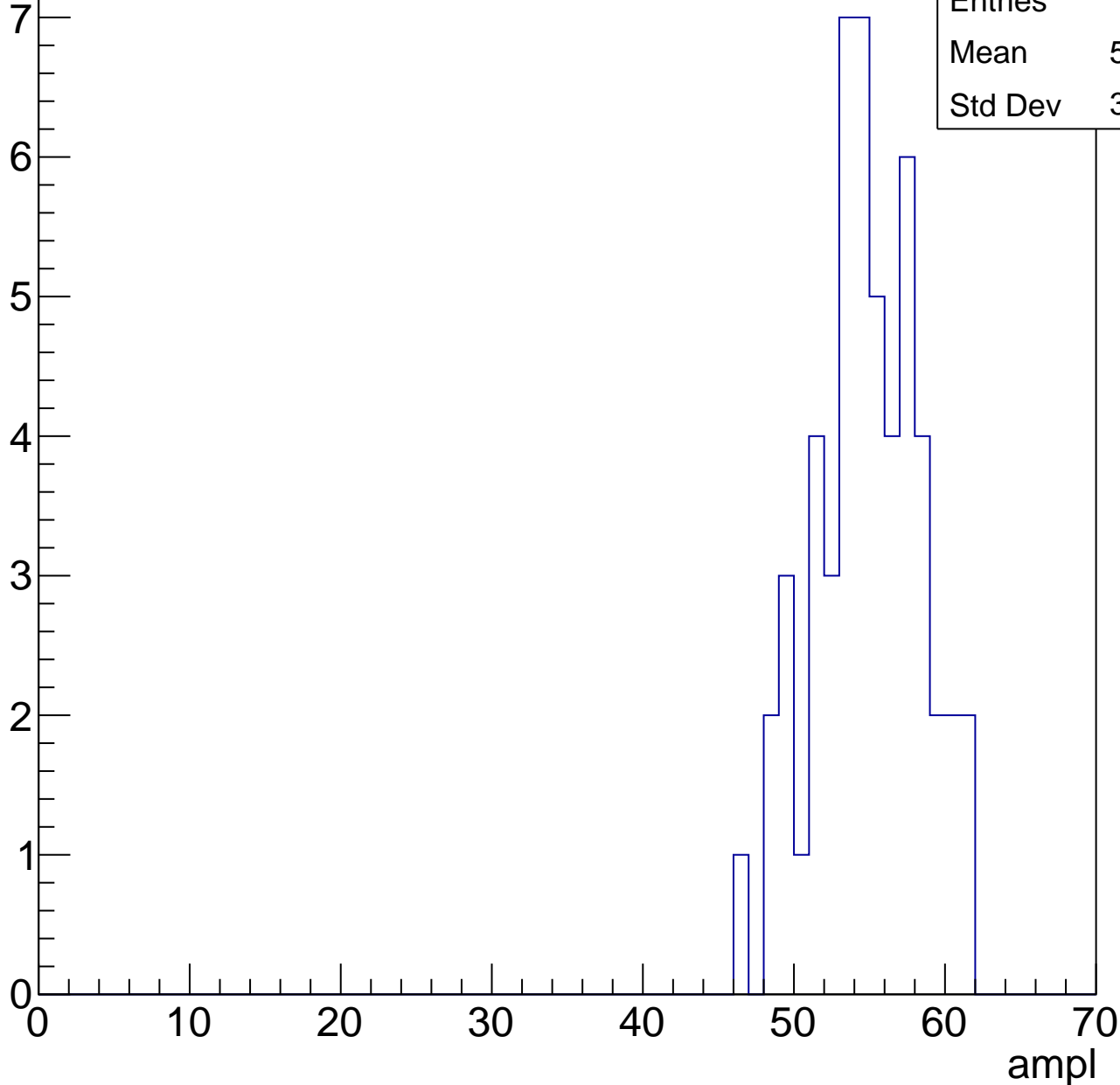


# B0L000S, U13-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	54.36
Std Dev	3.459

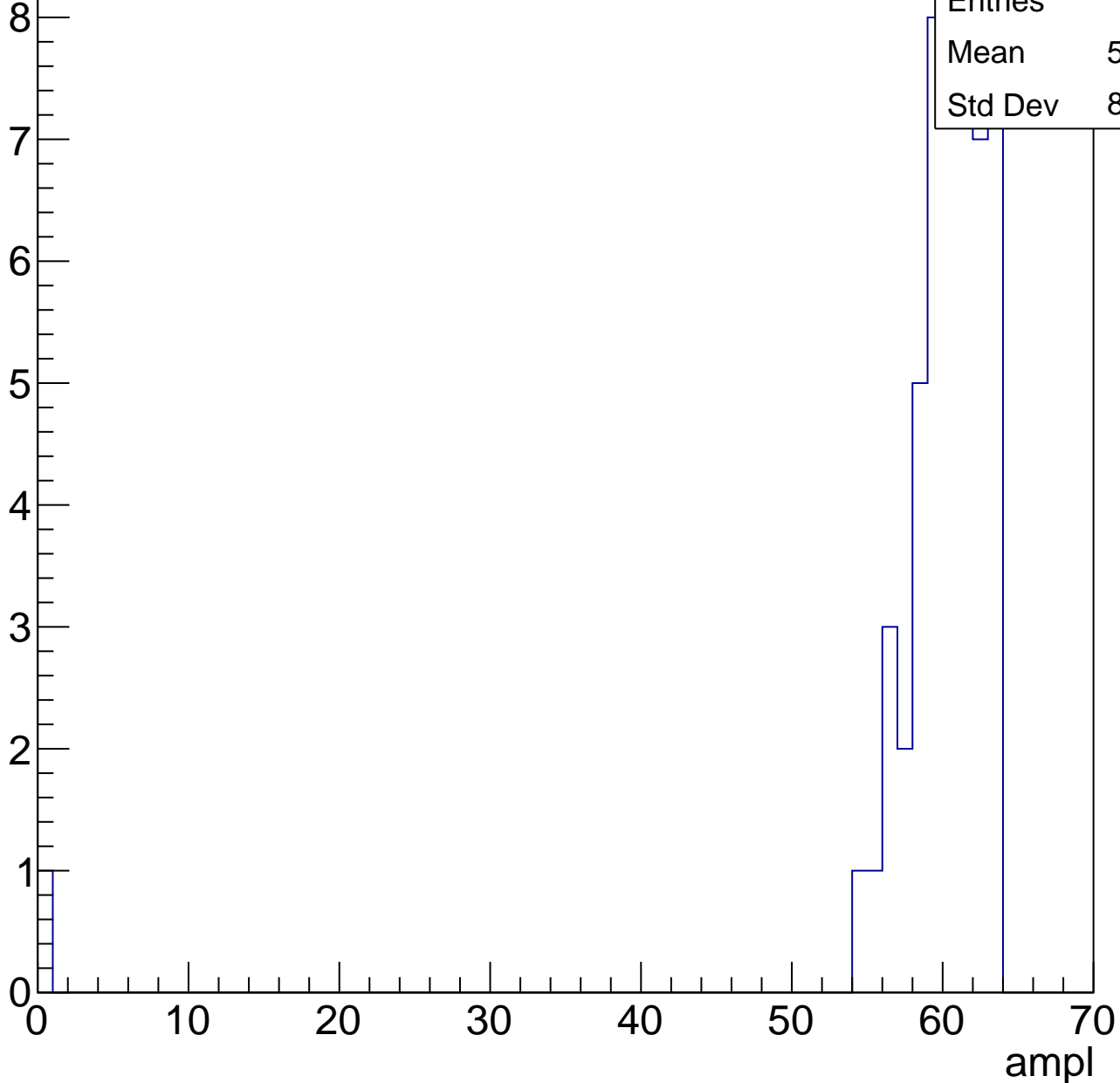


# B0L000S, U13-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.83
Std Dev	8.539



# B0L000S, U13-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.4

Std Dev

0.4899



# B0L000S, U13-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B0L000S, U13-ch85, adc0

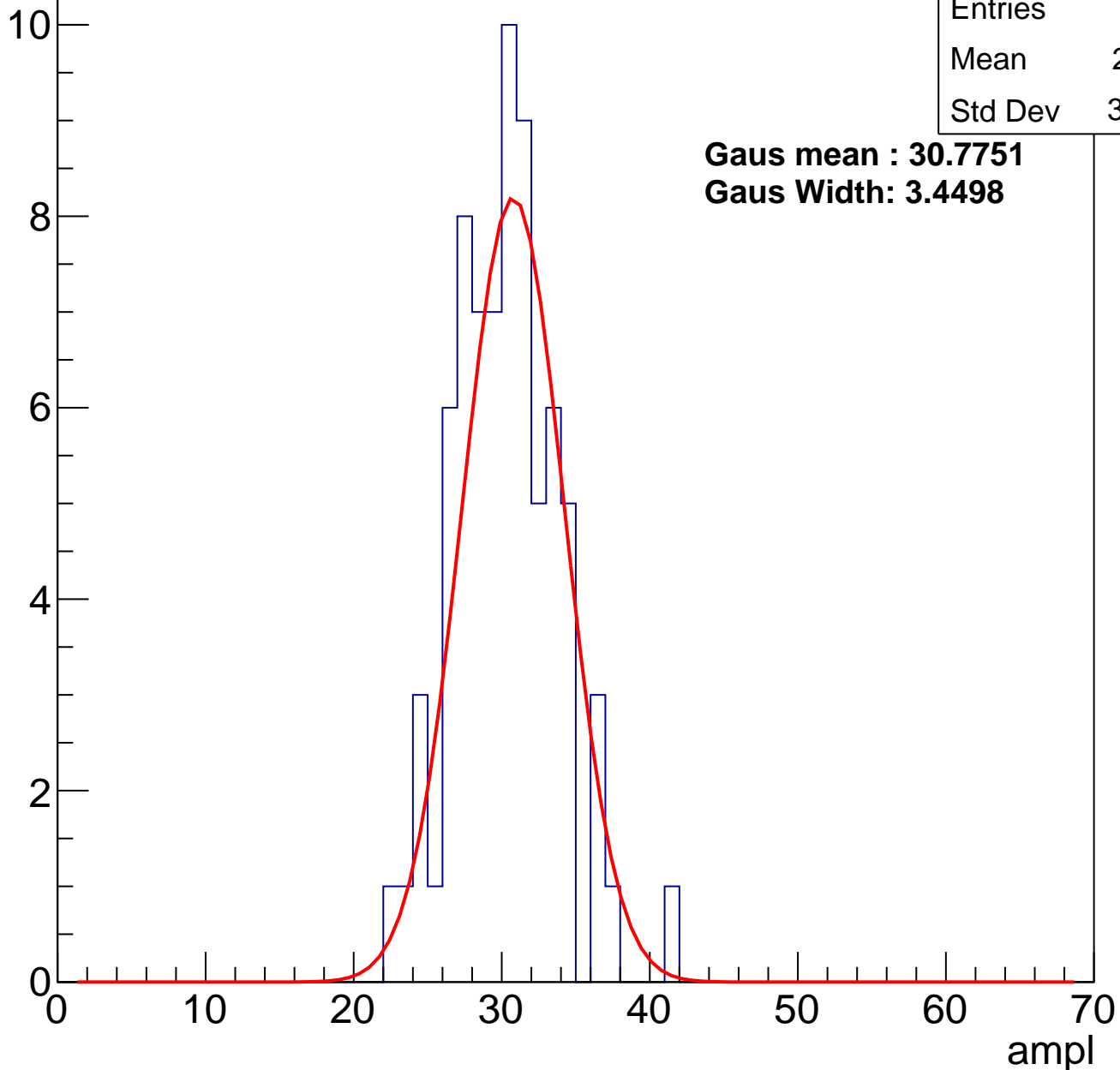
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	29.81
Std Dev	3.463

**Gaus mean : 30.7751**

**Gaus Width: 3.4498**

Entry

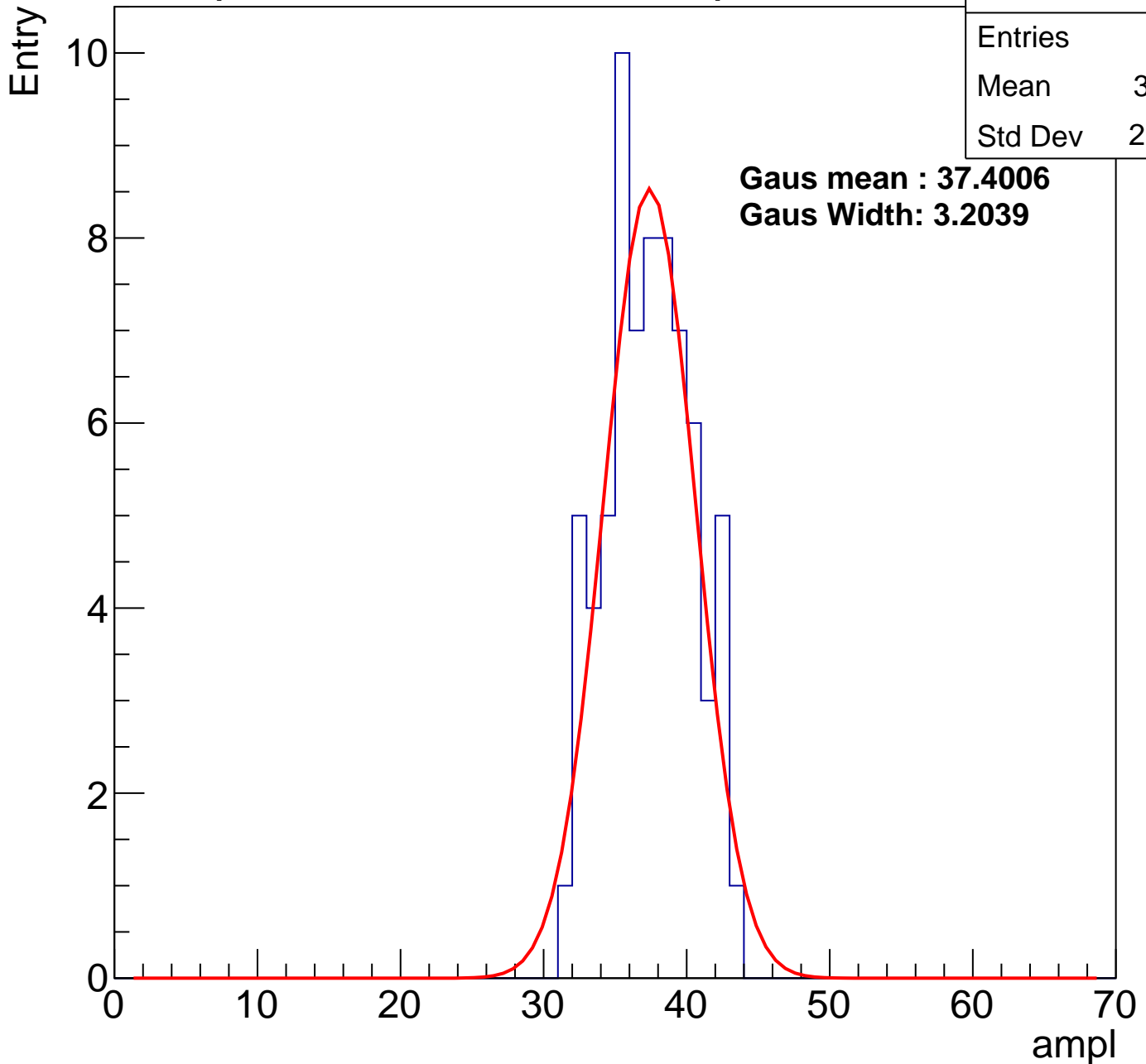


# B0L000S, U13-ch85, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	36.91
Std Dev	2.965

**Gaus mean : 37.4006**  
**Gaus Width: 3.2039**



# B0L000S, U13-ch85, adc2

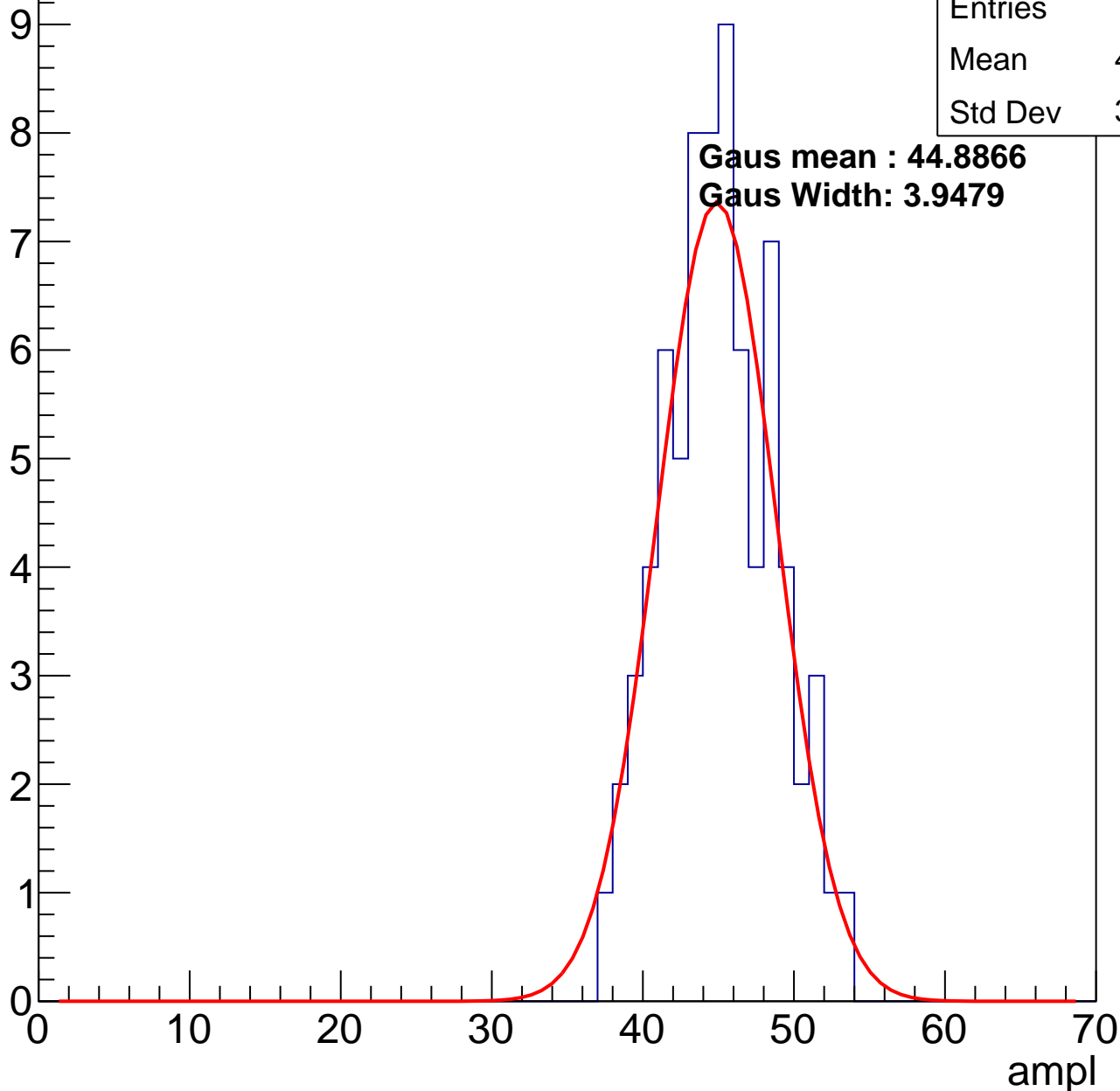
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	44.61
Std Dev	3.601

**Gaus mean : 44.8866**

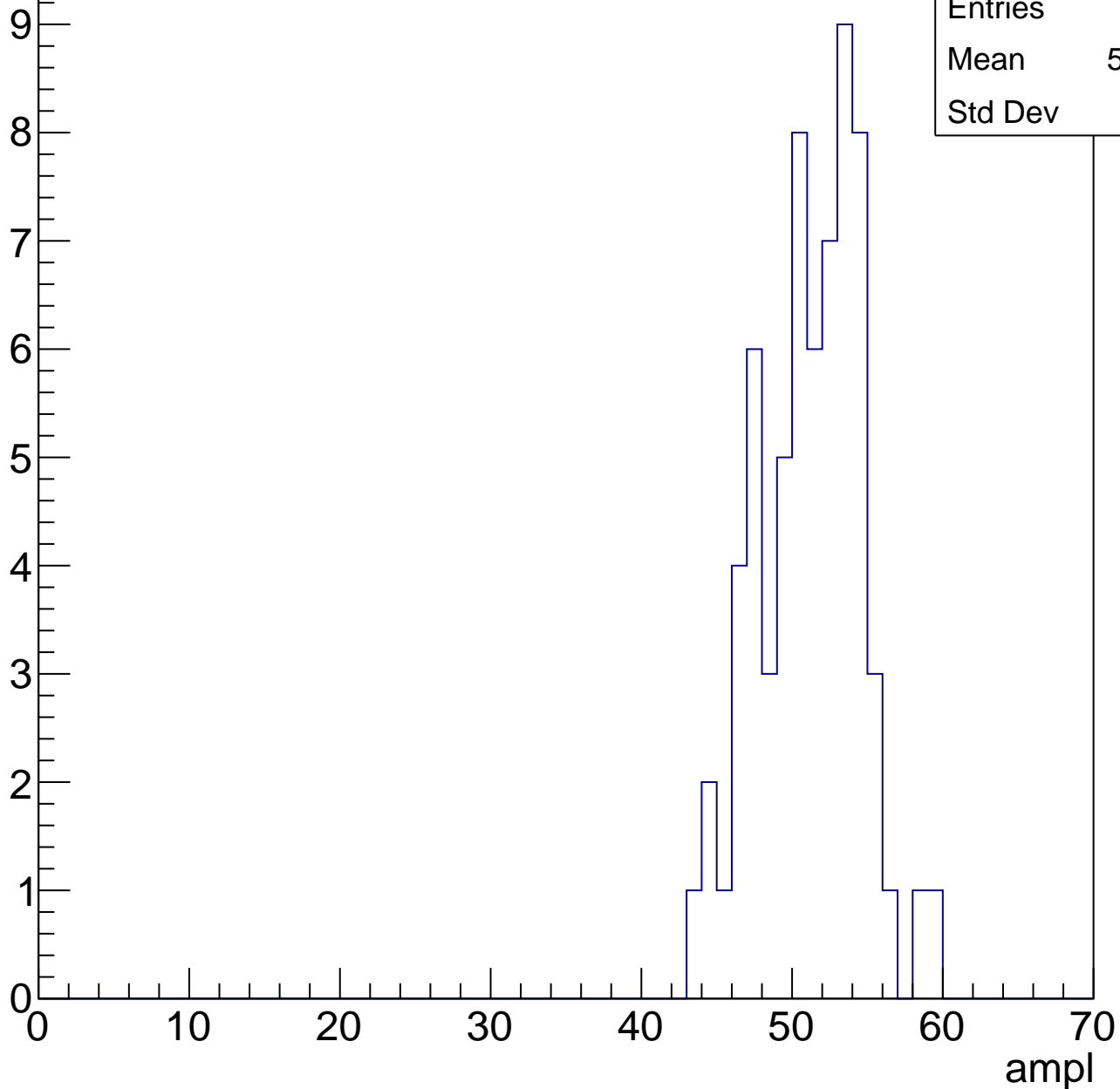
**Gaus Width: 3.9479**



# B0L000S, U13-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

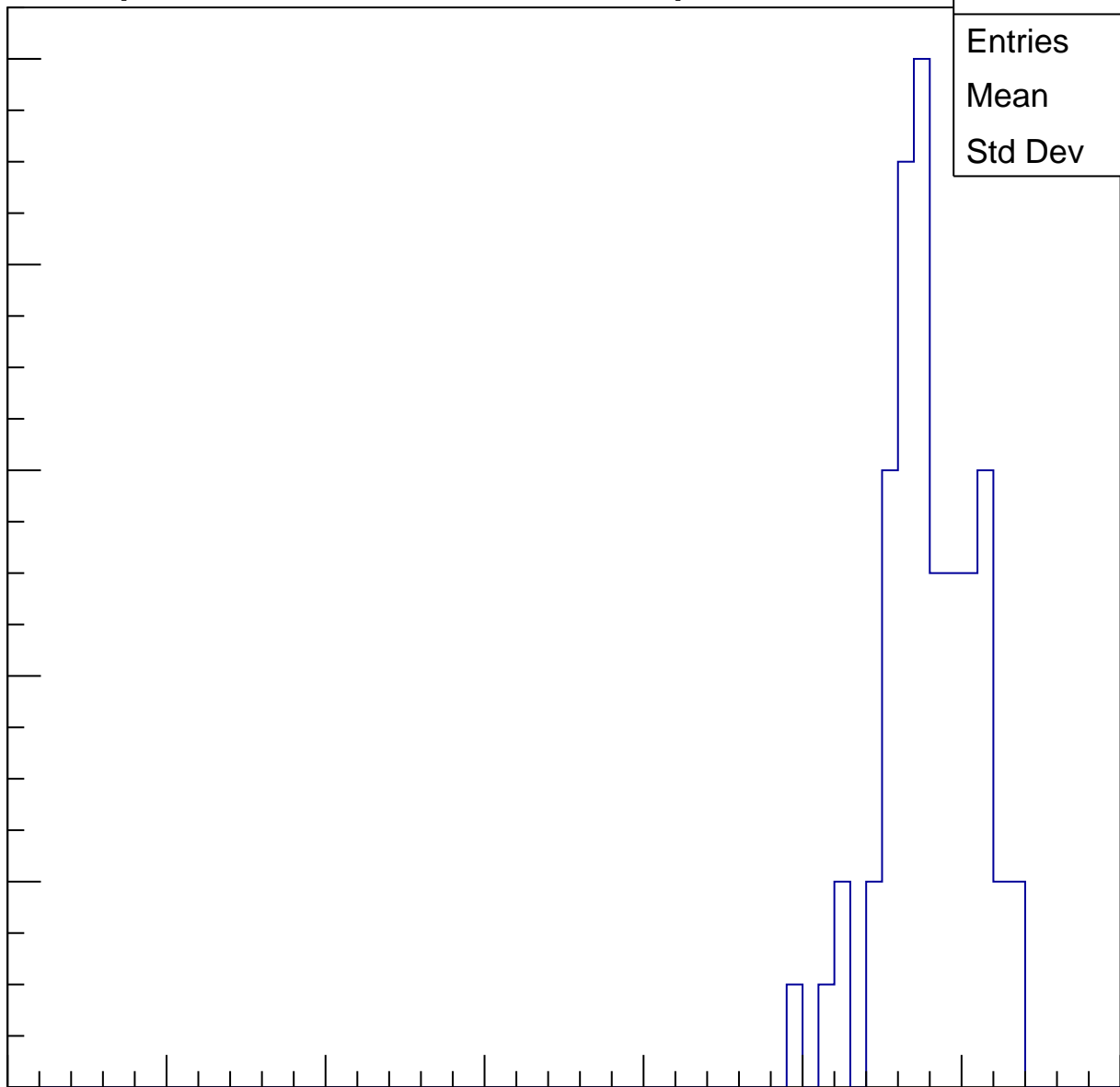
Entries	56
Mean	57.45
Std Dev	2.921

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

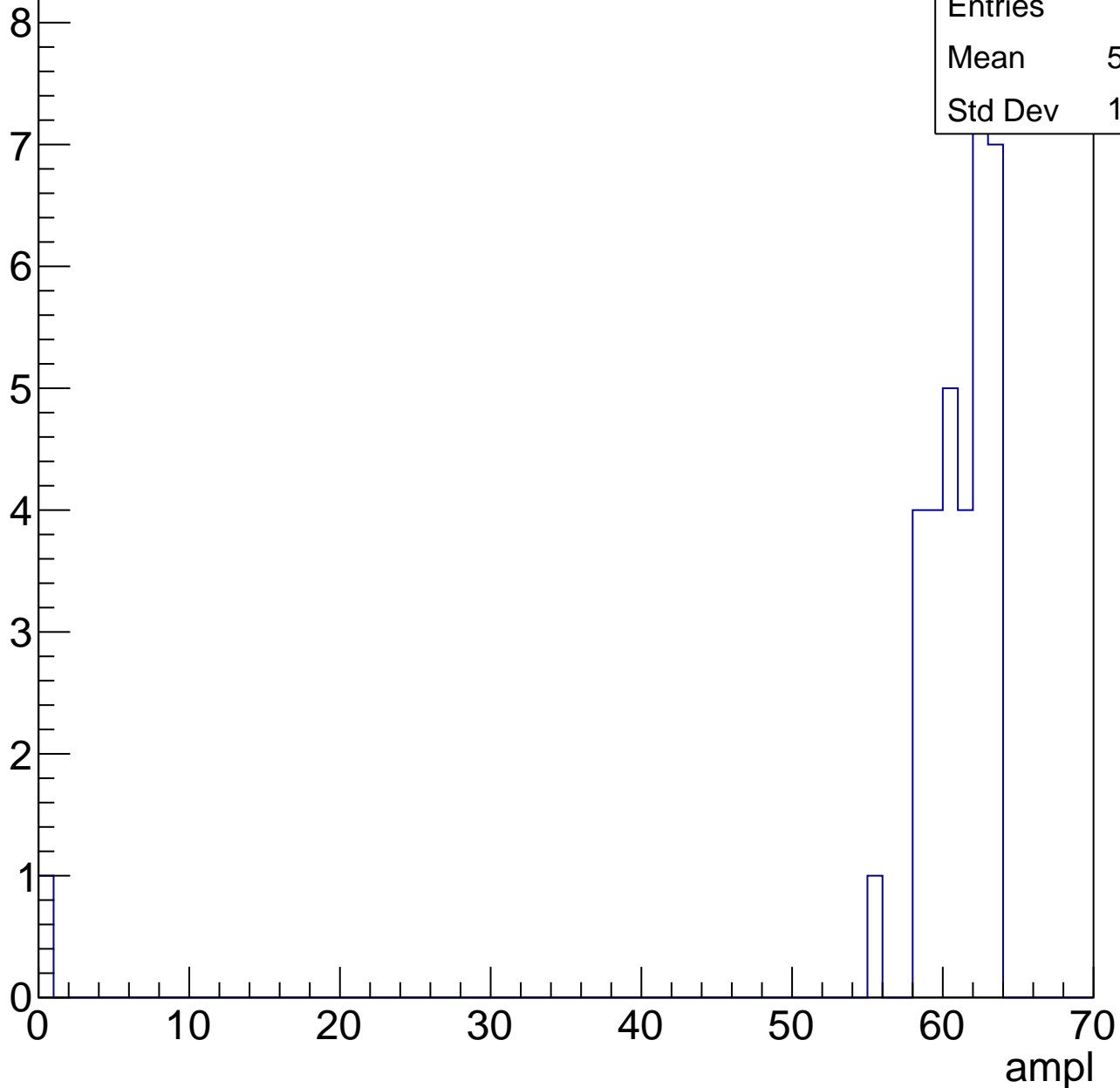


# B0L000S, U13-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.94
Std Dev	10.44



# B0L000S, U13-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	63
Std Dev	0



# B0L000S, U13-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch86, adc0

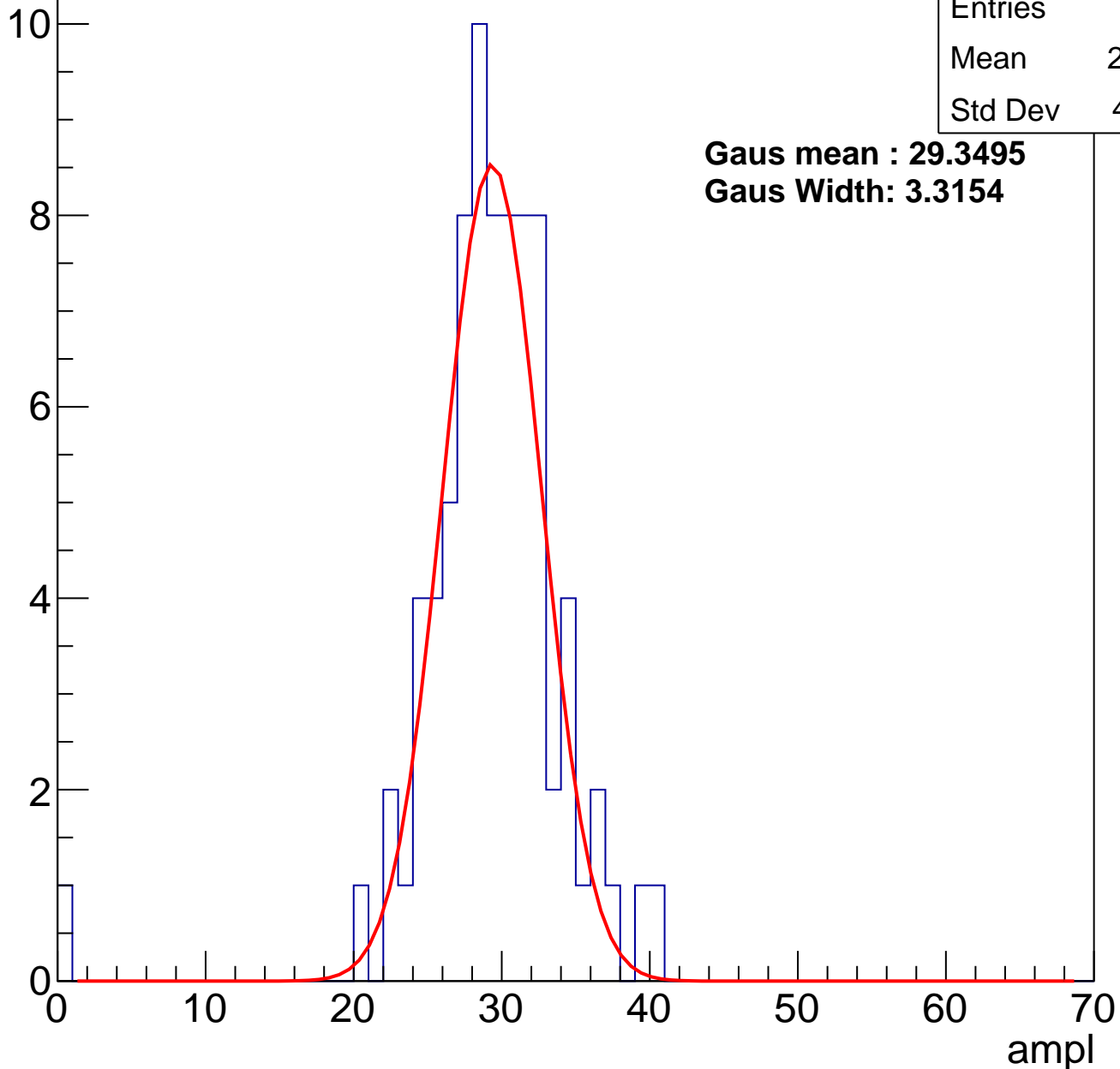
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	28.88
Std Dev	4.961

**Gaus mean : 29.3495**

**Gaus Width: 3.3154**

Entry



# B0L000S, U13-ch86, adc1

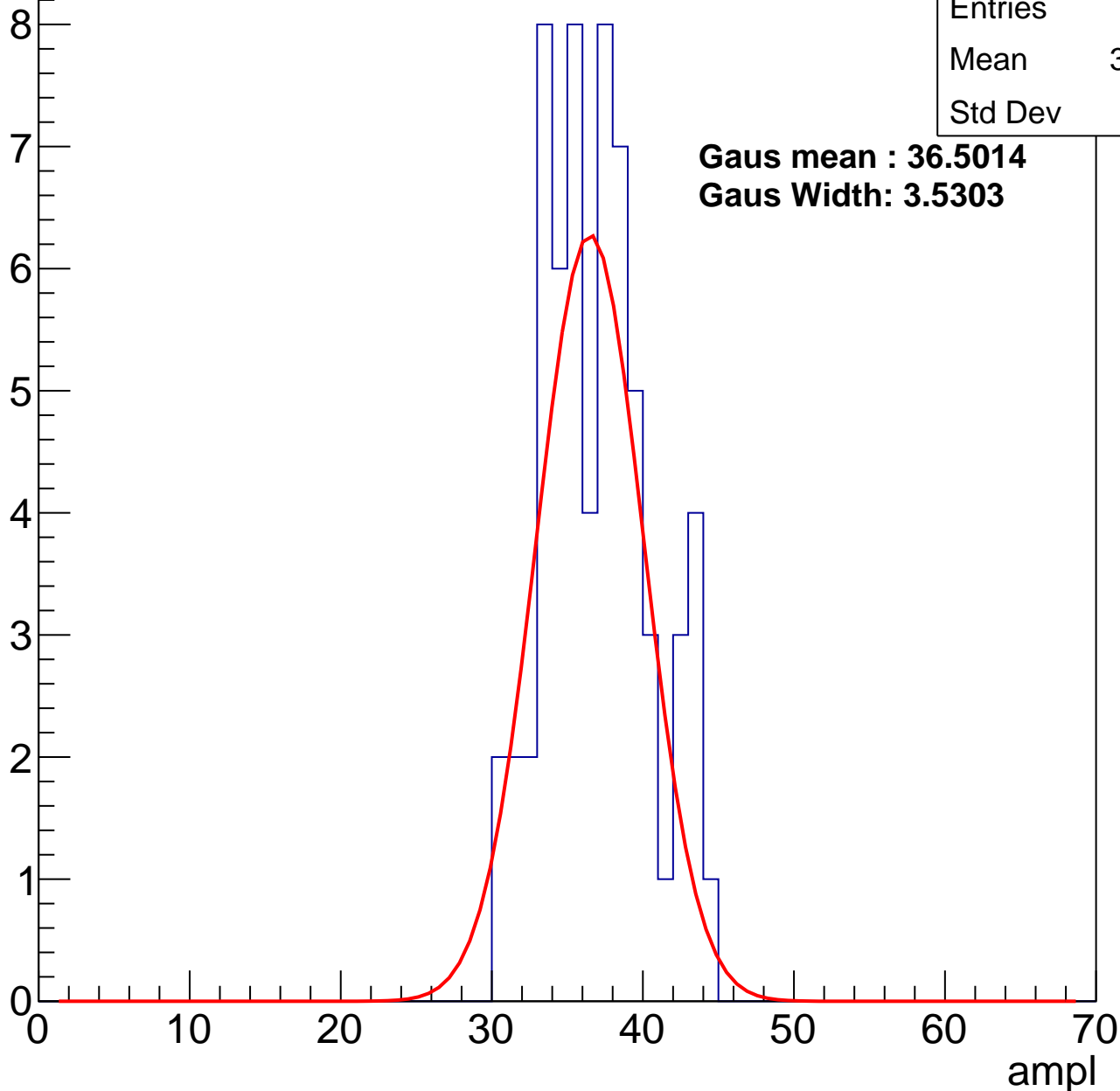
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.53
Std Dev	3.45

**Gaus mean : 36.5014**

**Gaus Width: 3.5303**



# B0L000S, U13-ch86, adc2

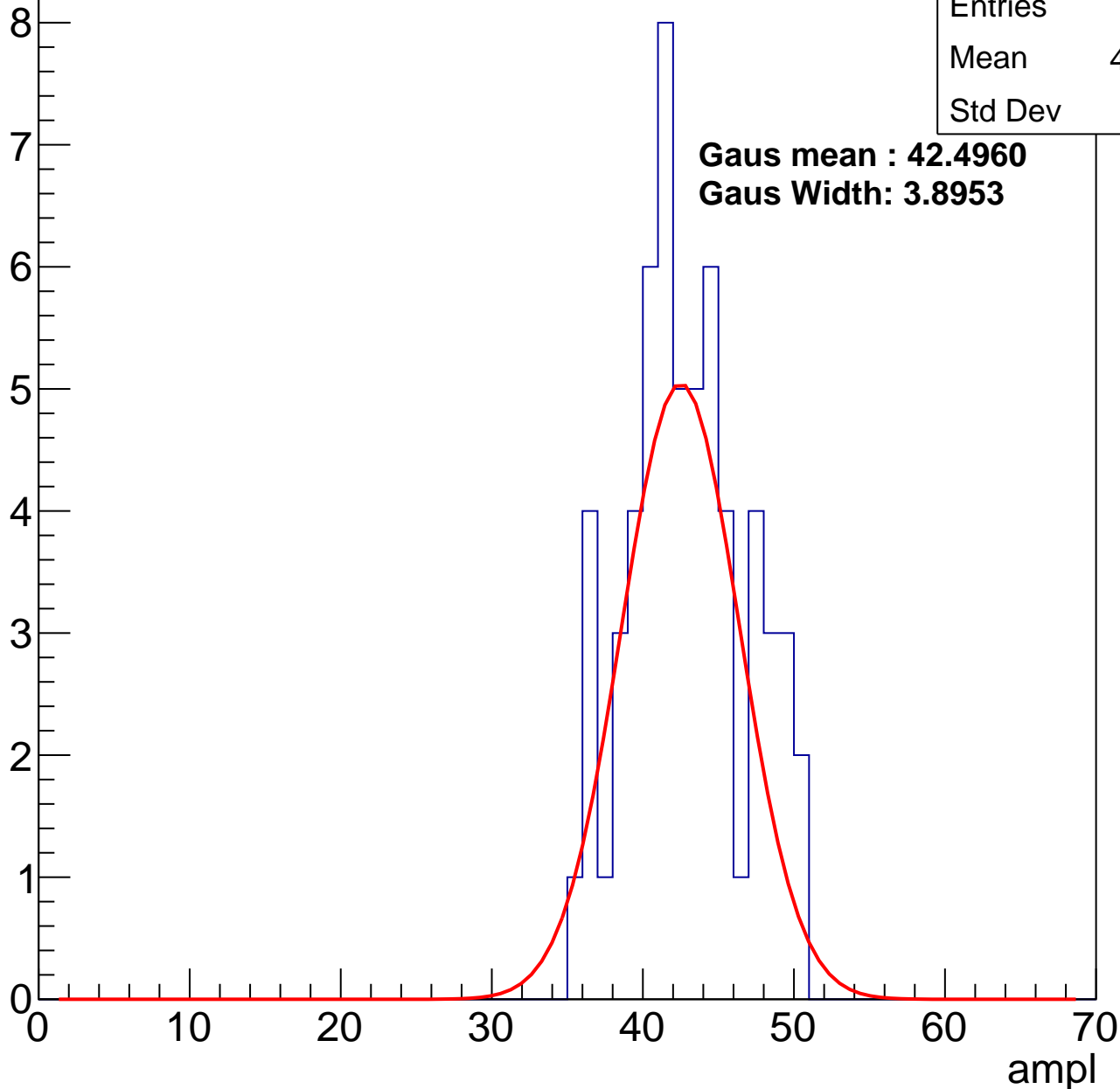
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.47
Std Dev	3.84

**Gaus mean : 42.4960**

**Gaus Width: 3.8953**



# B0L000S, U13-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

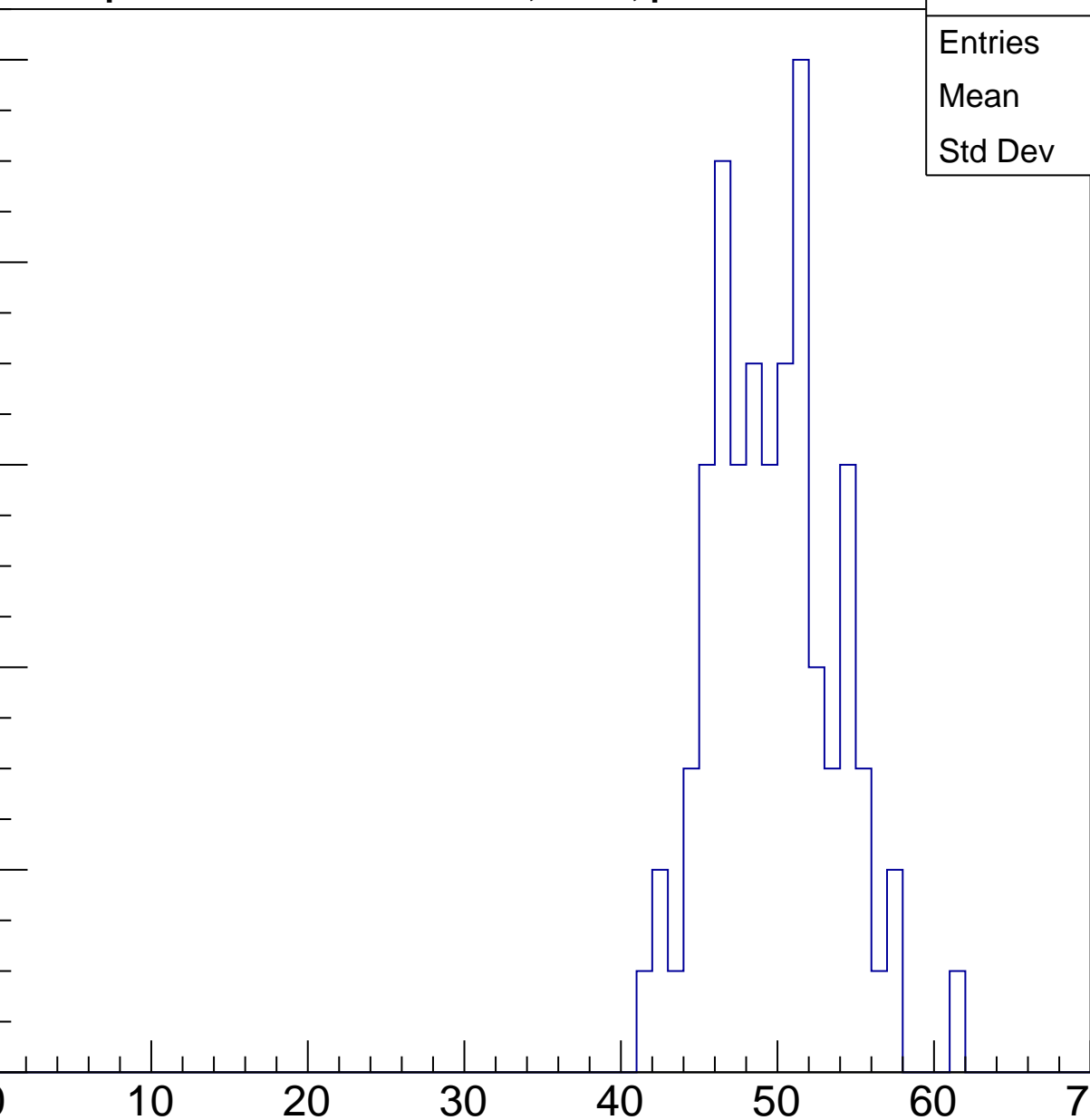
Entries	78
Mean	49.27
Std Dev	3.898

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

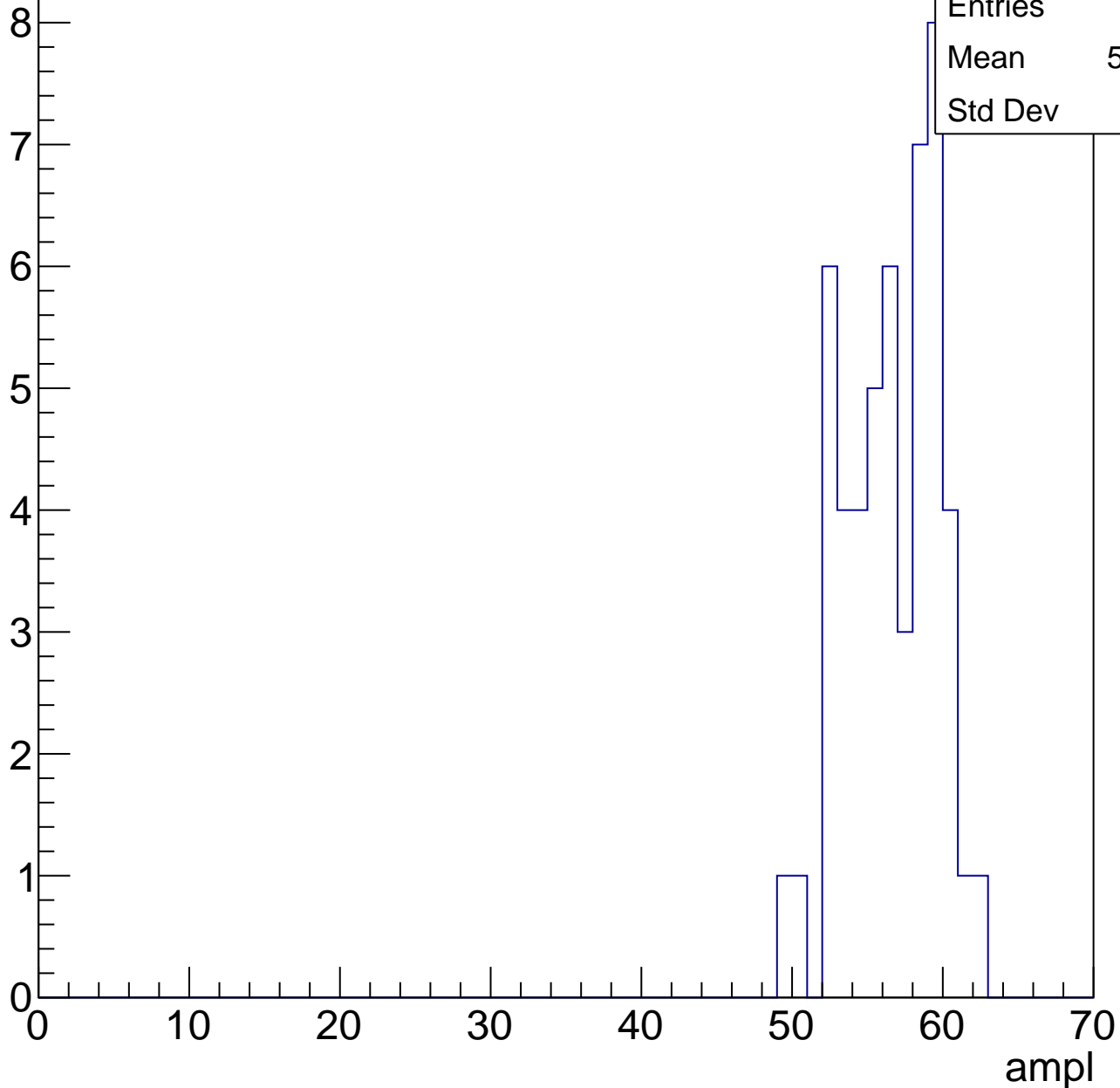


# B0L000S, U13-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	56.12
Std Dev	3.02

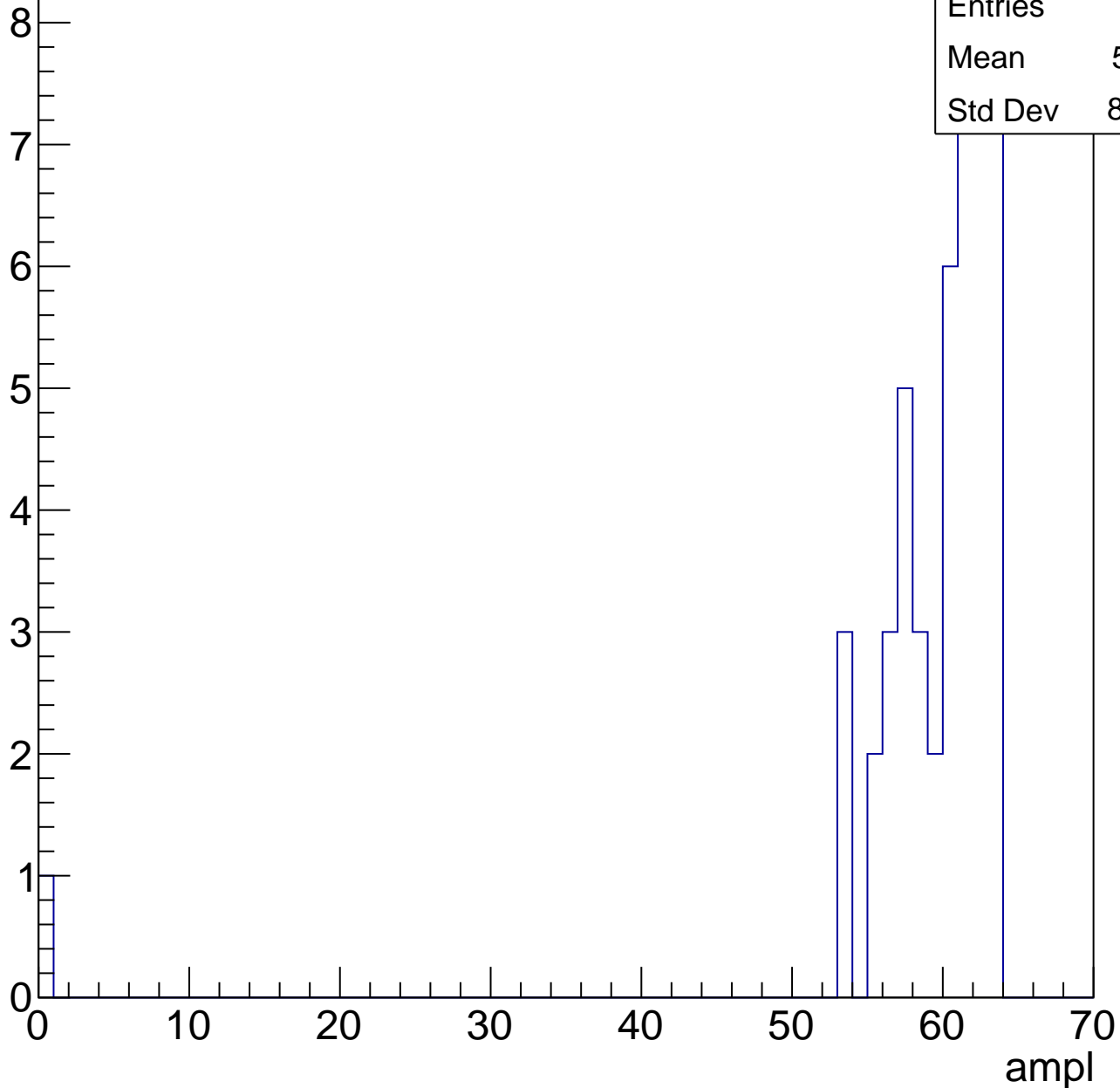


# B0L000S, U13-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.41
Std Dev	8.912



# B0L000S, U13-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch87, adc0

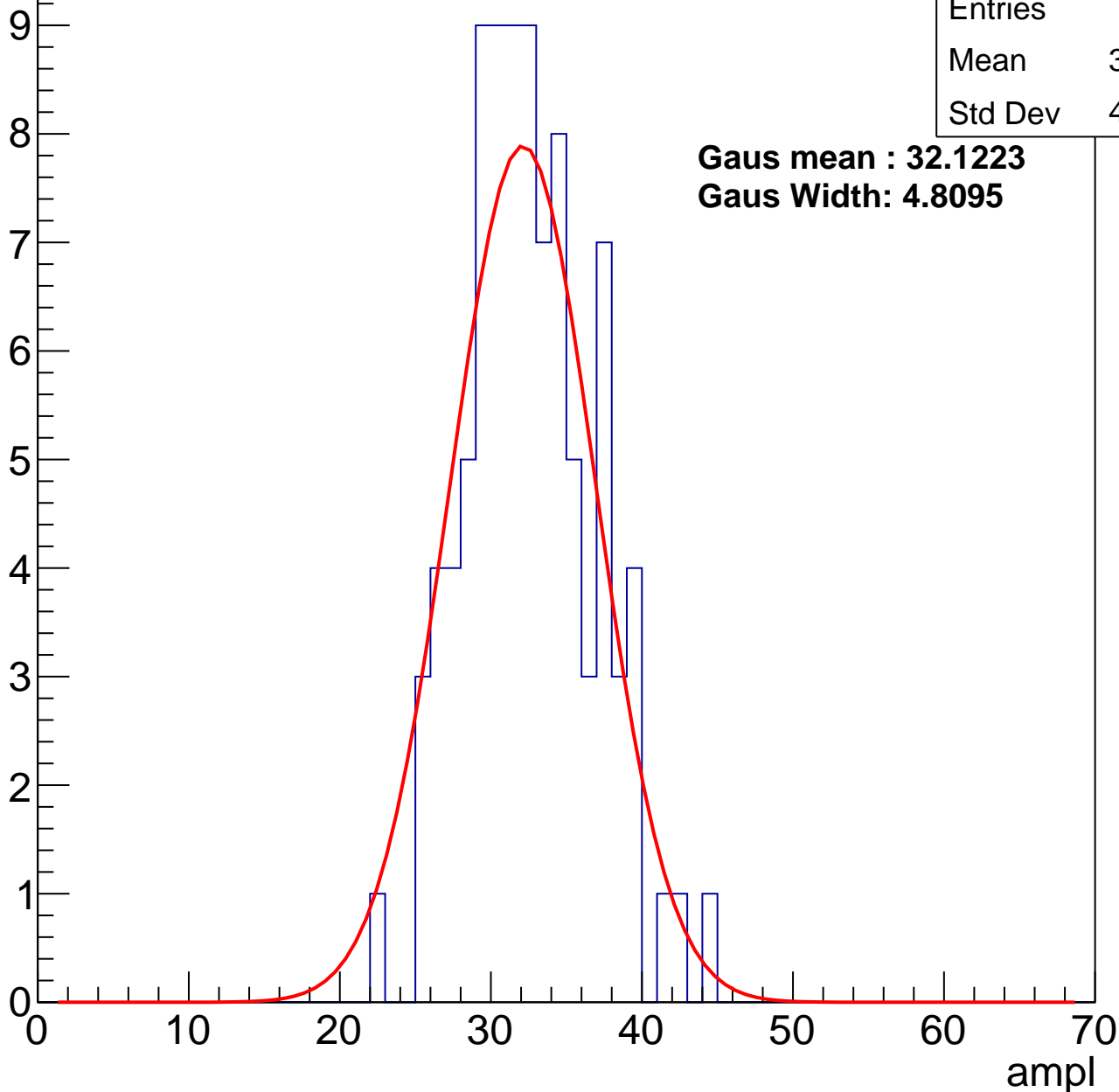
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	93
Mean	32.14
Std Dev	4.188

**Gaus mean : 32.1223**

**Gaus Width: 4.8095**



# B0L000S, U13-ch87, adc1

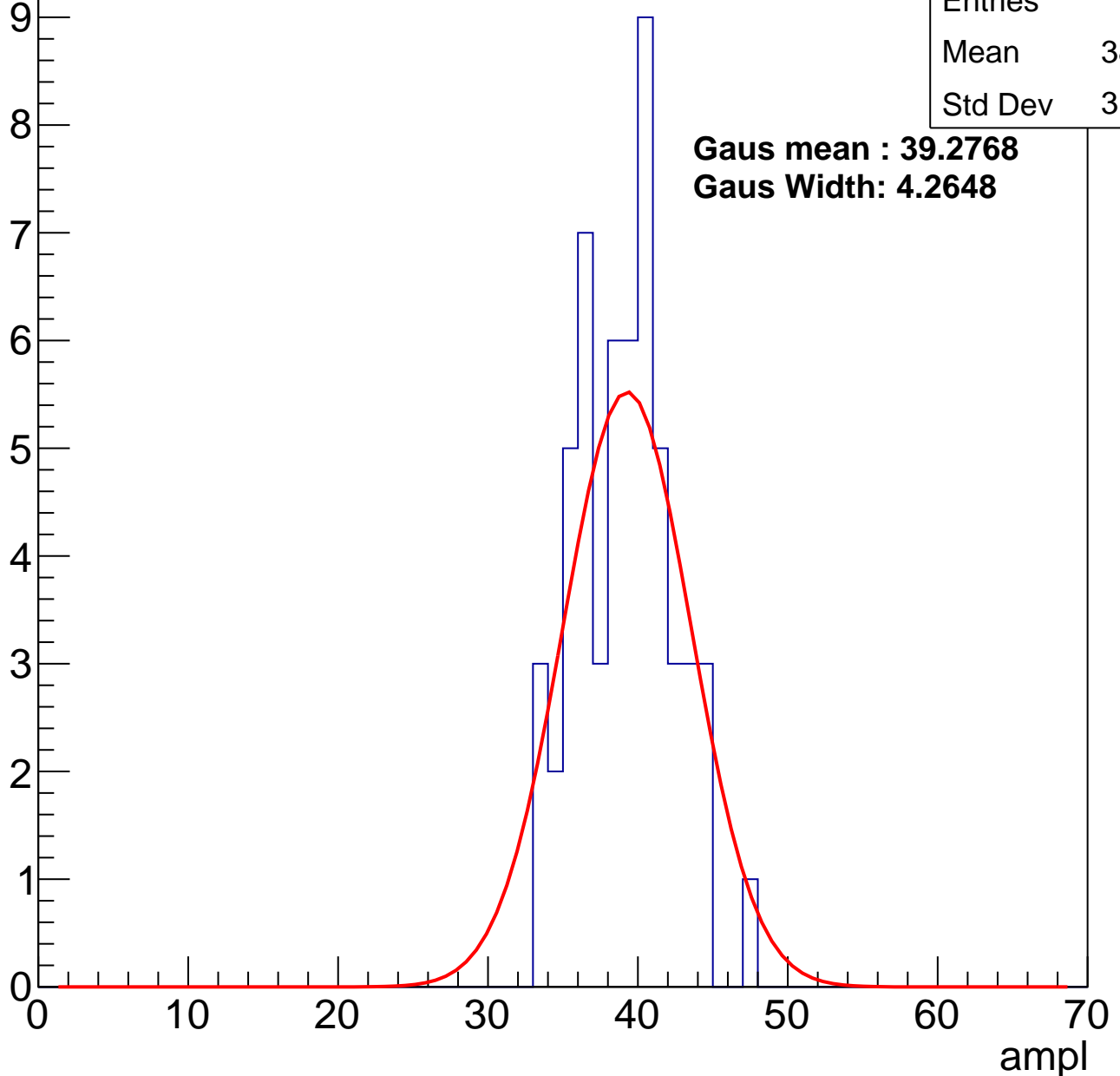
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	38.68
Std Dev	3.152

**Gaus mean : 39.2768**

**Gaus Width: 4.2648**



# B0L000S, U13-ch87, adc2

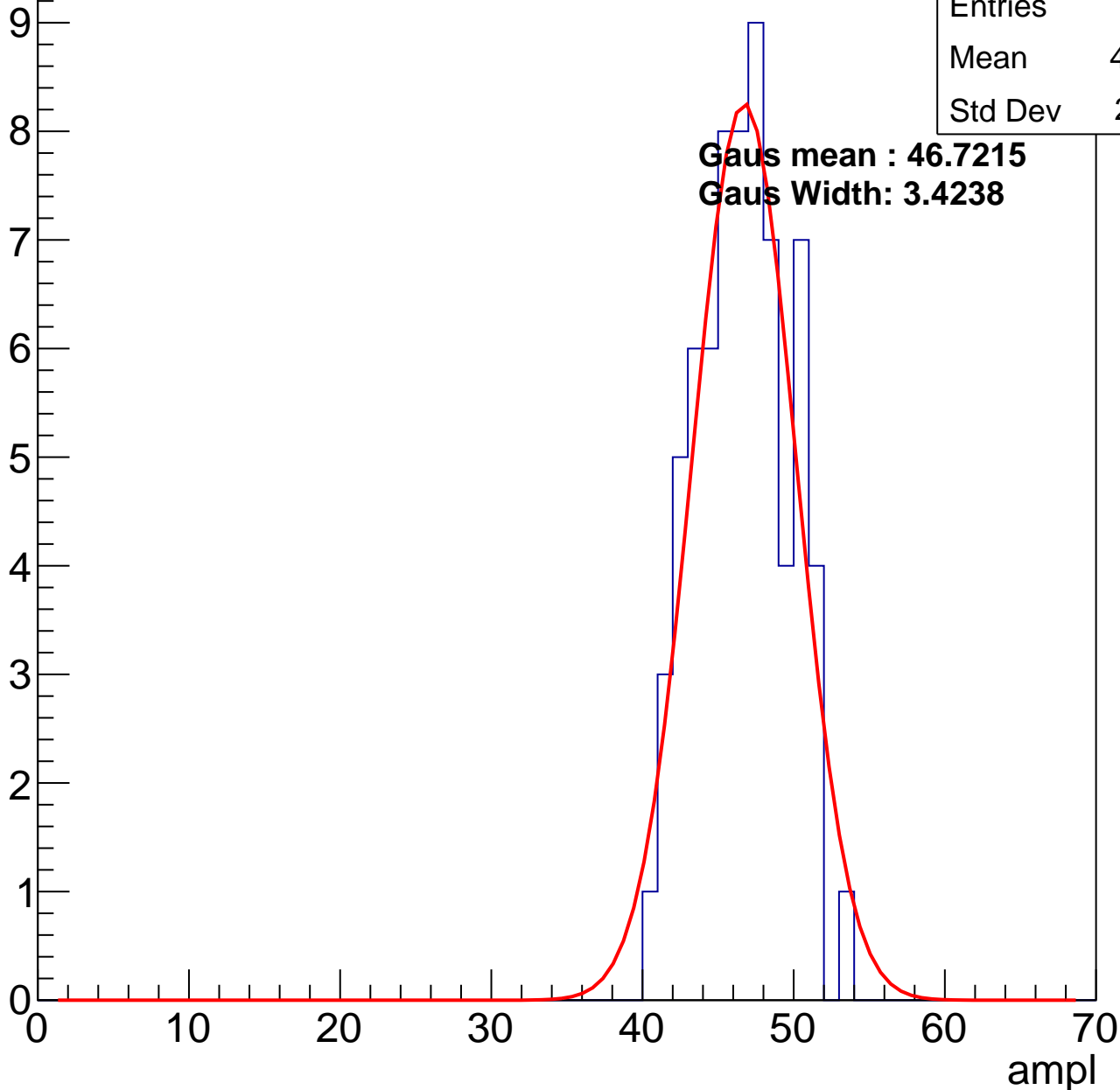
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	46.16
Std Dev	2.971

**Gaus mean : 46.7215**

**Gaus Width: 3.4238**

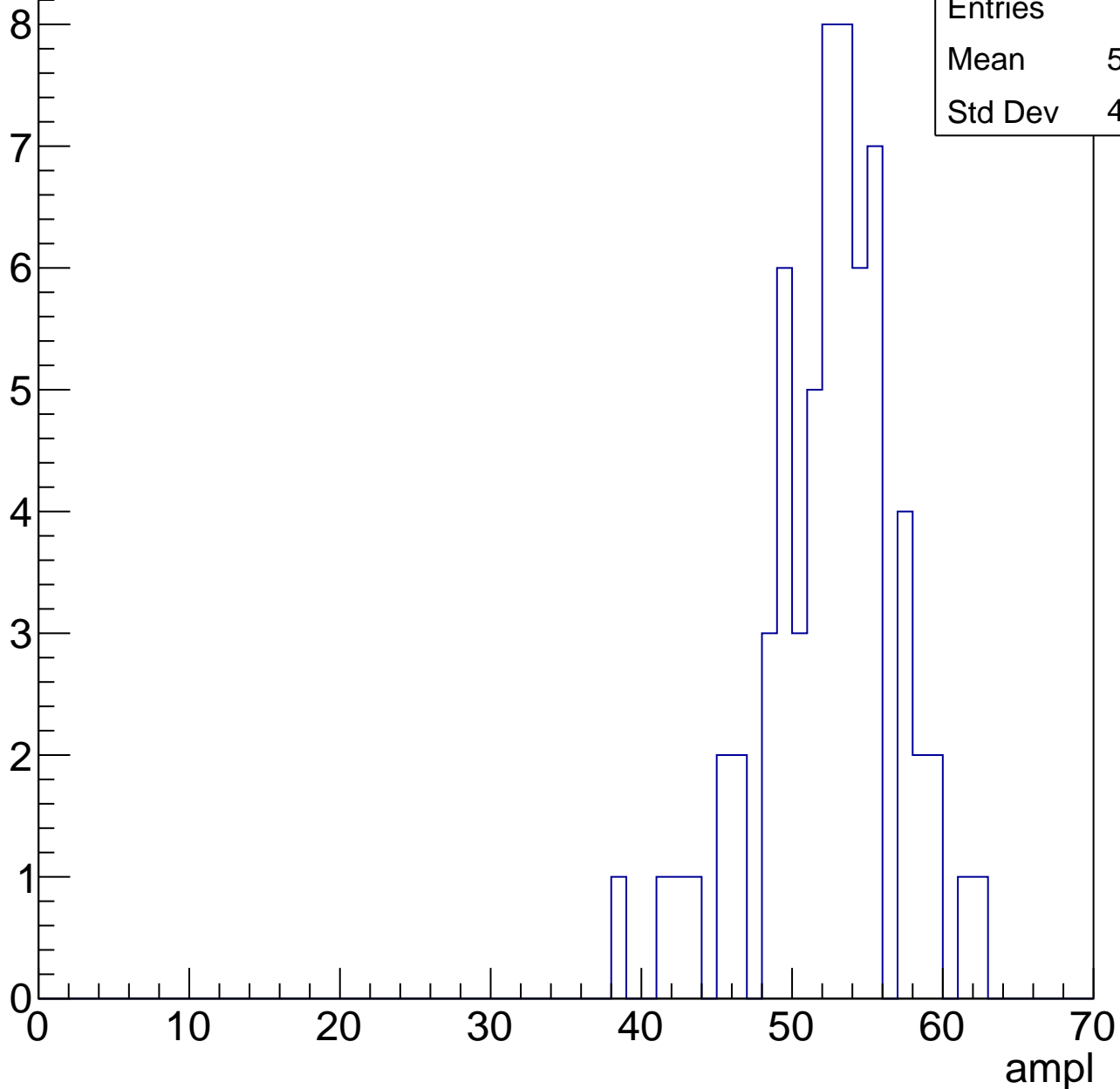


# B0L000S, U13-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

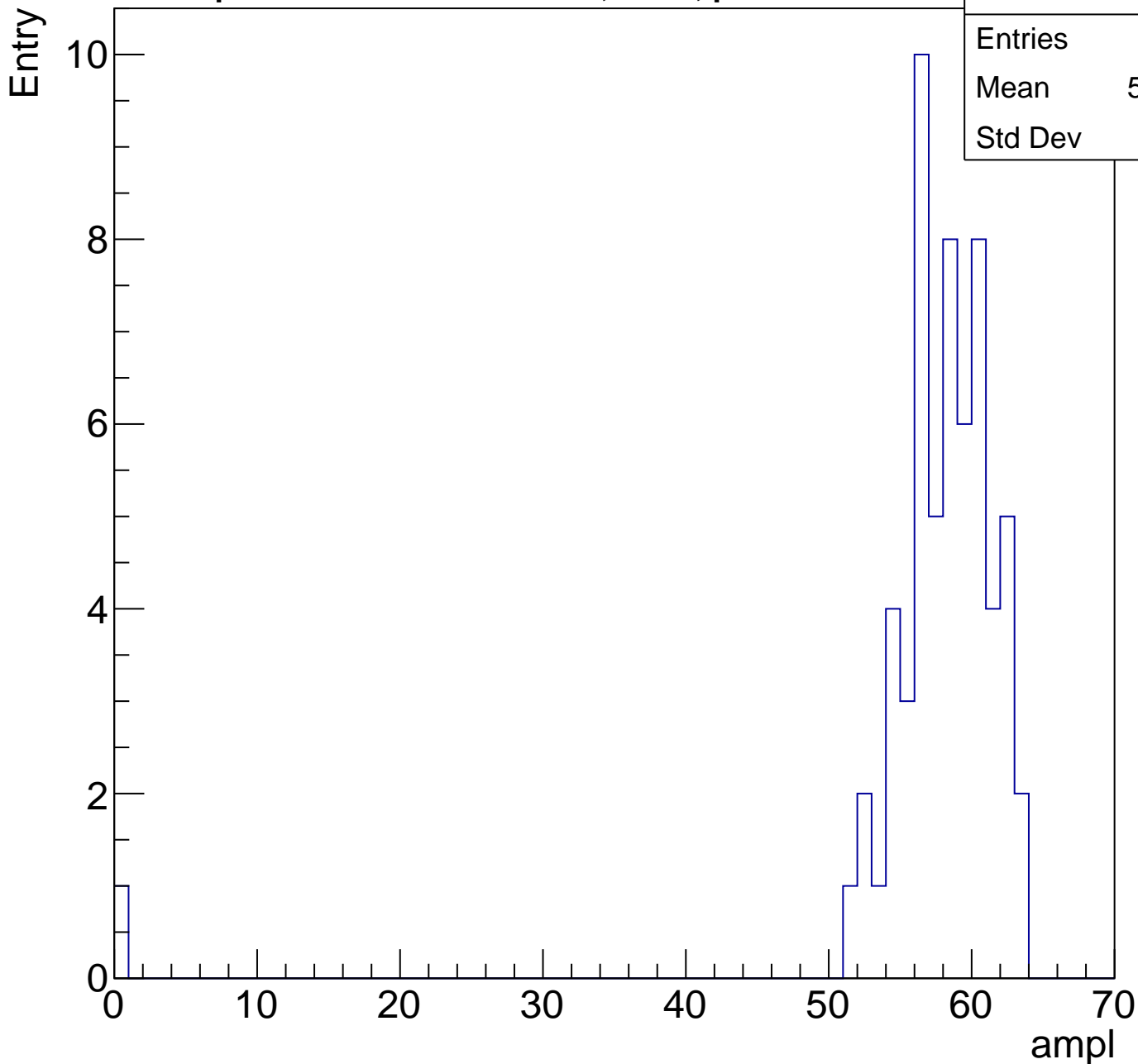
Entries	64
Mean	51.92
Std Dev	4.563



# B0L000S, U13-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	60
Mean	56.87
Std Dev	7.93

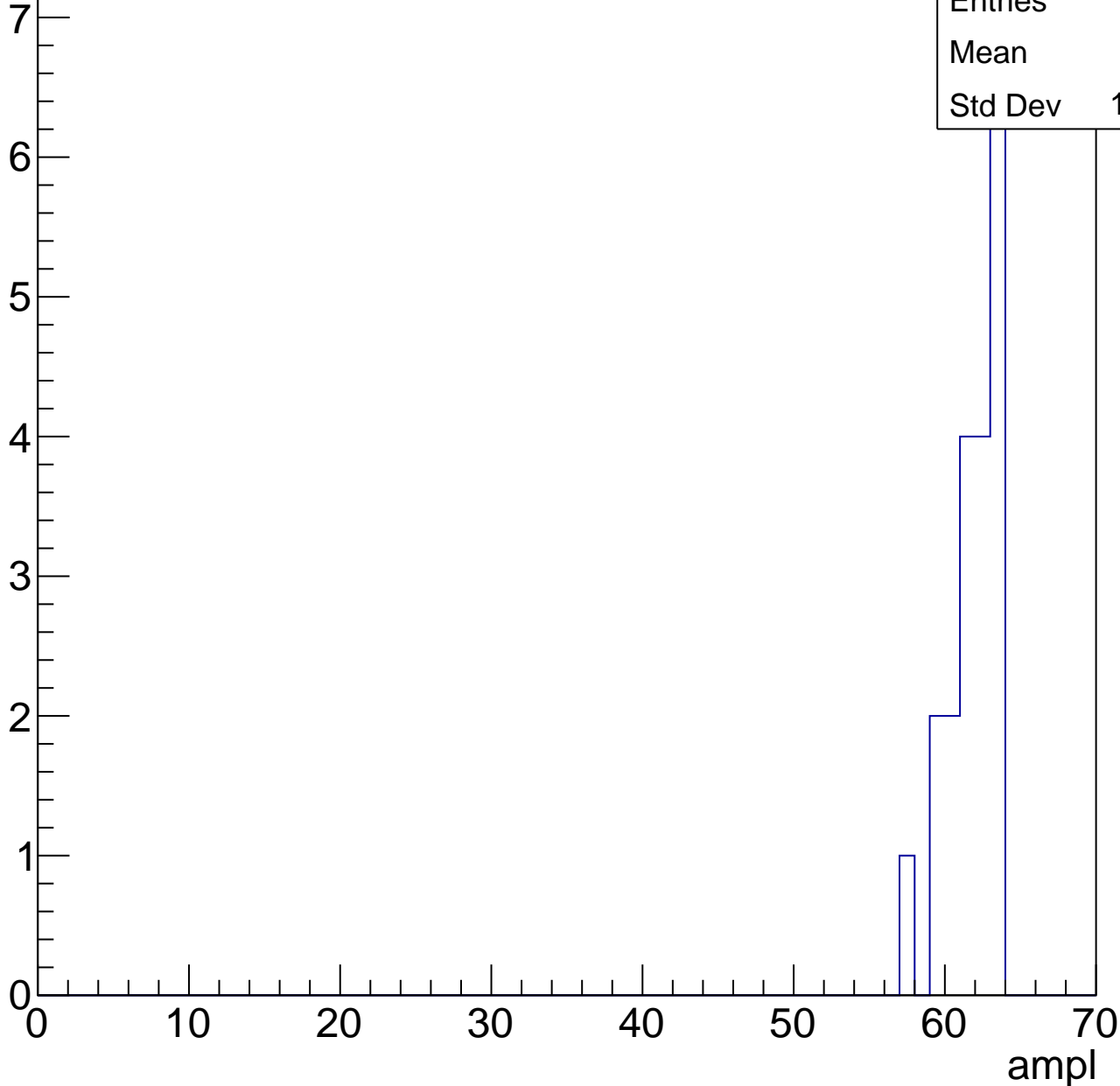


# B0L000S, U13-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	61.4
Std Dev	1.655



# B0L000S, U13-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	62
Std Dev	1



# B0L000S, U13-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch88, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	31.28
Std Dev	5.22

**Gaus mean : 32.4105**

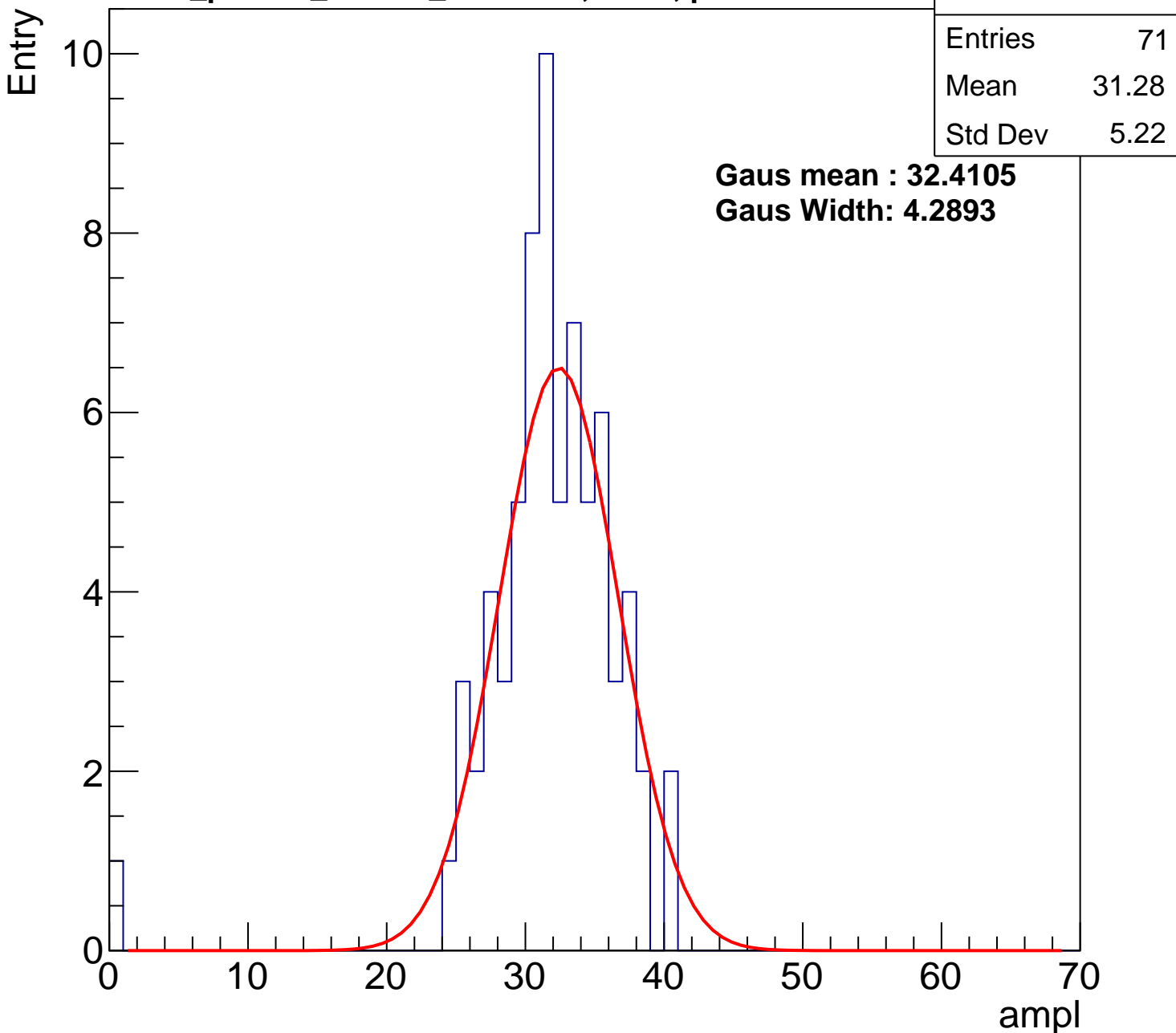
**Gaus Width: 4.2893**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch88, adc1

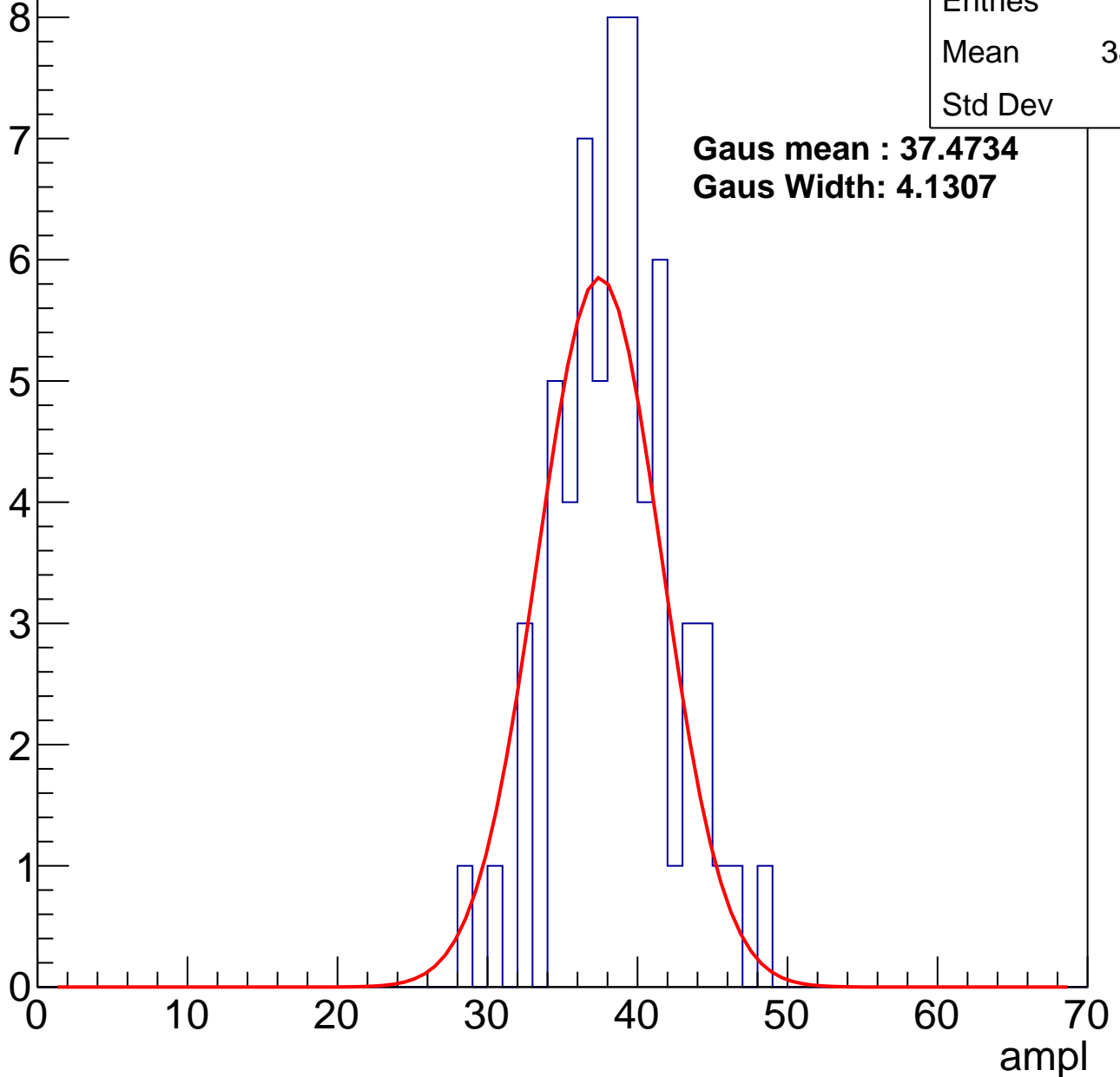
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	38.15
Std Dev	3.83

**Gaus mean : 37.4734**

**Gaus Width: 4.1307**



# B0L000S, U13-ch88, adc2

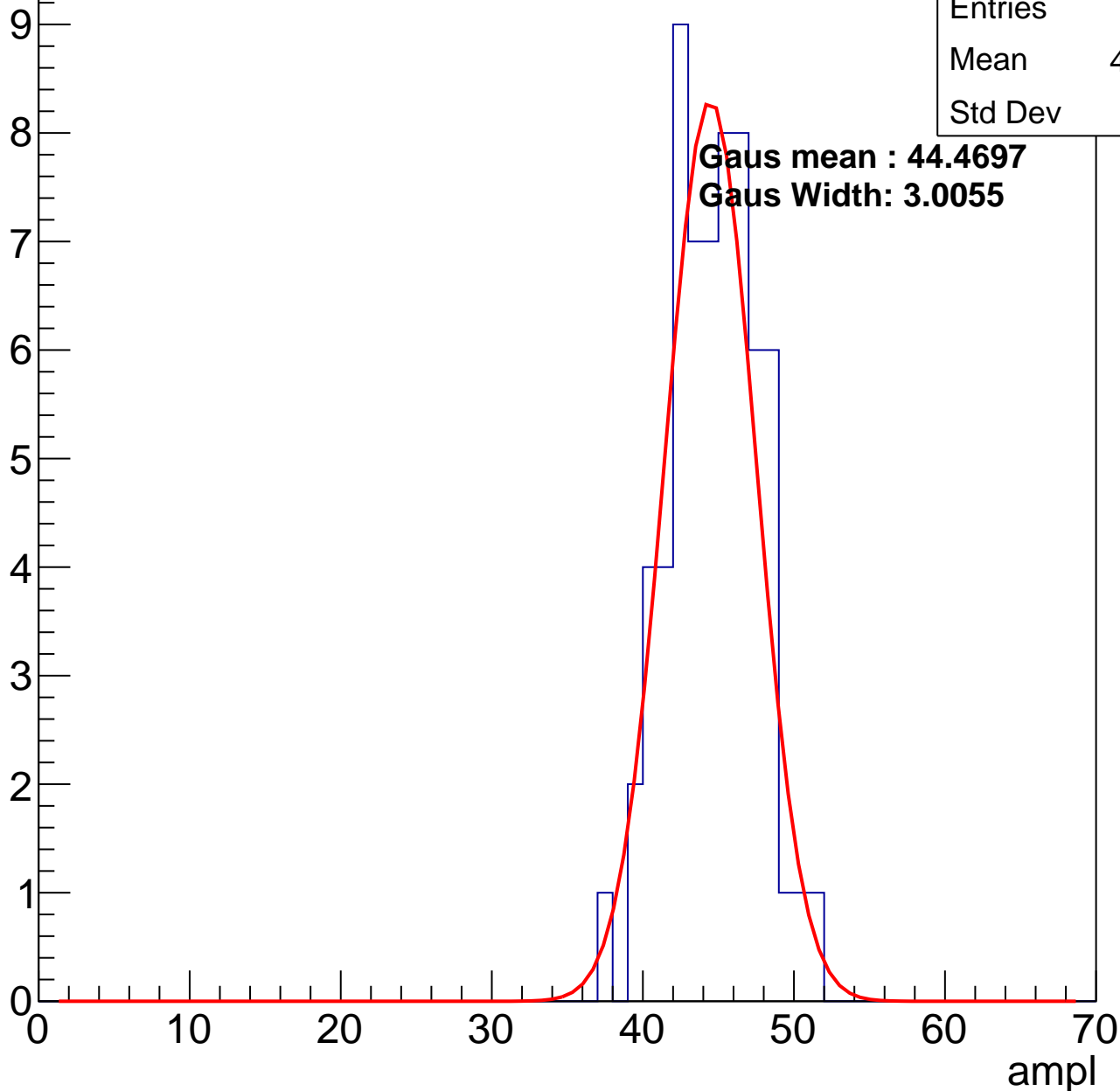
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.22
Std Dev	2.88

**Gaus mean : 44.4697**

**Gaus Width: 3.0055**

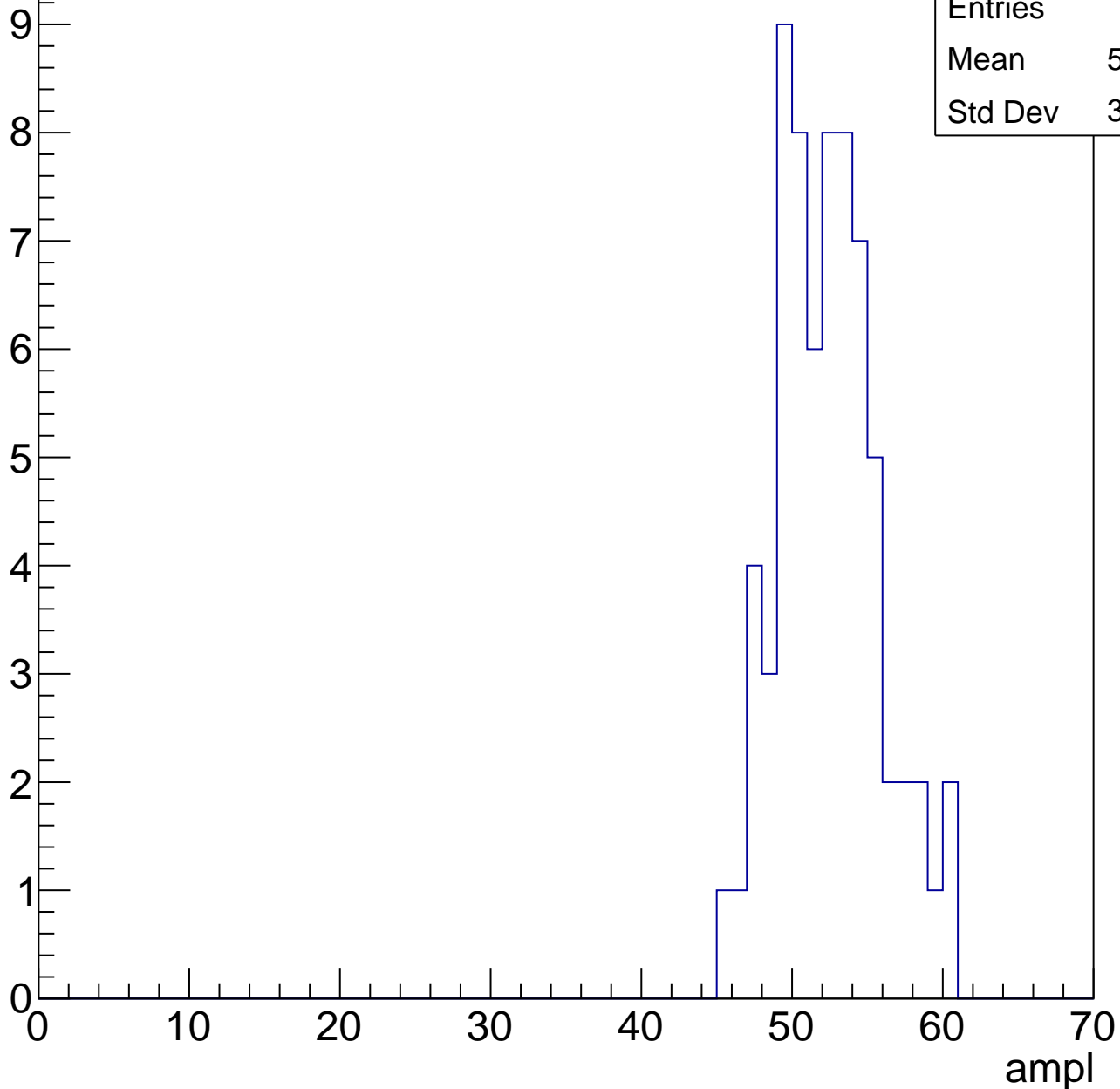


# B0L000S, U13-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	51.94
Std Dev	3.327

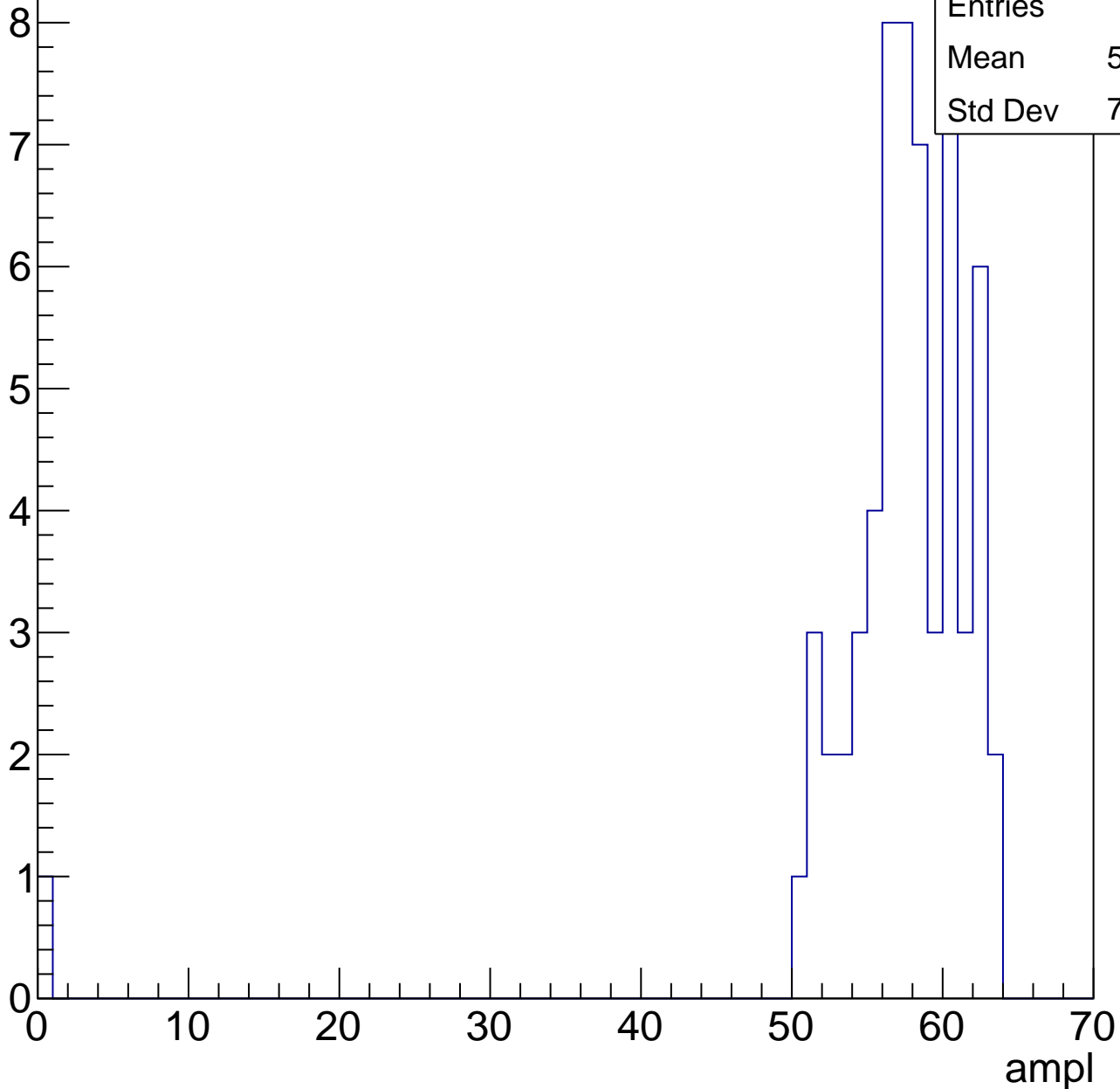


# B0L000S, U13-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56.44
Std Dev	7.976

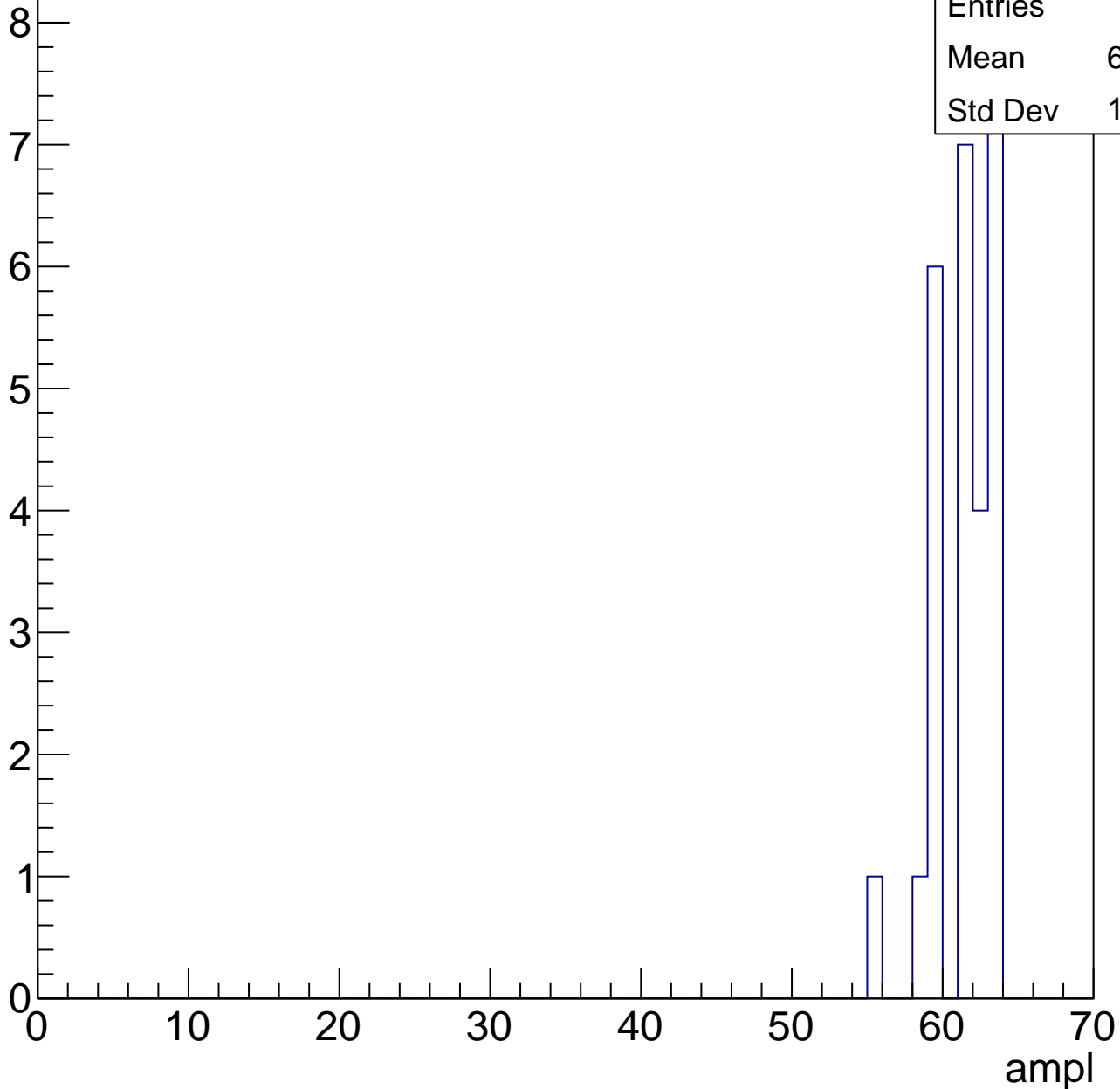


# B0L000S, U13-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	60.96
Std Dev	1.972



# B0L000S, U13-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch89, adc0

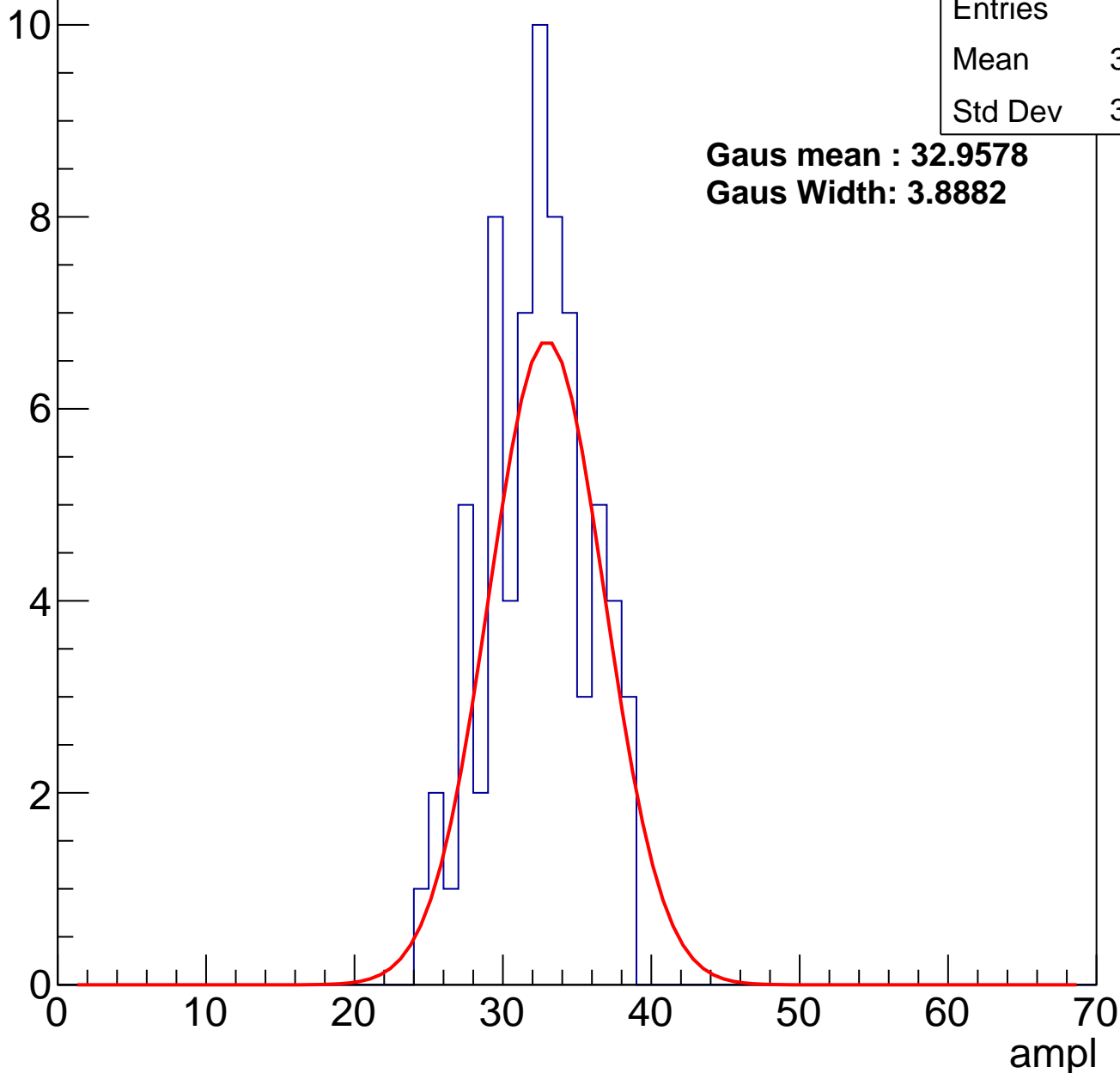
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	31.84
Std Dev	3.379

**Gaus mean : 32.9578**

**Gaus Width: 3.8882**

Entry



# B0L000S, U13-ch89, adc1

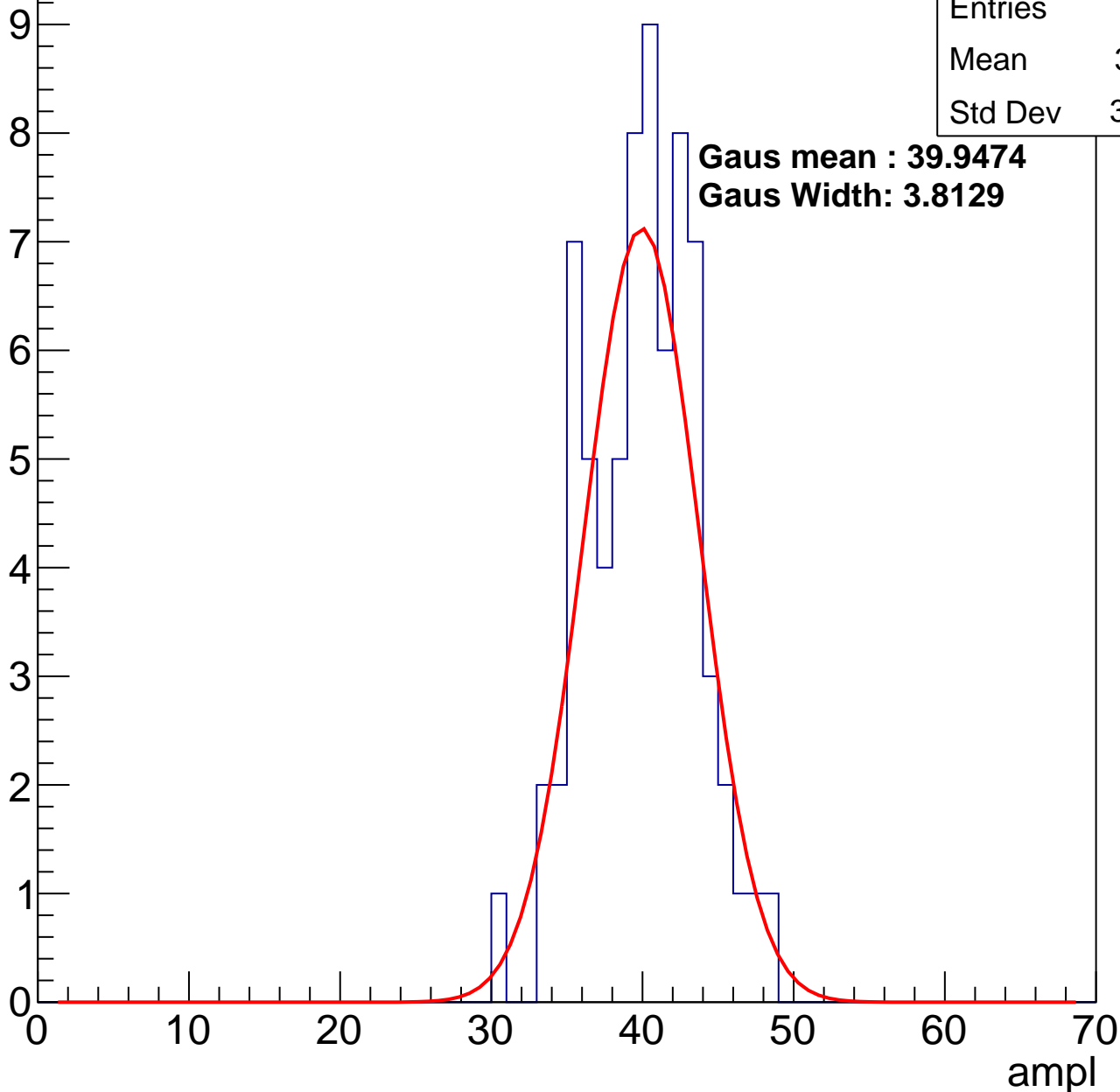
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	39.51
Std Dev	3.575

**Gaus mean : 39.9474**

**Gaus Width: 3.8129**



# B0L000S, U13-ch89, adc2

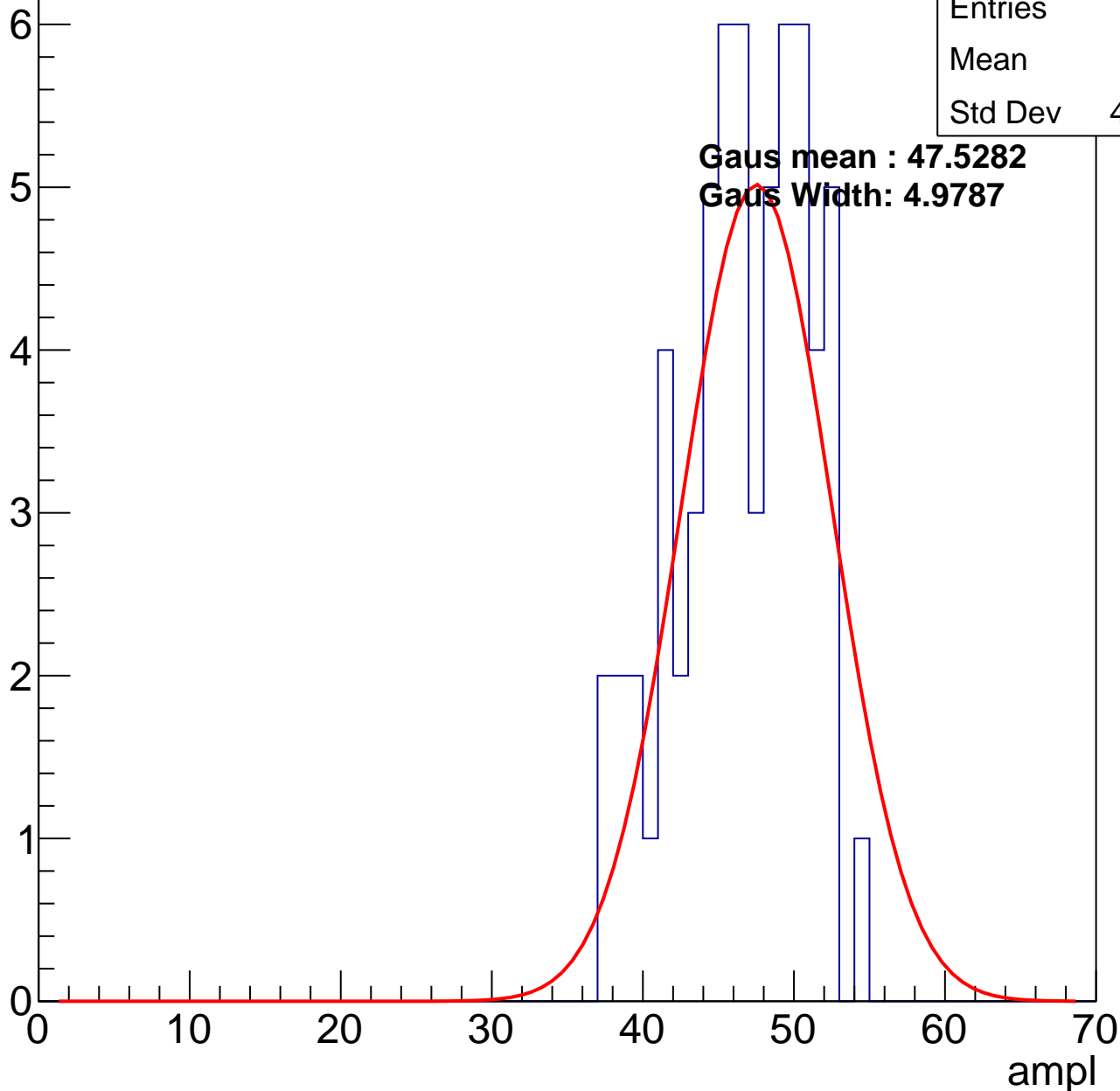
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	46.1
Std Dev	4.227

**Gaus mean : 47.5282**

**Gaus Width: 4.9787**

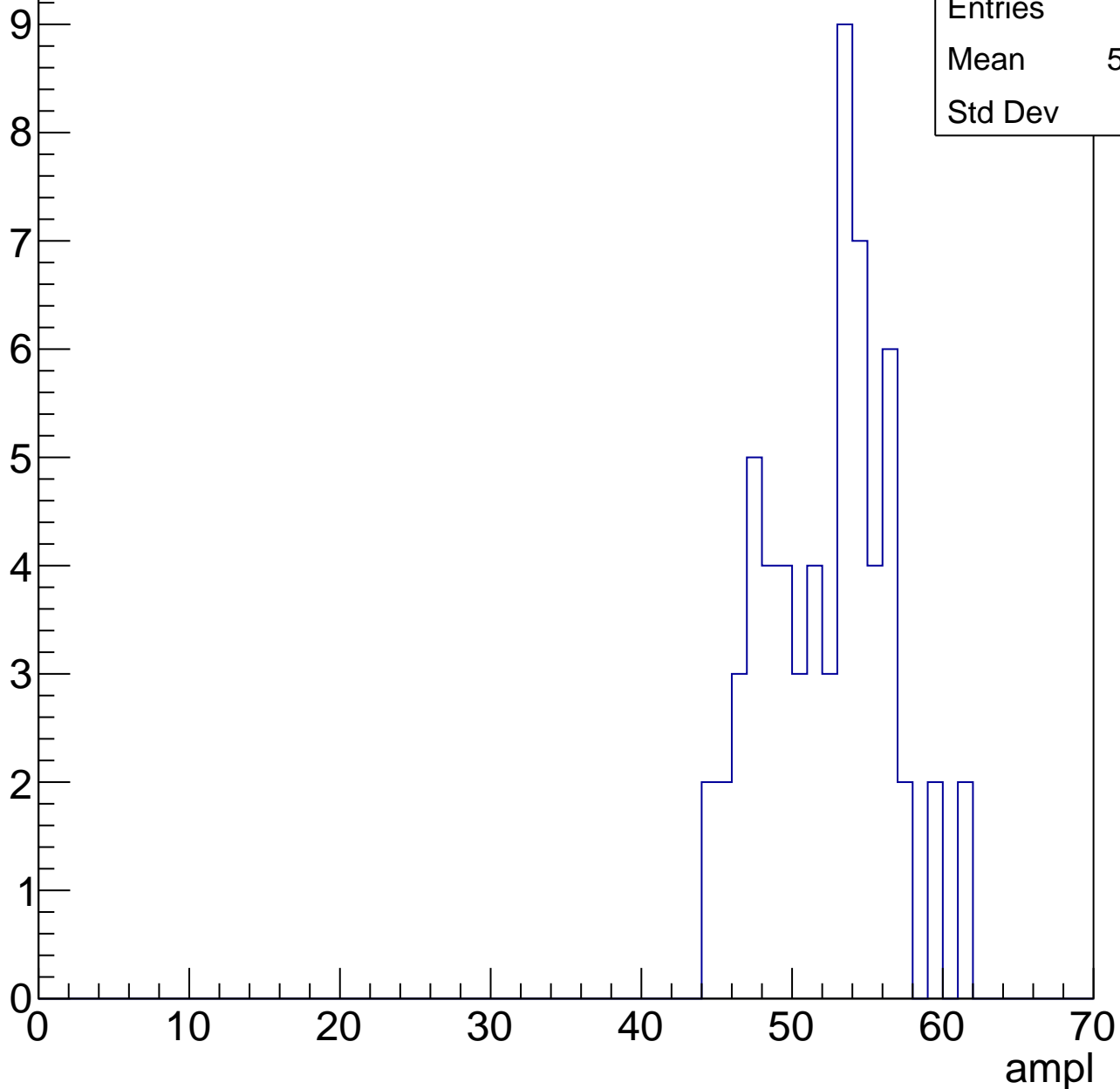


# B0L000S, U13-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	51.84
Std Dev	4.12

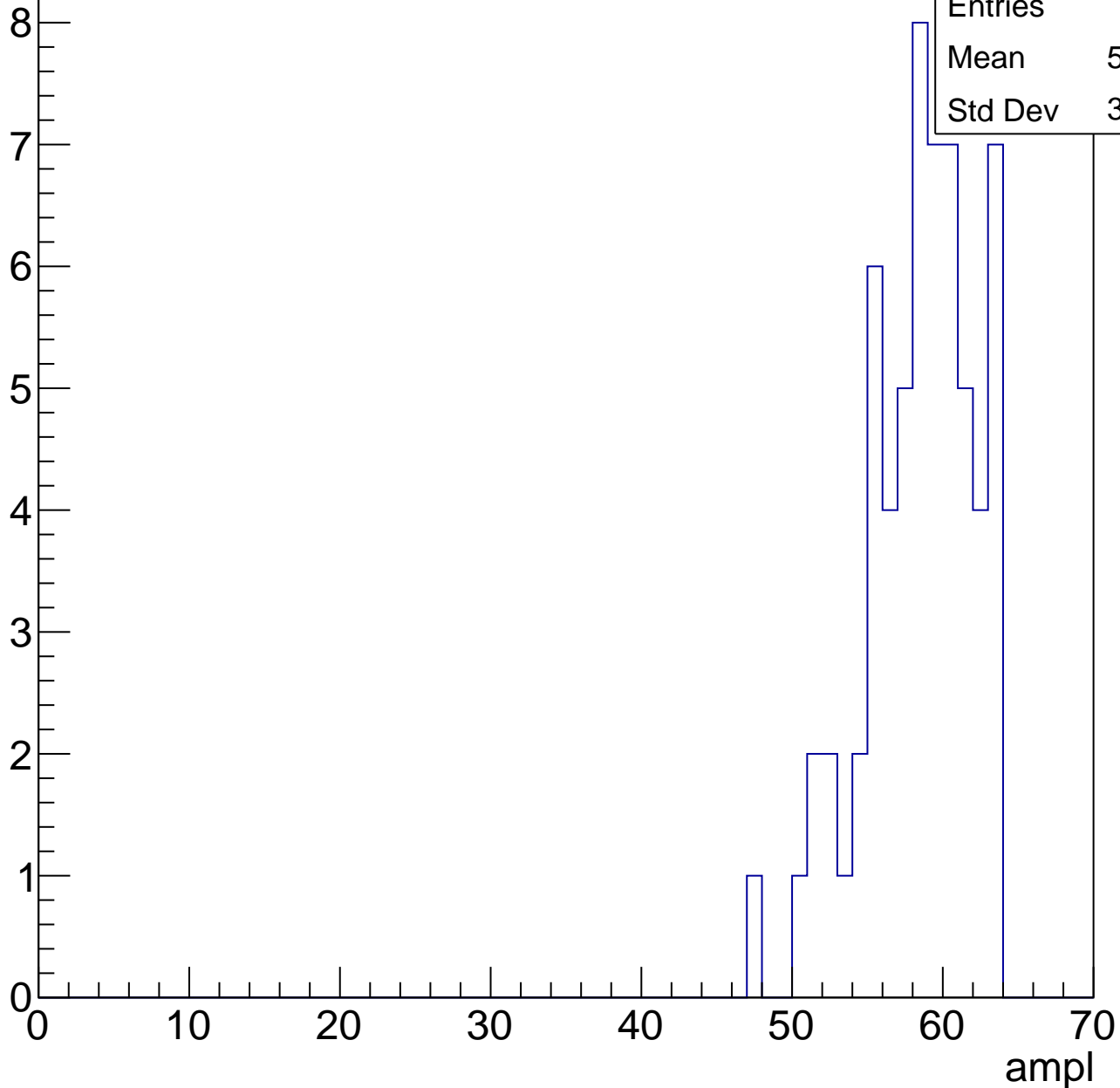


# B0L000S, U13-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	57.97
Std Dev	3.605

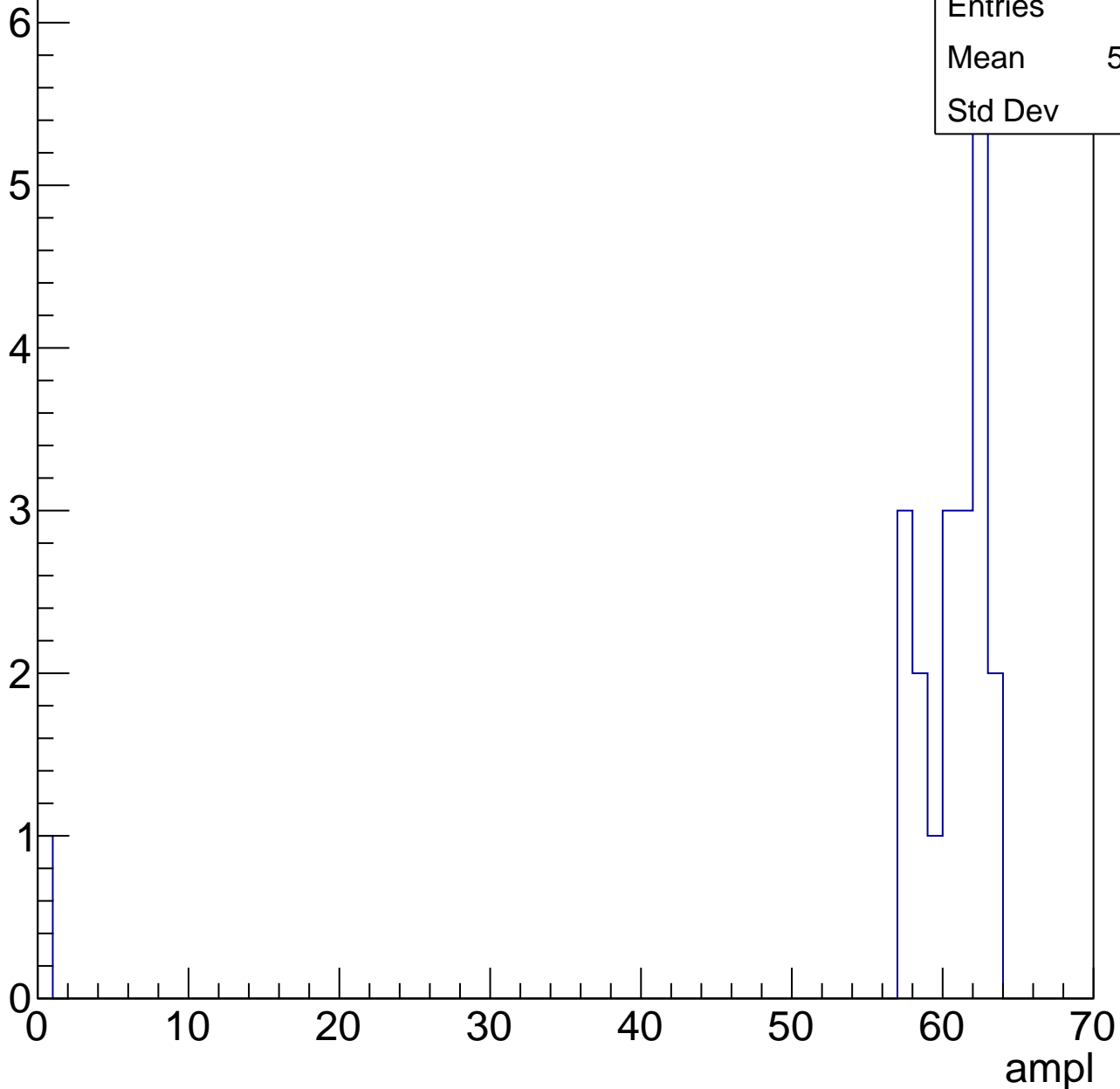


# B0L000S, U13-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	57.48
Std Dev	13



# B0L000S, U13-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

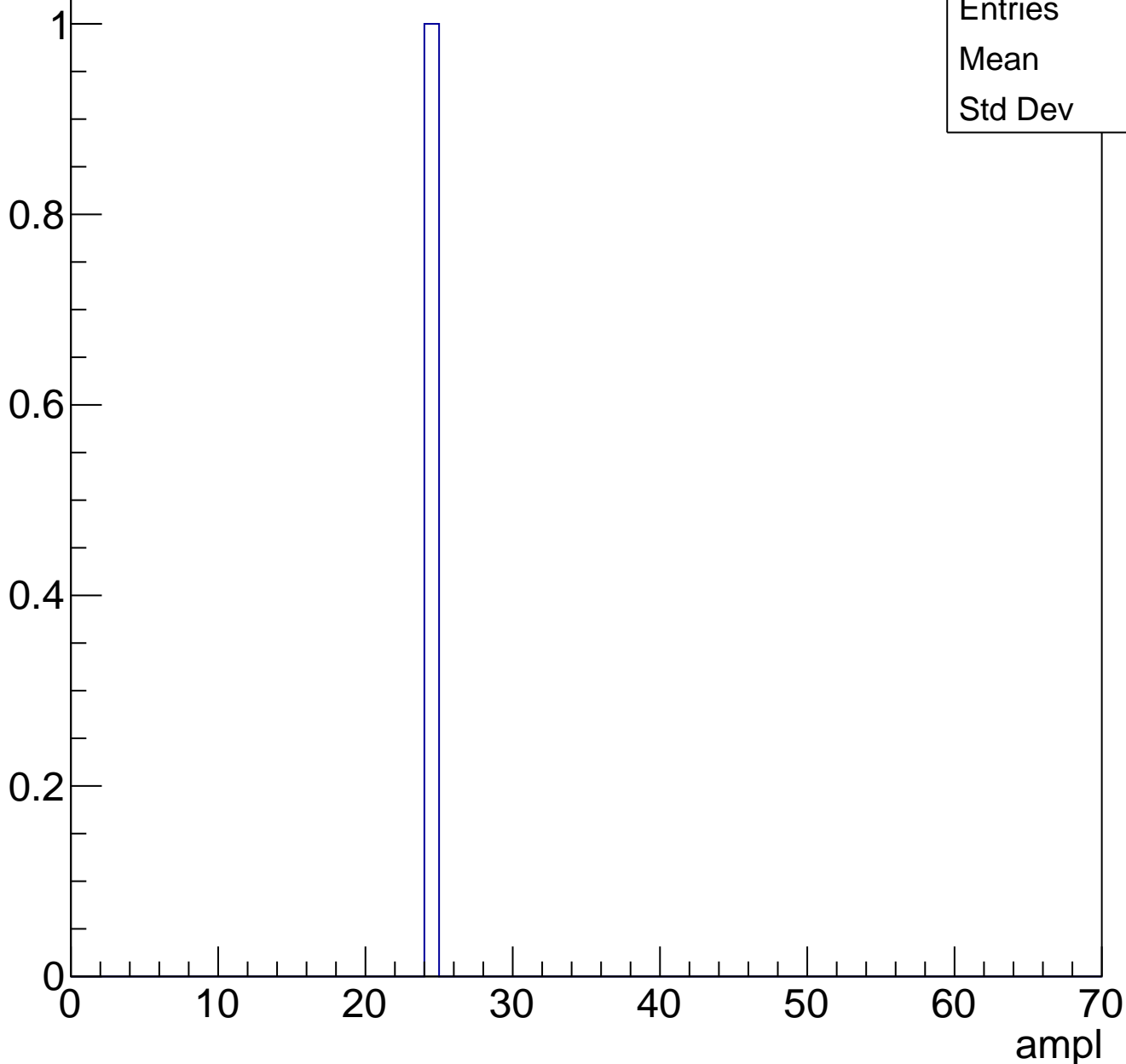




# B0L000S, U13-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U13-ch90, adc0

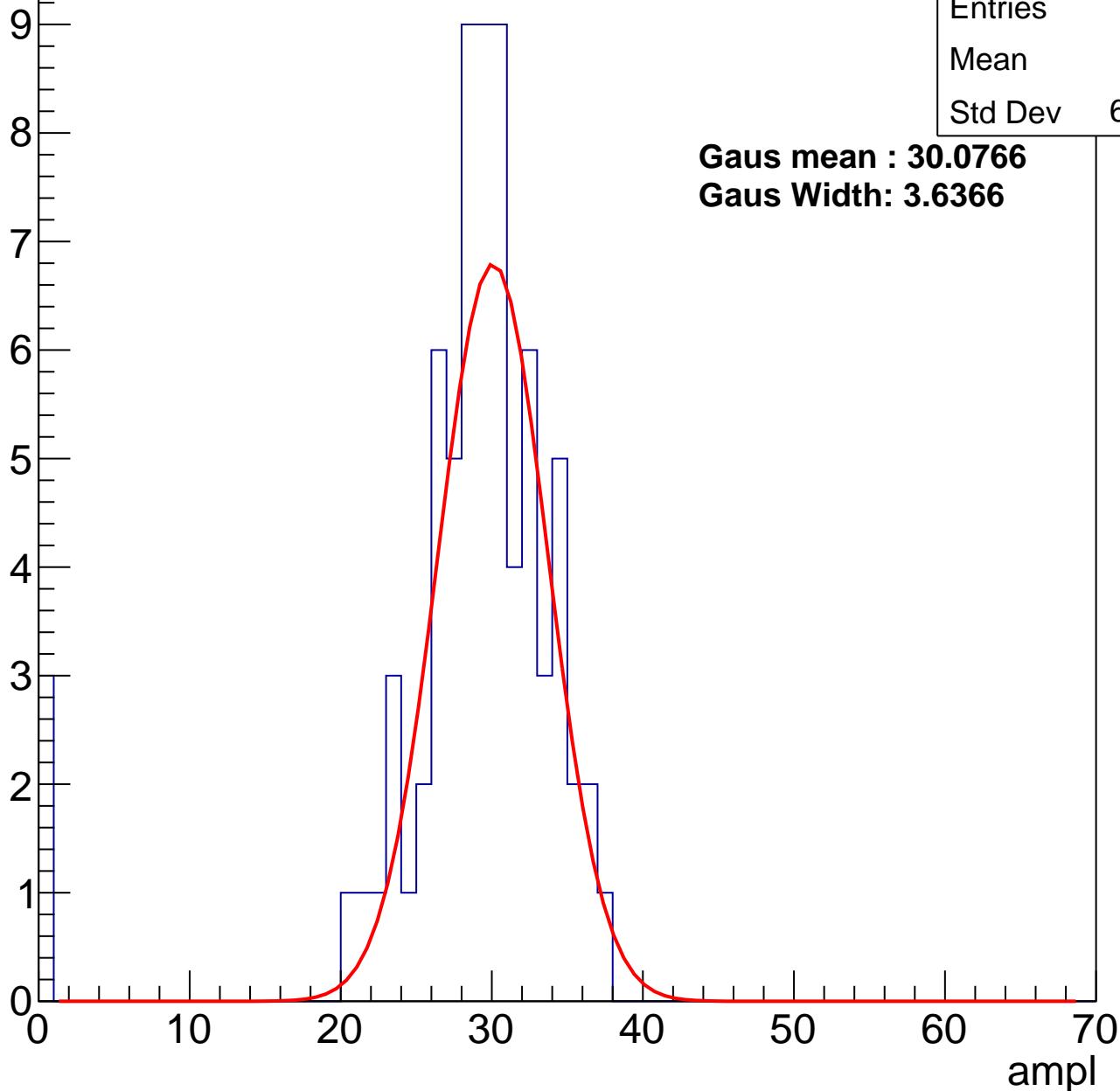
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	28
Std Dev	6.802

**Gaus mean : 30.0766**

**Gaus Width: 3.6366**



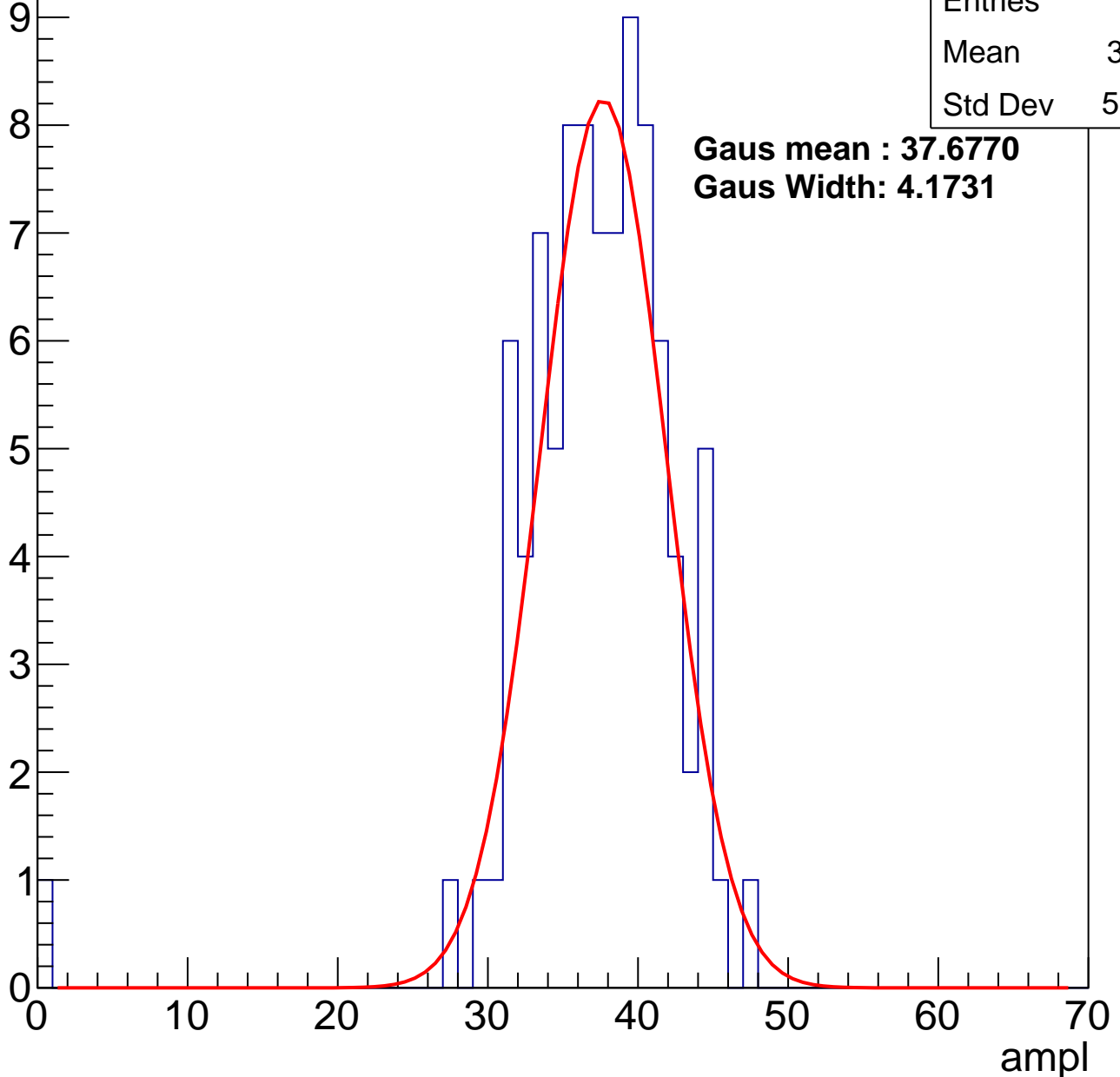
# B0L000S, U13-ch90, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	92
Mean	36.71
Std Dev	5.594

**Gaus mean : 37.6770**  
**Gaus Width: 4.1731**



# B0L000S, U13-ch90, adc2

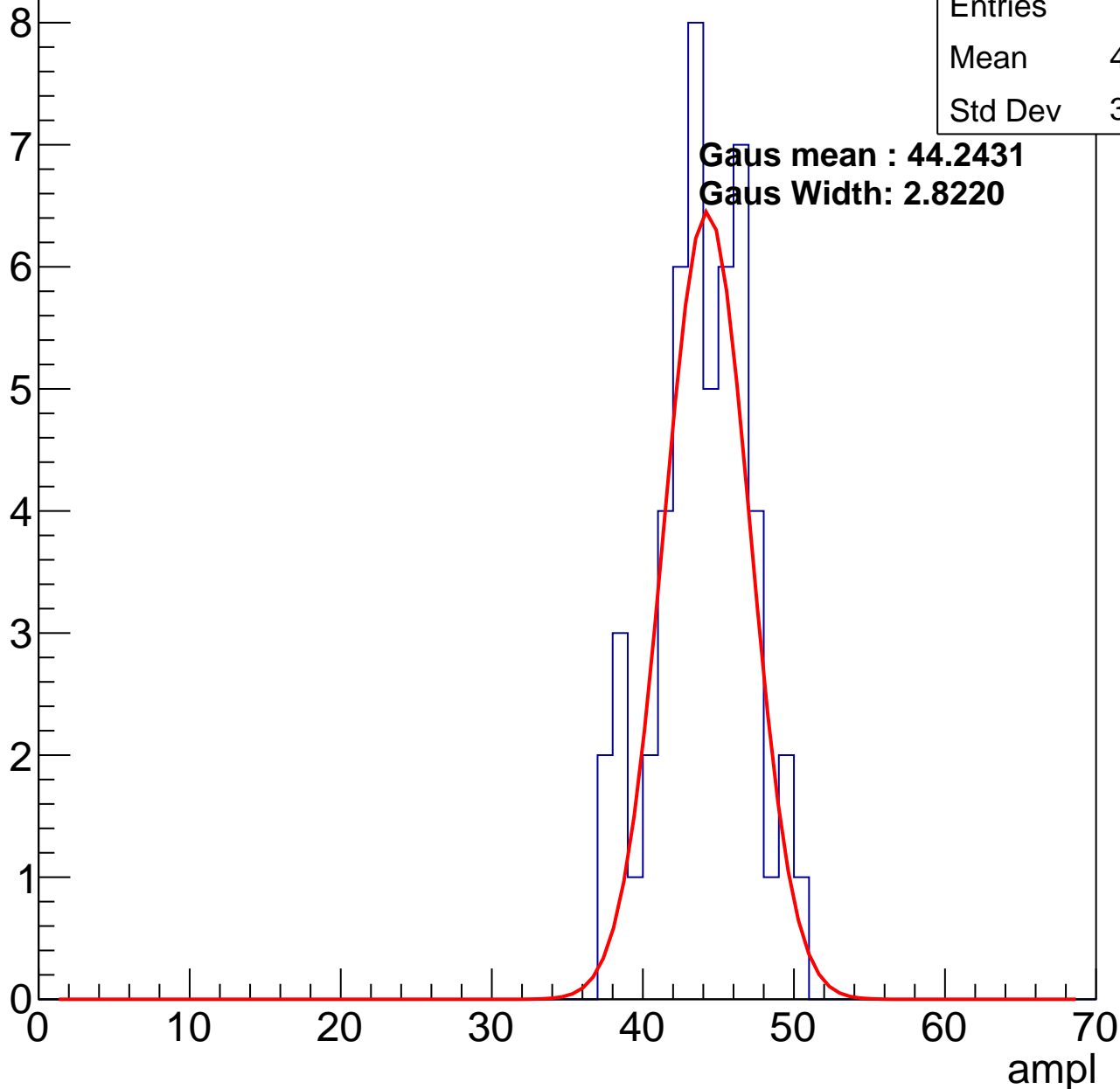
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	43.52
Std Dev	3.073

**Gaus mean : 44.2431**

**Gaus Width: 2.8220**

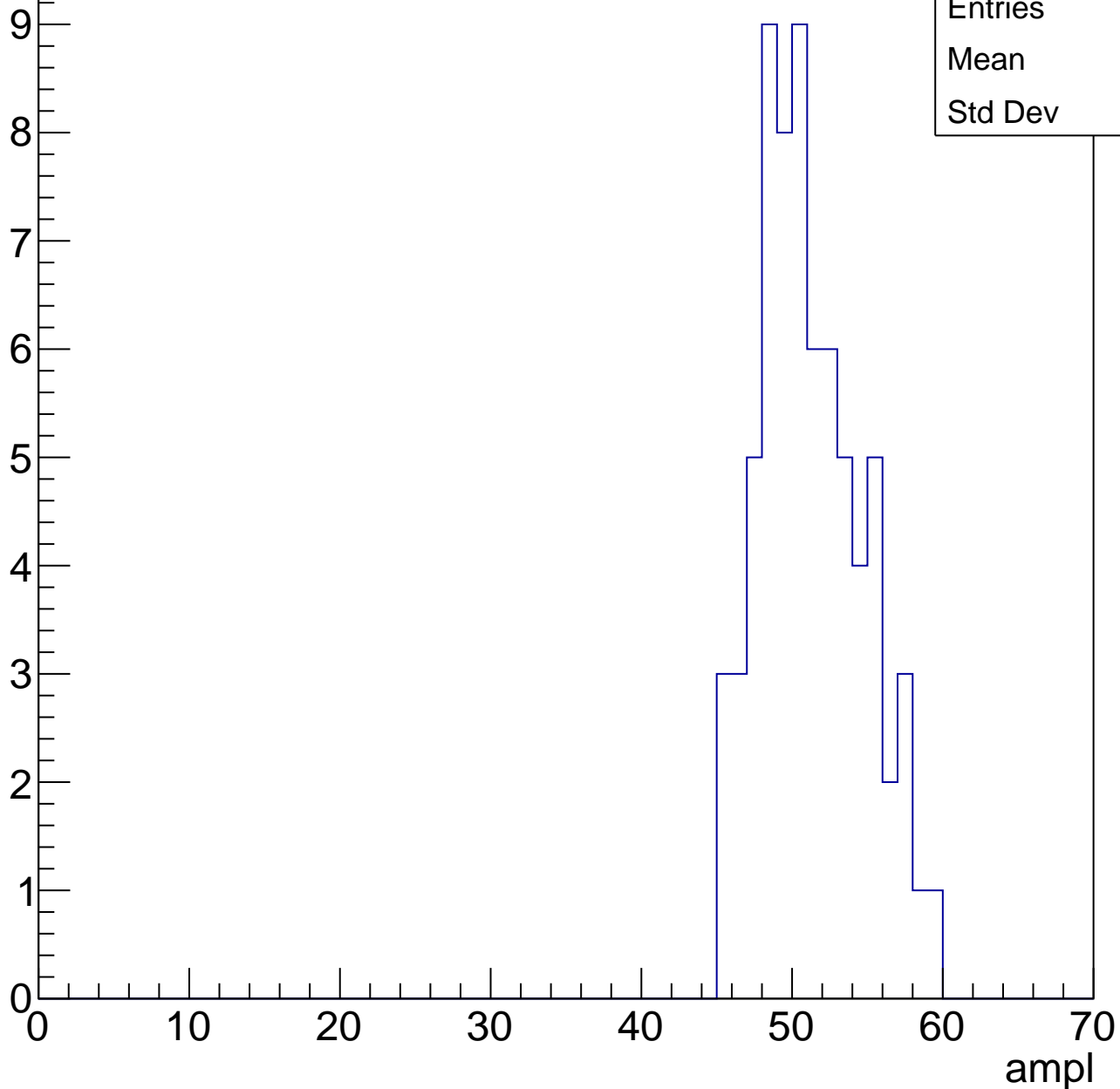


# B0L000S, U13-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.8
Std Dev	3.37

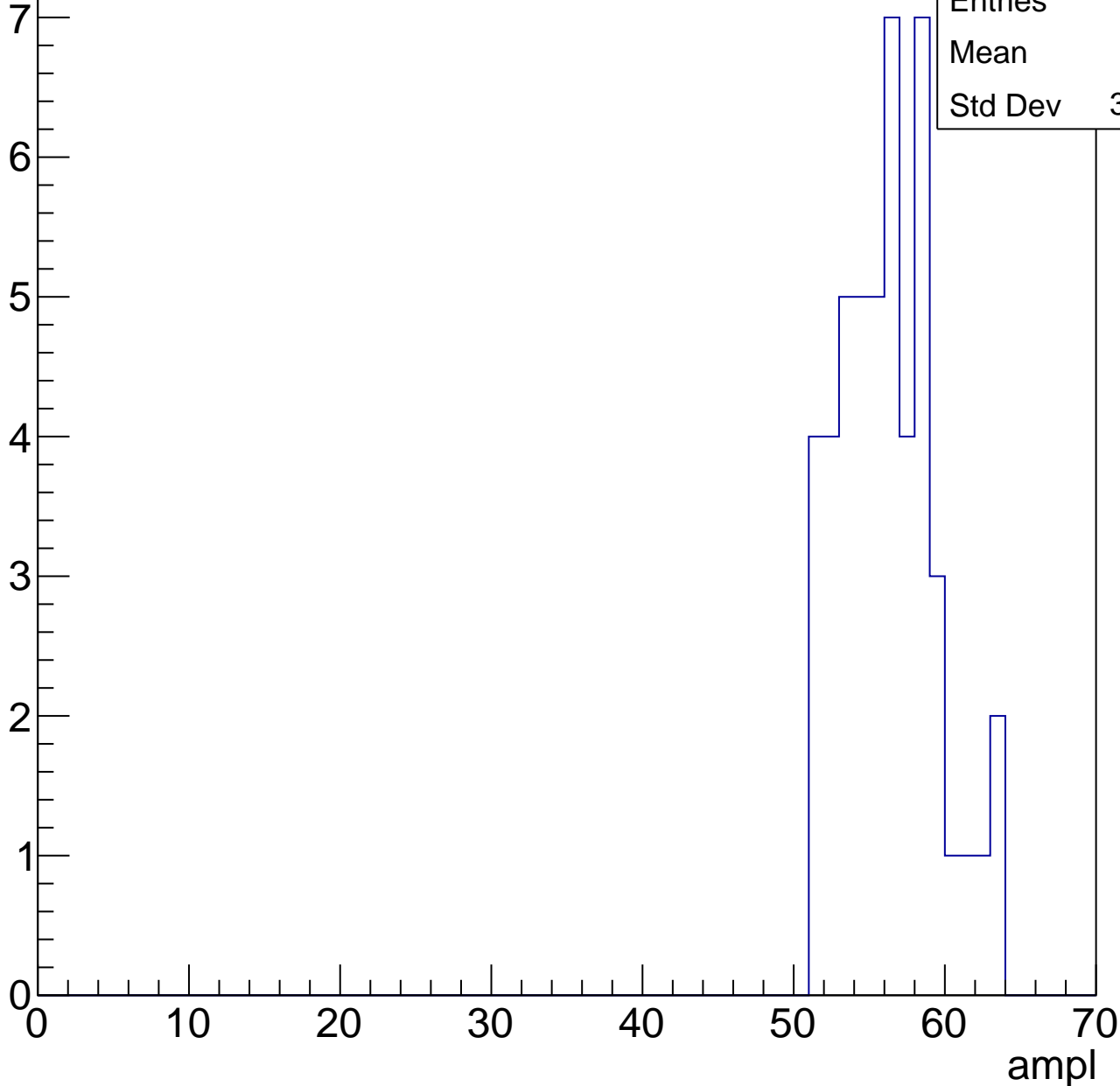


# B0L000S, U13-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

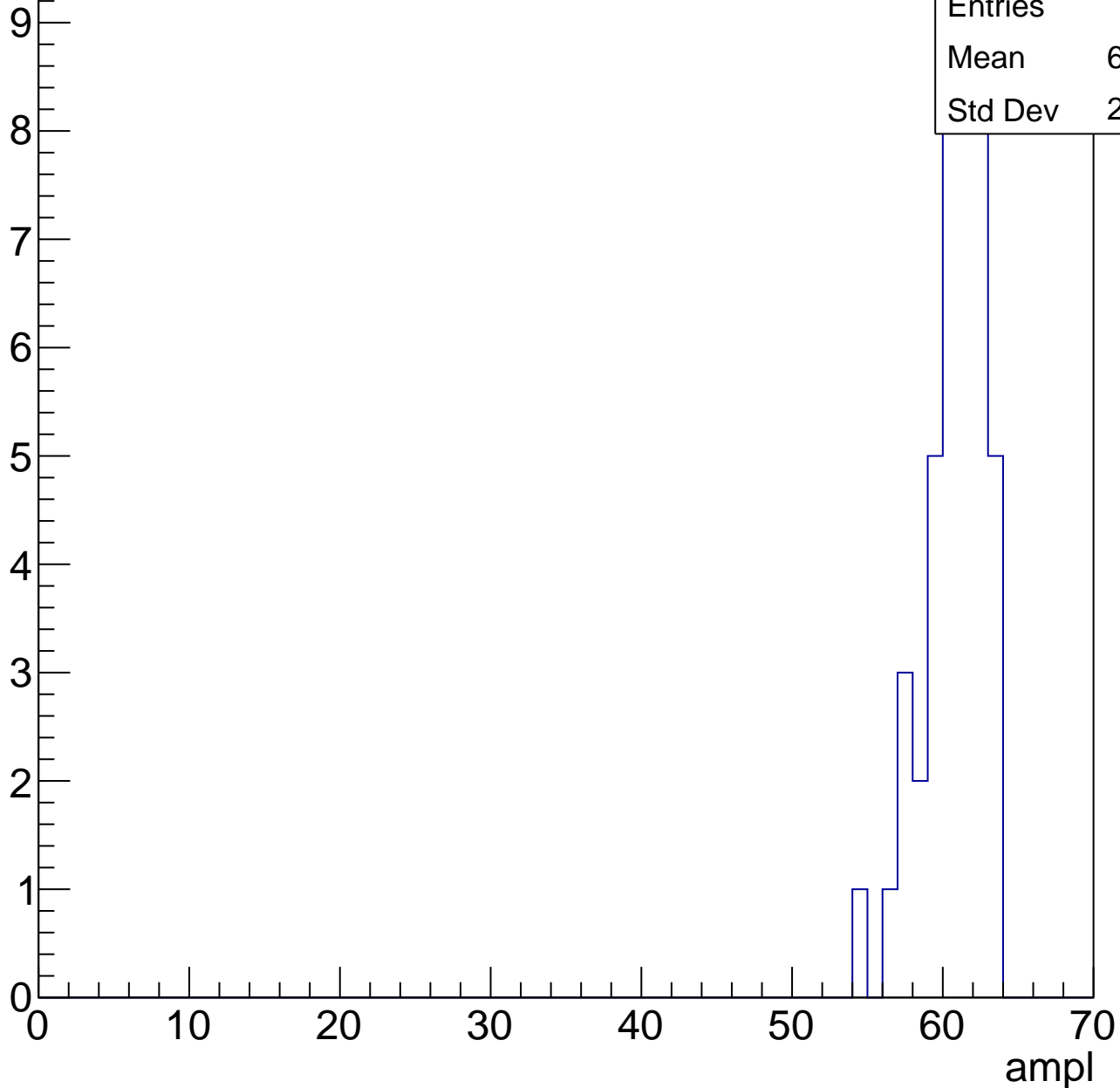
Entries	49
Mean	55.8
Std Dev	3.084



# B0L000S, U13-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

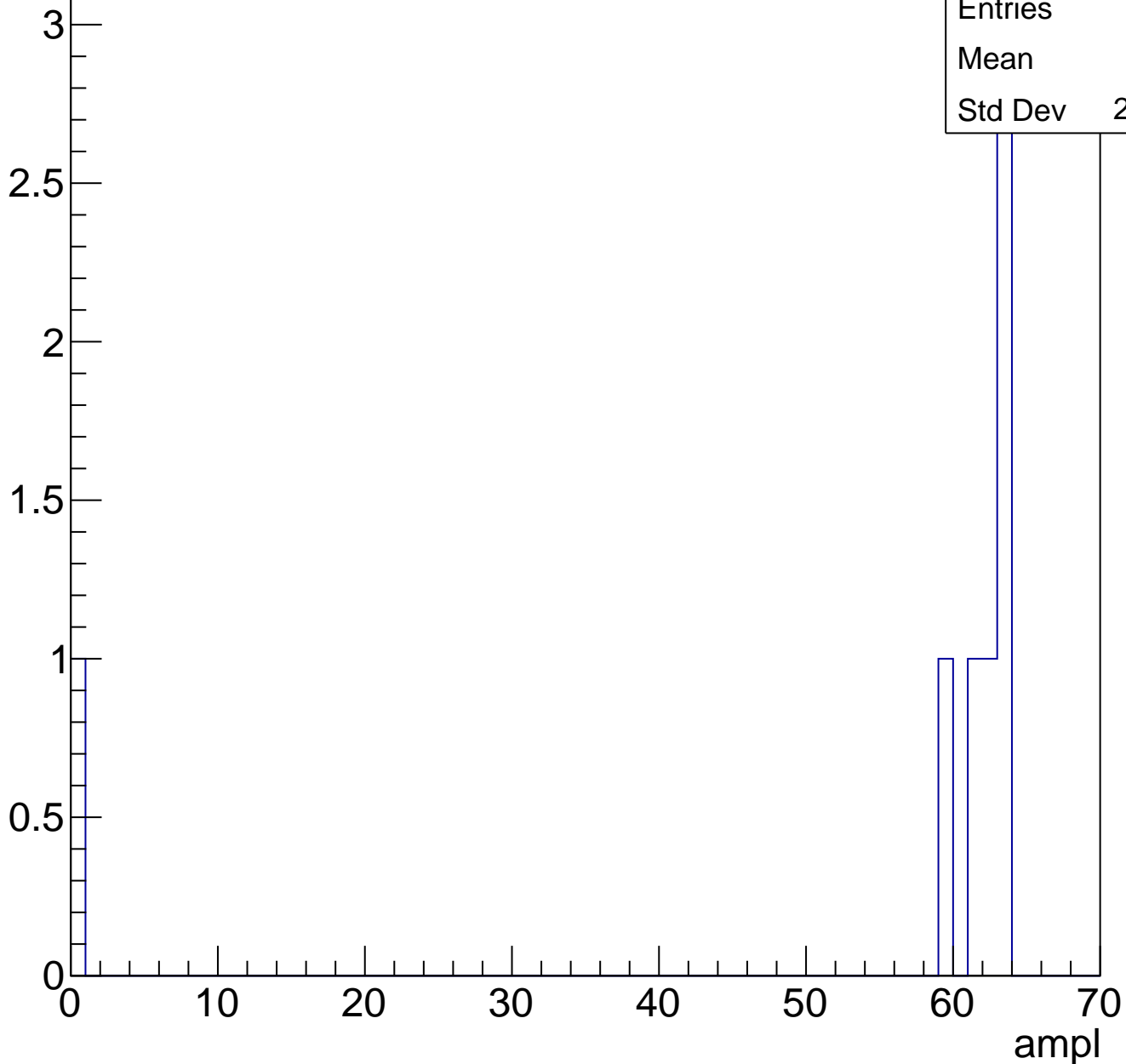
Entry



# B0L000S, U13-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch91, adc0

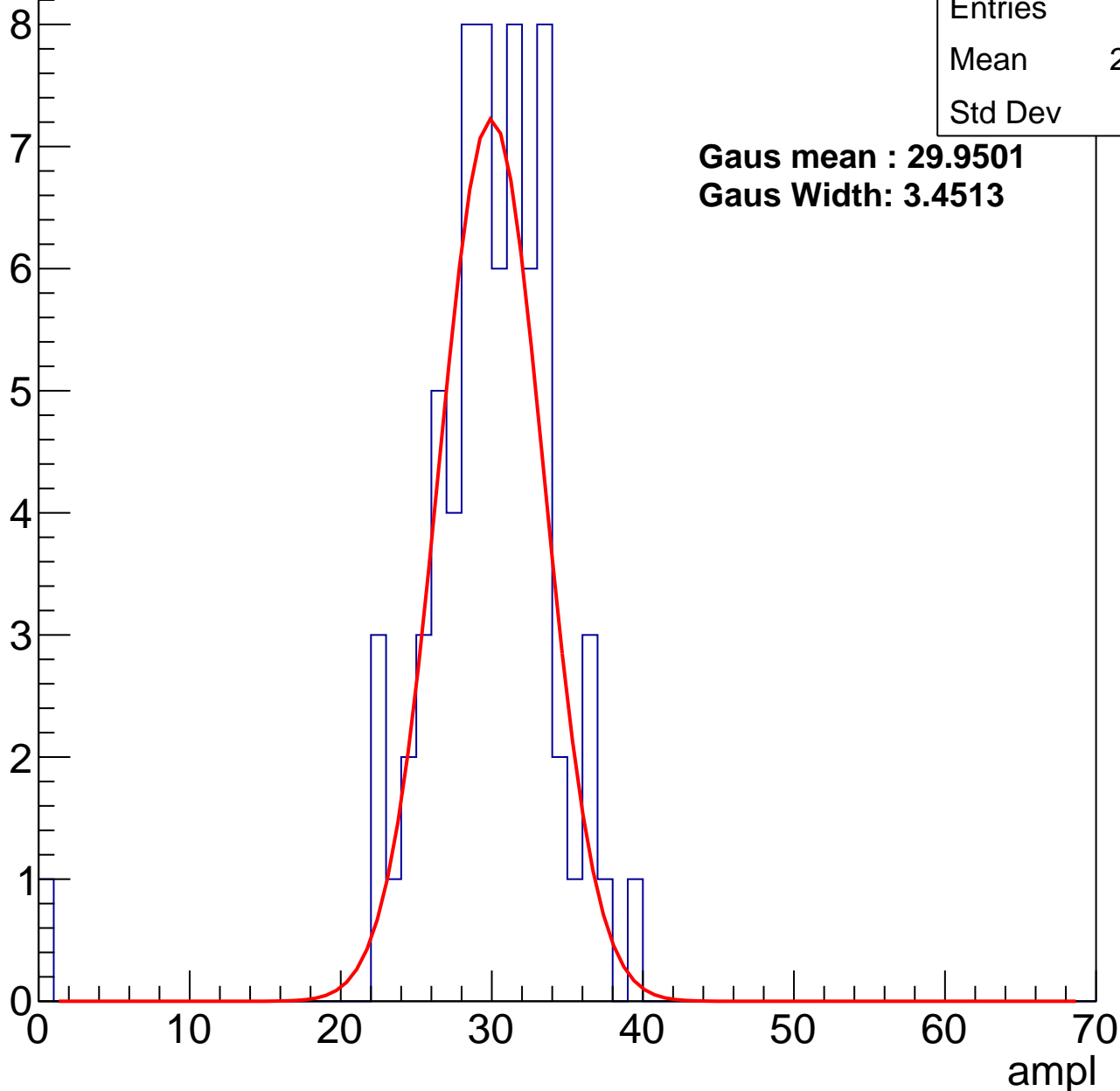
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	29.25
Std Dev	5.04

**Gaus mean : 29.9501**

**Gaus Width: 3.4513**



# B0L000S, U13-ch91, adc1

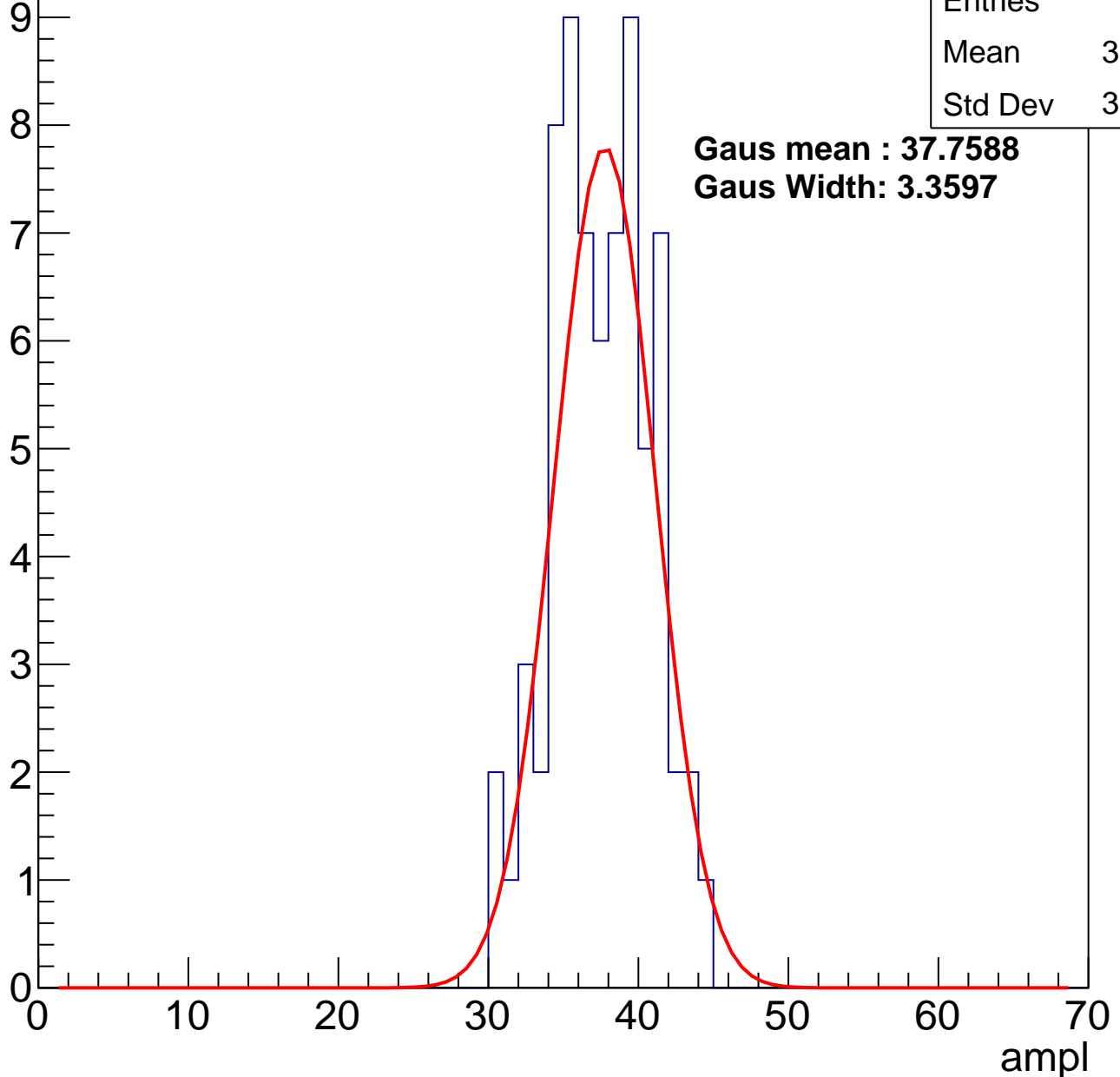
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.07
Std Dev	3.199

**Gaus mean : 37.7588**

**Gaus Width: 3.3597**



# B0L000S, U13-ch91, adc2

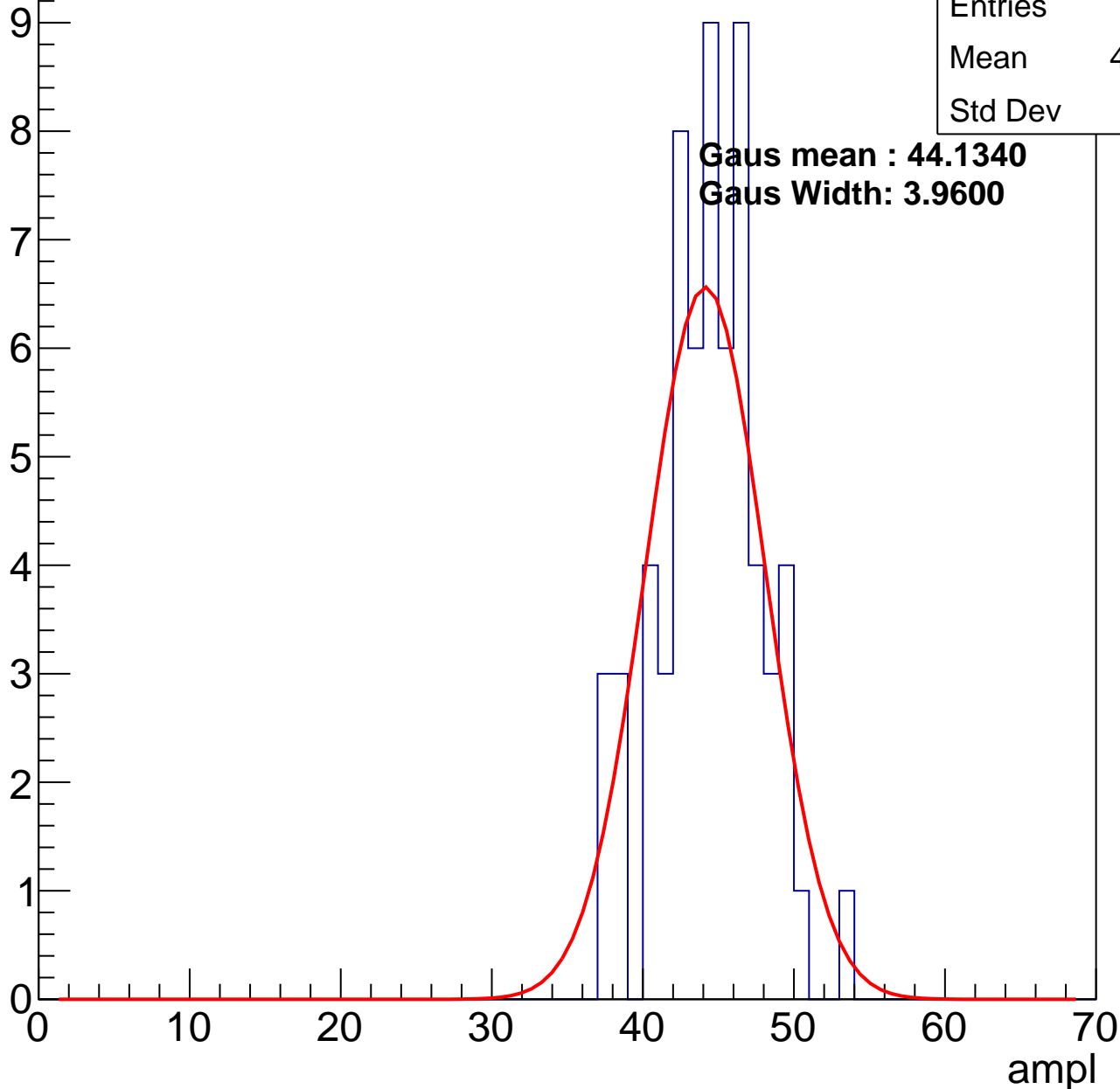
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.95
Std Dev	3.37

**Gaus mean : 44.1340**

**Gaus Width: 3.9600**

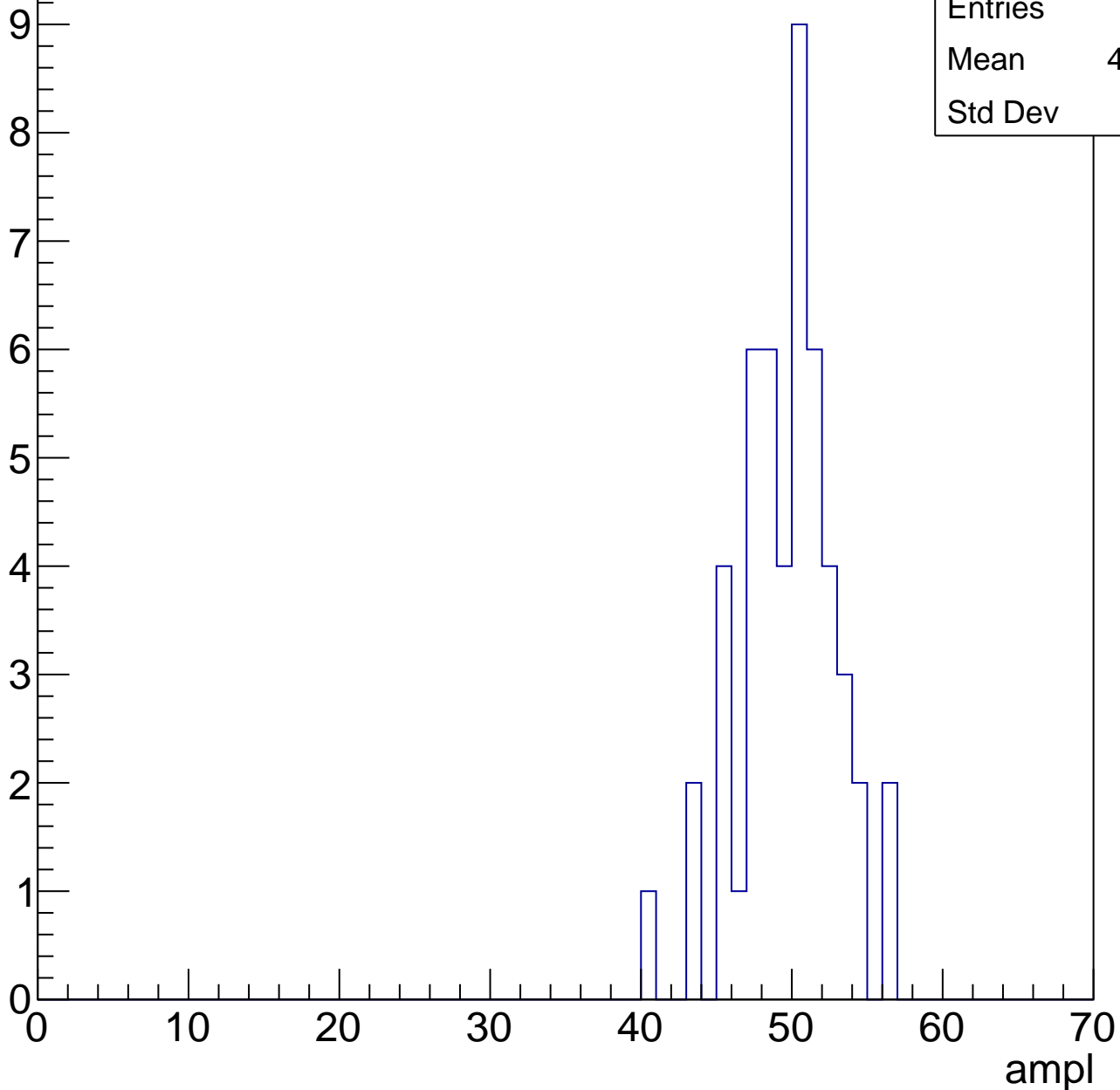


# B0L000S, U13-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	49.22
Std Dev	3.22

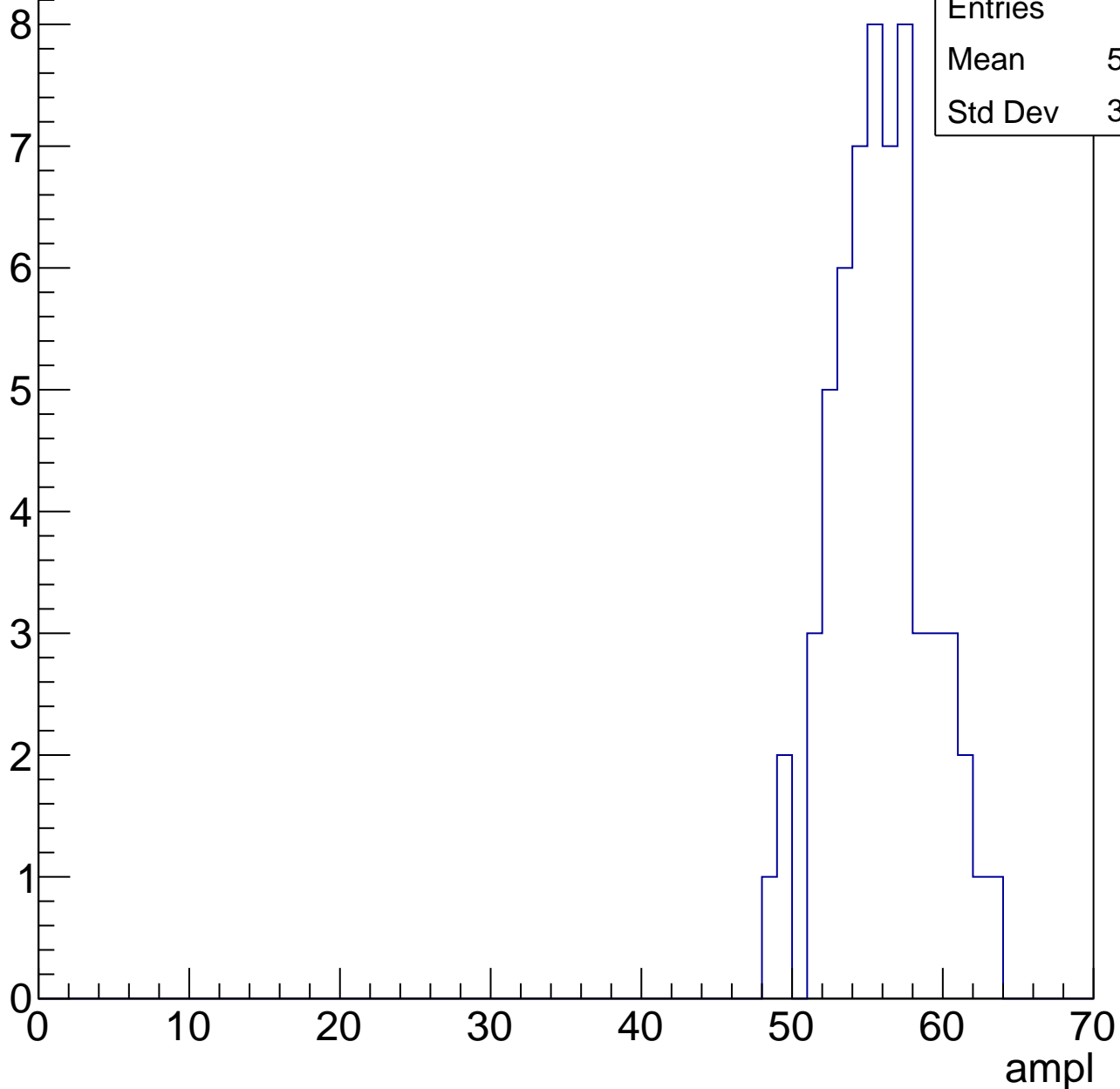


# B0L000S, U13-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

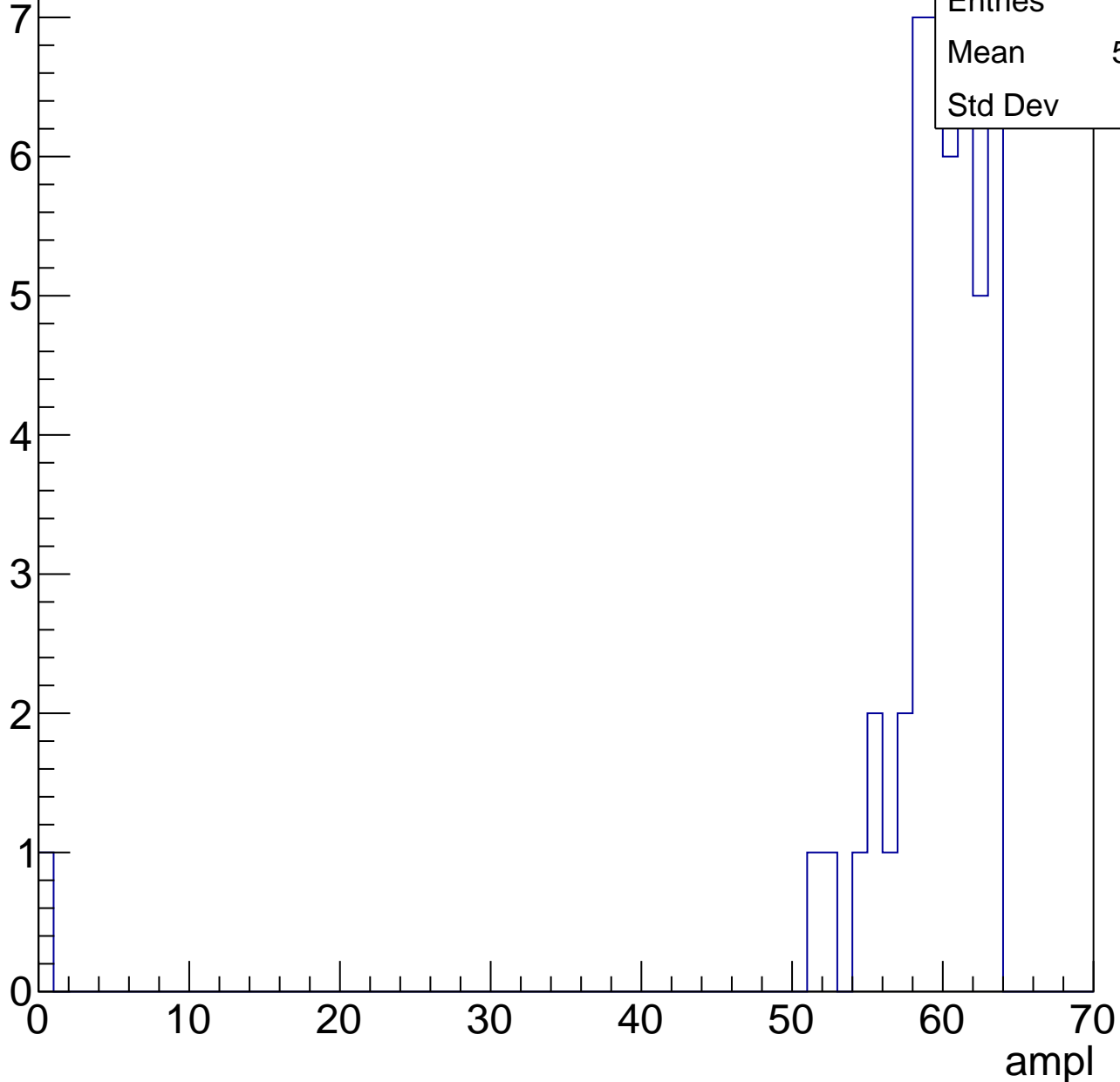
Entries	60
Mean	55.35
Std Dev	3.193



# B0L000S, U13-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

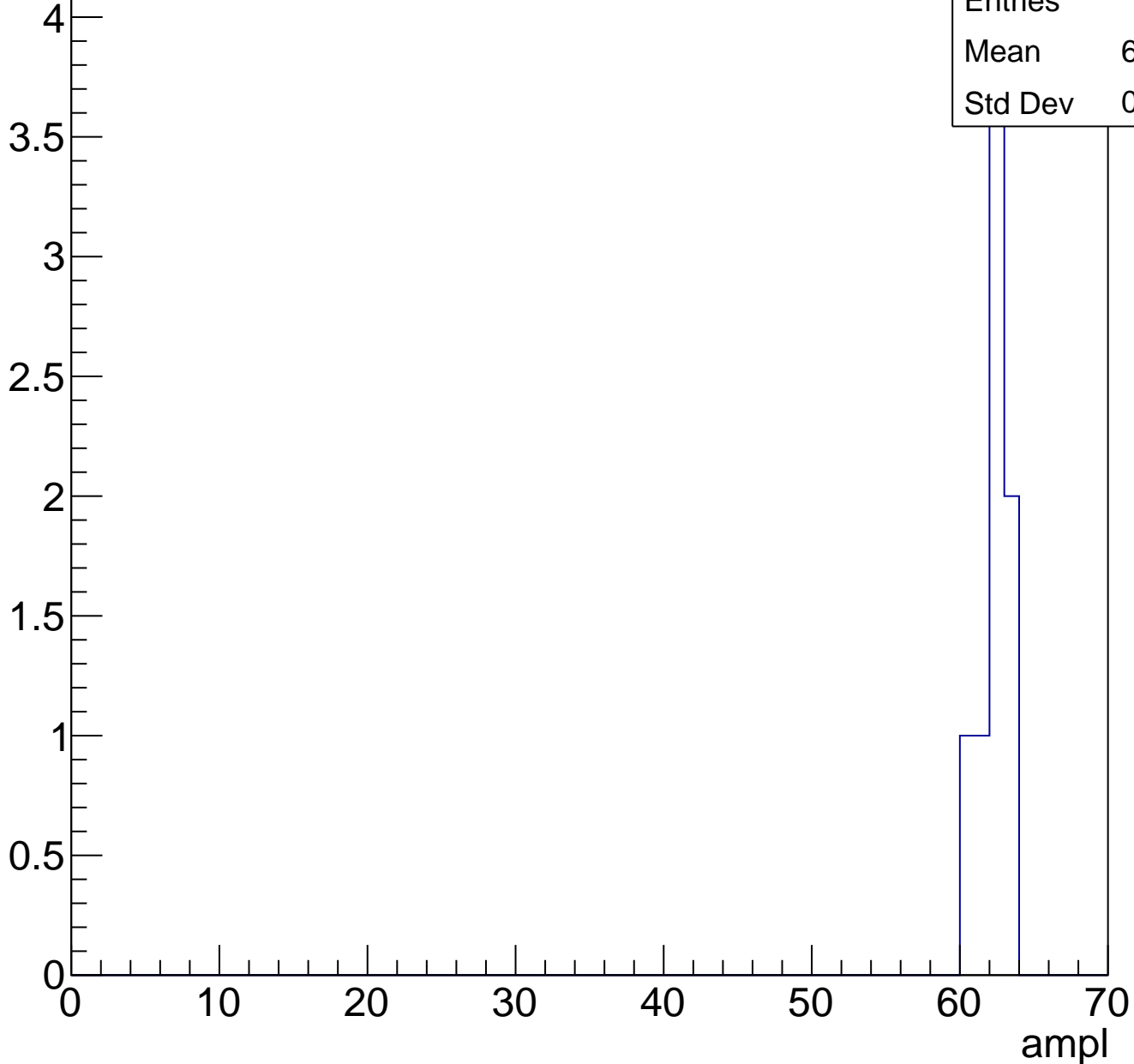
Entry



# B0L000S, U13-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch92, adc0

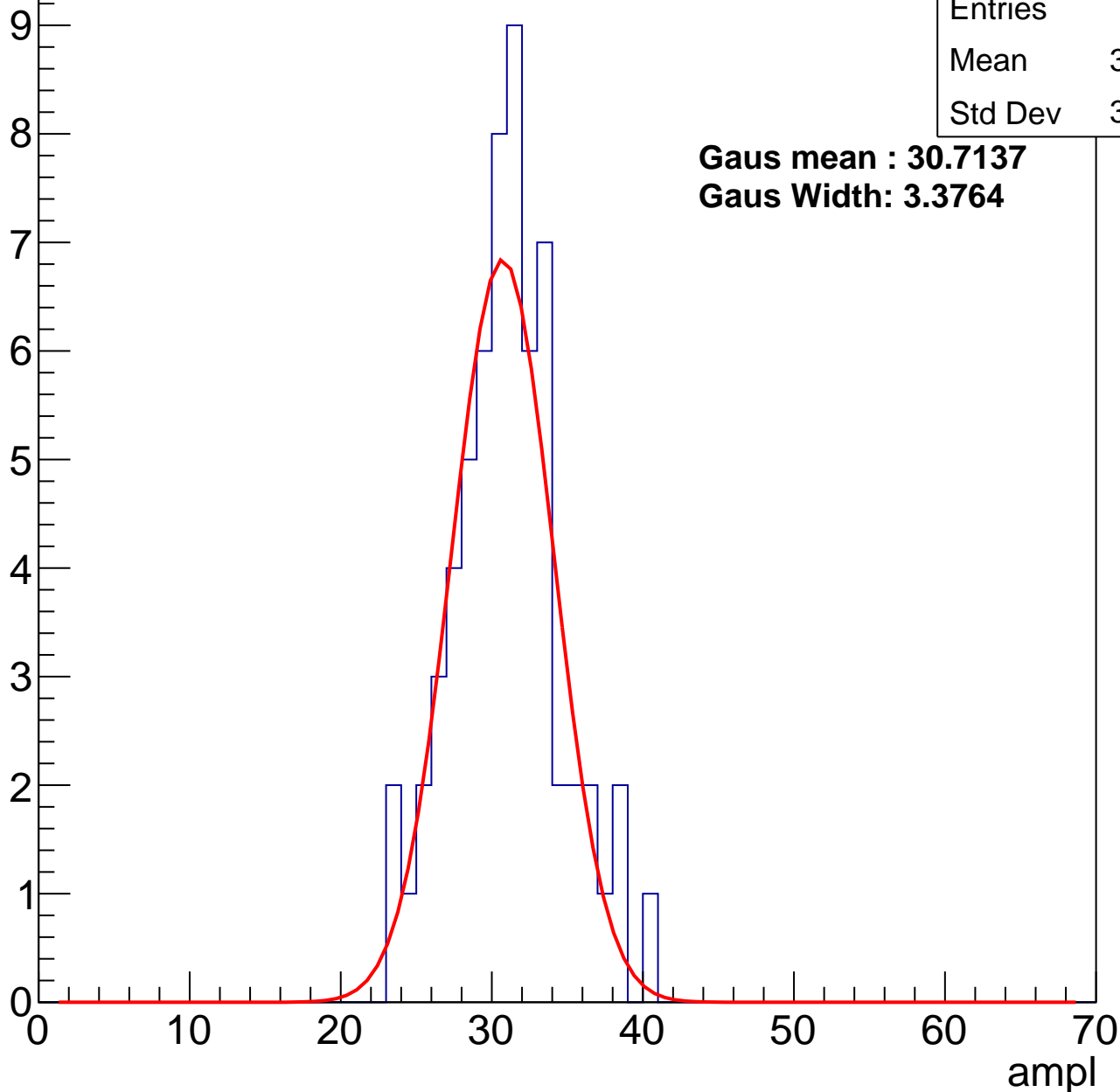
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.56
Std Dev	3.567

**Gaus mean : 30.7137**

**Gaus Width: 3.3764**



# B0L000S, U13-ch92, adc1

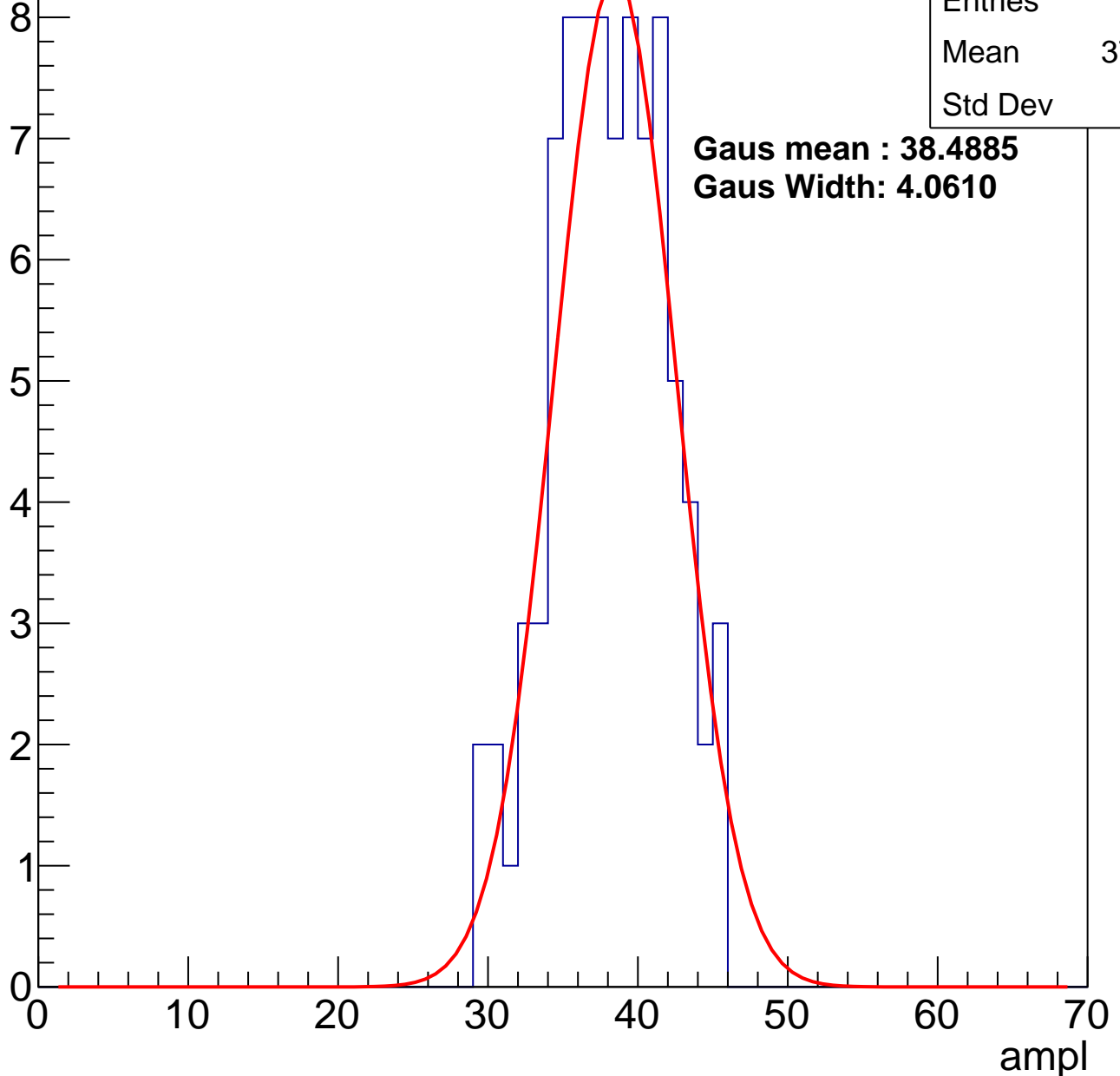
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	37.64
Std Dev	3.8

**Gaus mean : 38.4885**

**Gaus Width: 4.0610**



# B0L000S, U13-ch92, adc2

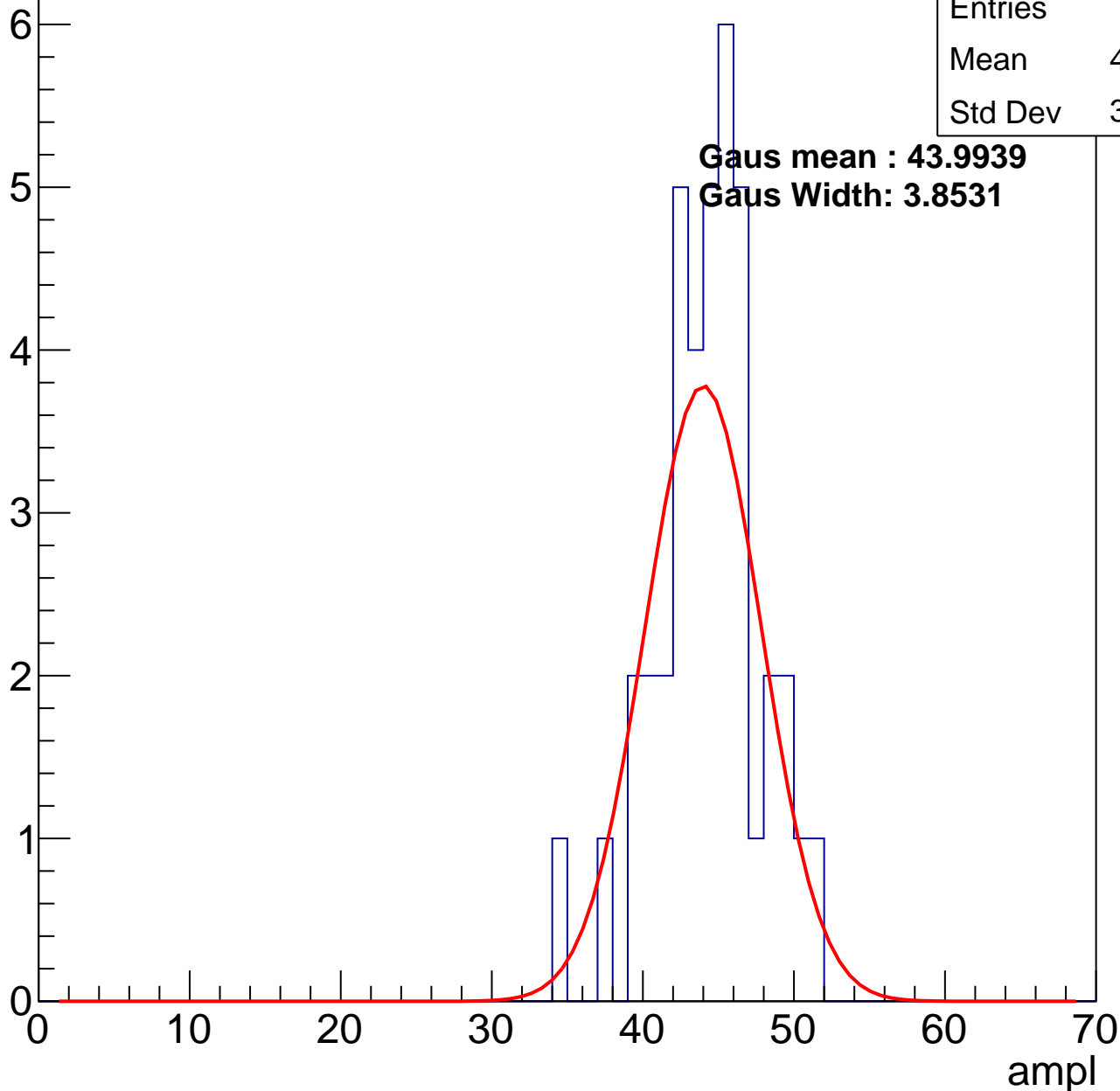
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	43.88
Std Dev	3.444

**Gaus mean : 43.9939**

**Gaus Width: 3.8531**

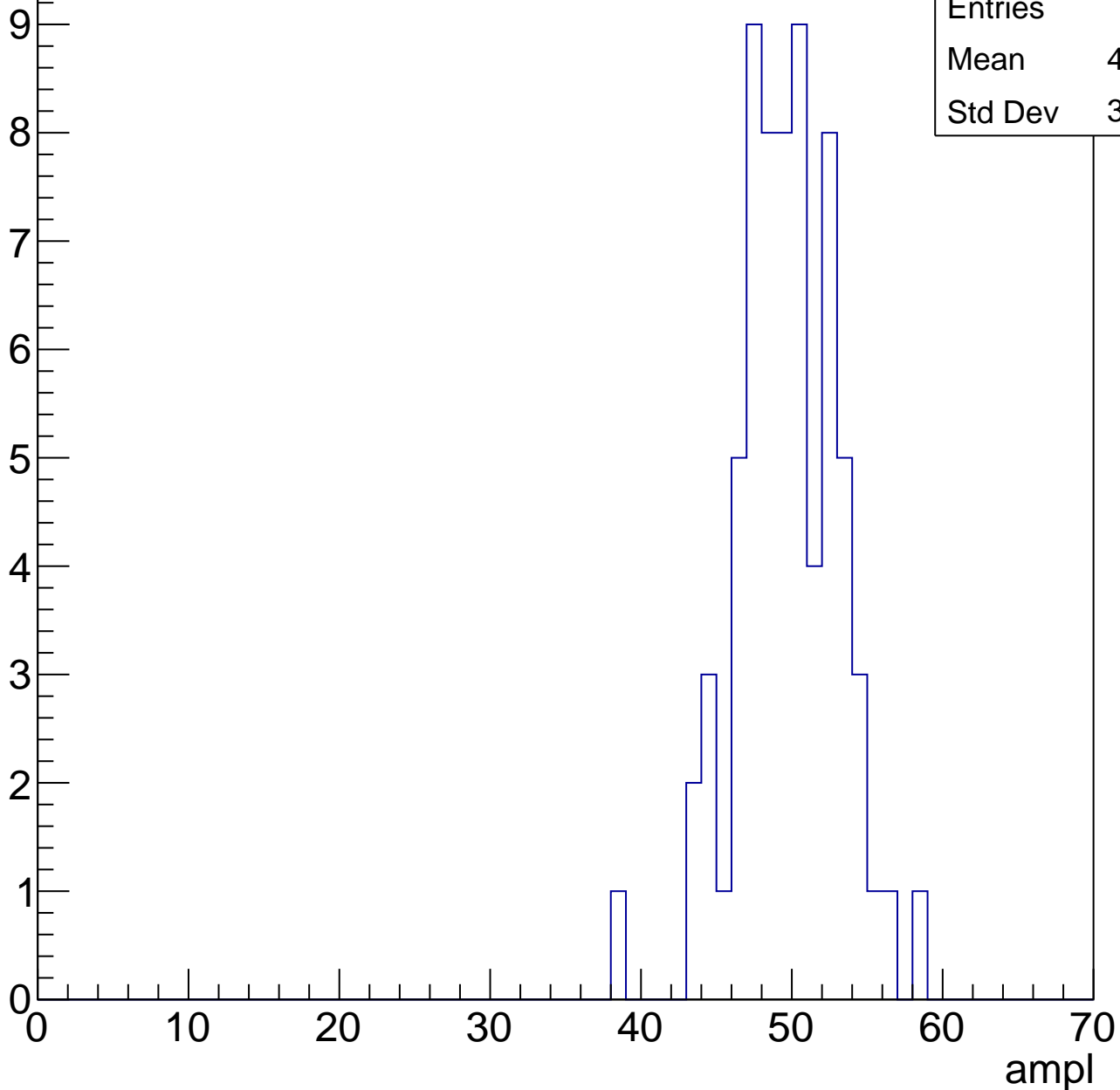


# B0L000S, U13-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.22
Std Dev	3.379

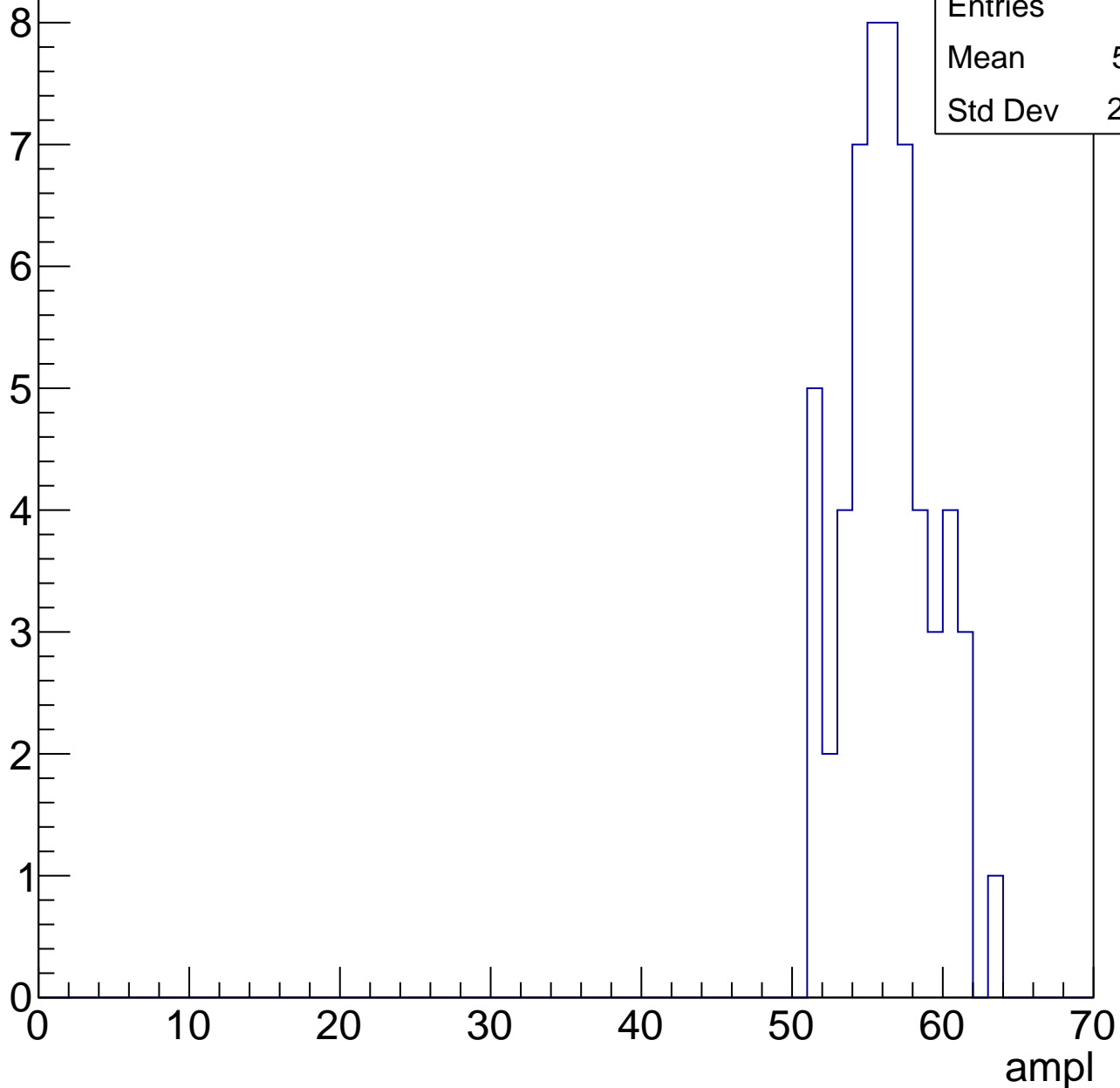


# B0L000S, U13-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.91
Std Dev	2.886

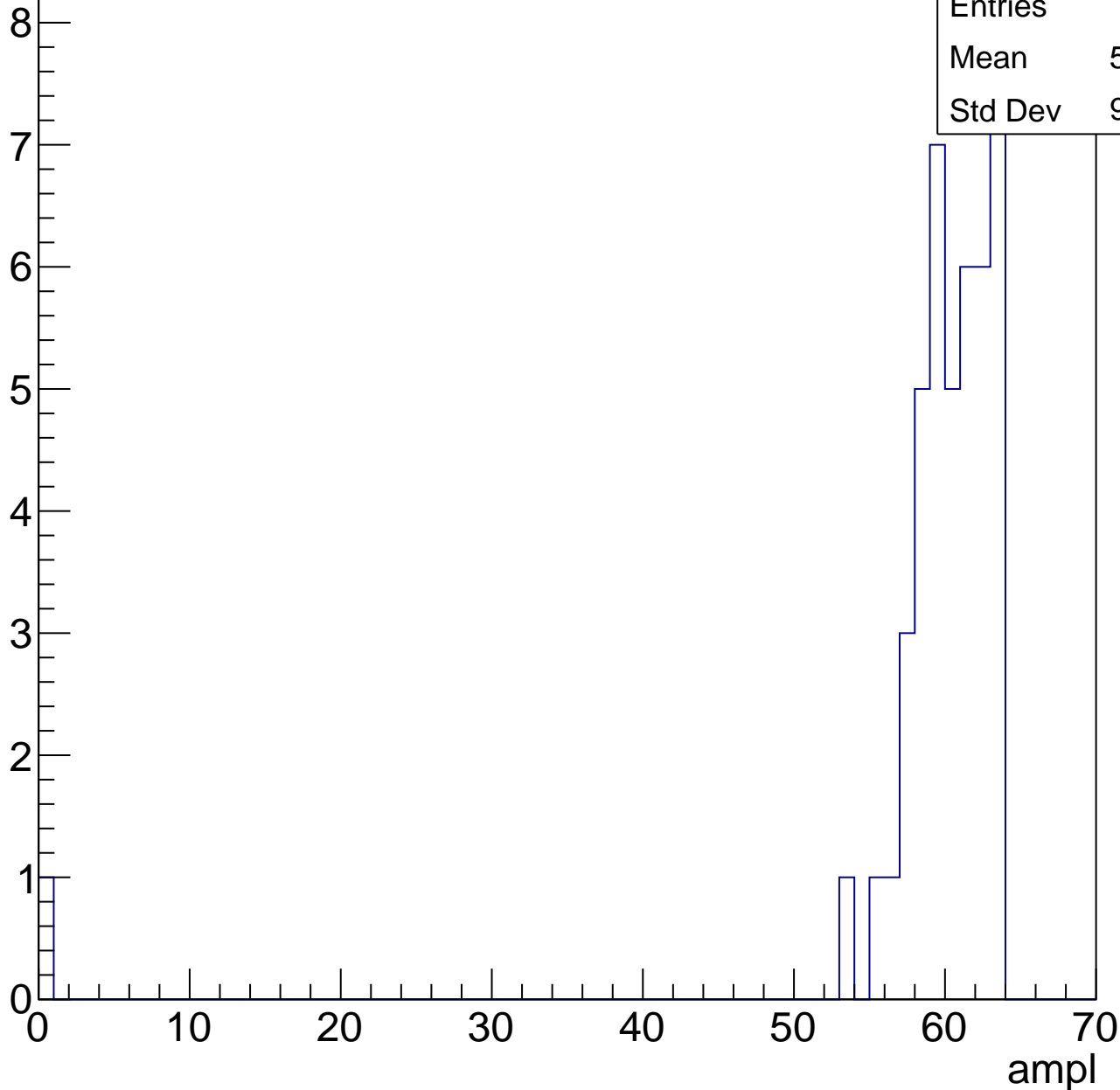


# B0L000S, U13-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

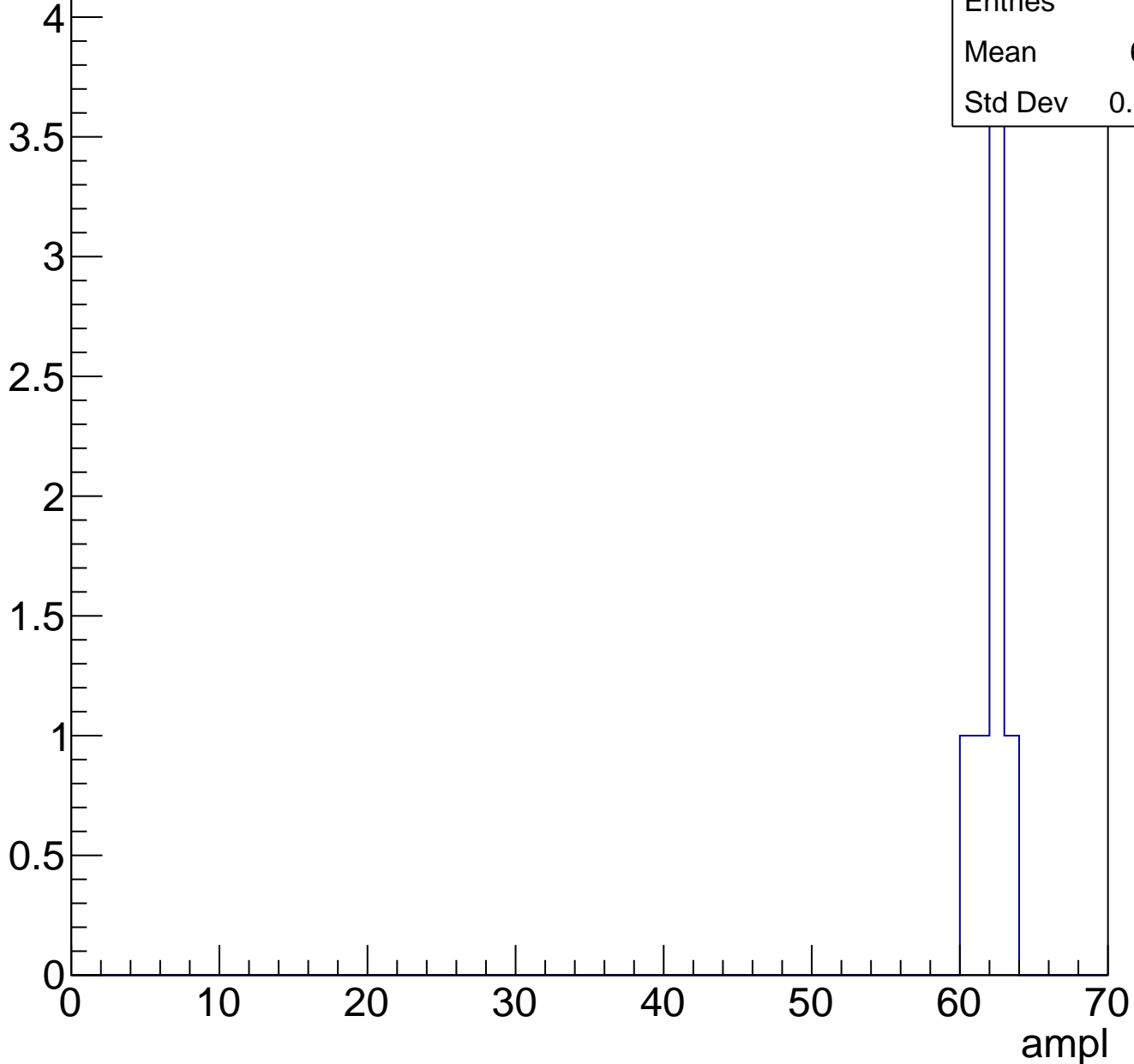
Entries	44
Mean	58.64
Std Dev	9.249



# B0L000S, U13-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch93, adc0

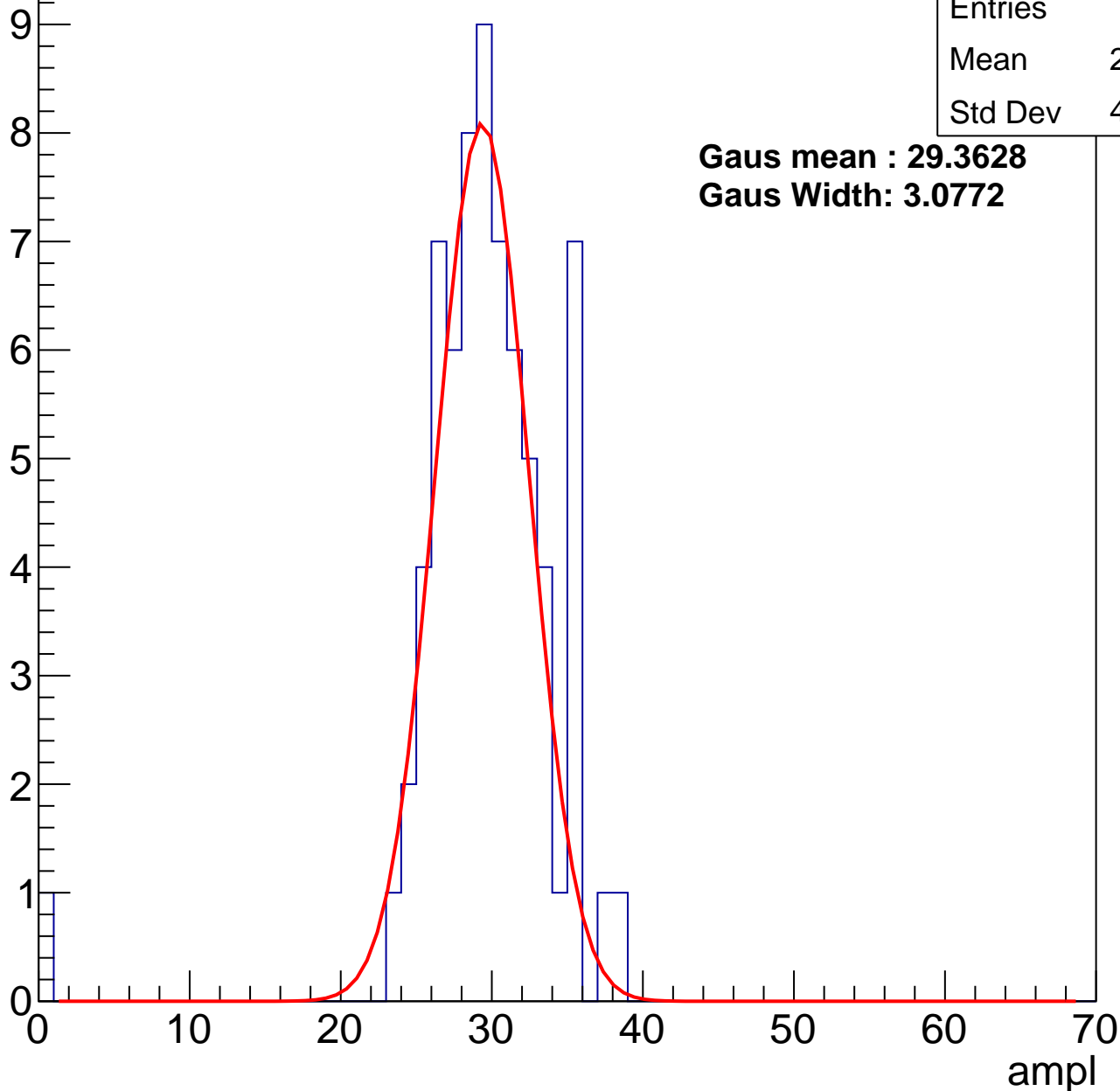
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.17
Std Dev	4.849

**Gaus mean : 29.3628**

**Gaus Width: 3.0772**



# B0L000S, U13-ch93, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	37.08
Std Dev	3.73

**Gaus mean : 37.5086**

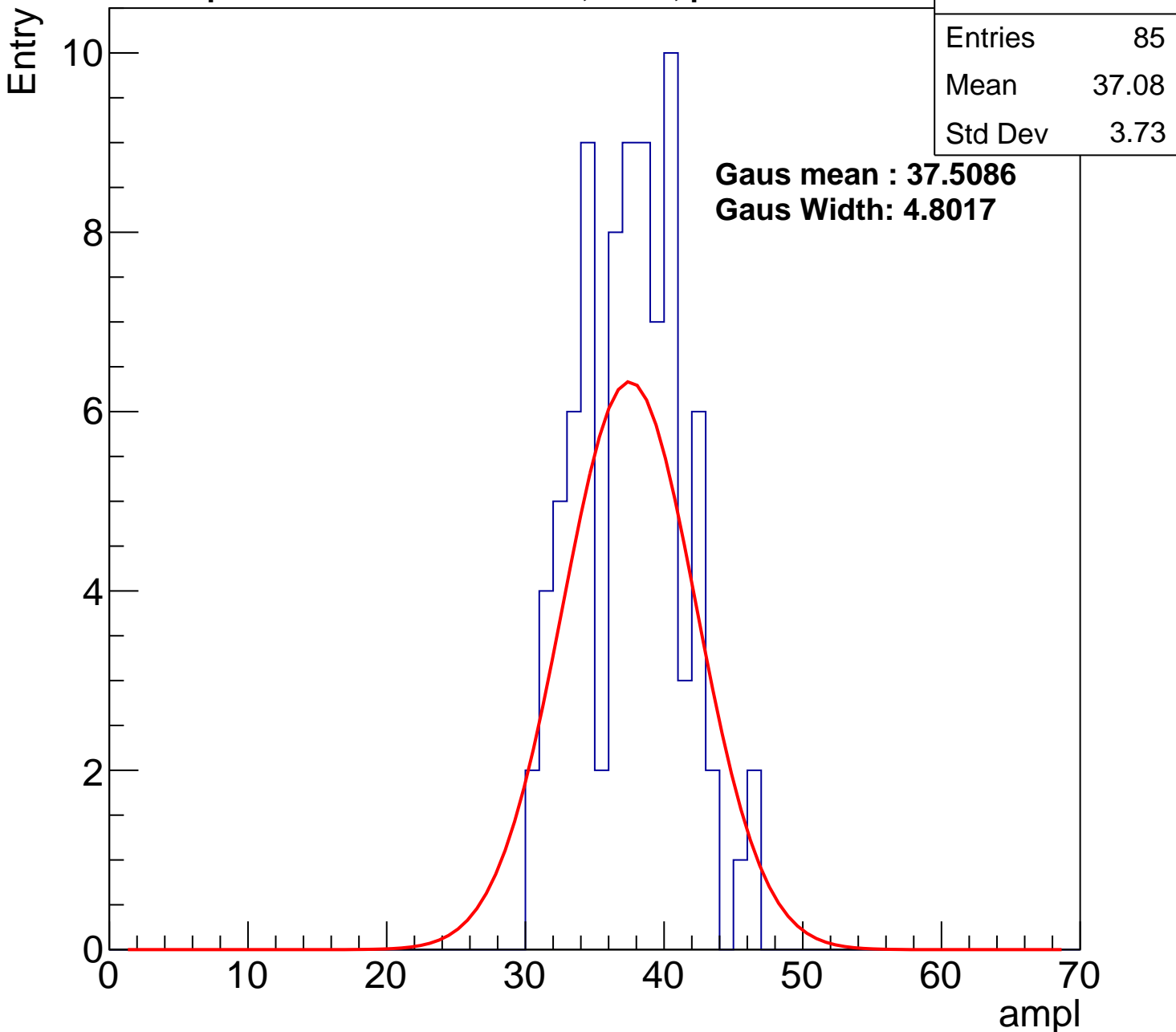
**Gaus Width: 4.8017**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch93, adc2

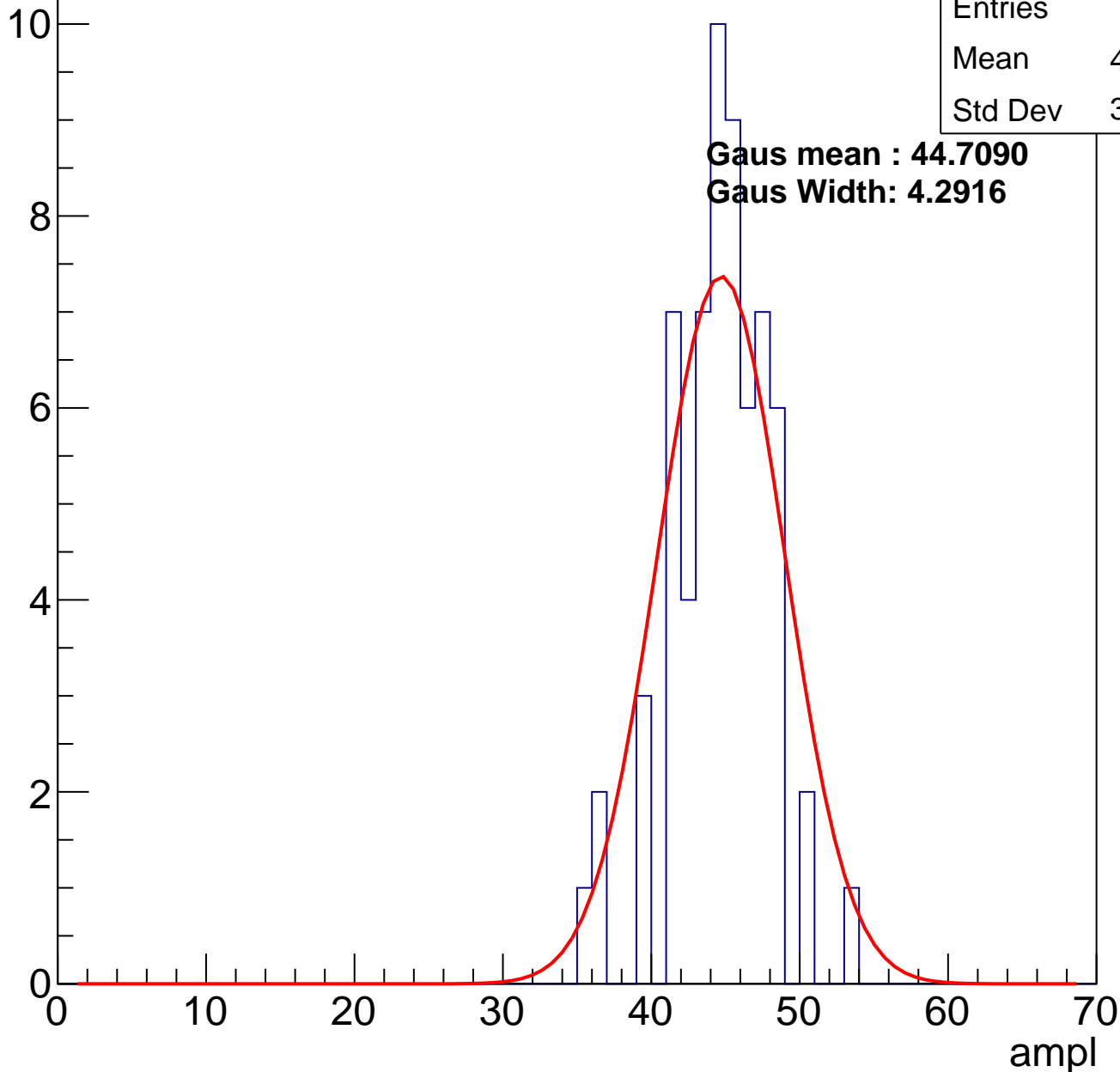
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	44.17
Std Dev	3.312

**Gaus mean : 44.7090**

**Gaus Width: 4.2916**

Entry



# B0L000S, U13-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	52
Mean	50.77
Std Dev	3.042

Entry

10

8

6

4

2

0

0

10

20

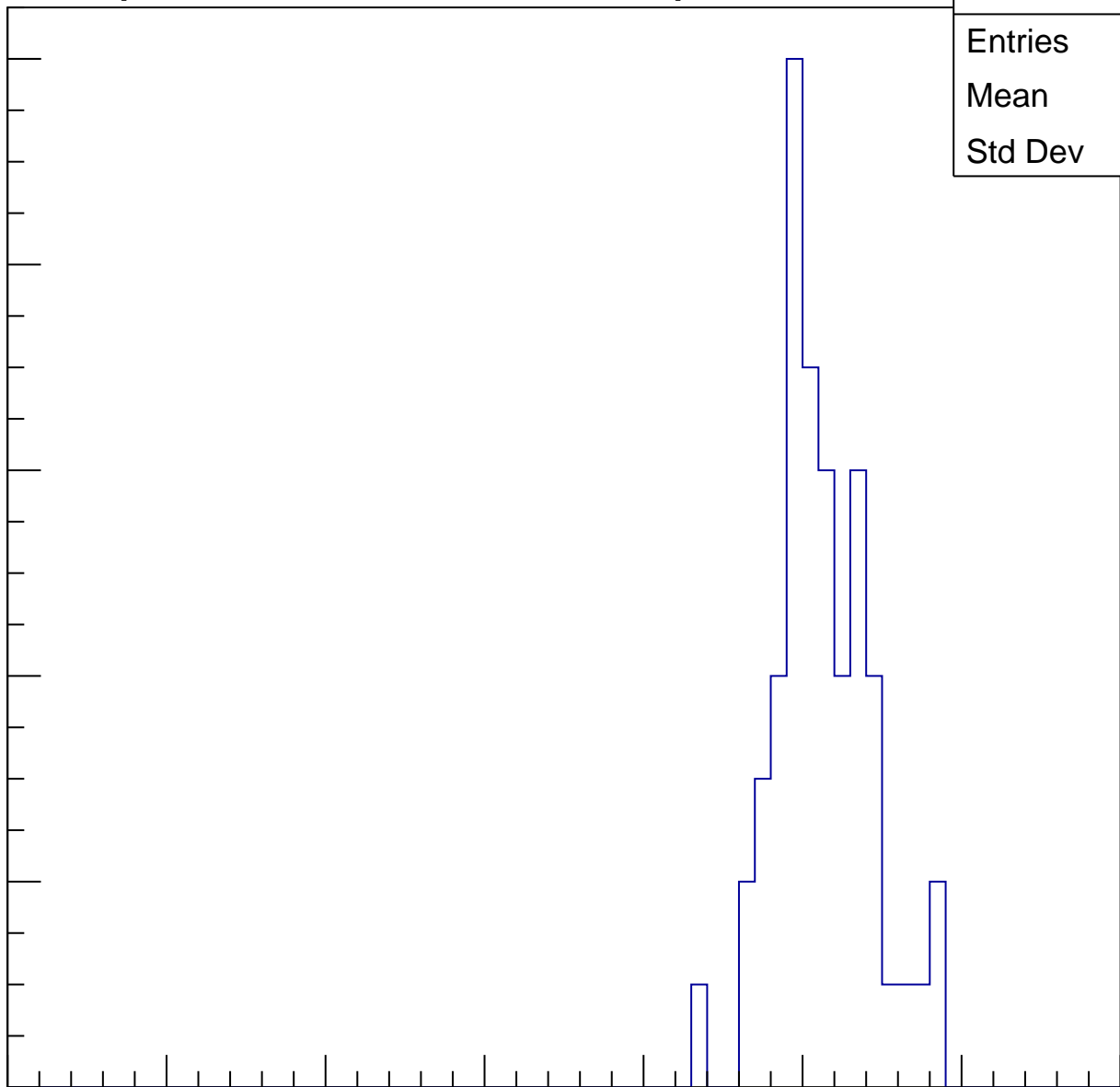
30

40

50

60

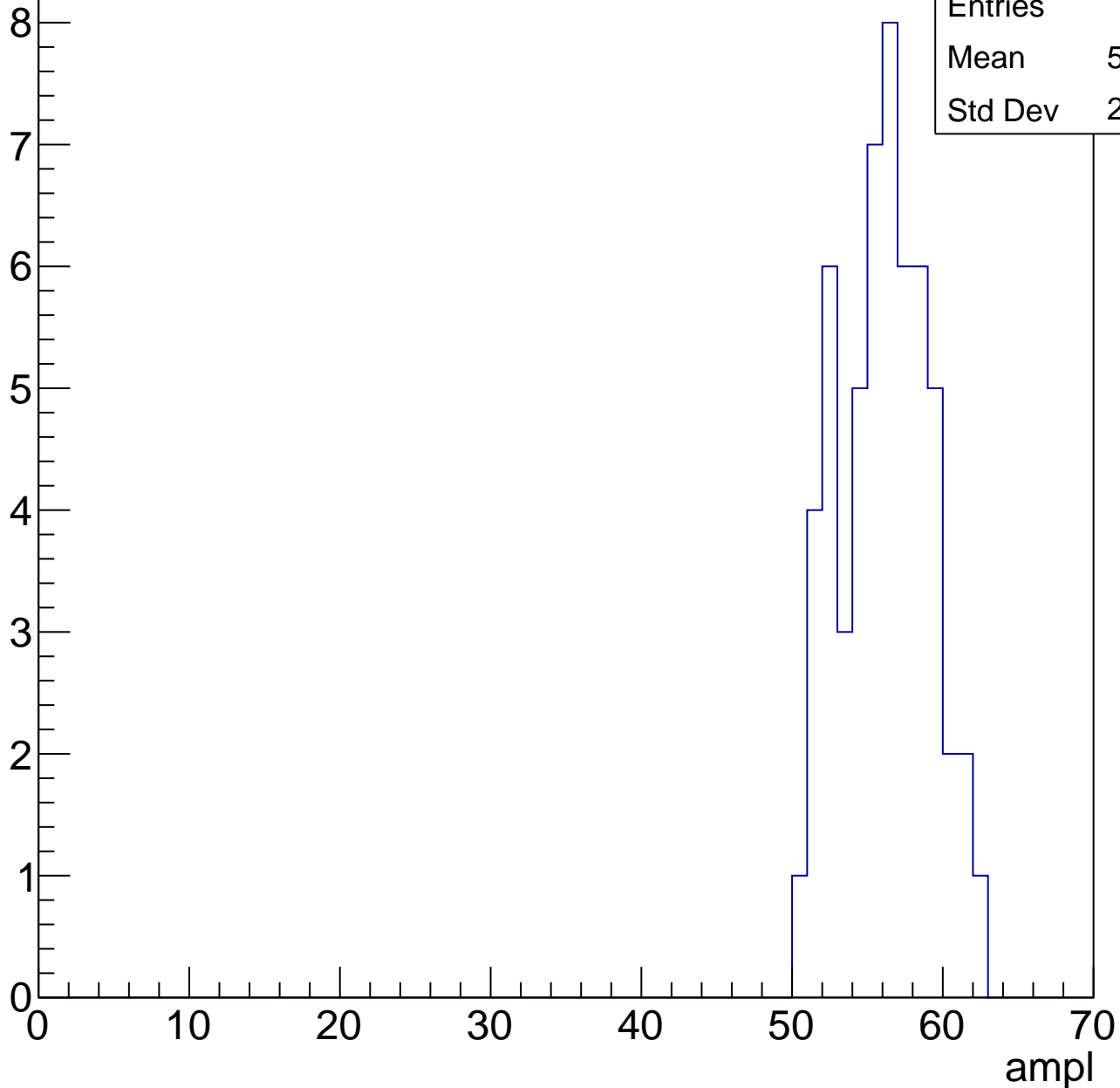
ampl



# B0L000S, U13-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

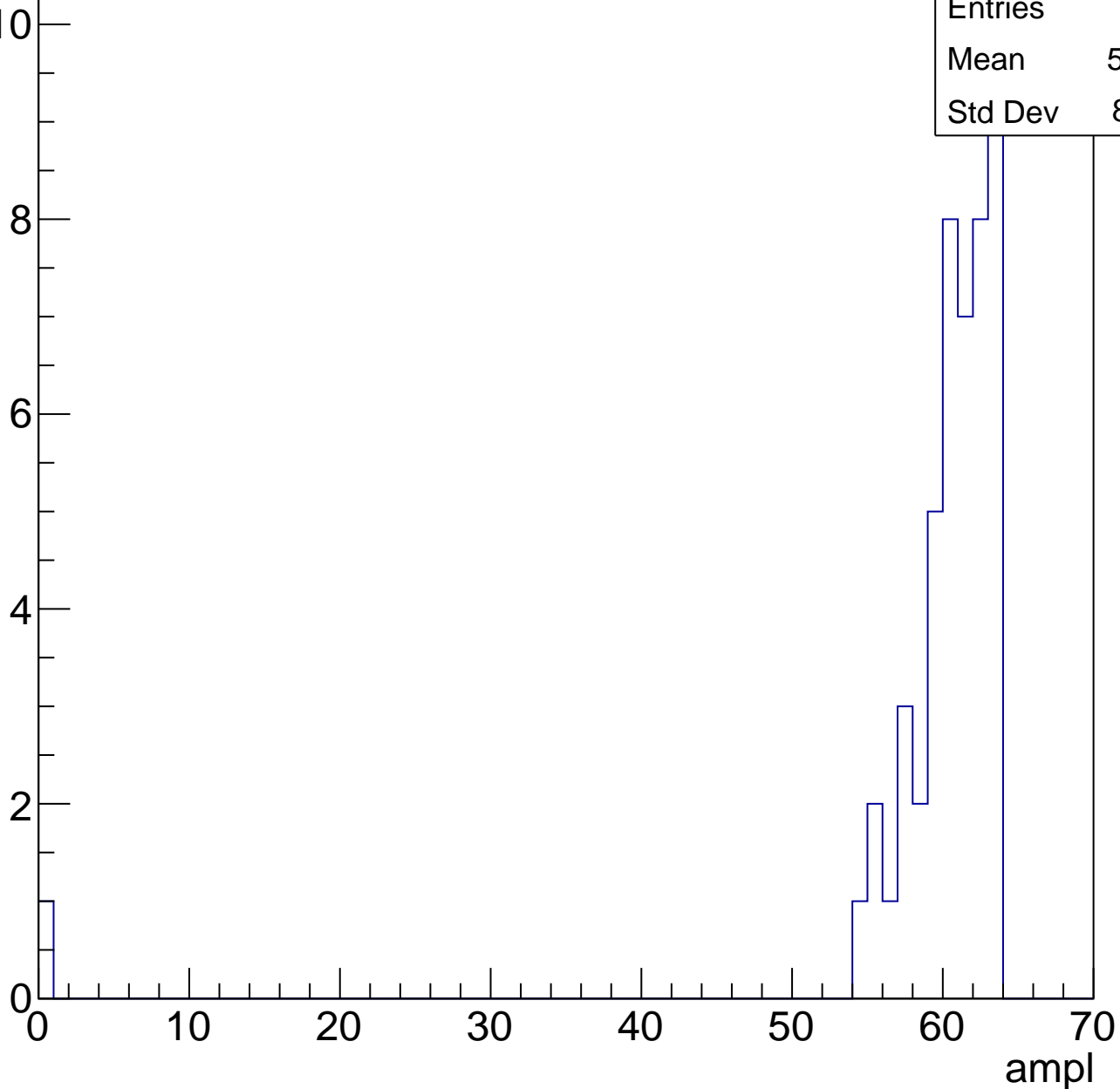


# B0L000S, U13-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	59.06
Std Dev	8.931



# B0L000S, U13-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U13-ch94, adc0

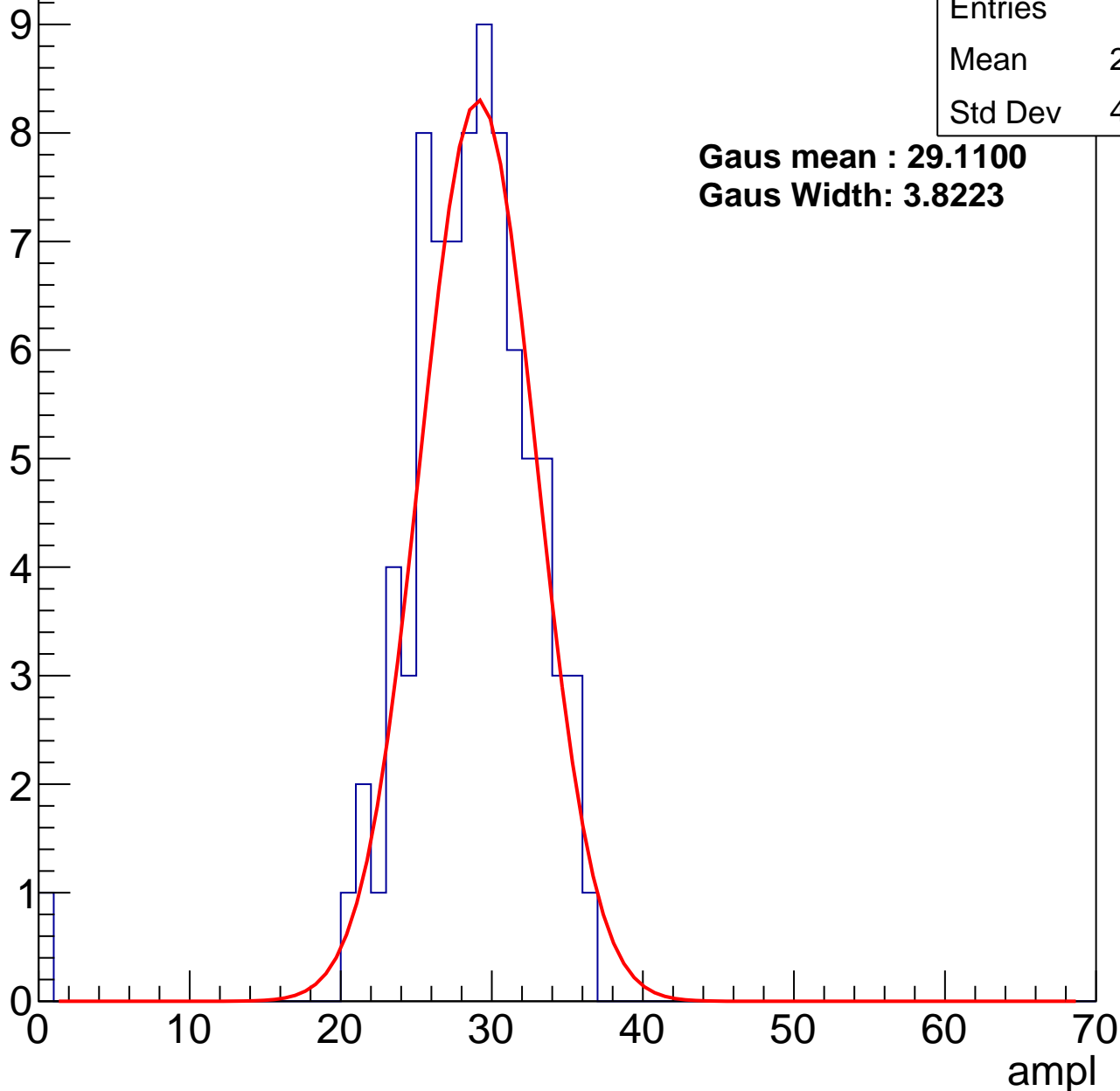
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	28.02
Std Dev	4.757

**Gaus mean : 29.1100**

**Gaus Width: 3.8223**



# B0L000S, U13-ch94, adc1

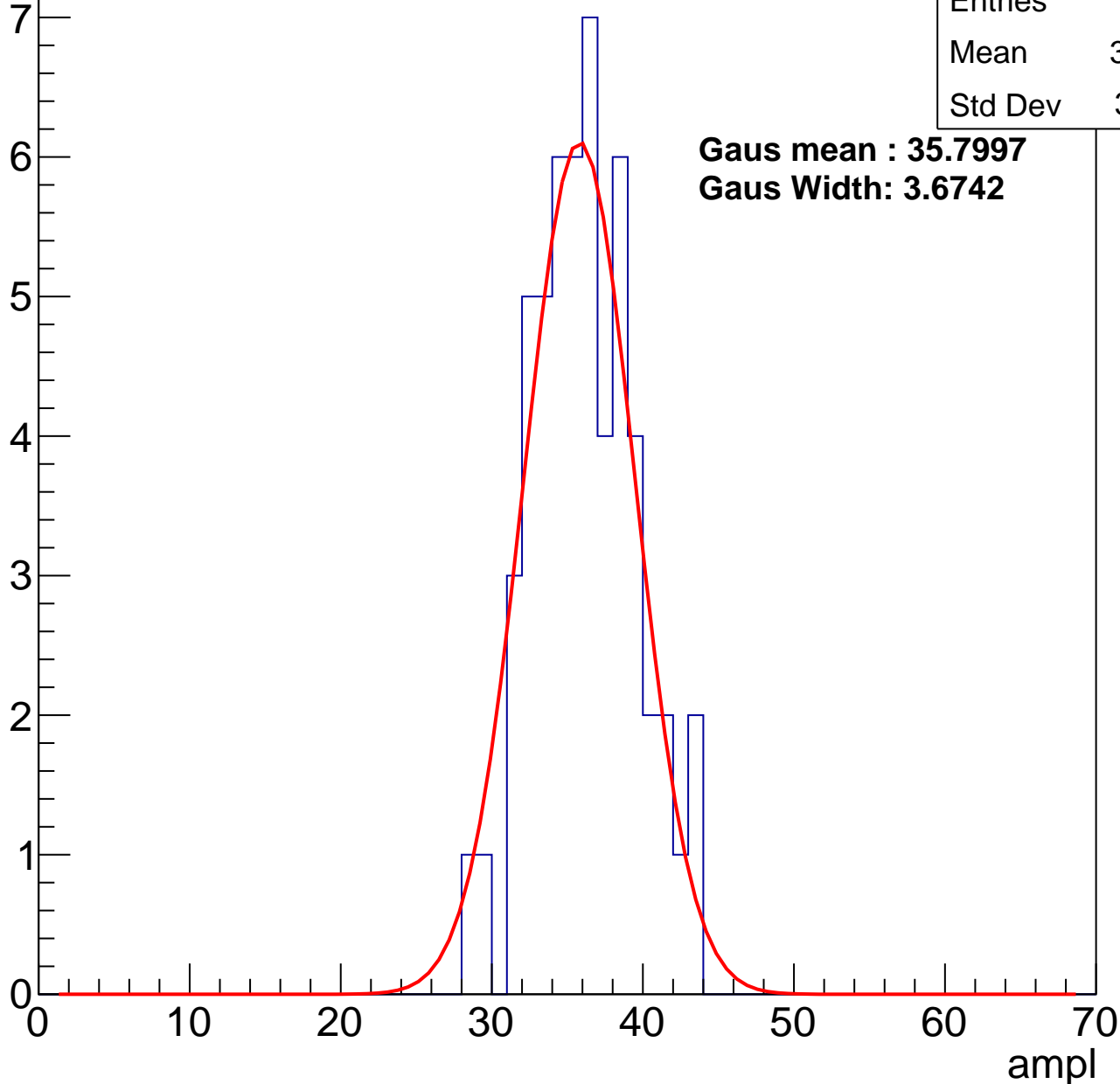
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.69
Std Dev	3.351

**Gaus mean : 35.7997**

**Gaus Width: 3.6742**



# B0L000S, U13-ch94, adc2

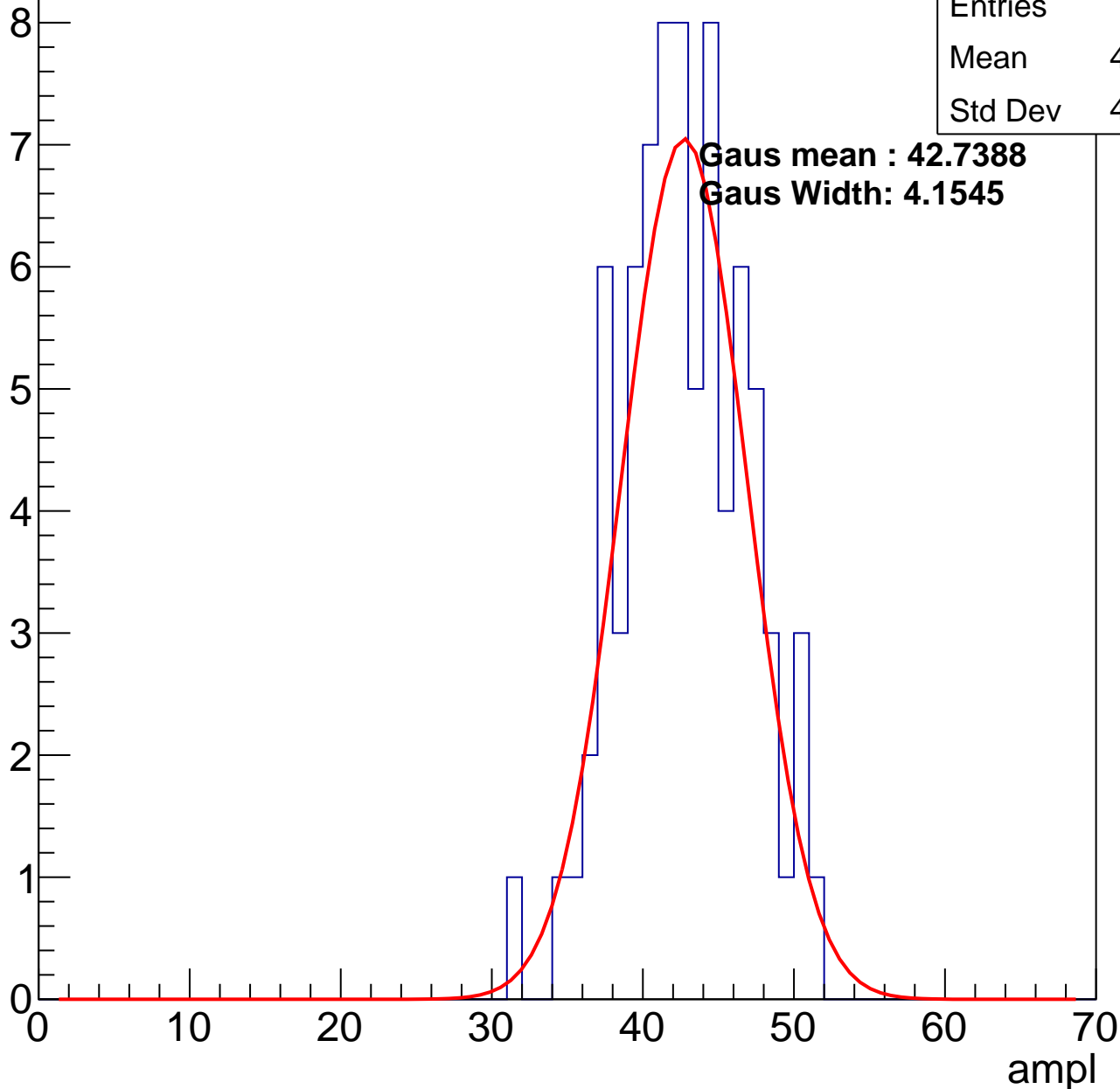
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	42.25
Std Dev	4.077

**Gaus mean : 42.7388**

**Gaus Width: 4.1545**

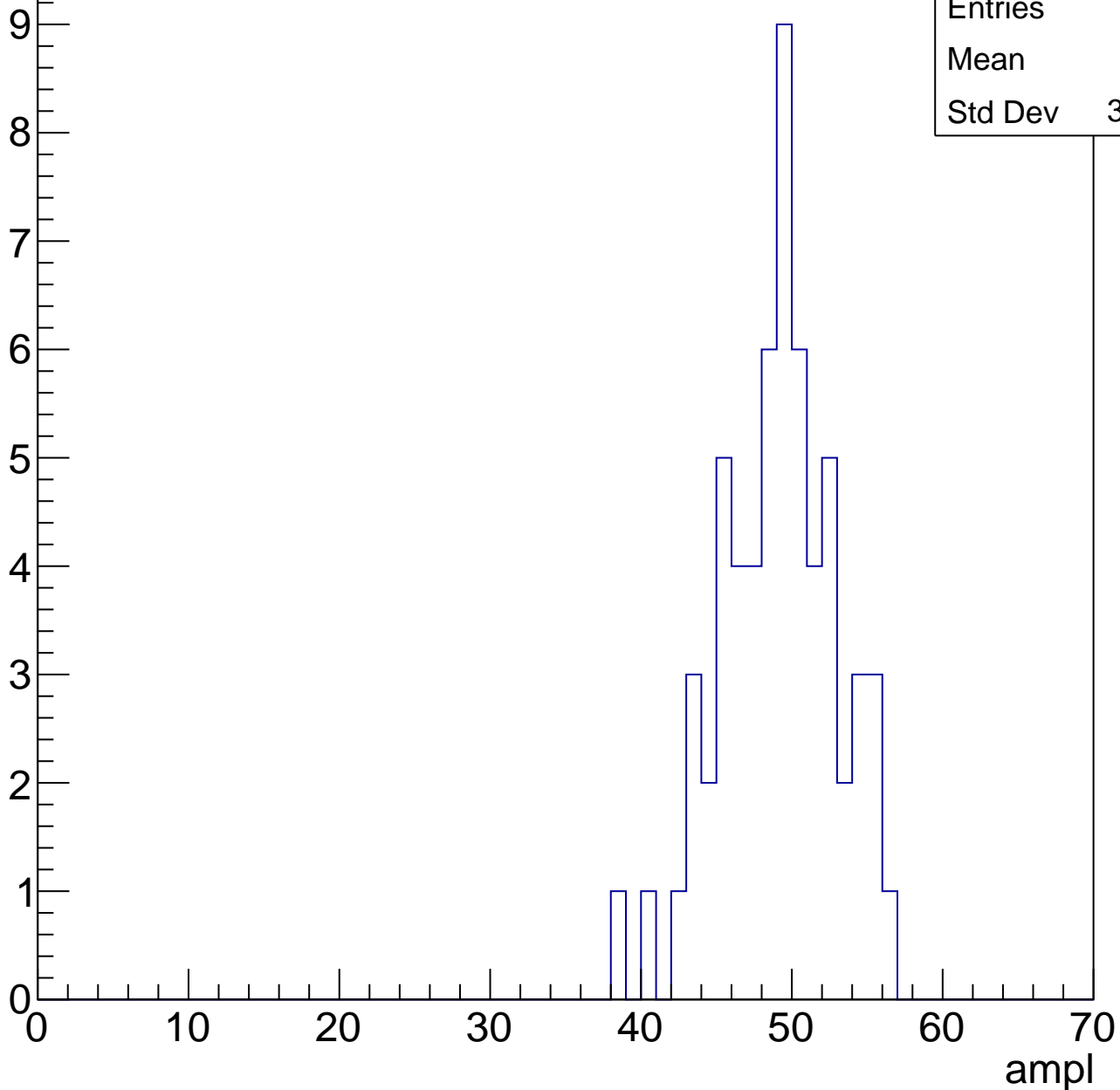


# B0L000S, U13-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	48.6
Std Dev	3.804

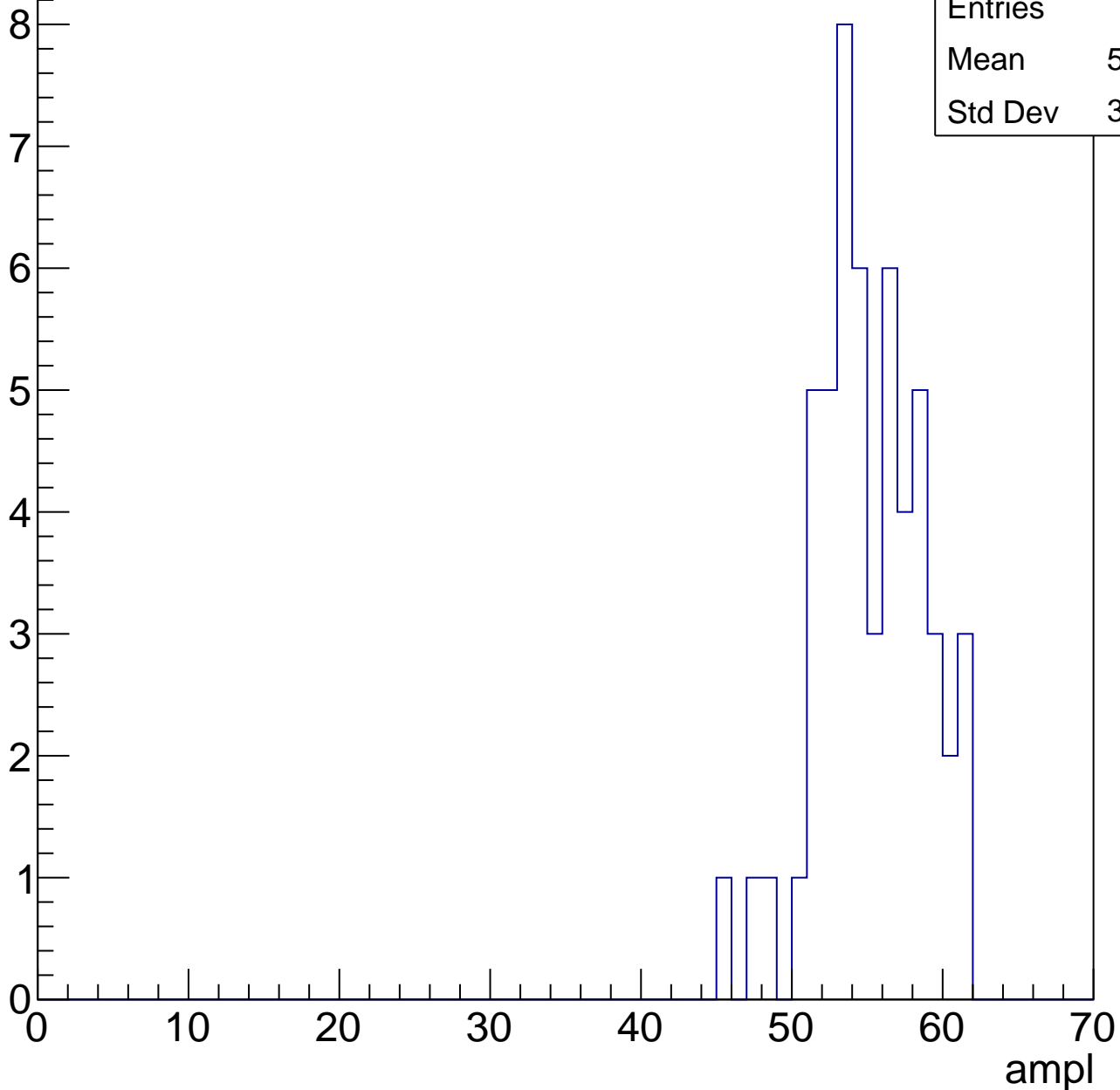


# B0L000S, U13-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	54.67
Std Dev	3.517

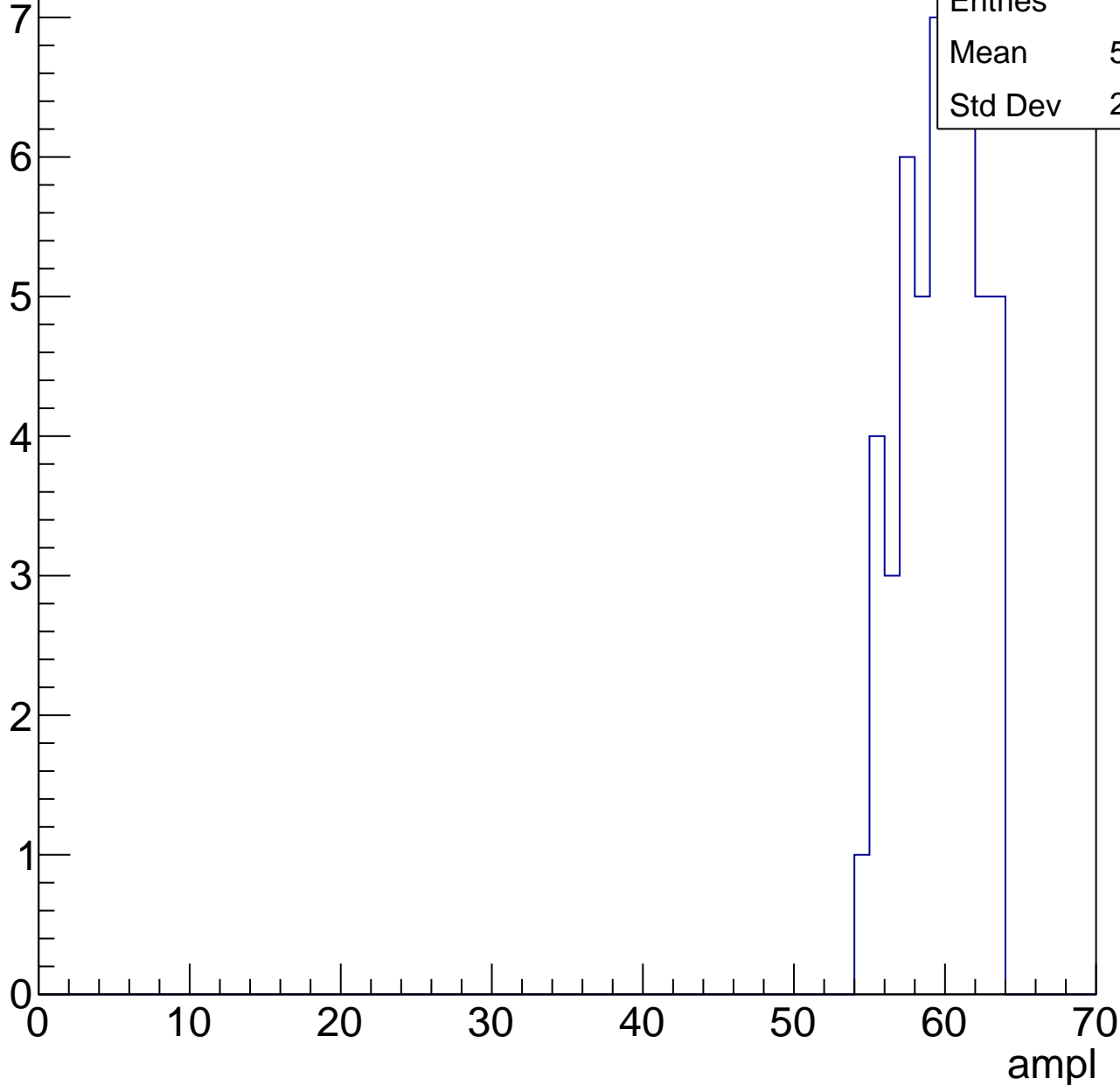


# B0L000S, U13-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	59.18
Std Dev	2.463

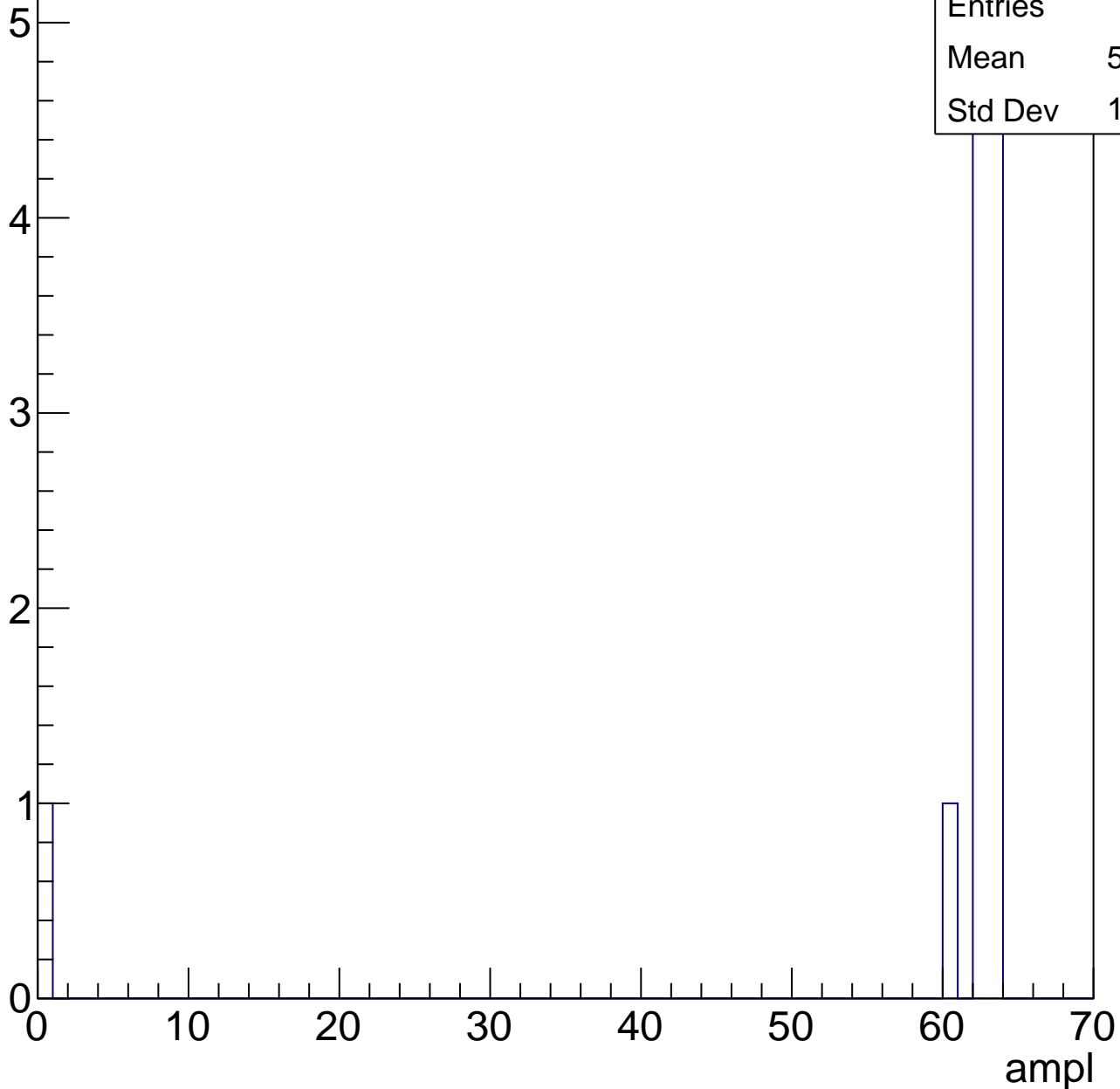


# B0L000S, U13-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	57.08
Std Dev	17.23





# B0L000S, U13-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch95, adc0

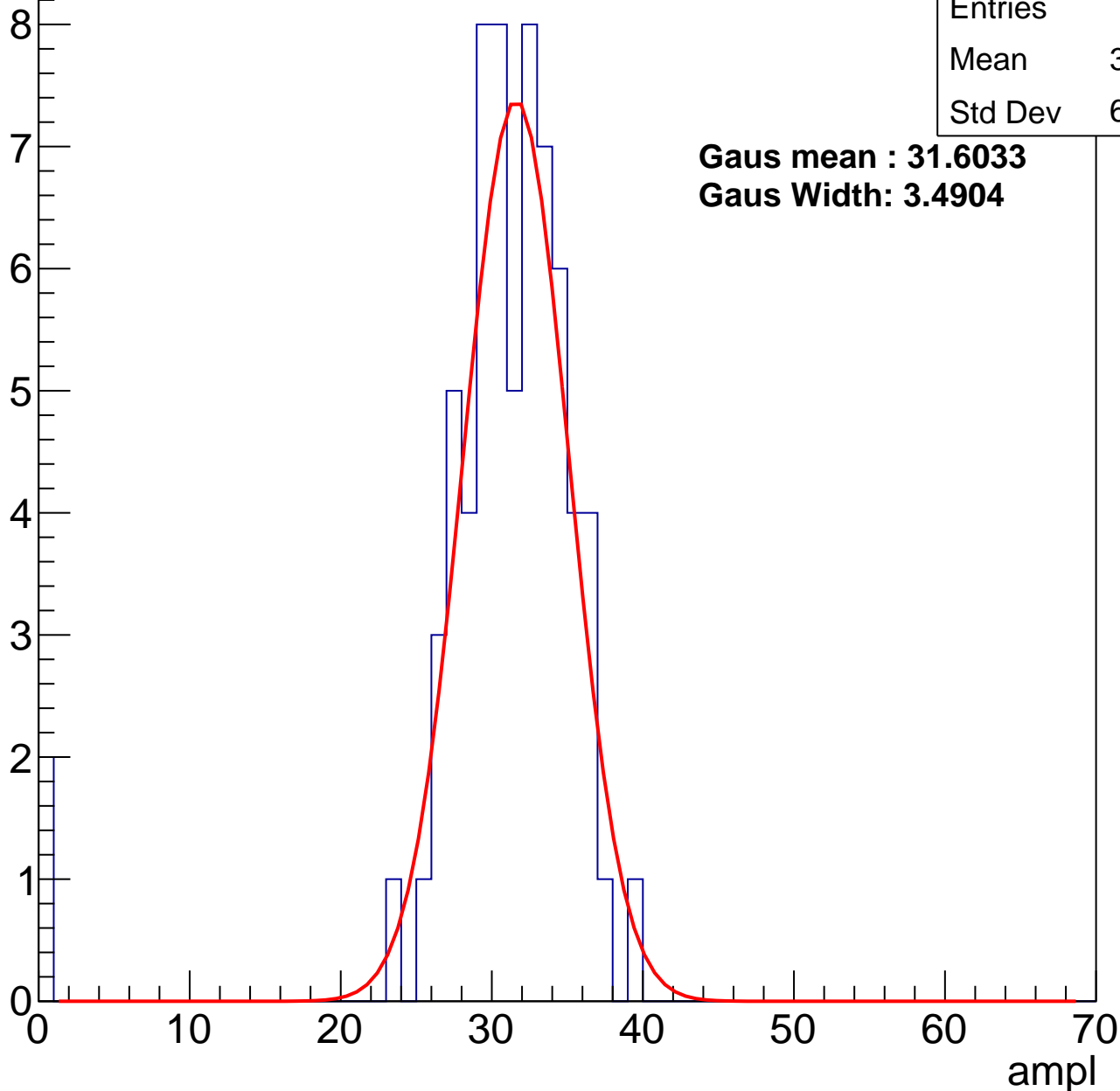
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.16
Std Dev	6.135

**Gaus mean : 31.6033**

**Gaus Width: 3.4904**



# B0L000S, U13-ch95, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	38.45
Std Dev	3.799

**Gaus mean : 38.9610**

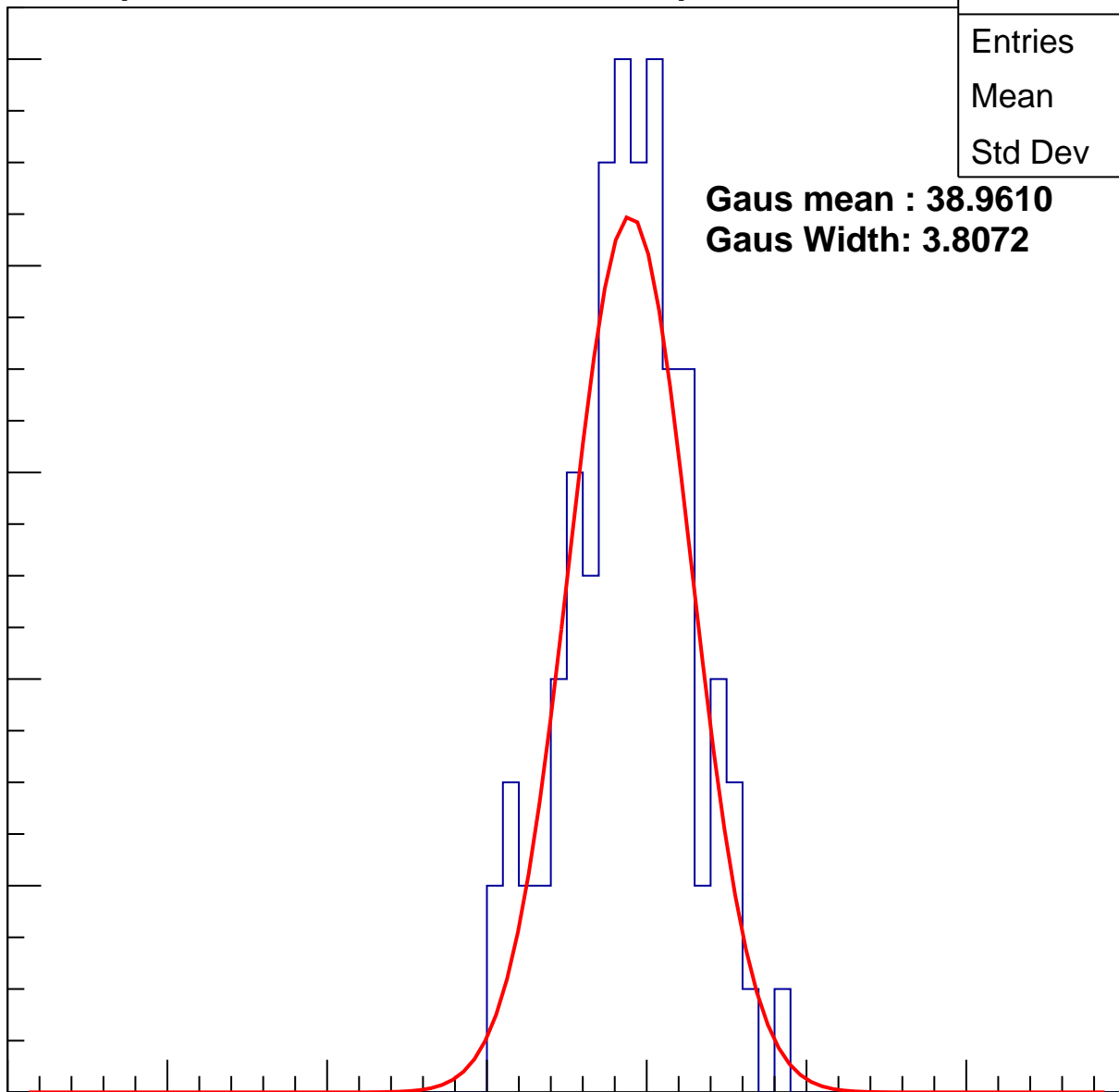
**Gaus Width: 3.8072**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch95, adc2

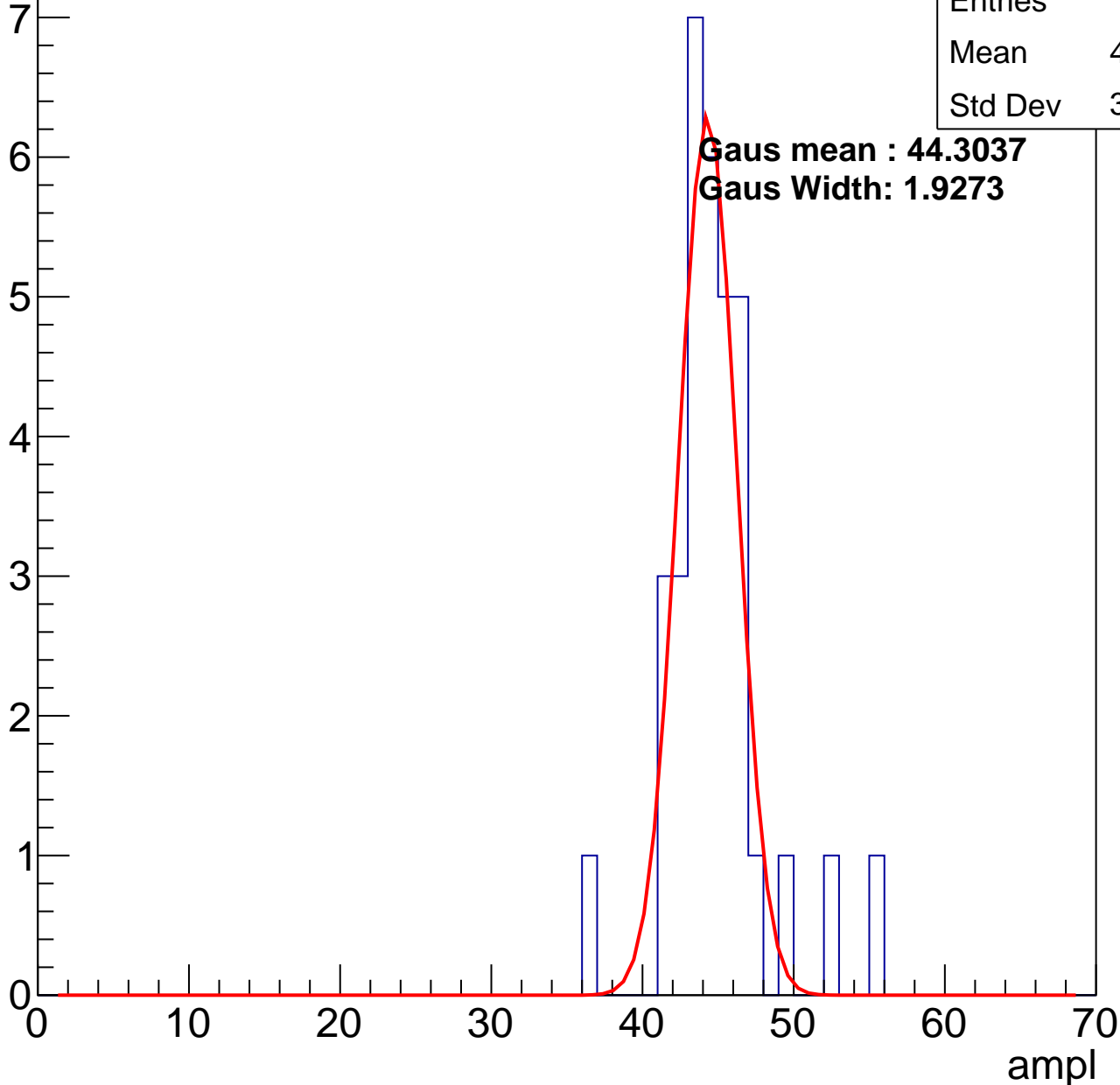
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	44.35
Std Dev	3.207

**Gaus mean : 44.3037**

**Gaus Width: 1.9273**

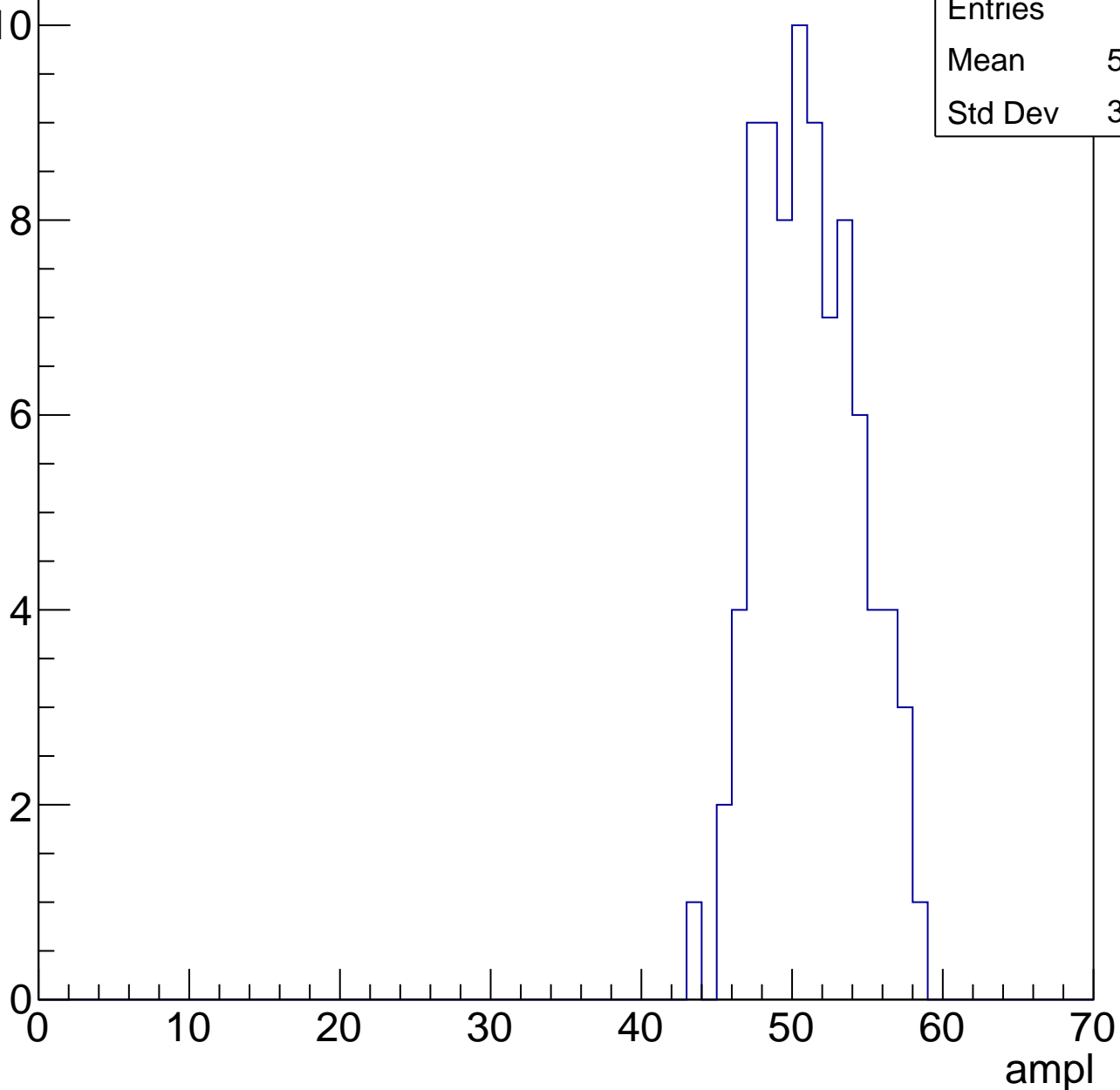


# B0L000S, U13-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	50.68
Std Dev	3.269

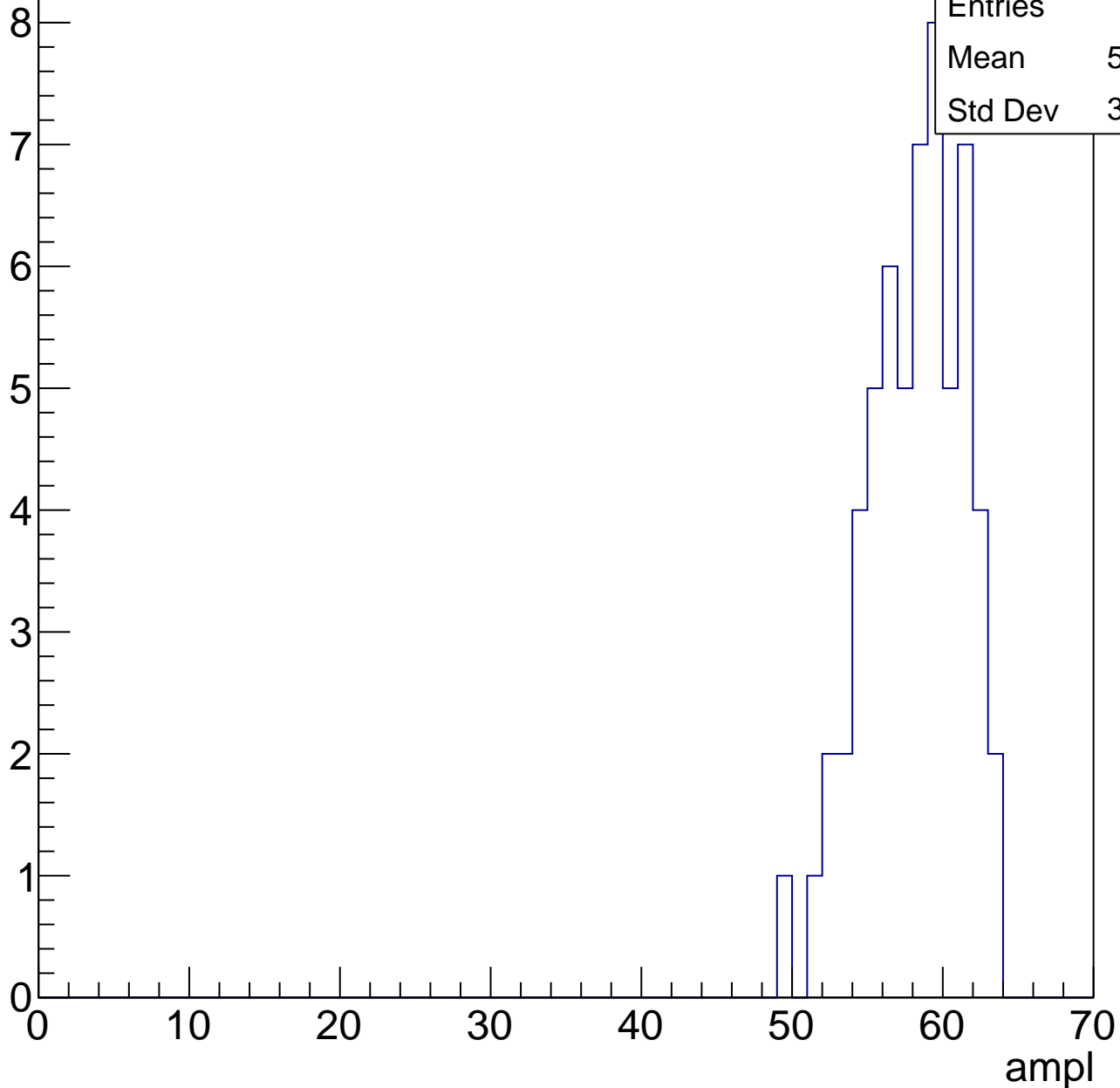


# B0L000S, U13-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	57.64
Std Dev	3.156

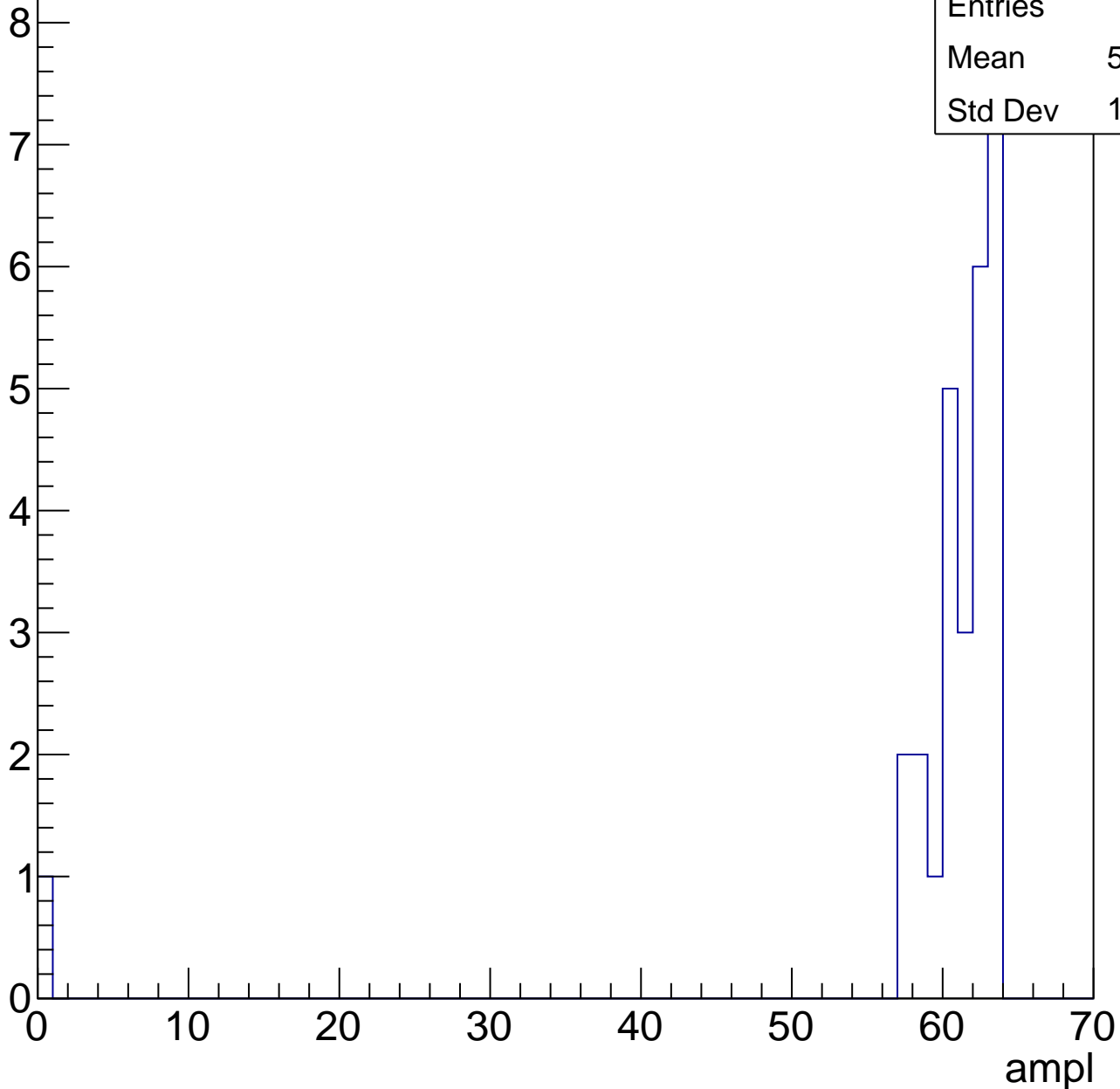


# B0L000S, U13-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.86
Std Dev	11.48



# B0L000S, U13-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

0 10 20 30 40 50 60 70

ampl

Entries	1
Mean	59
Std Dev	0



# B0L000S, U13-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch96, adc0

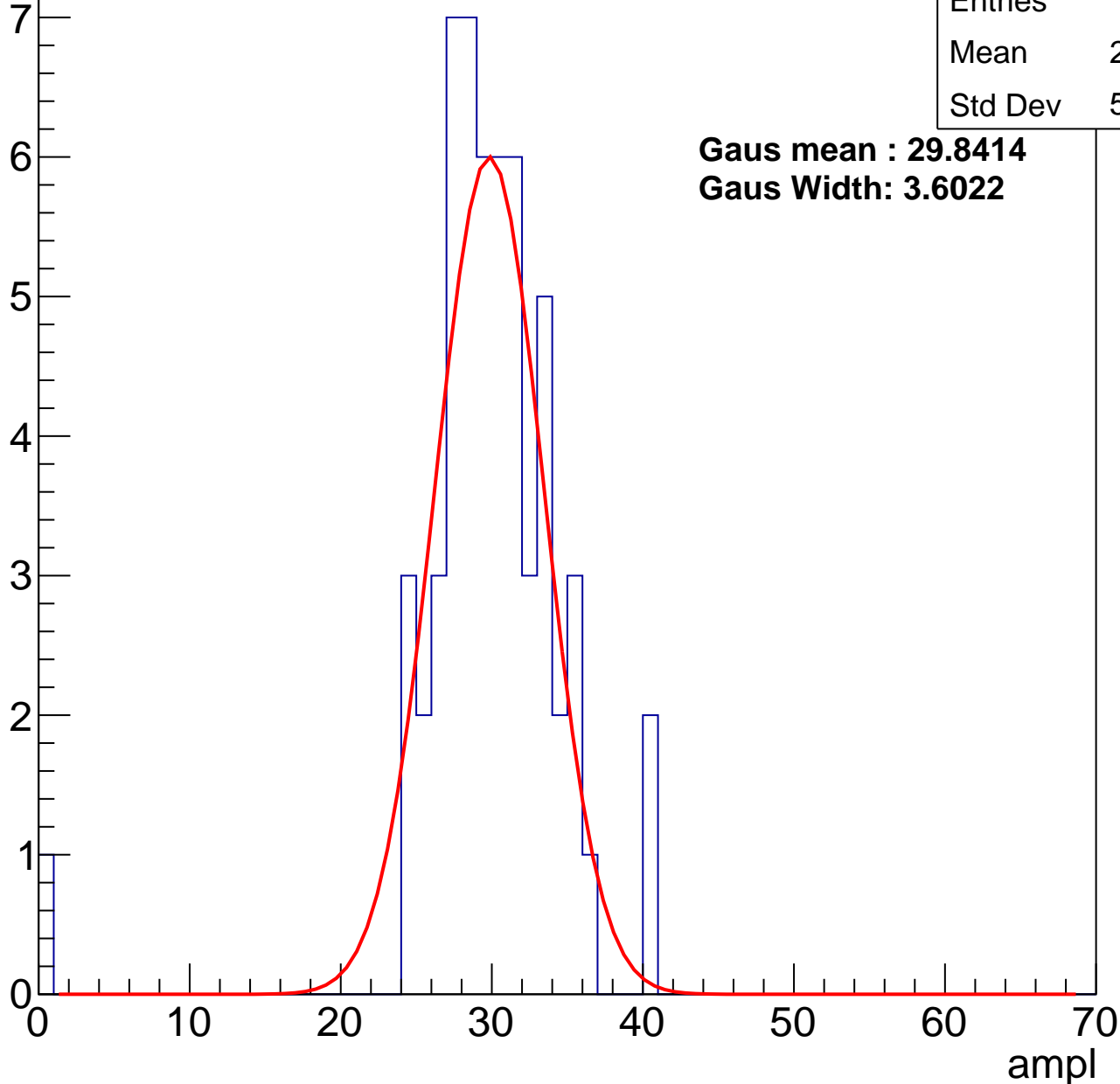
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	29.39
Std Dev	5.277

**Gaus mean : 29.8414**

**Gaus Width: 3.6022**



# B0L000S, U13-ch96, adc1

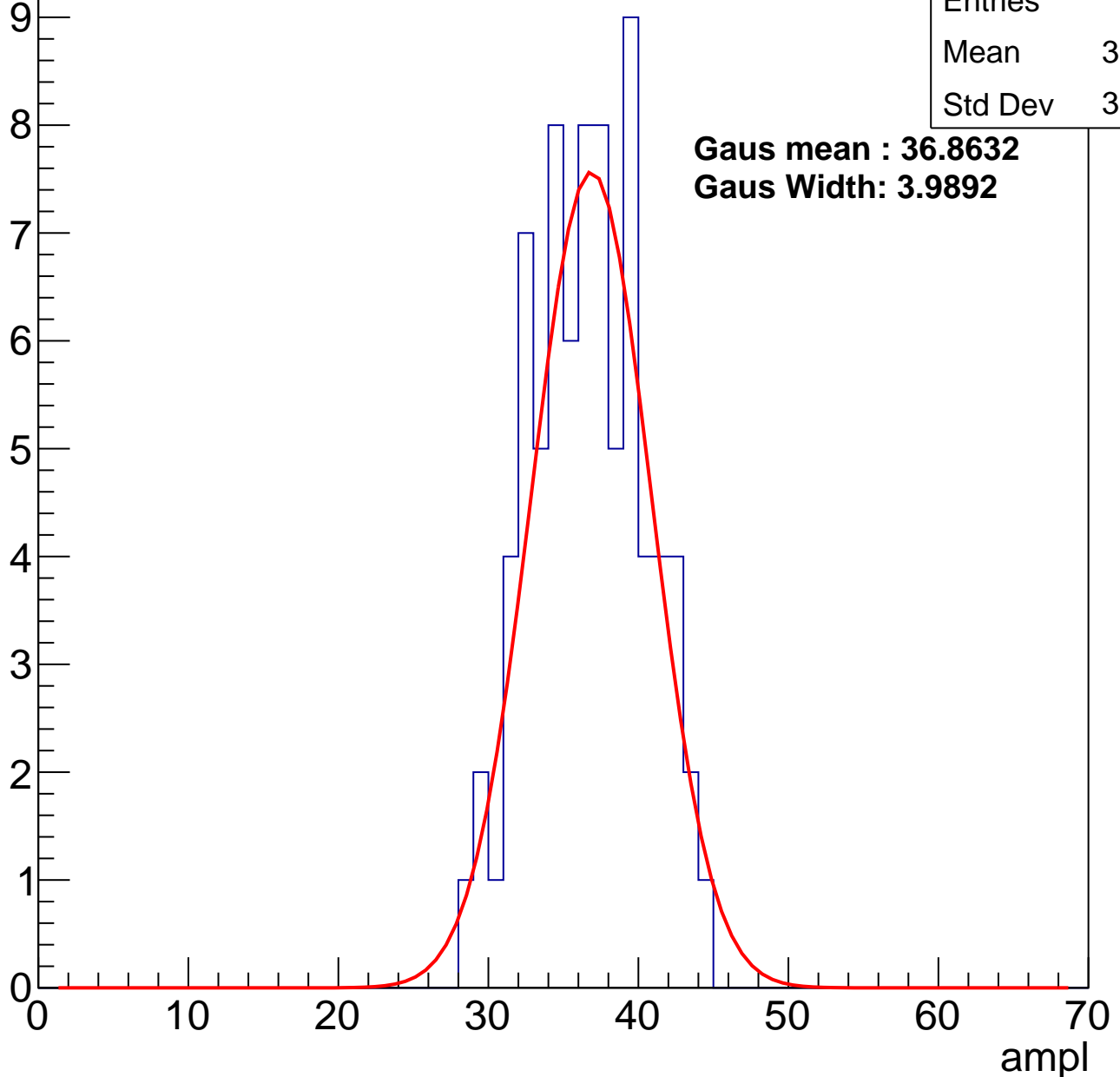
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	36.18
Std Dev	3.679

**Gaus mean : 36.8632**

**Gaus Width: 3.9892**



# B0L000S, U13-ch96, adc2

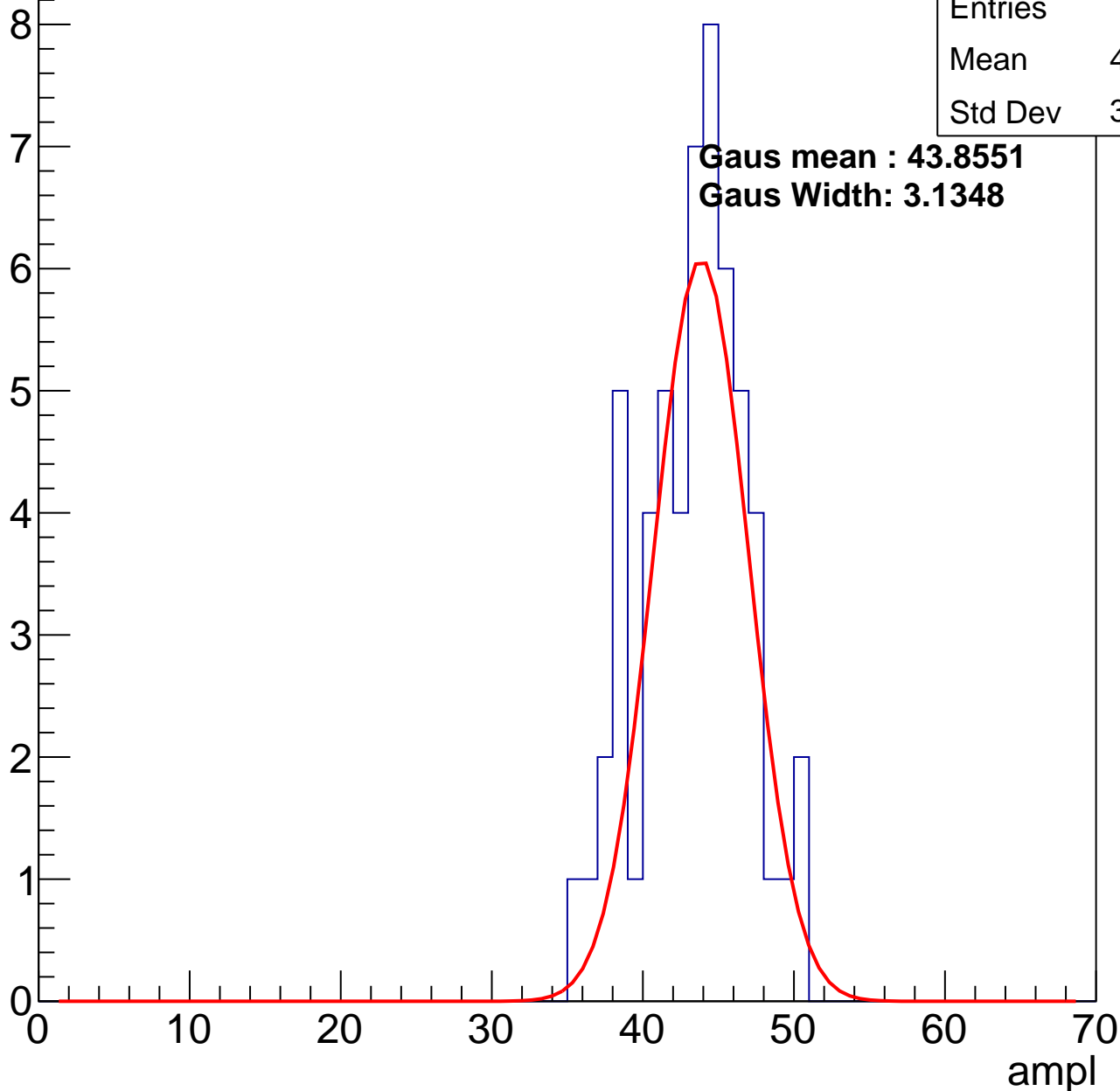
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.89
Std Dev	3.468

**Gaus mean : 43.8551**

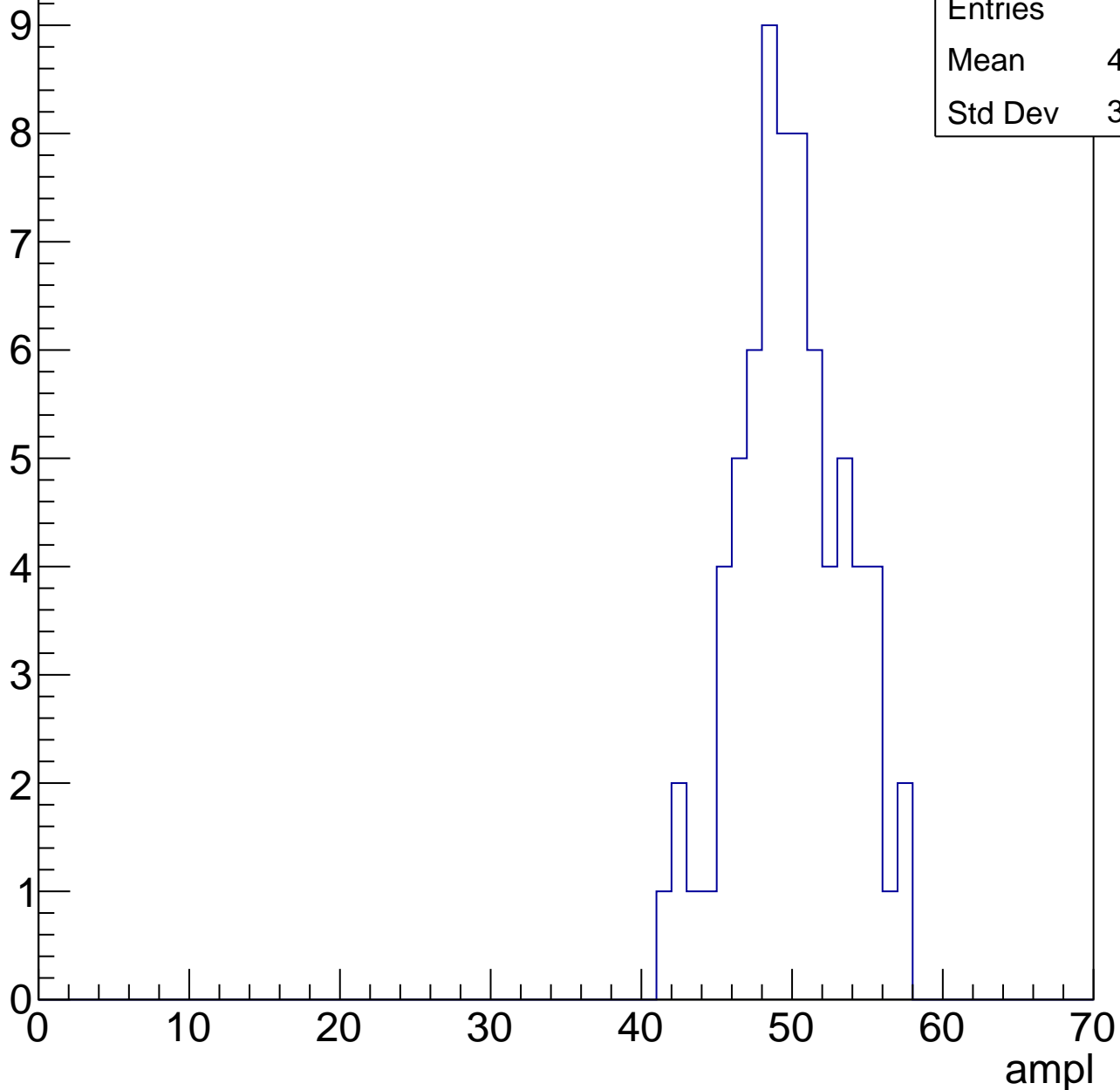
**Gaus Width: 3.1348**



# B0L000S, U13-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

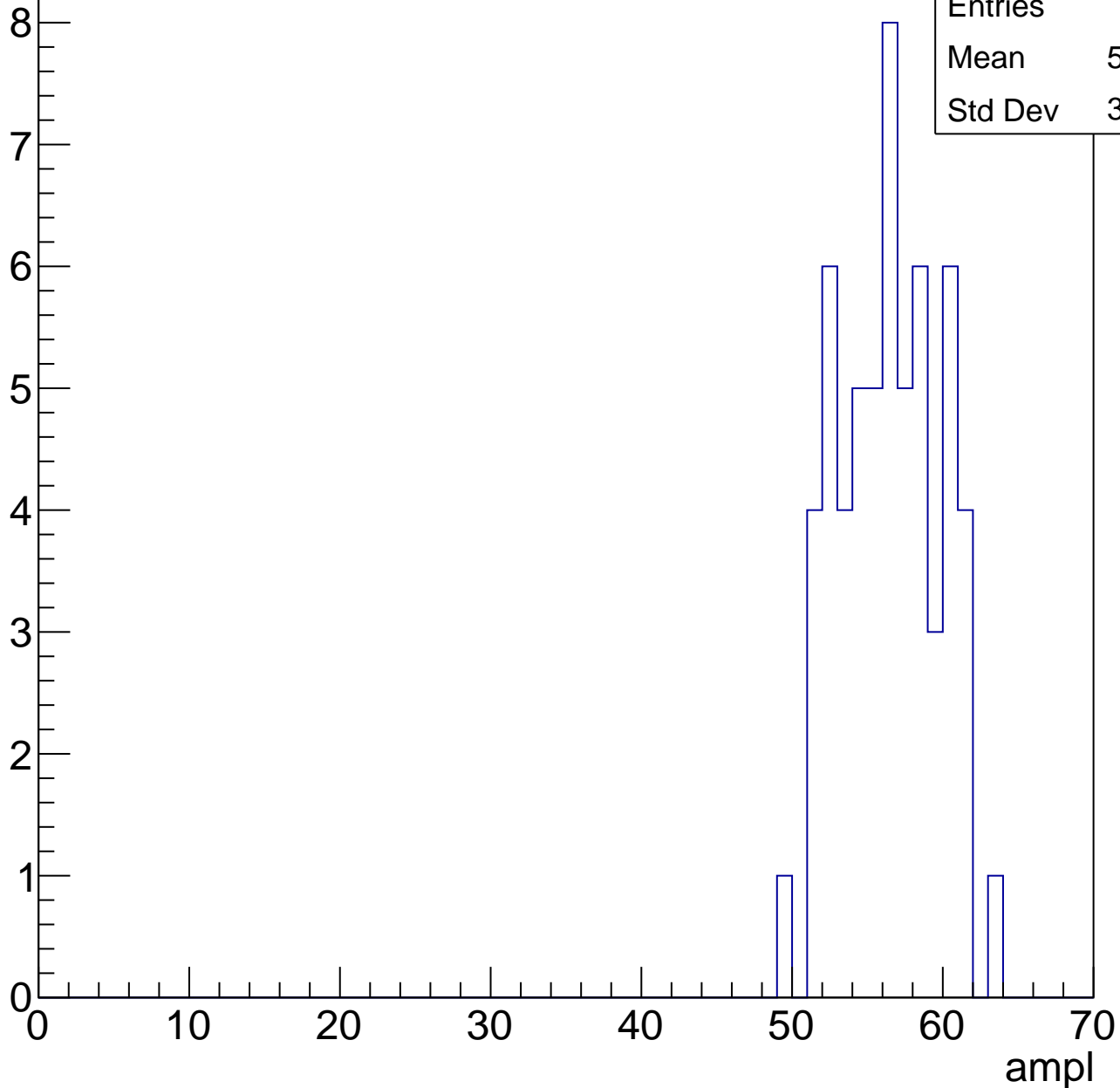


# B0L000S, U13-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.98
Std Dev	3.235

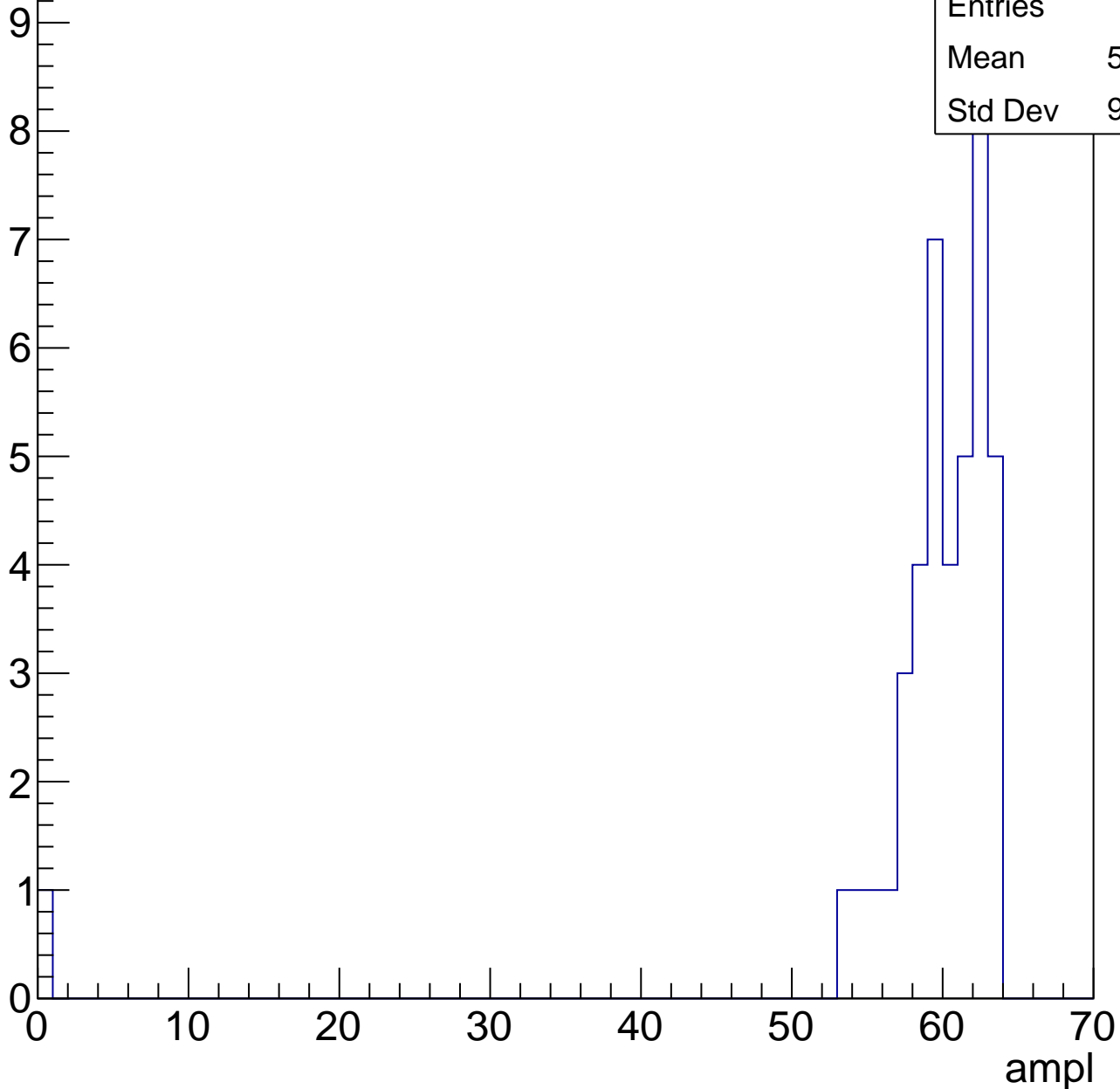


# B0L000S, U13-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	58.38
Std Dev	9.452



# B0L000S, U13-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch97, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	29.34
Std Dev	5.771

**Gaus mean : 30.3239**

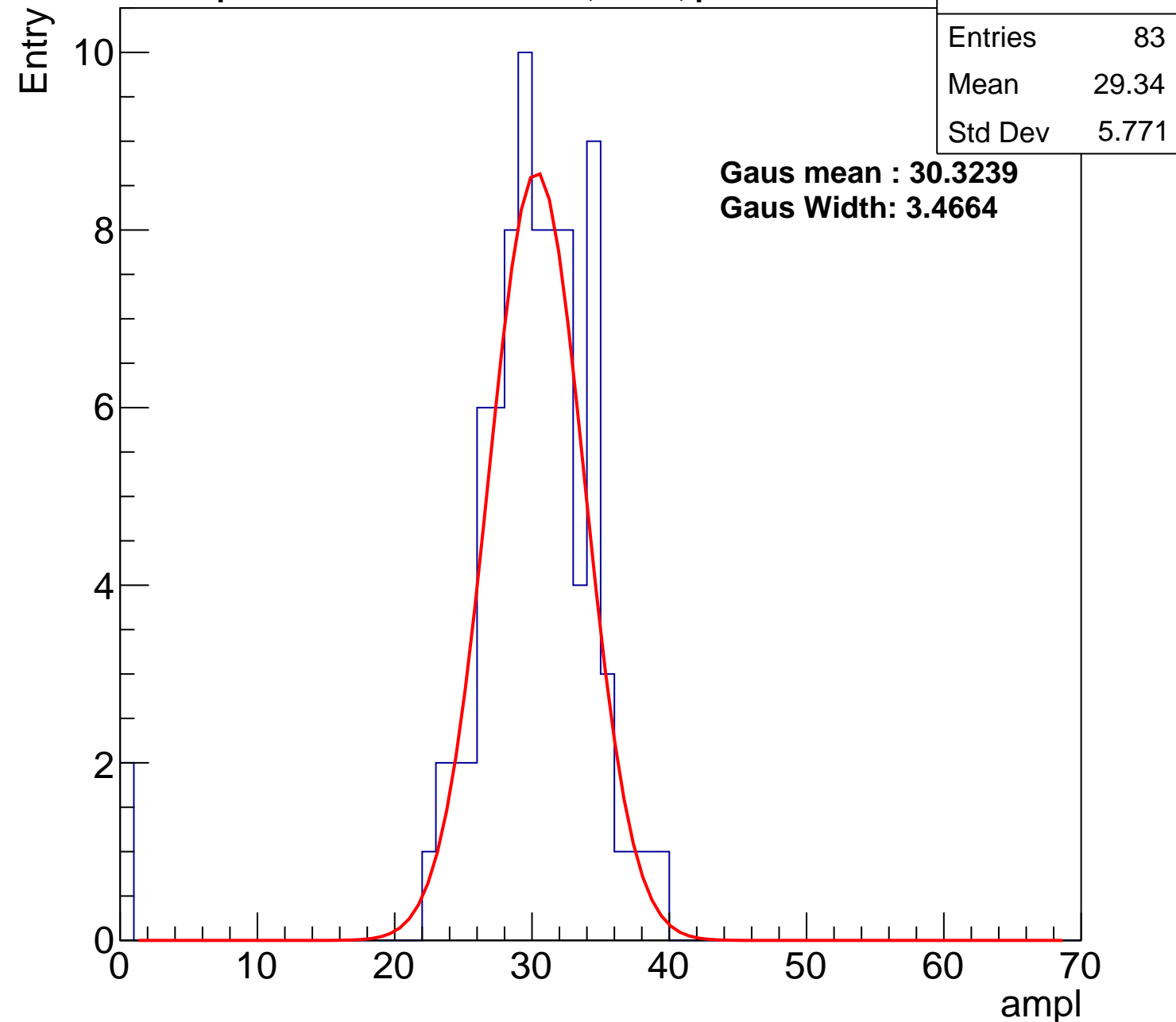
**Gaus Width: 3.4664**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch97, adc1

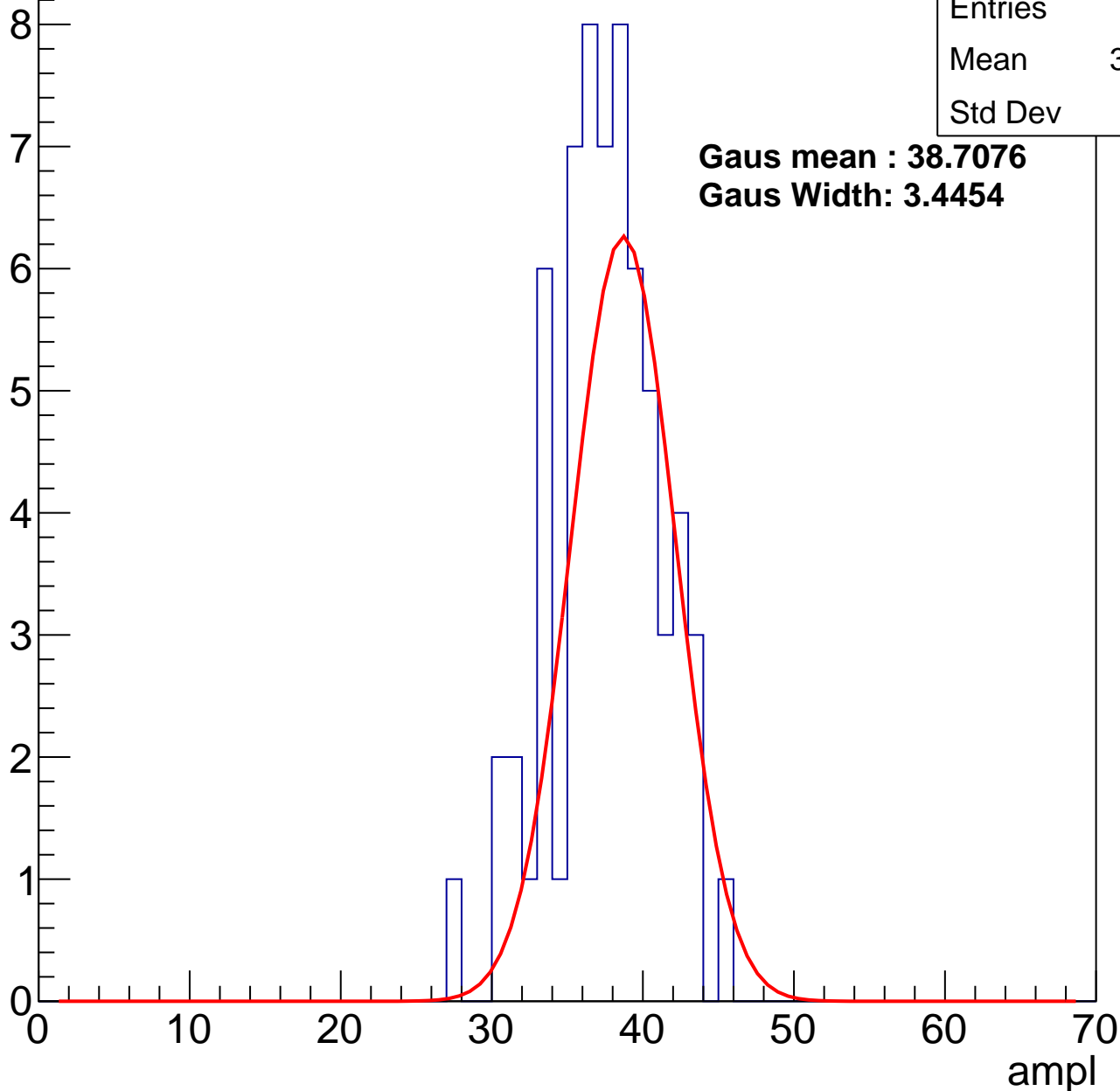
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.05
Std Dev	3.58

**Gaus mean : 38.7076**

**Gaus Width: 3.4454**



# B0L000S, U13-ch97, adc2

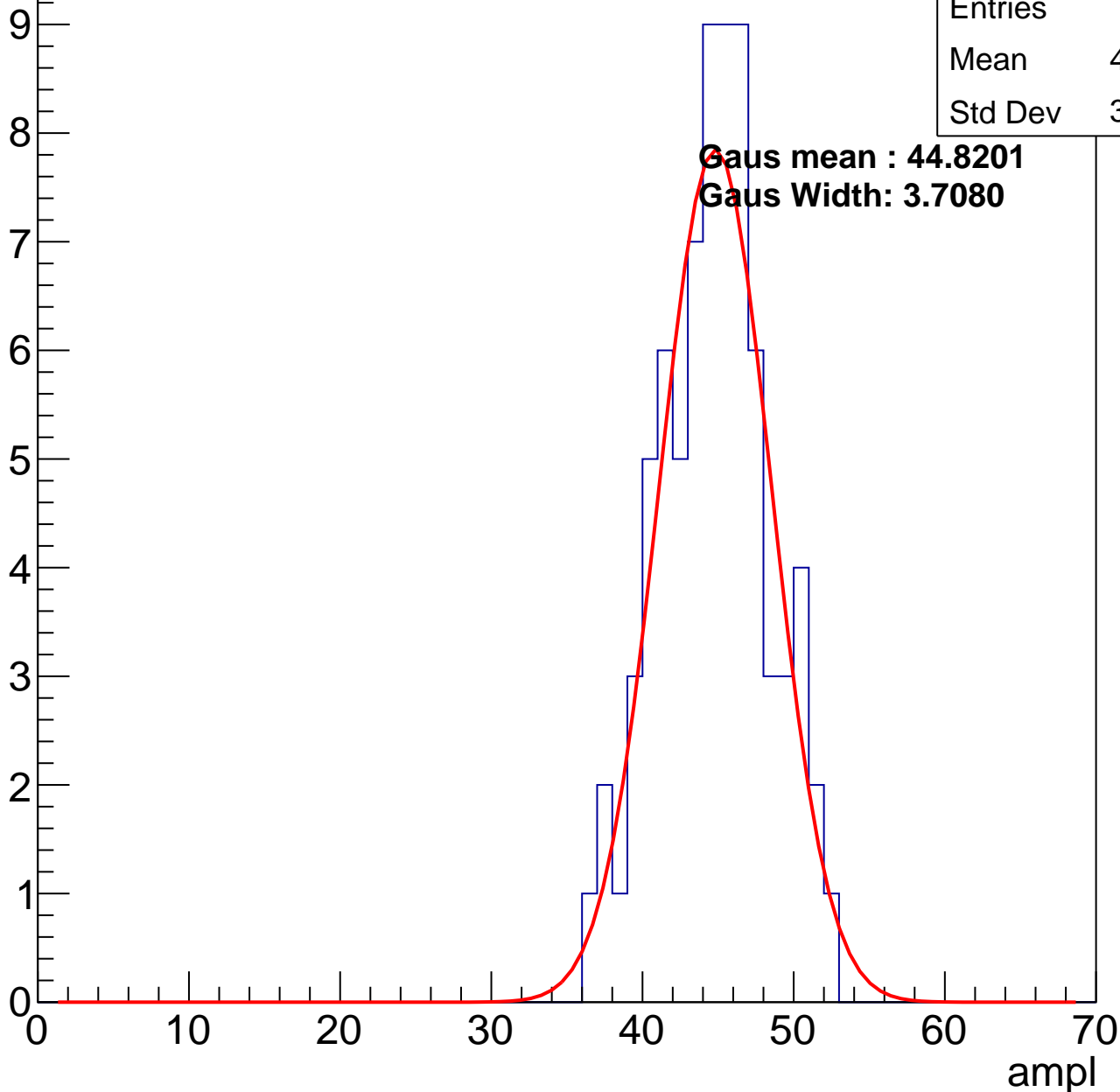
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	44.26
Std Dev	3.548

**Gaus mean : 44.8201**

**Gaus Width: 3.7080**

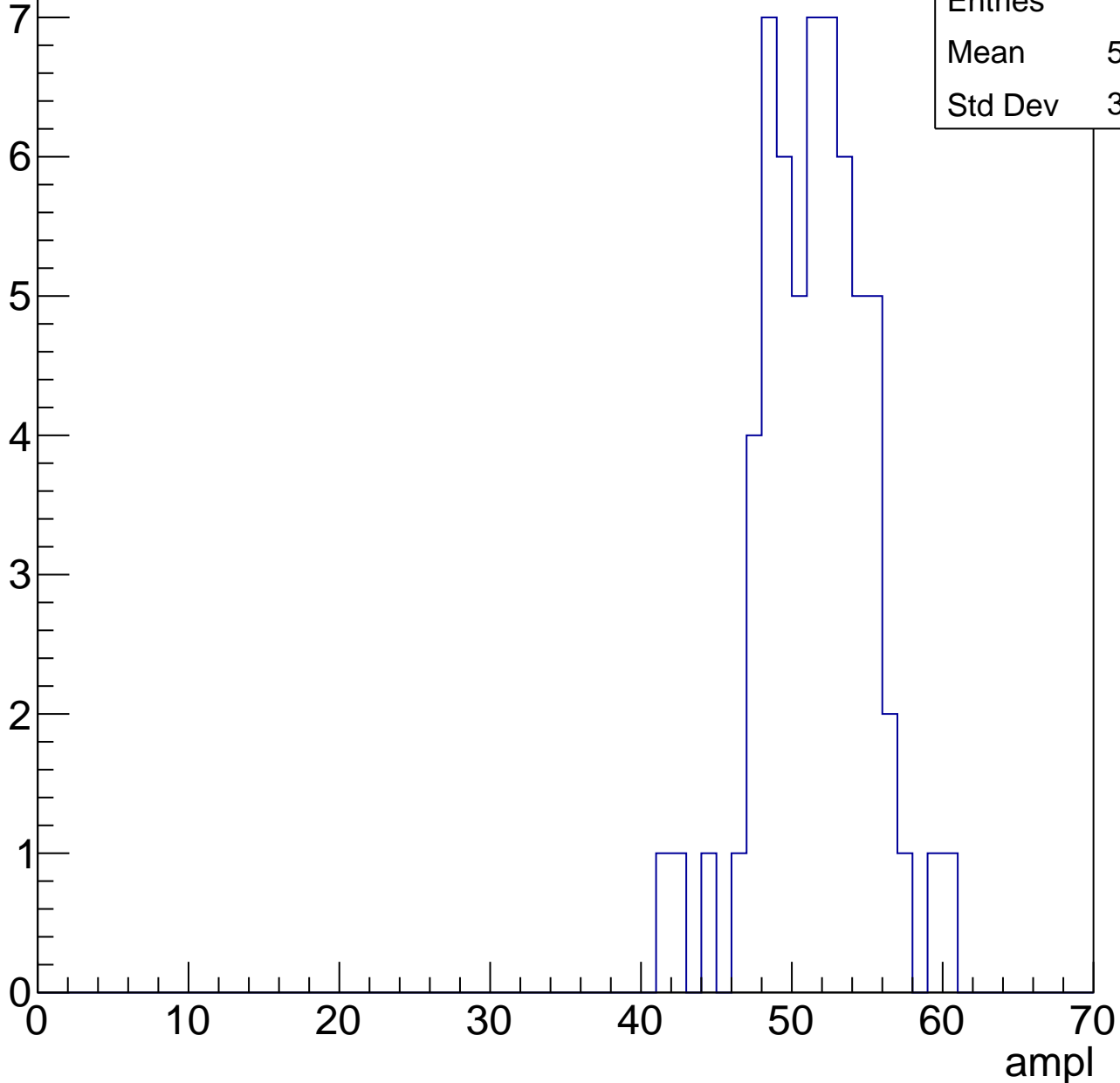


# B0L000S, U13-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

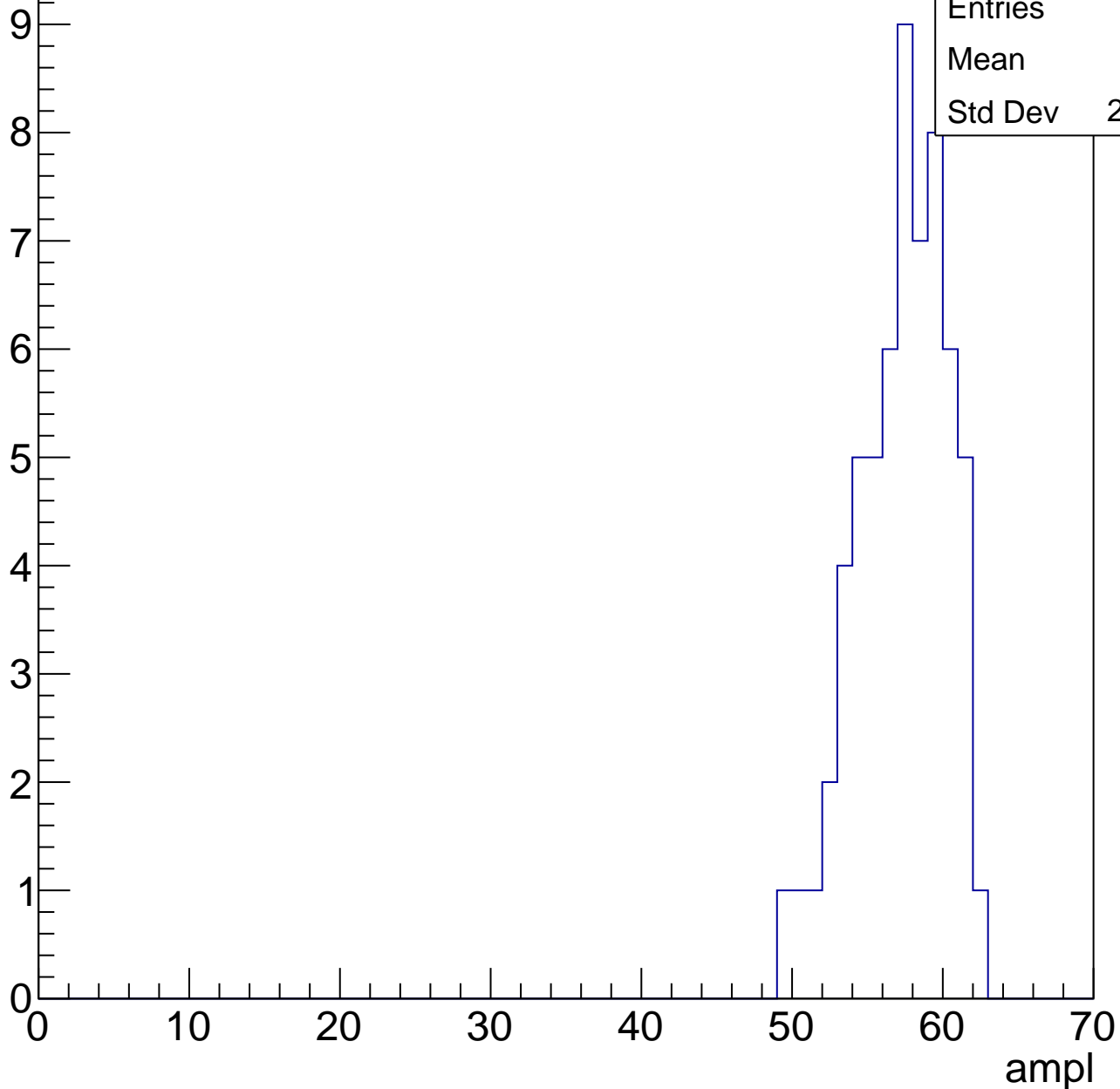
Entries	61
Mean	51.03
Std Dev	3.617



# B0L000S, U13-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

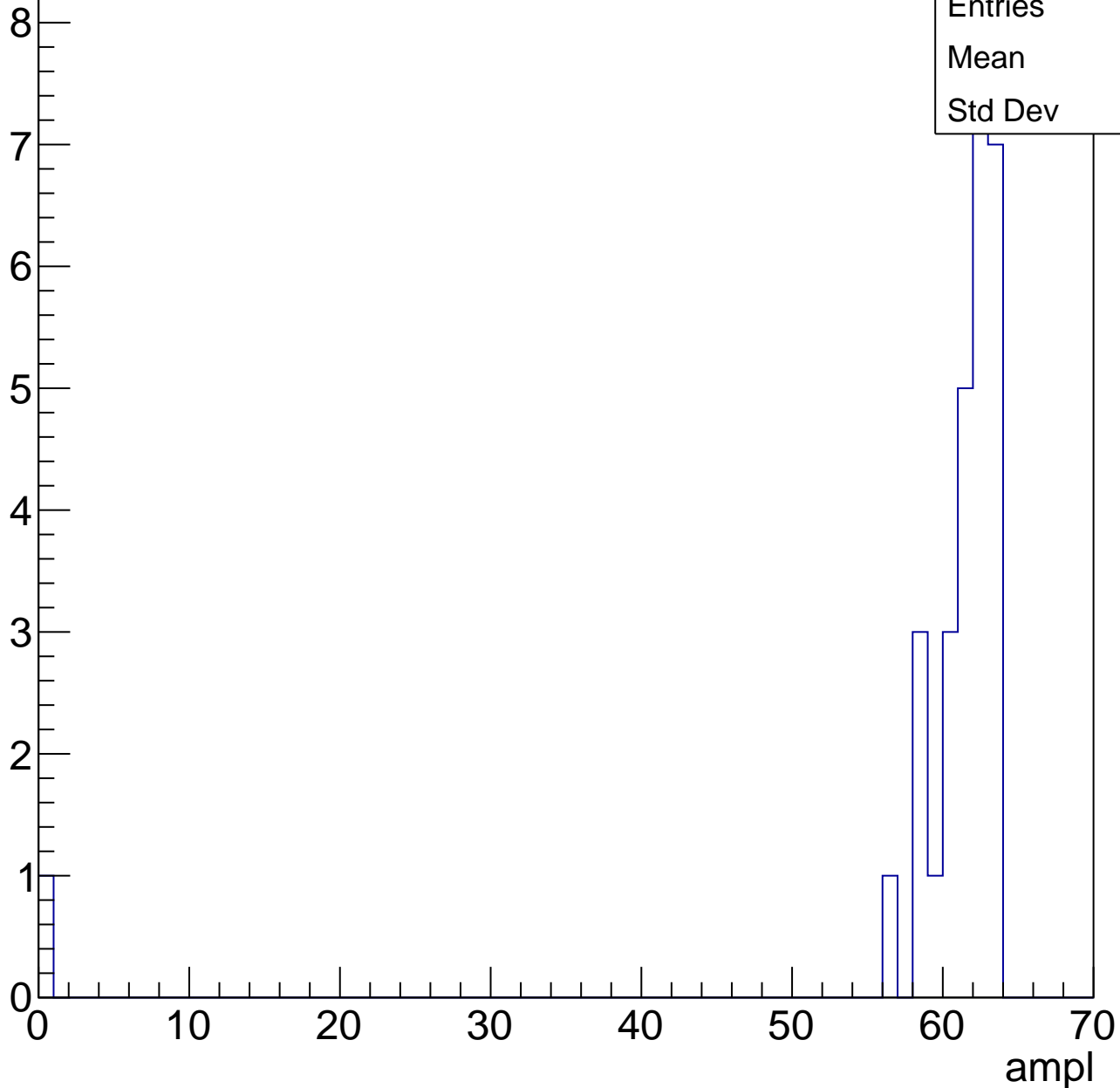


# B0L000S, U13-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

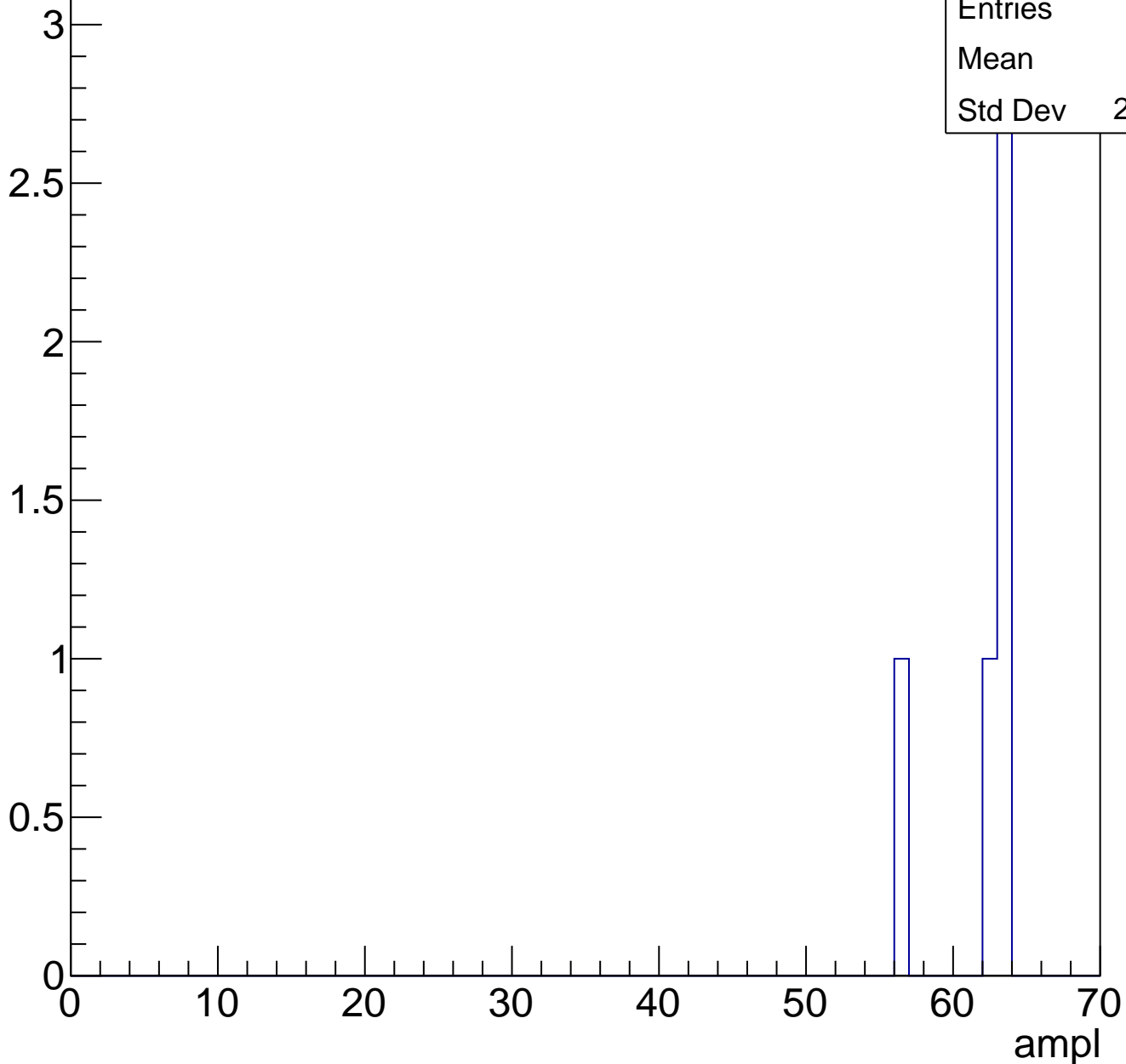
Entries	29
Mean	59
Std Dev	11.3



# B0L000S, U13-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch98, adc0

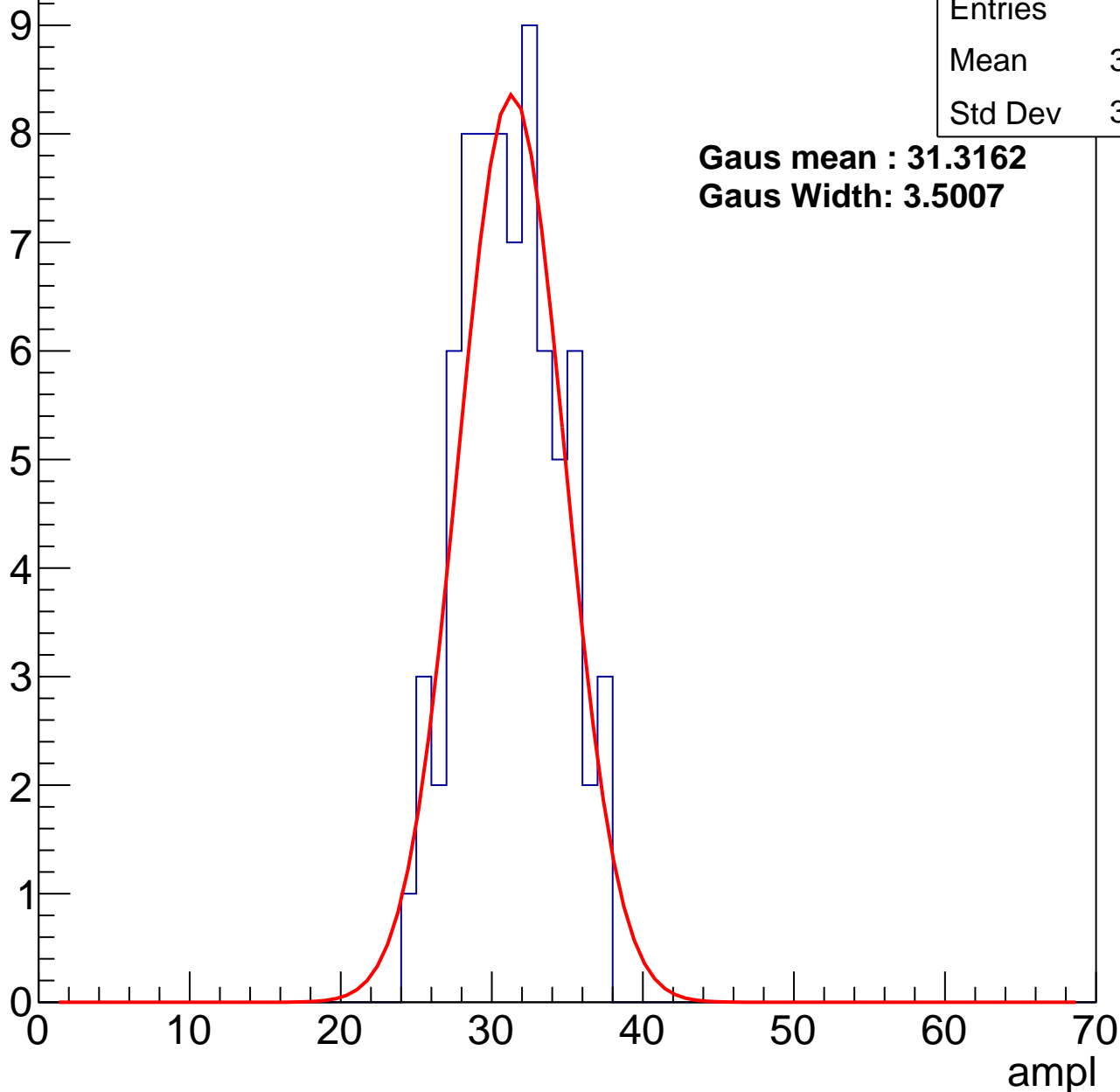
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	30.74
Std Dev	3.167

**Gaus mean : 31.3162**

**Gaus Width: 3.5007**



# B0L000S, U13-ch98, adc1

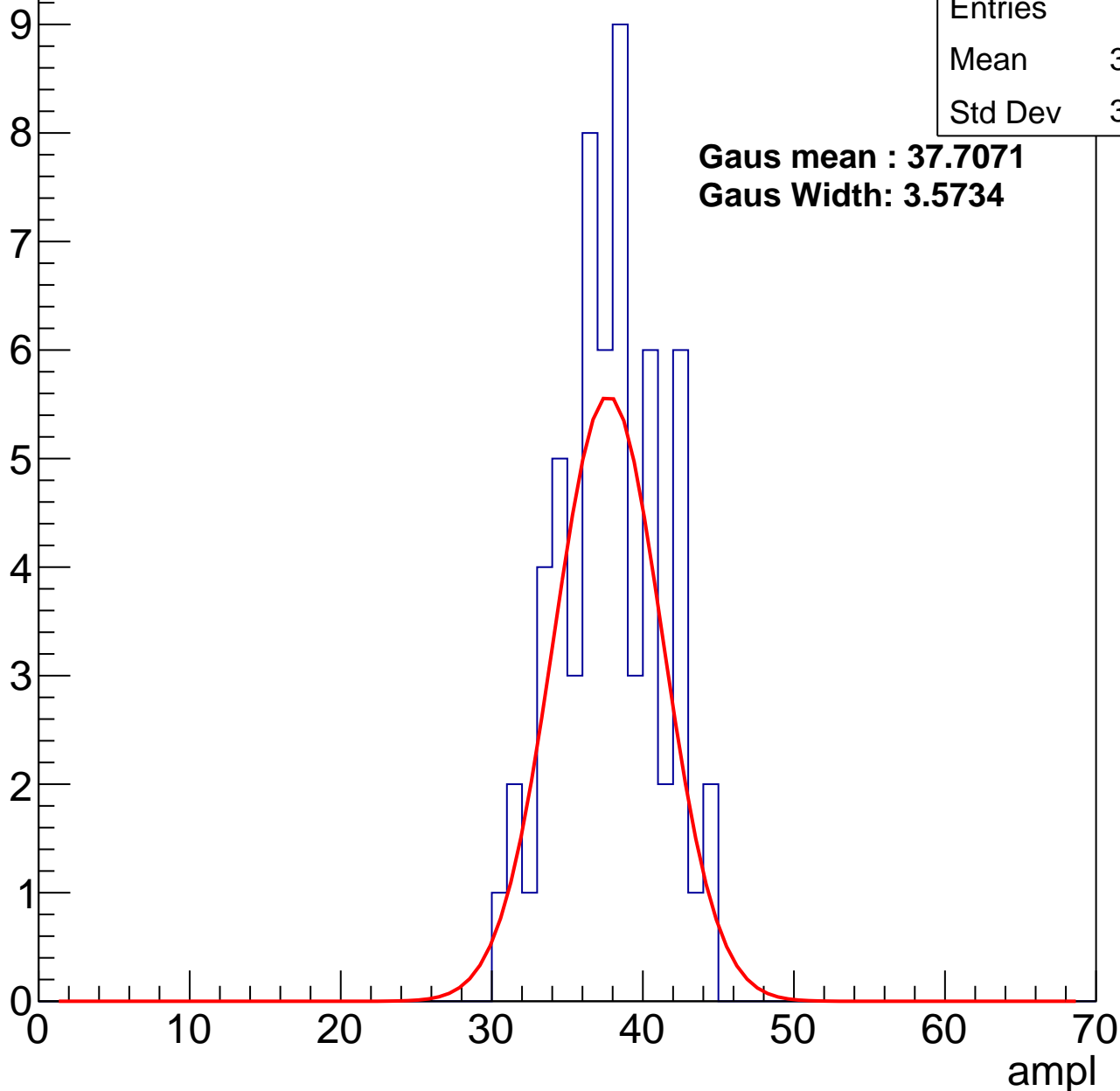
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.37
Std Dev	3.339

**Gaus mean : 37.7071**

**Gaus Width: 3.5734**



# B0L000S, U13-ch98, adc2

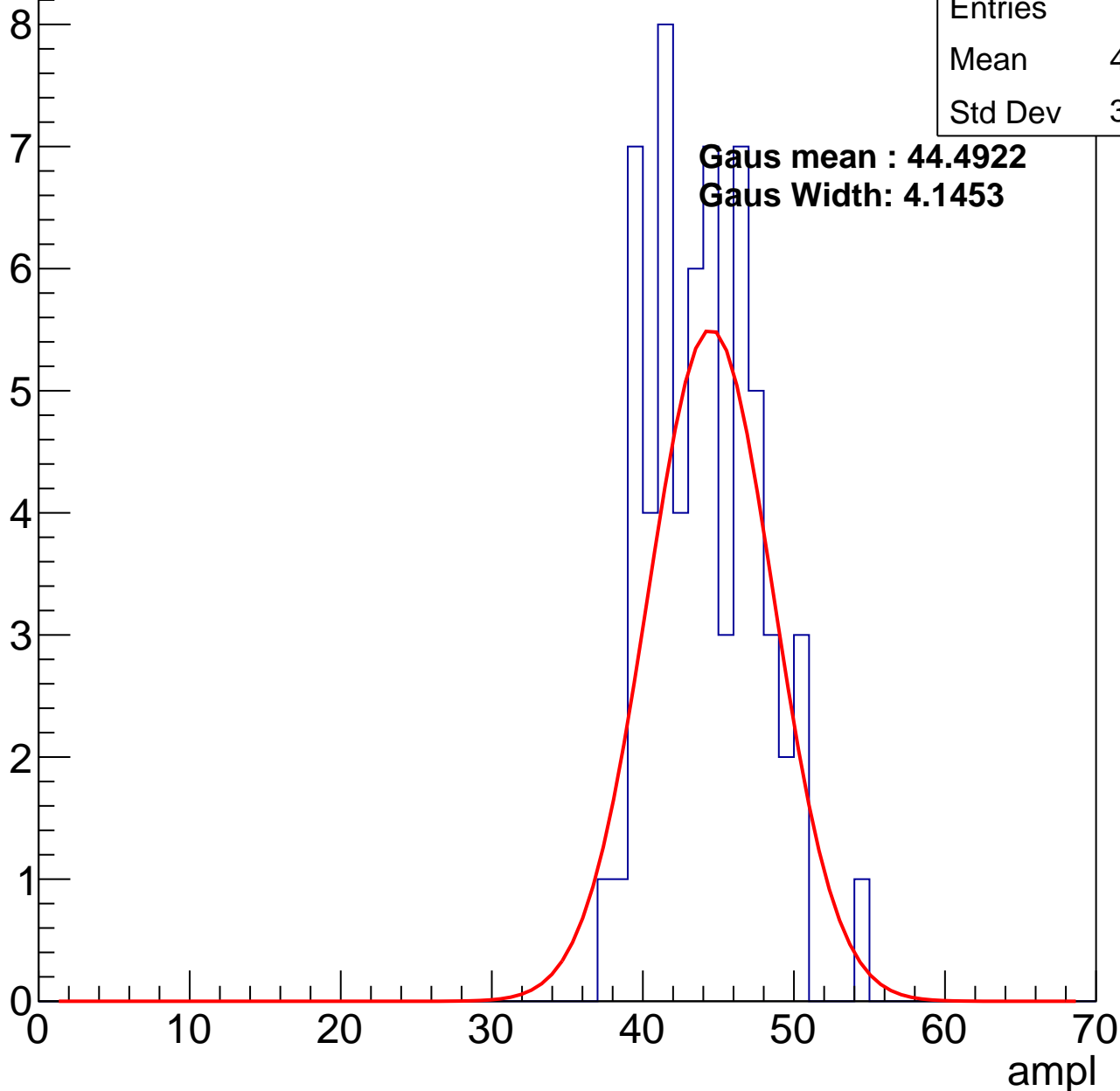
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.68
Std Dev	3.578

**Gaus mean : 44.4922**

**Gaus Width: 4.1453**

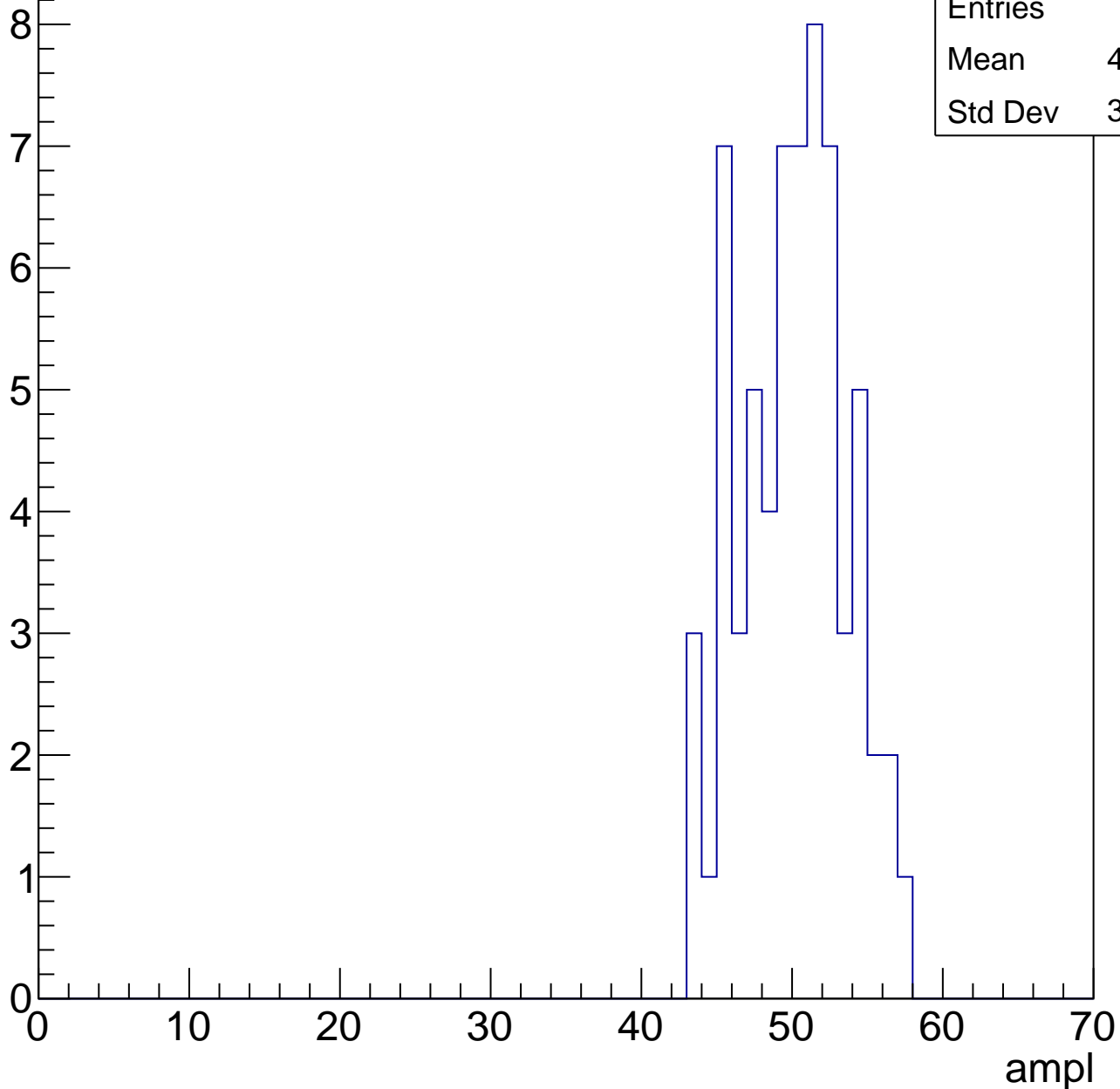


# B0L000S, U13-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	49.63
Std Dev	3.462

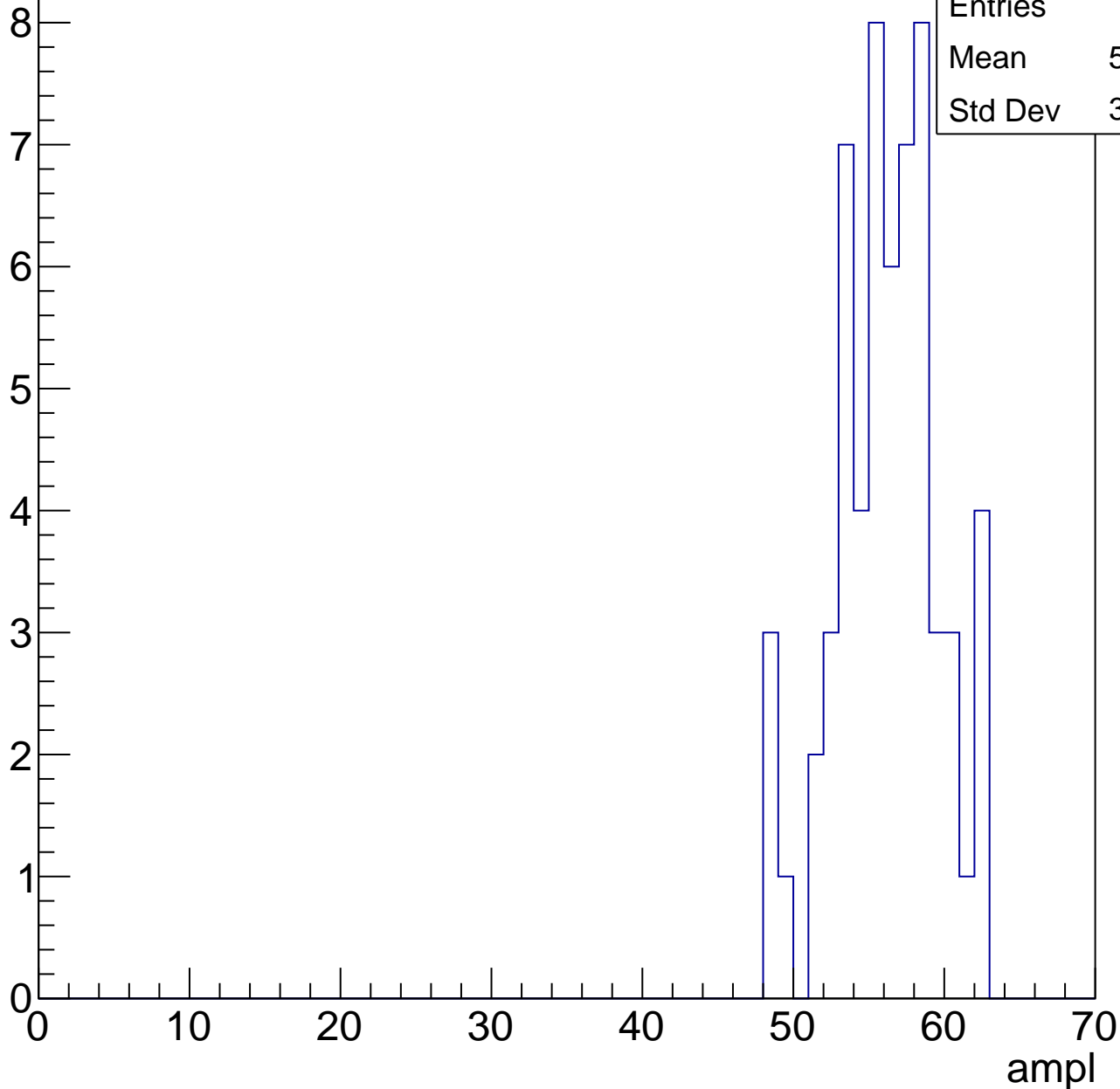


# B0L000S, U13-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

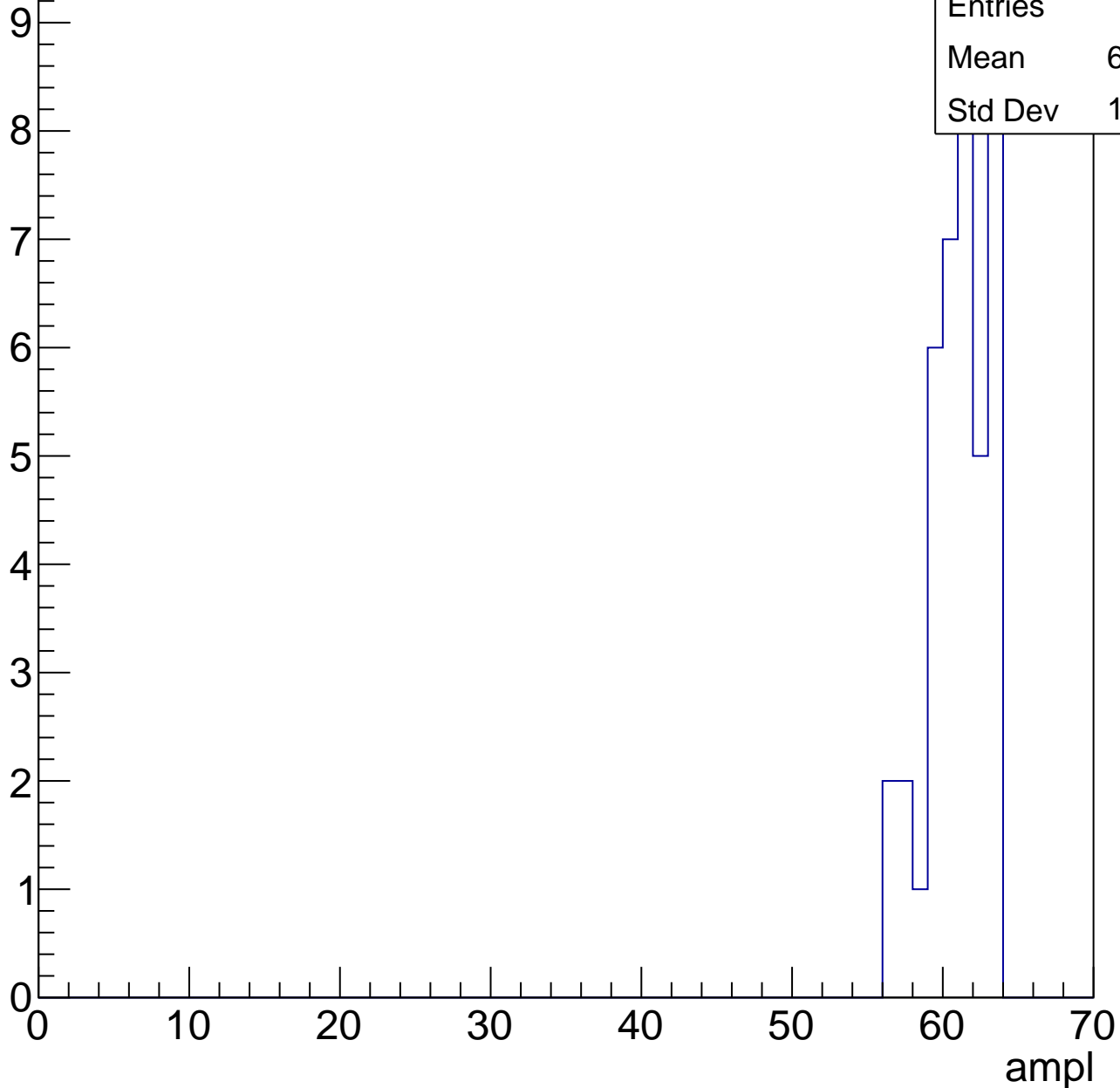
Entries	60
Mean	55.72
Std Dev	3.426



# B0L000S, U13-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

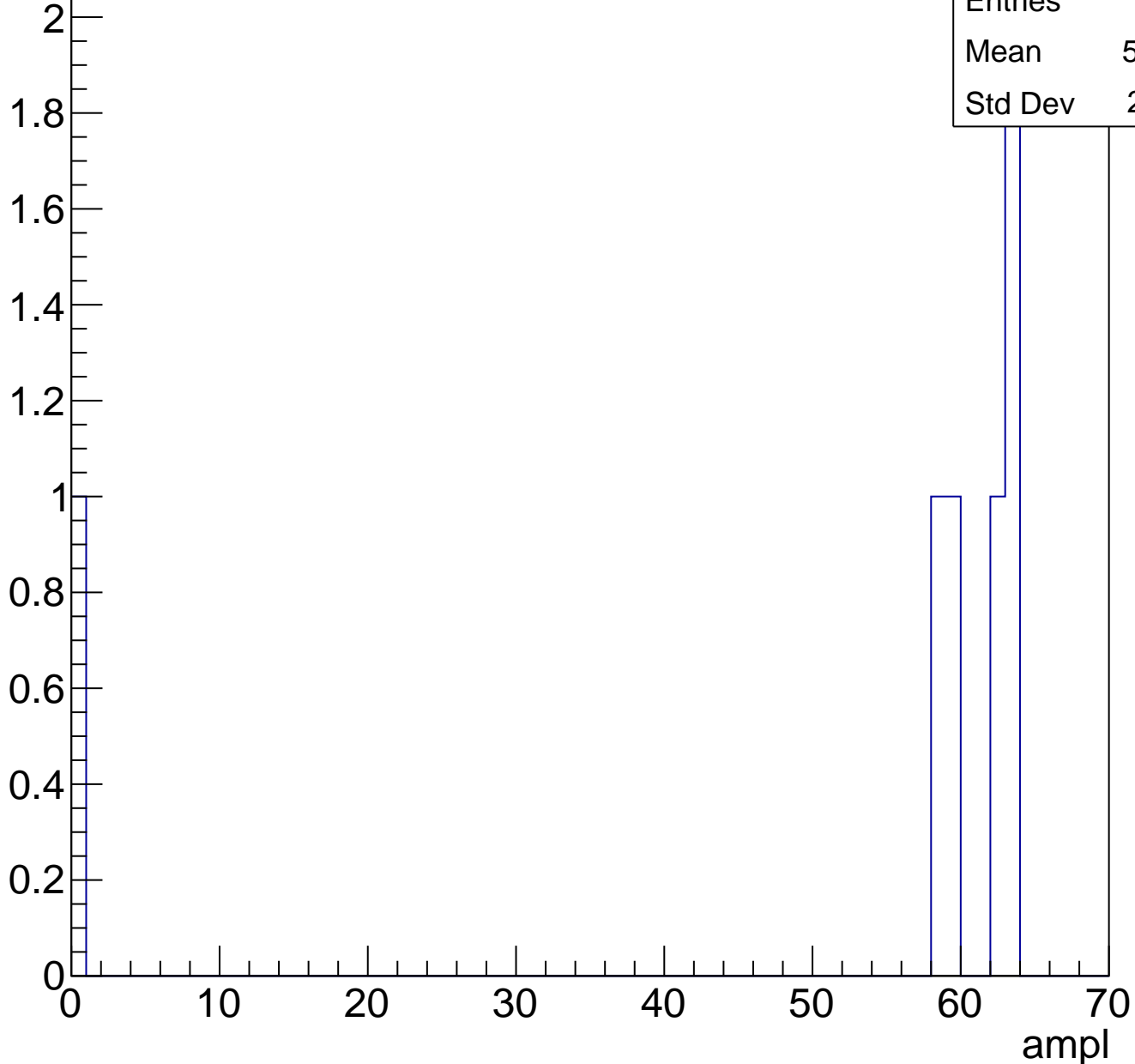
Entry



# B0L000S, U13-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch99, adc0

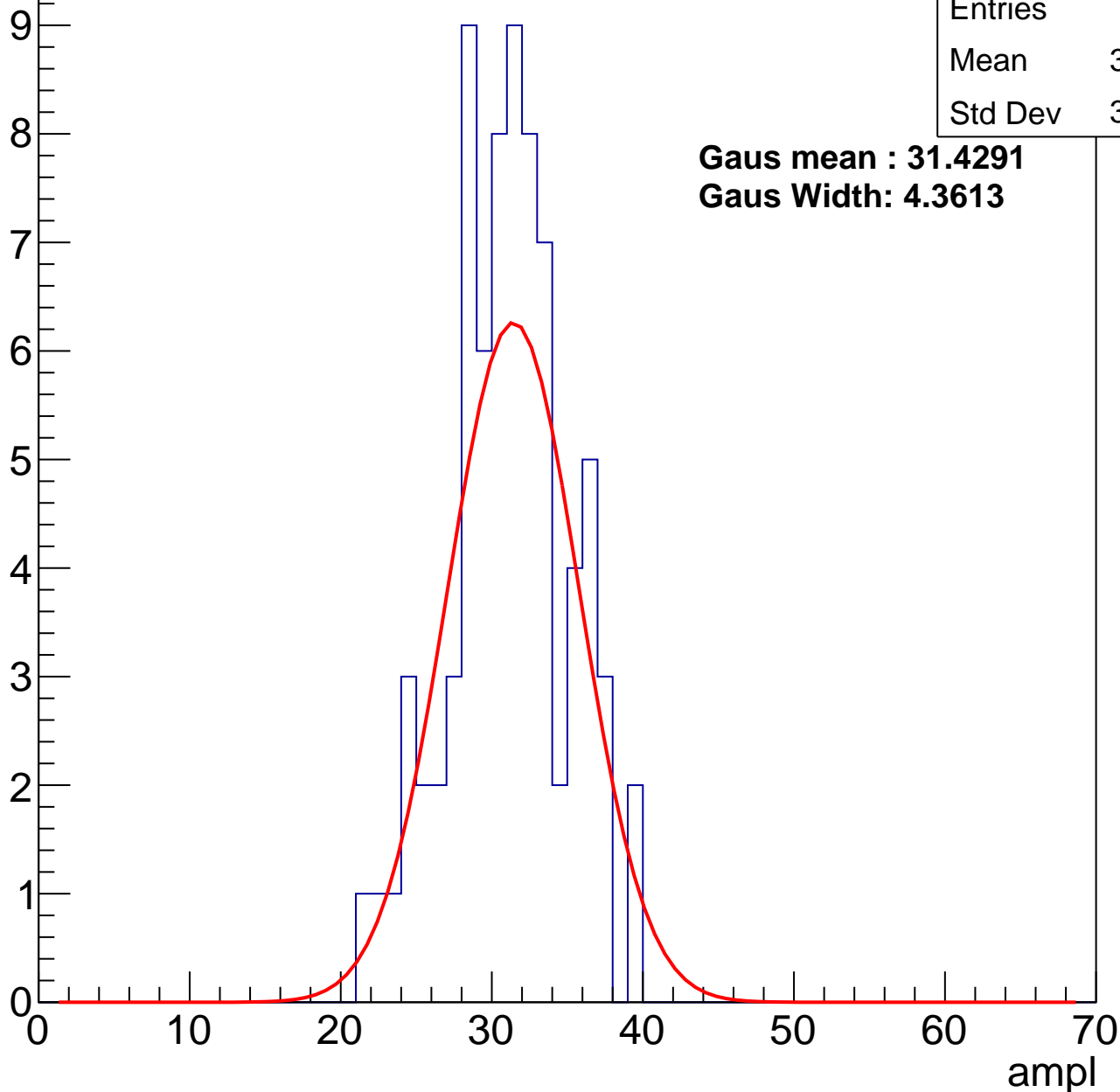
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.66
Std Dev	3.882

**Gaus mean : 31.4291**

**Gaus Width: 4.3613**



# B0L000S, U13-ch99, adc1

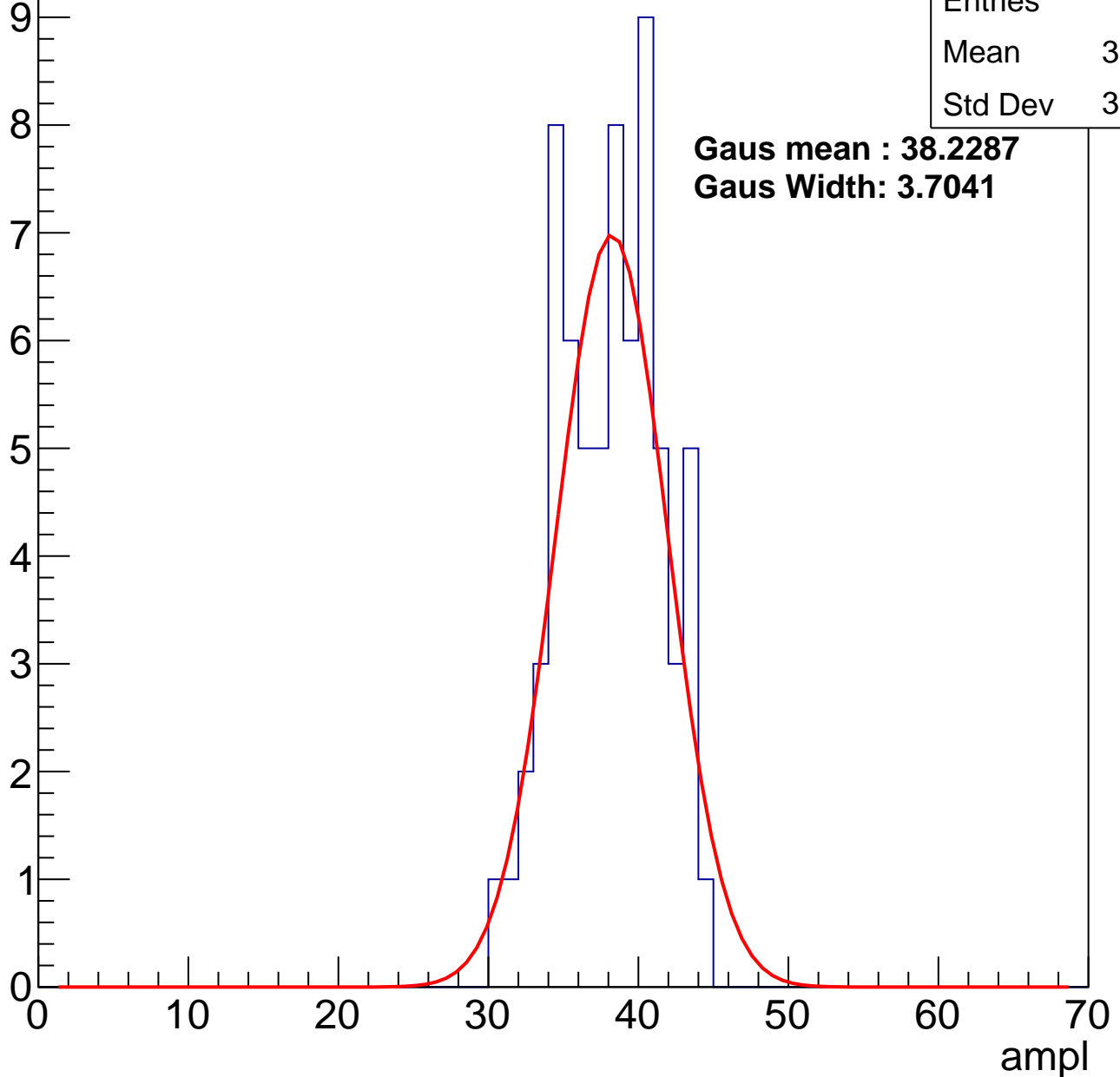
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.63
Std Dev	3.329

**Gaus mean : 38.2287**

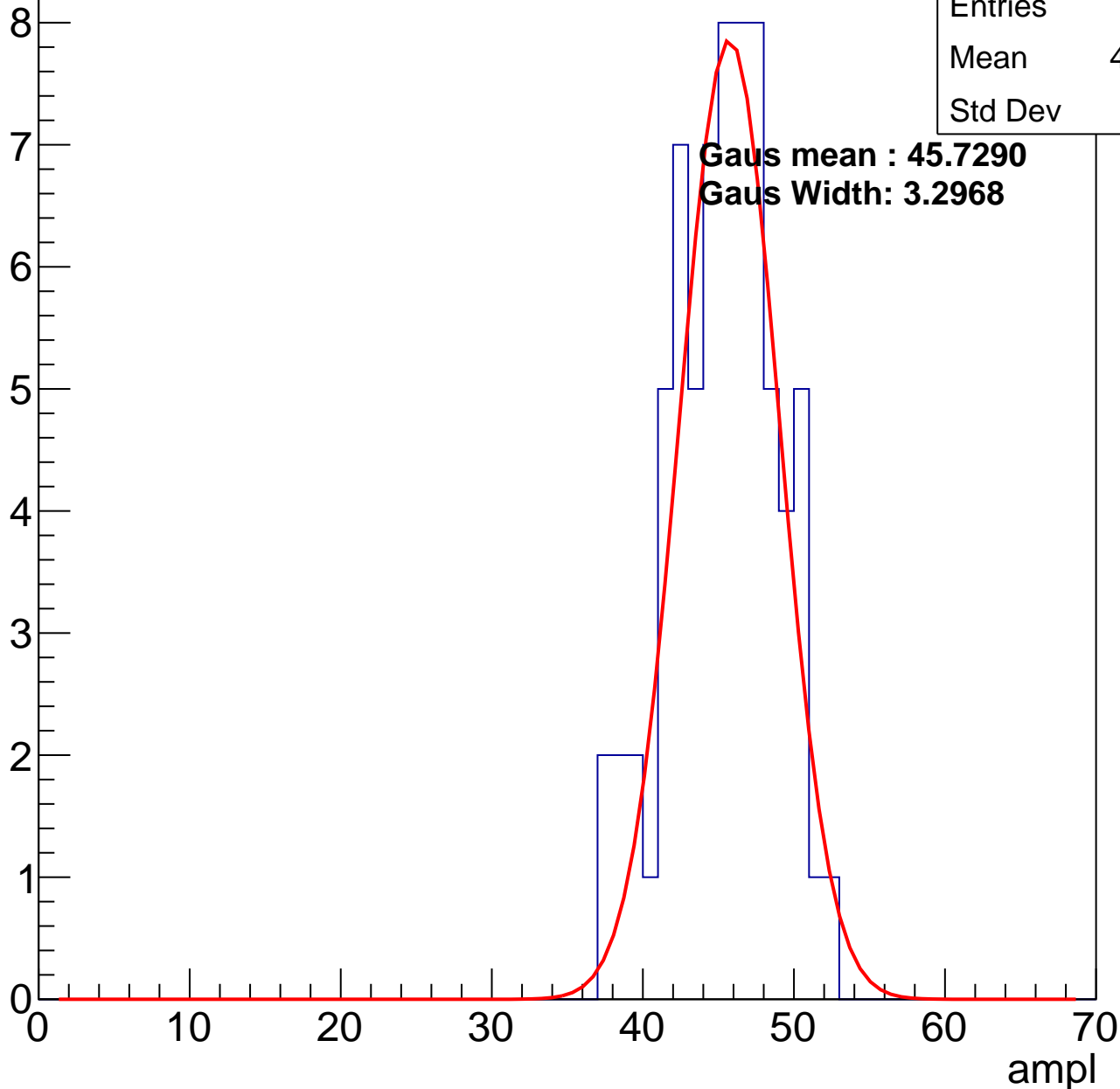
**Gaus Width: 3.7041**



# B0L000S, U13-ch99, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

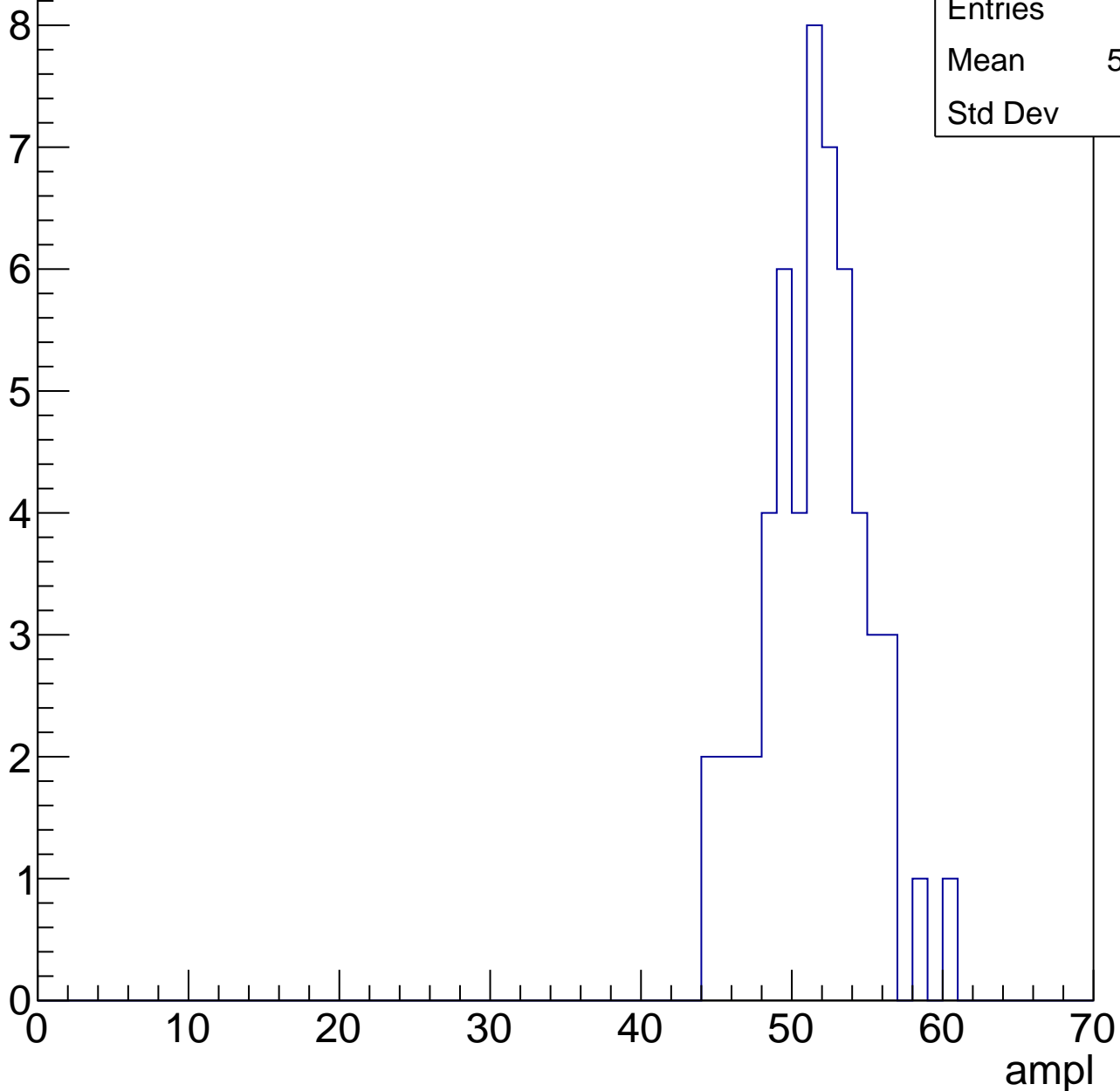


# B0L000S, U13-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	51.04
Std Dev	3.4

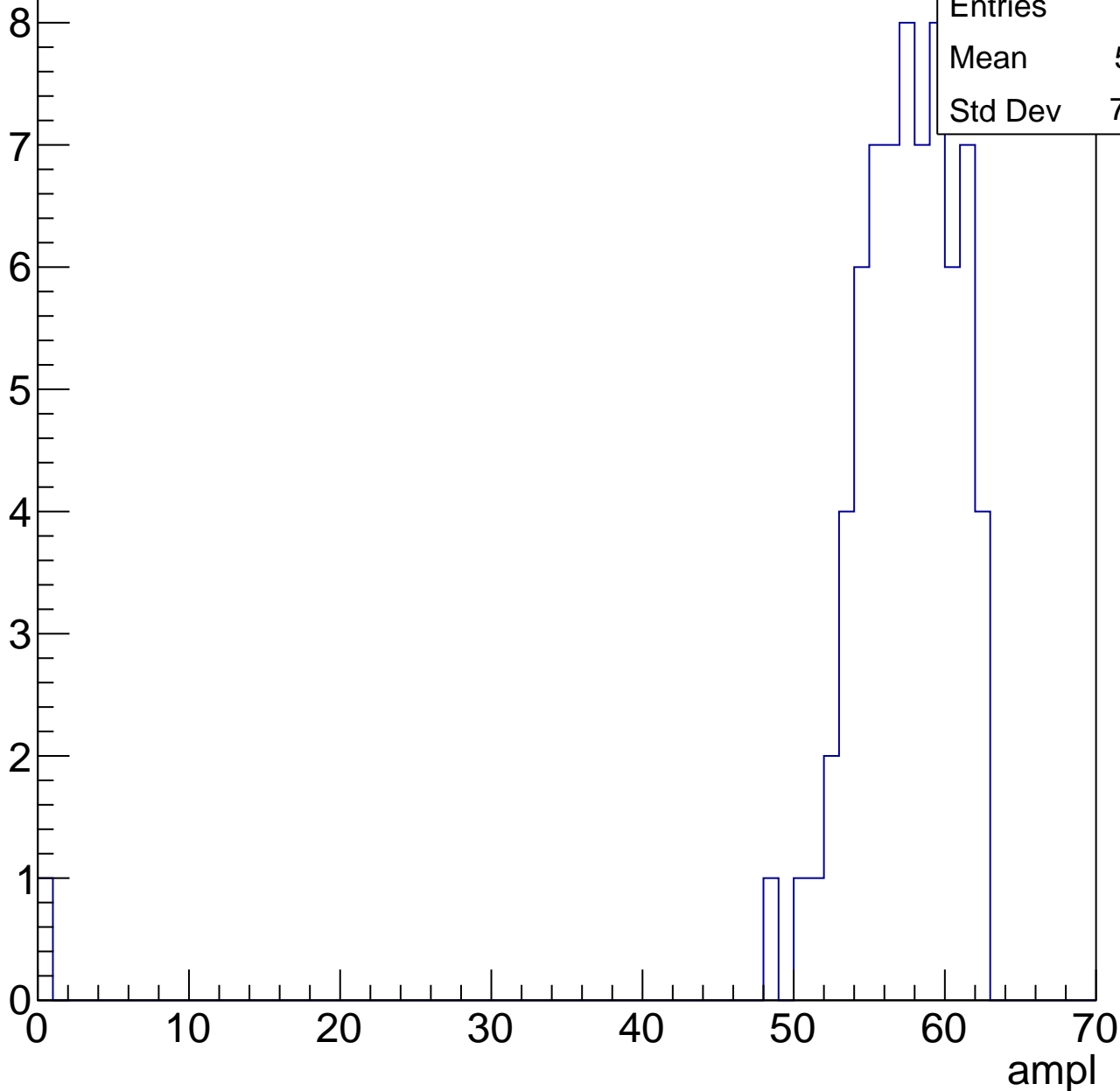


# B0L000S, U13-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

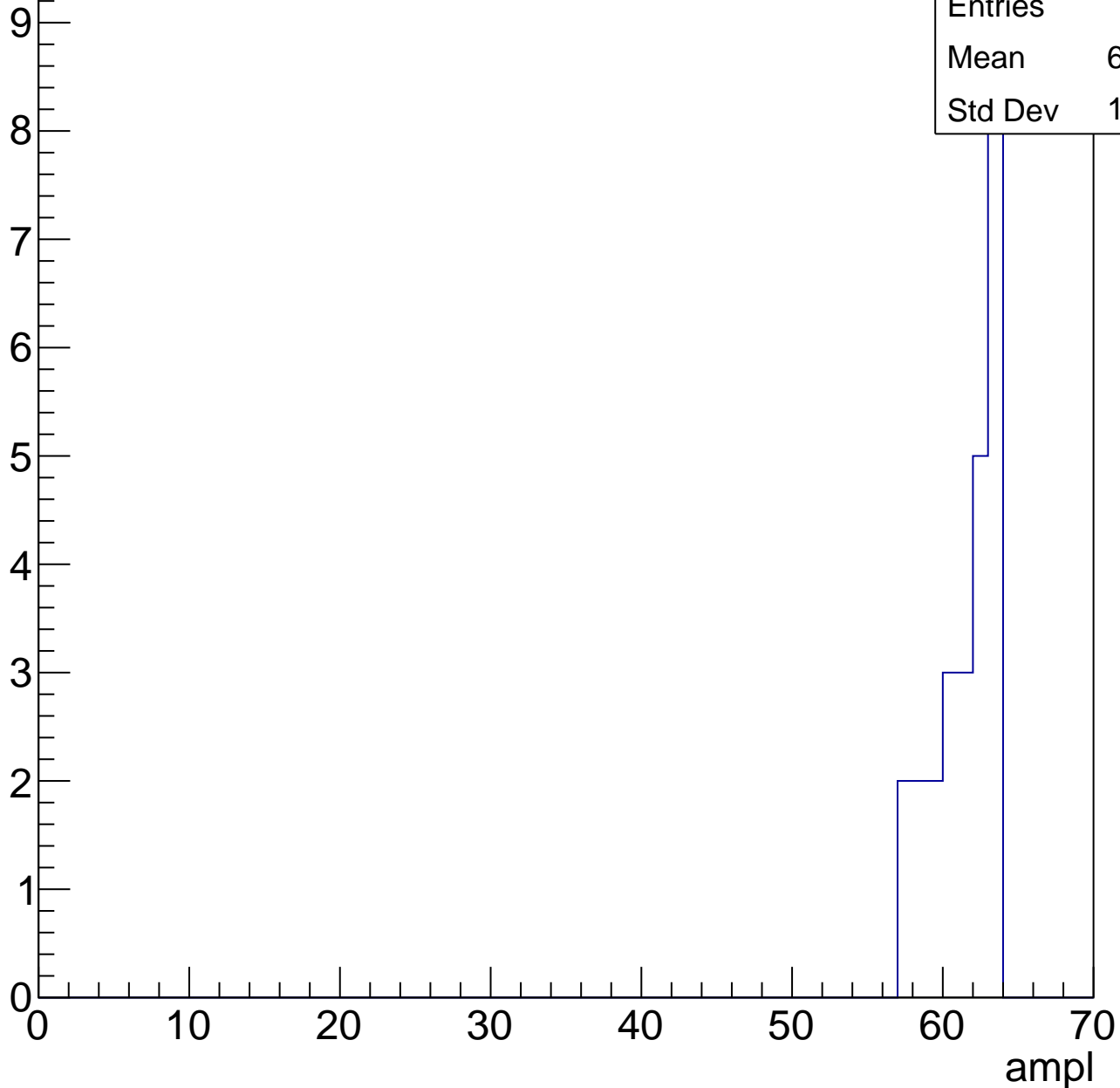
Entries	70
Mean	56.21
Std Dev	7.445



# B0L000S, U13-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

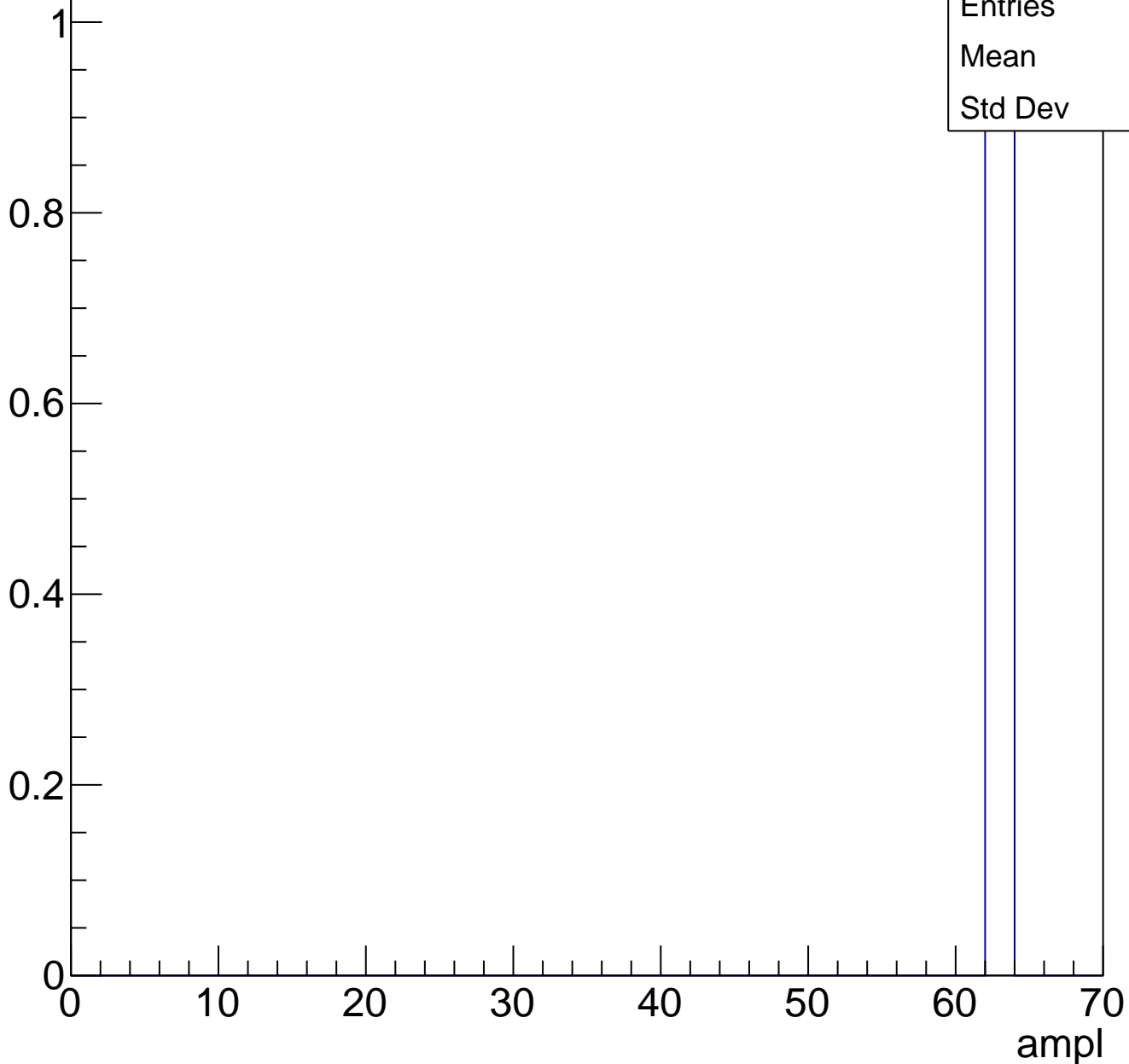
Entry



# B0L000S, U13-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch100, adc0

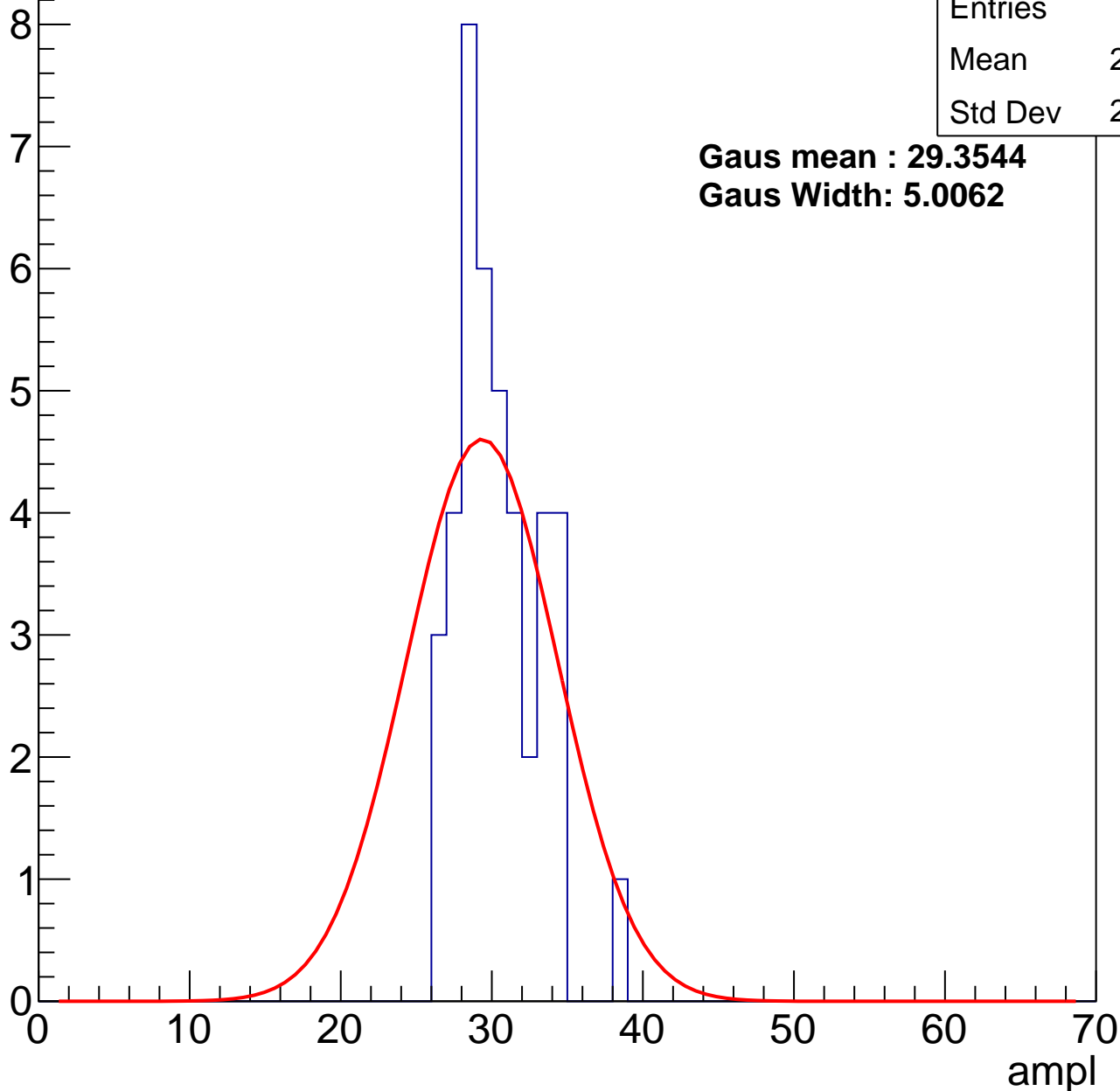
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	29.95
Std Dev	2.696

**Gaus mean : 29.3544**

**Gaus Width: 5.0062**



# B0L000S, U13-ch100, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	36.53
Std Dev	3.291

**Gaus mean : 36.3481**

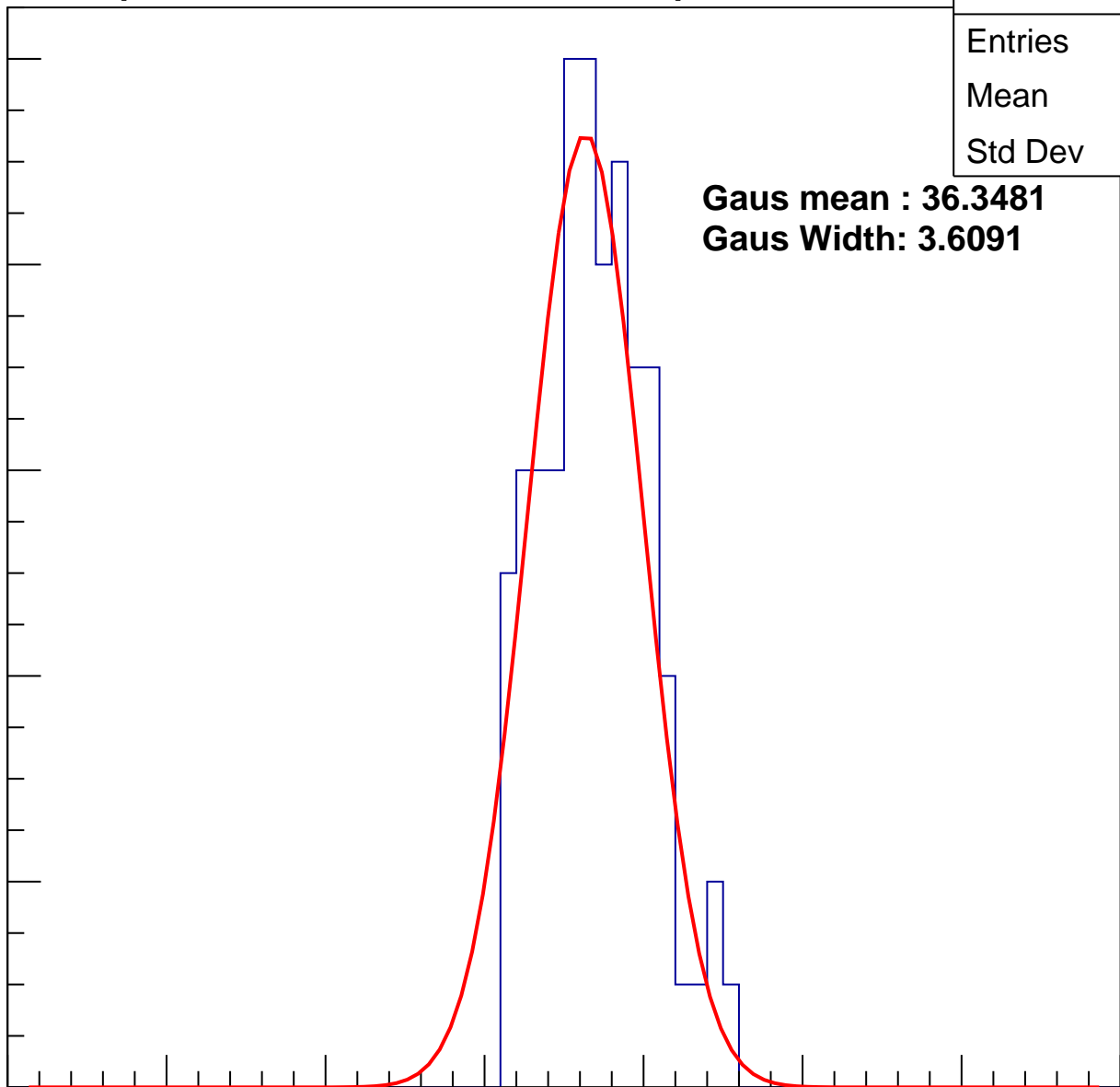
**Gaus Width: 3.6091**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch100, adc2

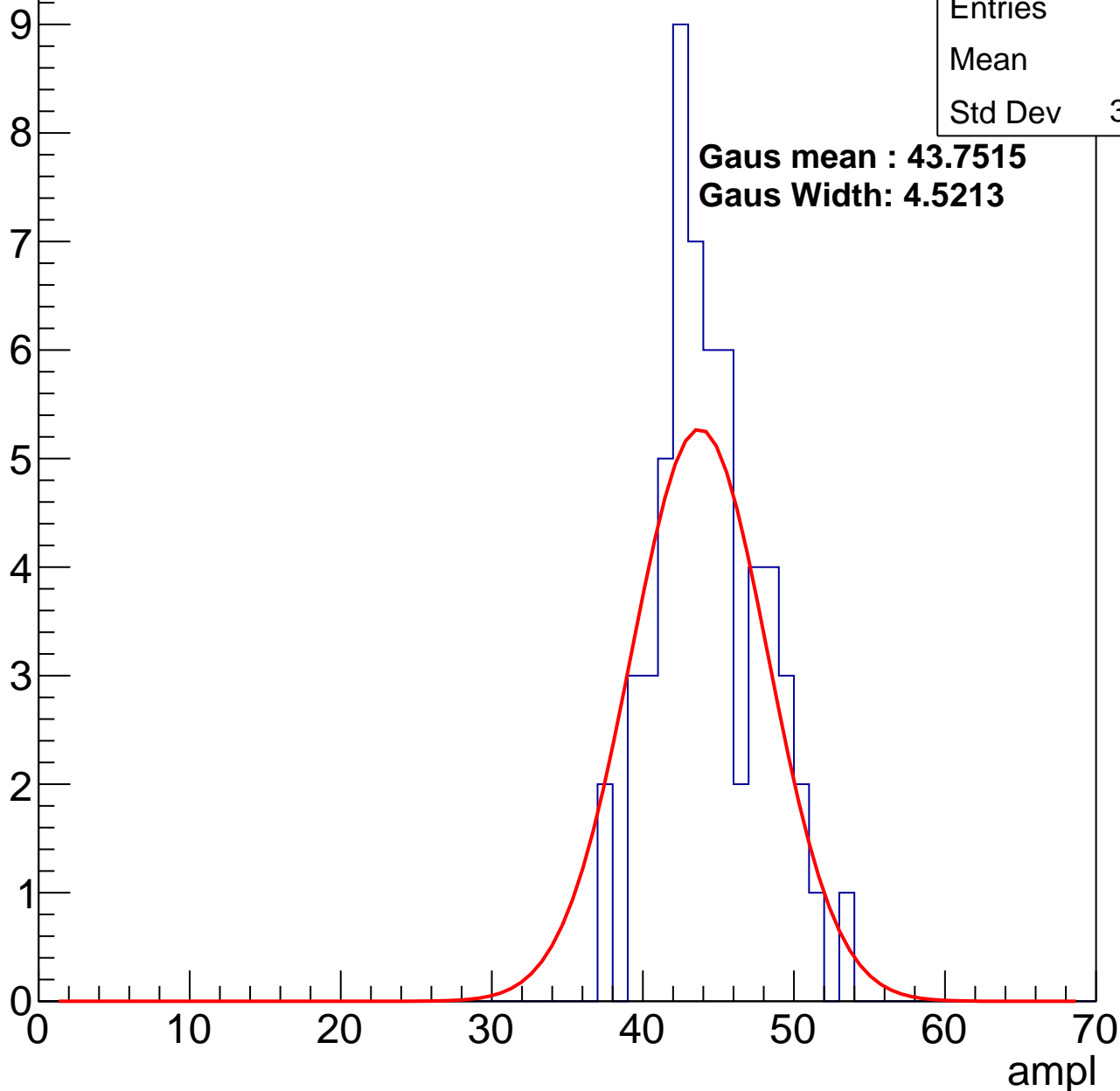
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	44
Std Dev	3.474

**Gaus mean : 43.7515**

**Gaus Width: 4.5213**

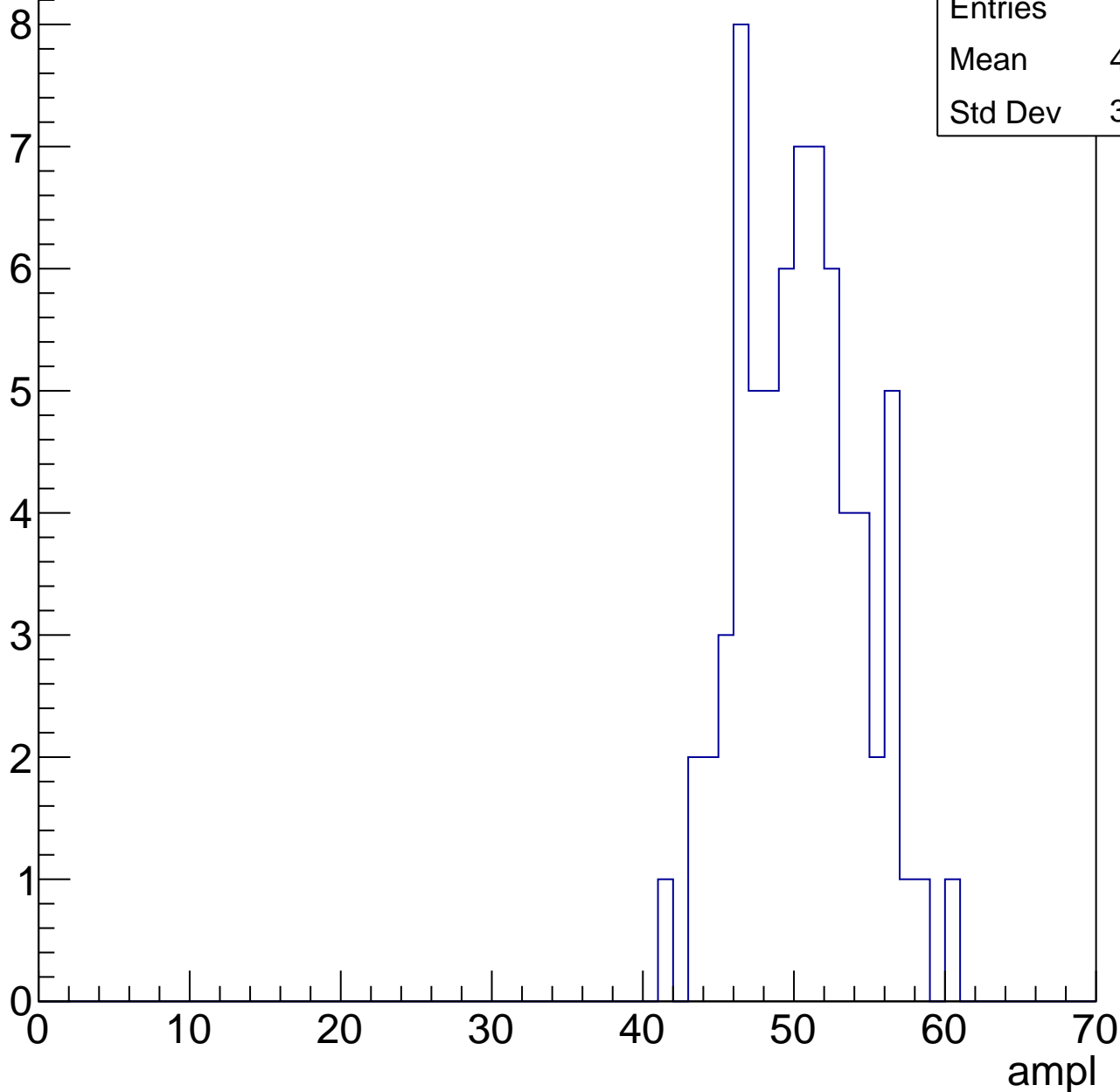


# B0L000S, U13-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.99
Std Dev	3.962

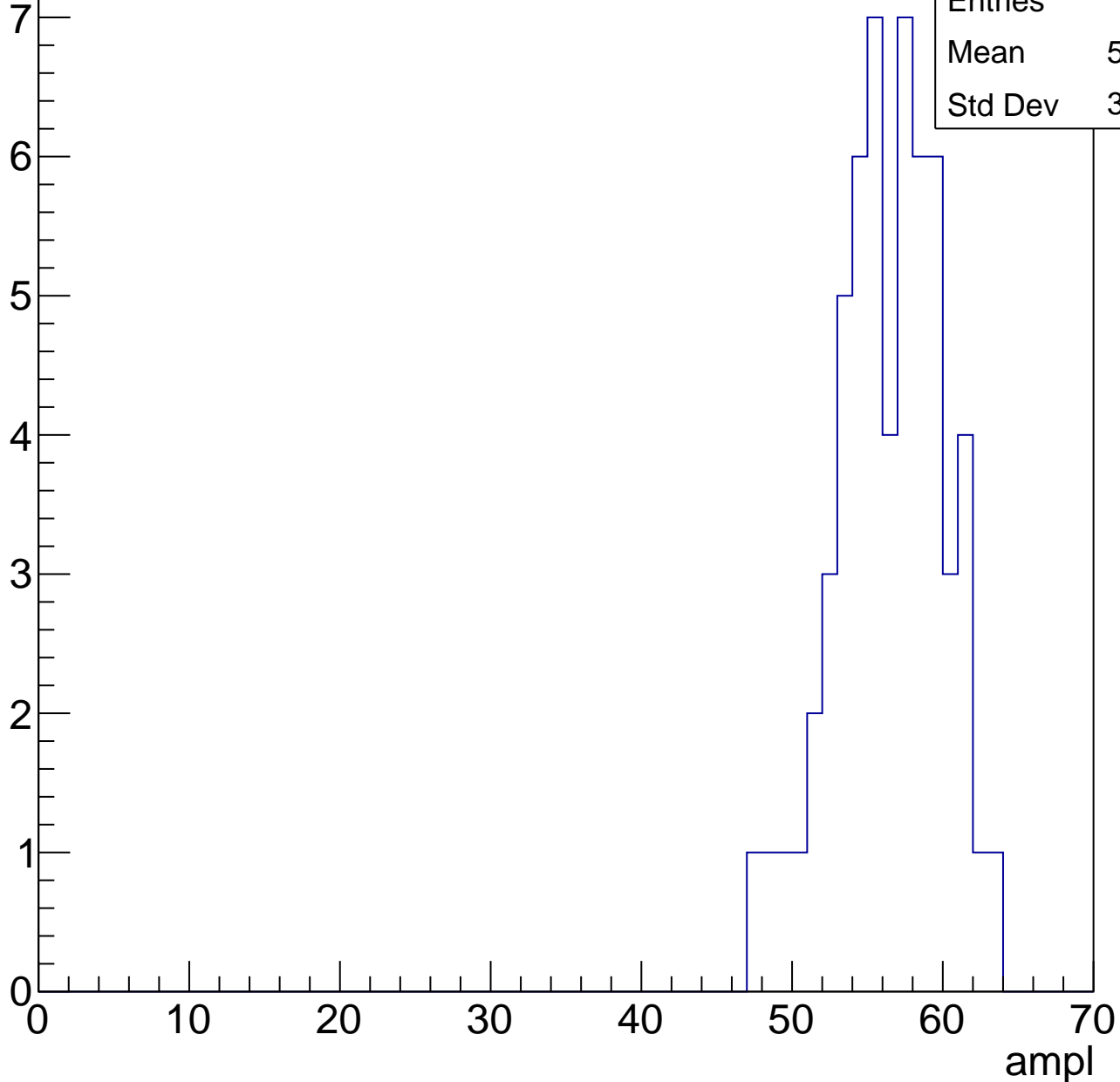


# B0L000S, U13-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.93
Std Dev	3.498

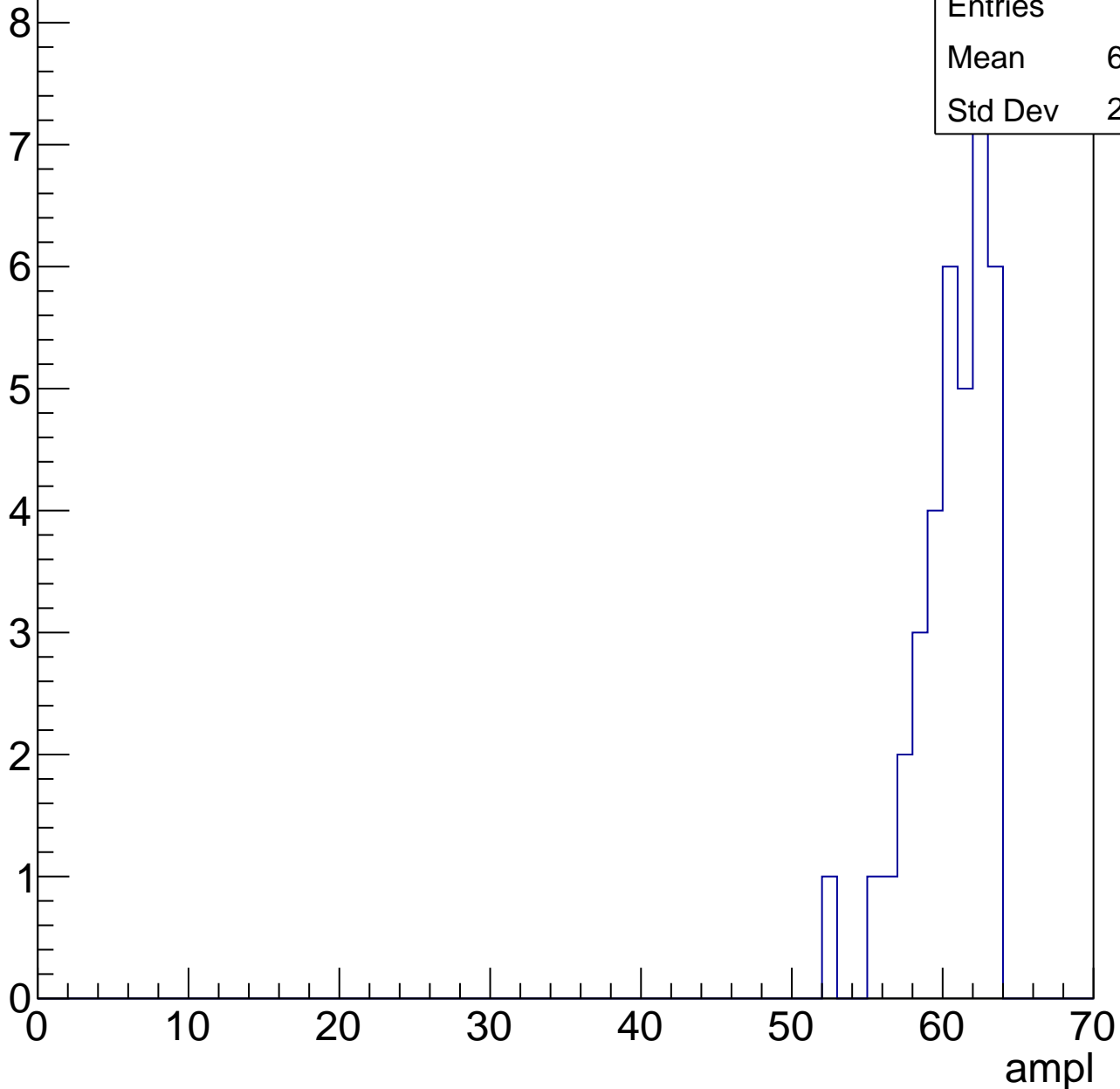


# B0L000S, U13-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	60.16
Std Dev	2.488



# B0L000S, U13-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	6
Mean	52
Std Dev	23.27



# B0L000S, U13-ch100, adc7

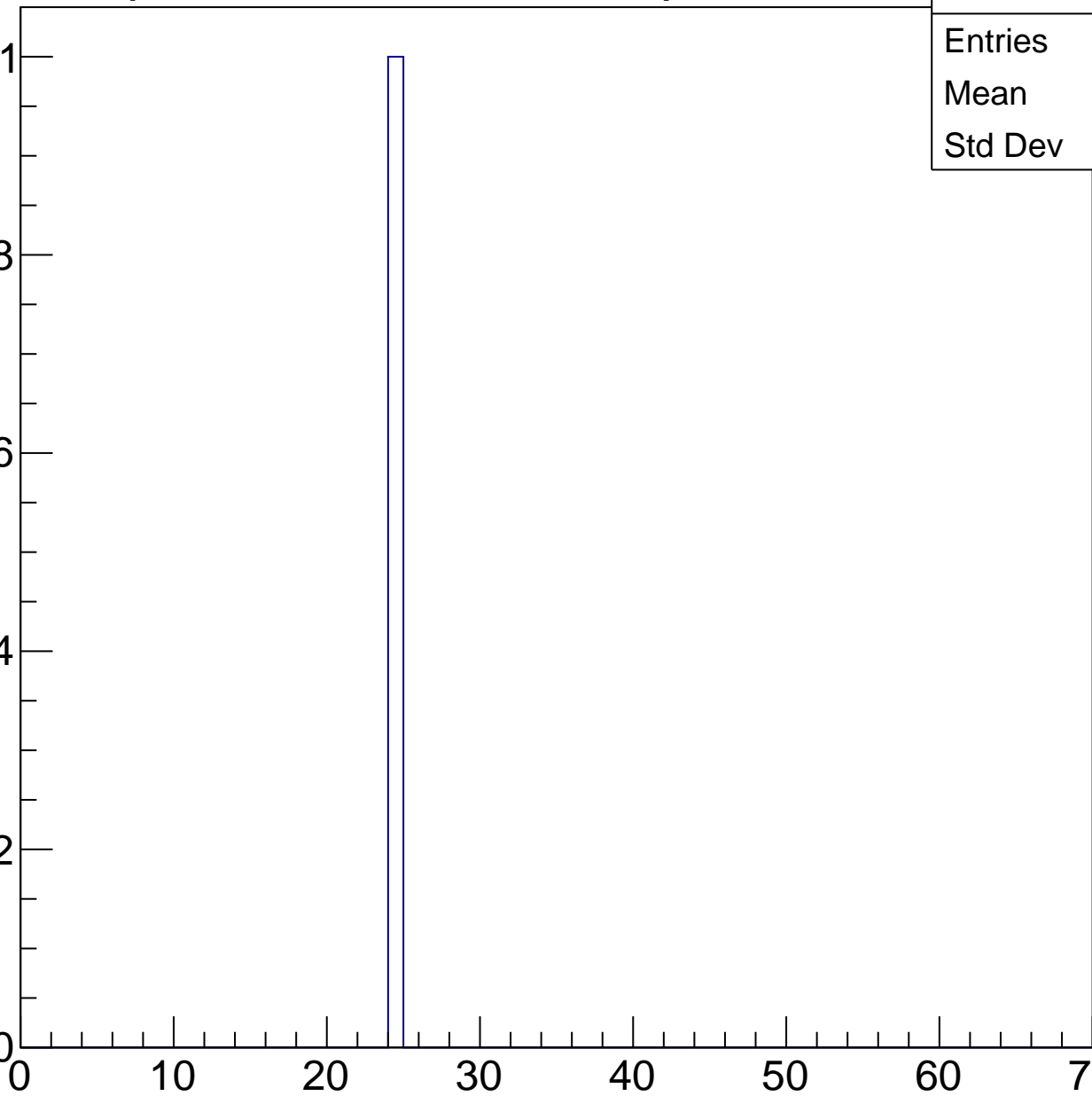
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	24
Std Dev	0

ampl



# B0L000S, U13-ch101, adc0

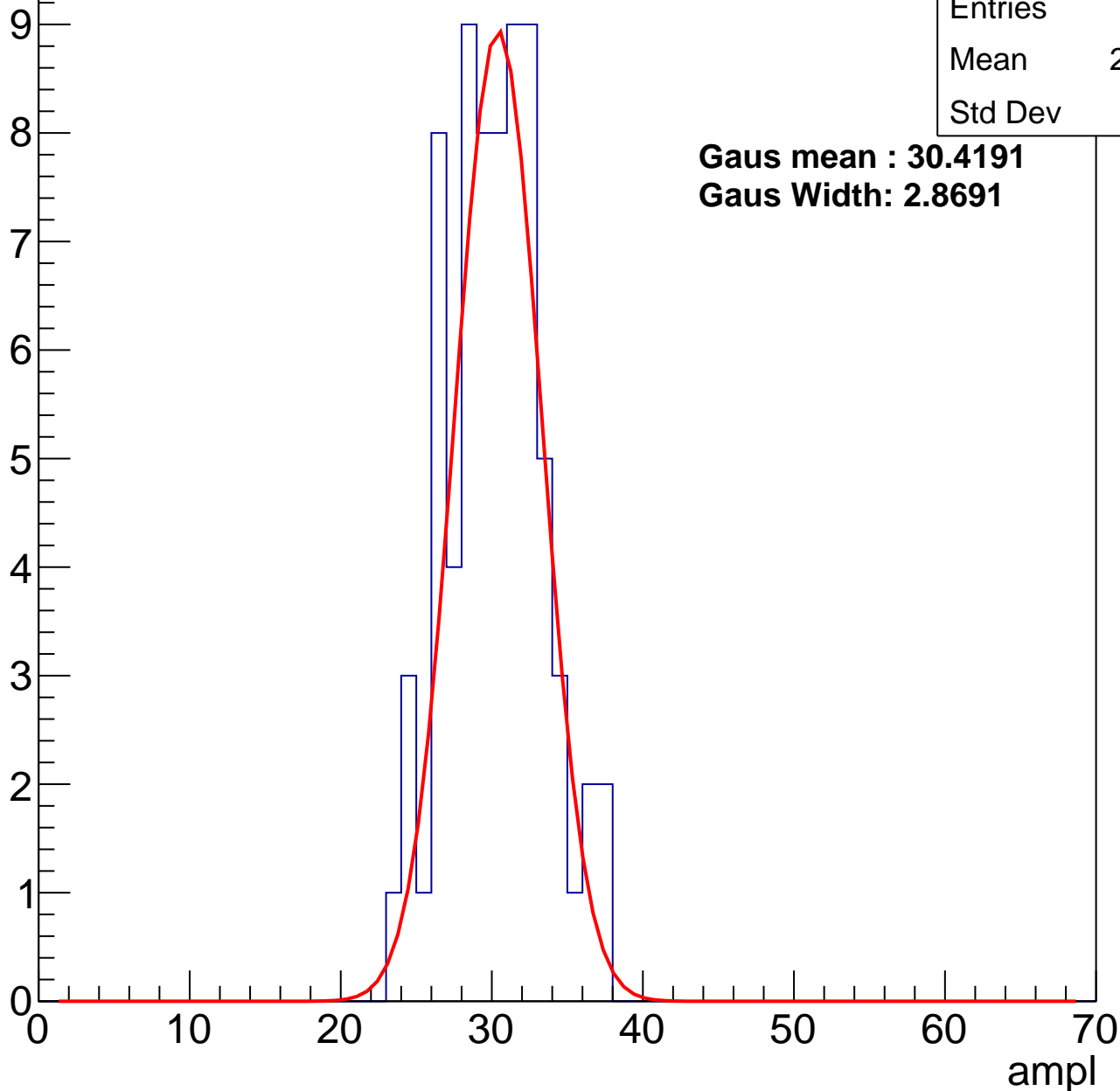
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.79
Std Dev	3.14

**Gaus mean : 30.4191**

**Gaus Width: 2.8691**



# B0L000S, U13-ch101, adc1

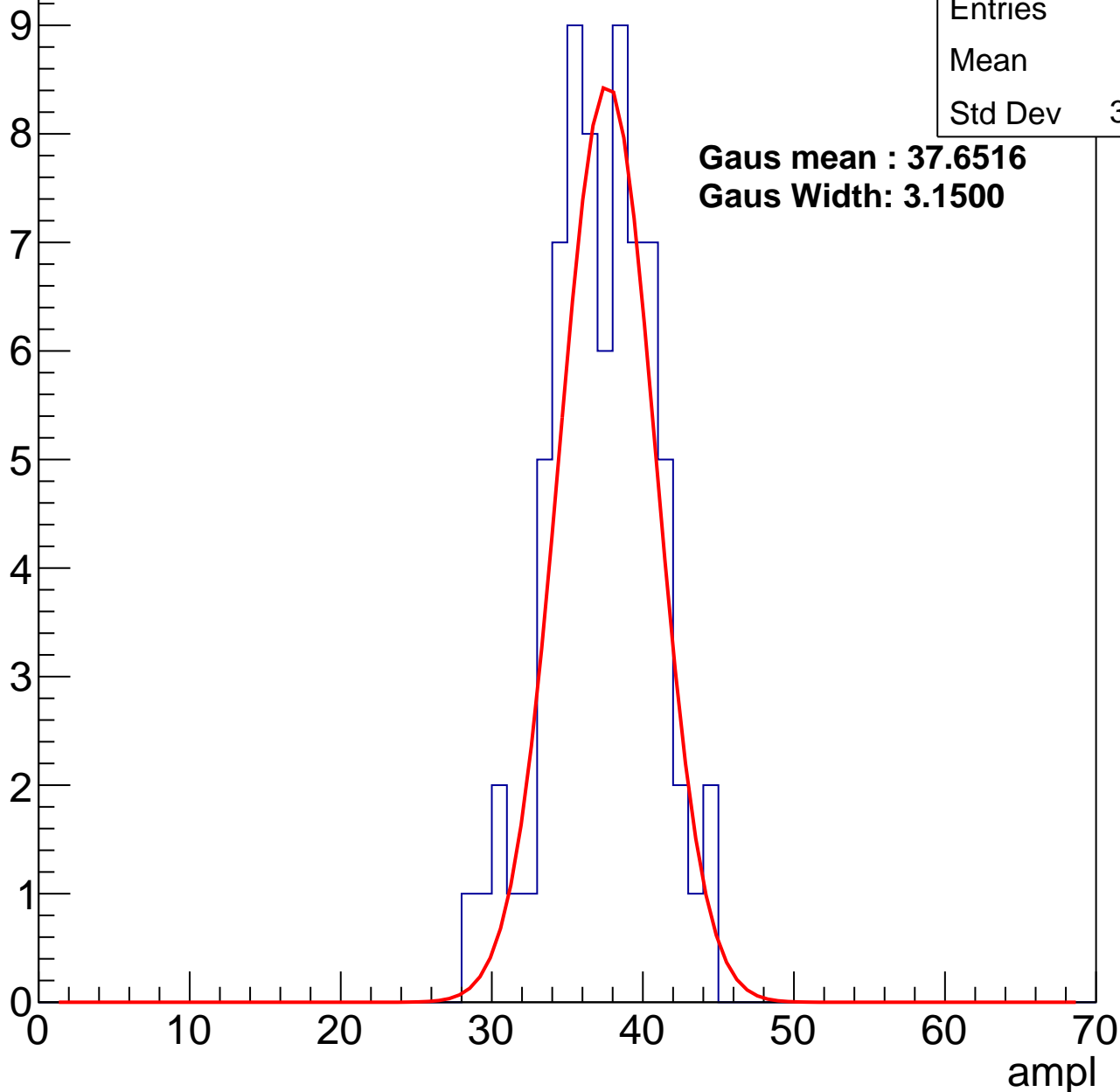
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.8
Std Dev	3.393

**Gaus mean : 37.6516**

**Gaus Width: 3.1500**



# B0L000S, U13-ch101, adc2

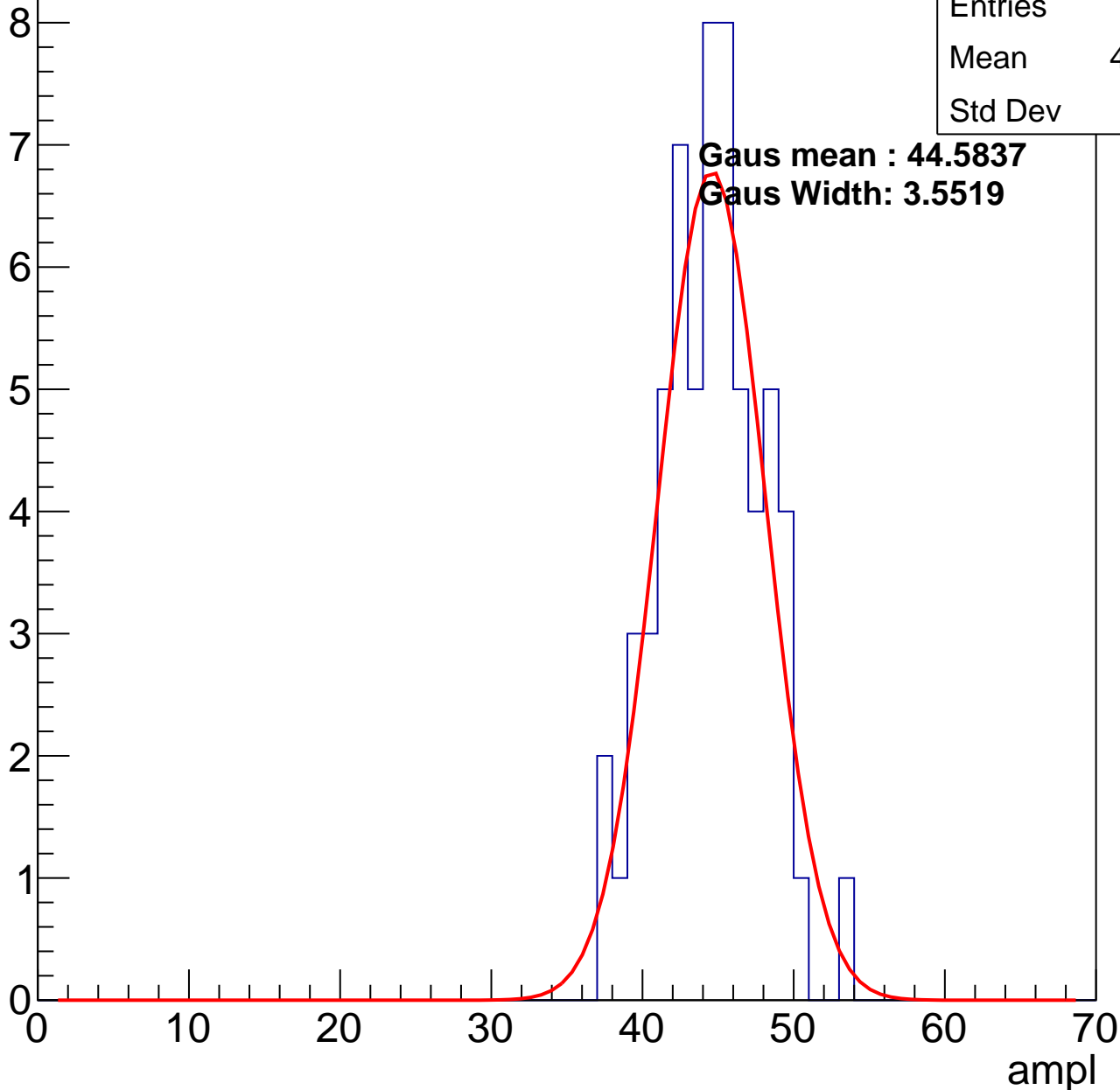
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.06
Std Dev	3.35

**Gaus mean : 44.5837**

**Gaus Width: 3.5519**

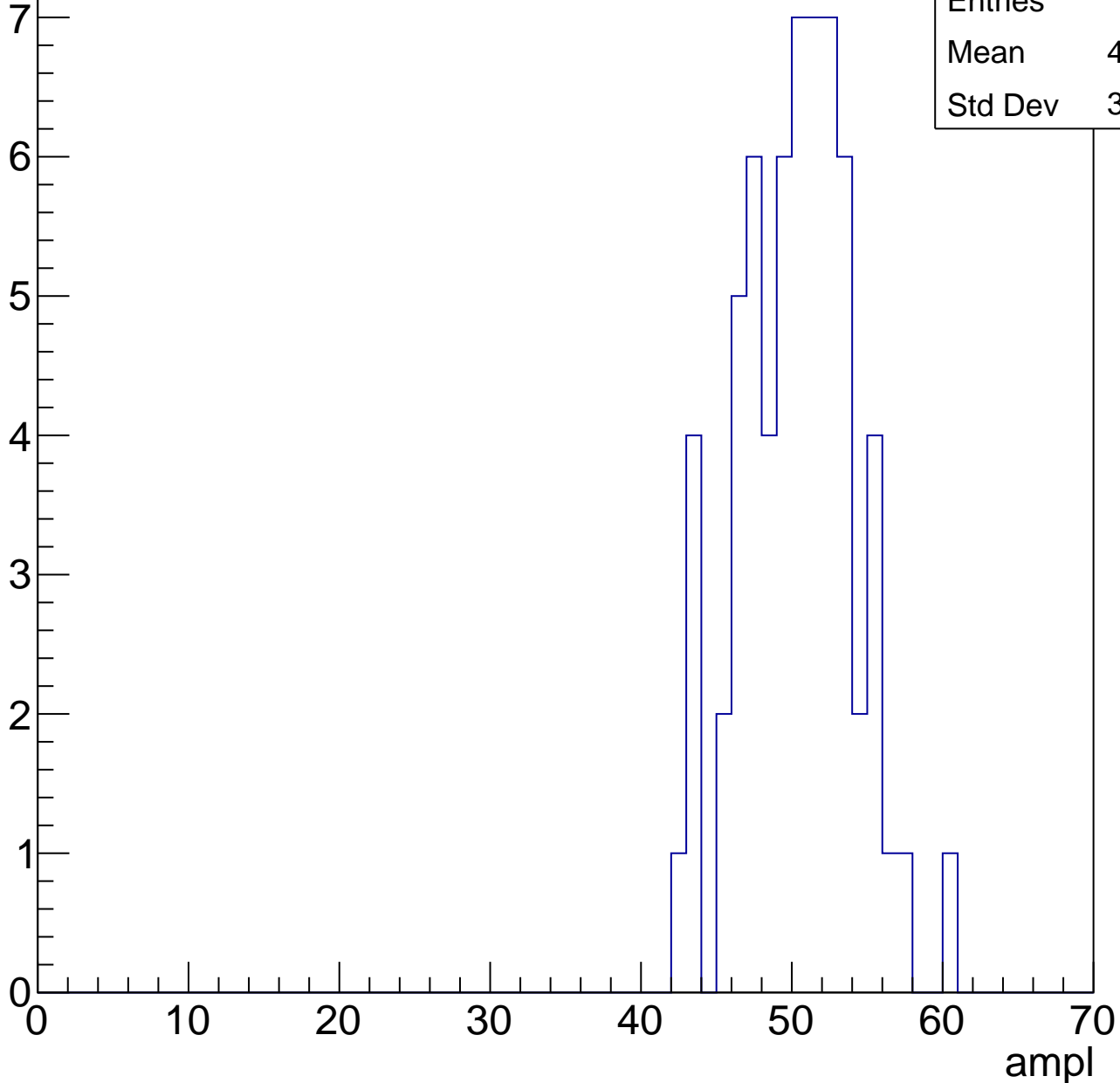


# B0L000S, U13-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	49.88
Std Dev	3.689

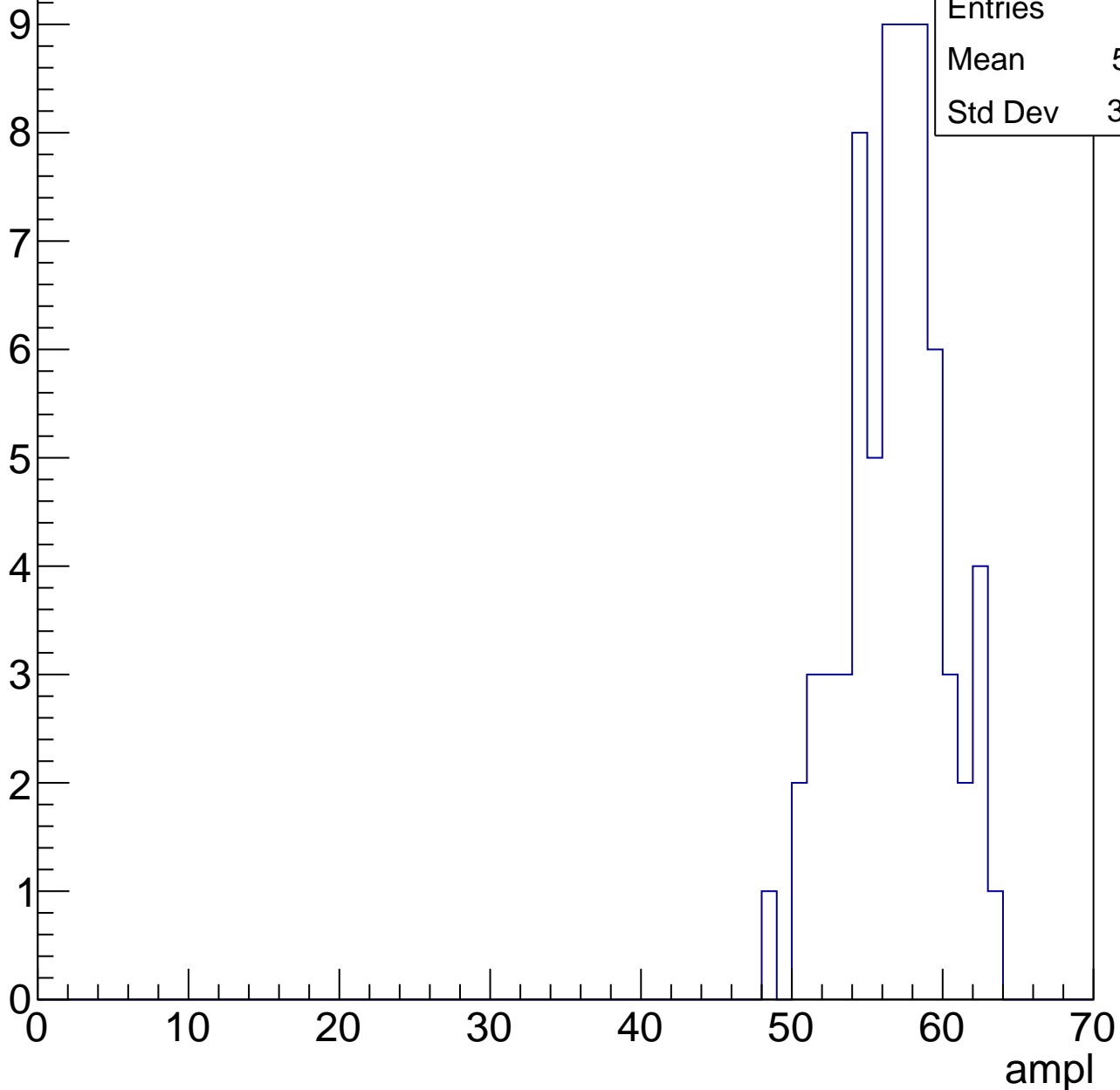


# B0L000S, U13-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	56.31
Std Dev	3.223

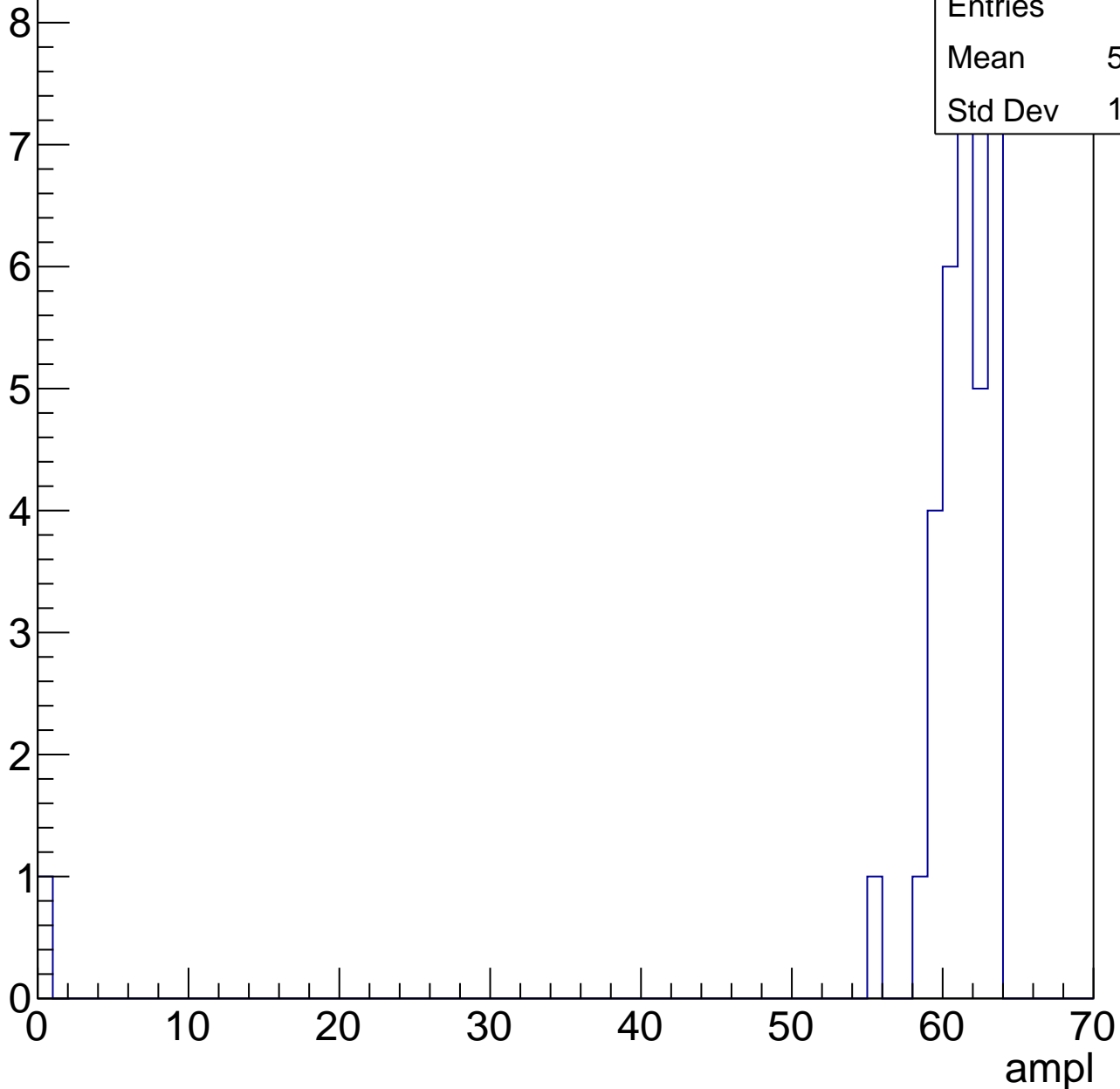


# B0L000S, U13-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.15
Std Dev	10.44



# B0L000S, U13-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U13-ch102, adc0

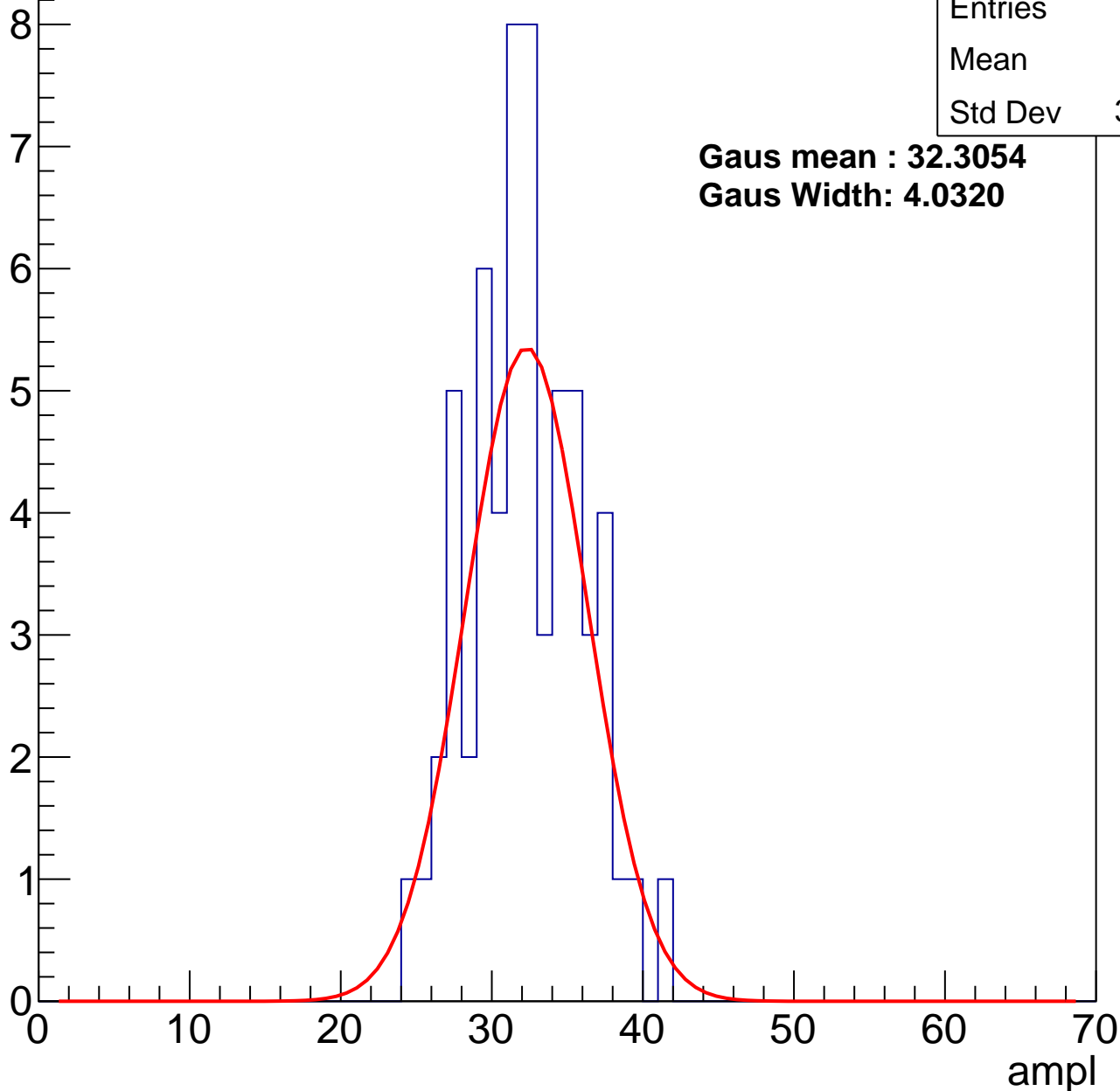
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	31.8
Std Dev	3.651

**Gaus mean : 32.3054**

**Gaus Width: 4.0320**



# B0L000S, U13-ch102, adc1

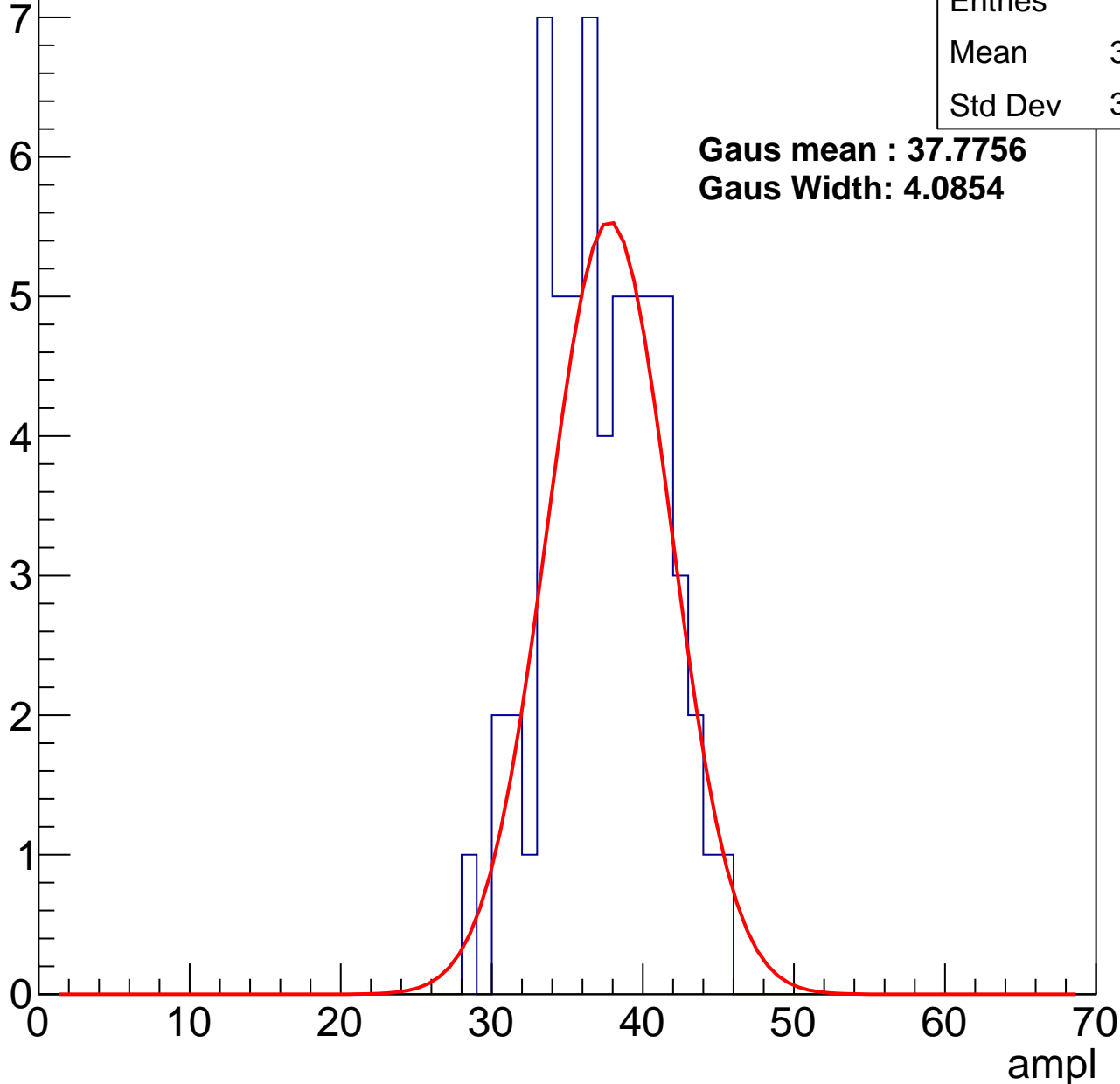
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.87
Std Dev	3.774

**Gaus mean : 37.7756**

**Gaus Width: 4.0854**



# B0L000S, U13-ch102, adc2

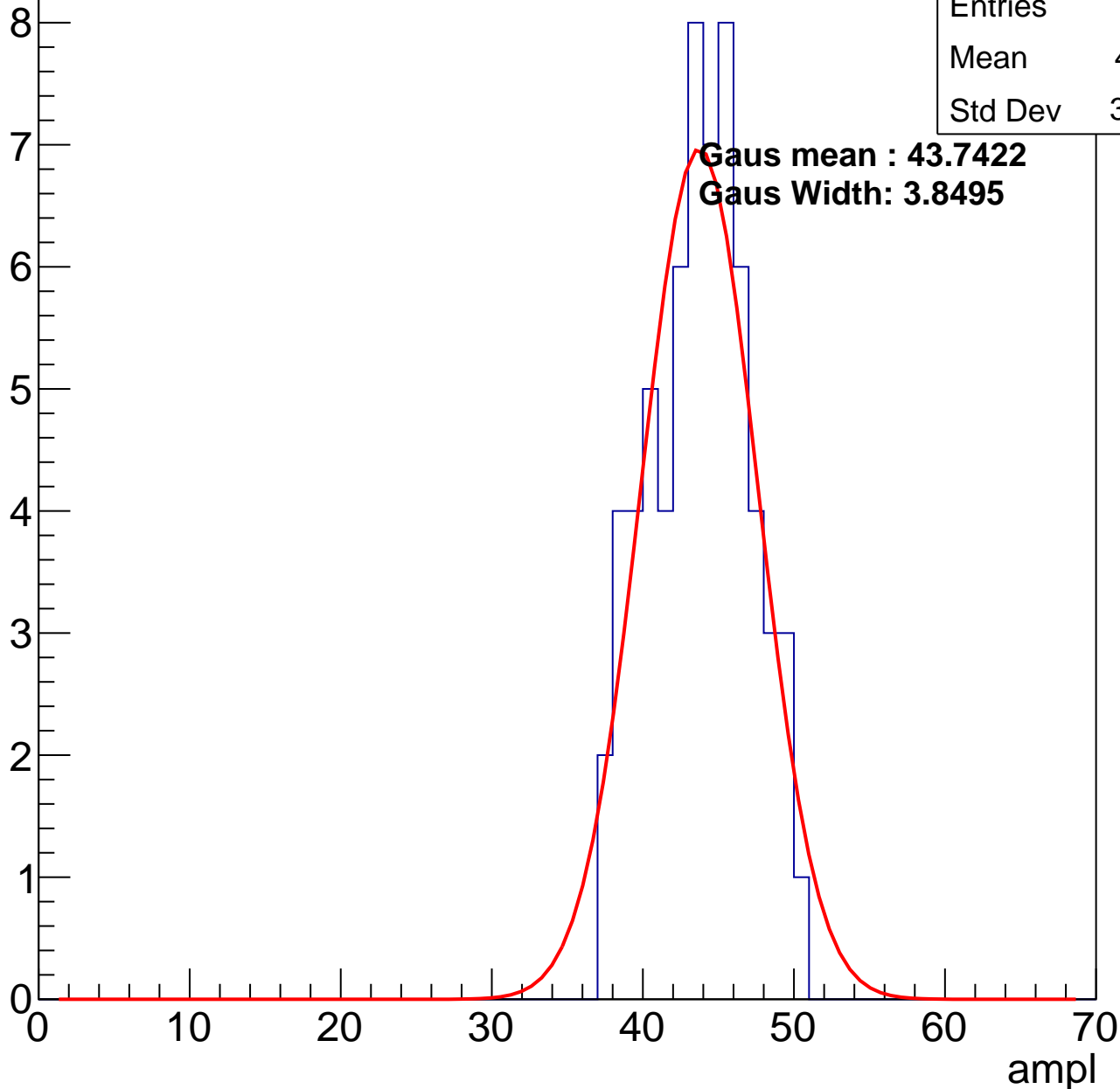
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.31
Std Dev	3.248

**Gaus mean : 43.7422**

**Gaus Width: 3.8495**

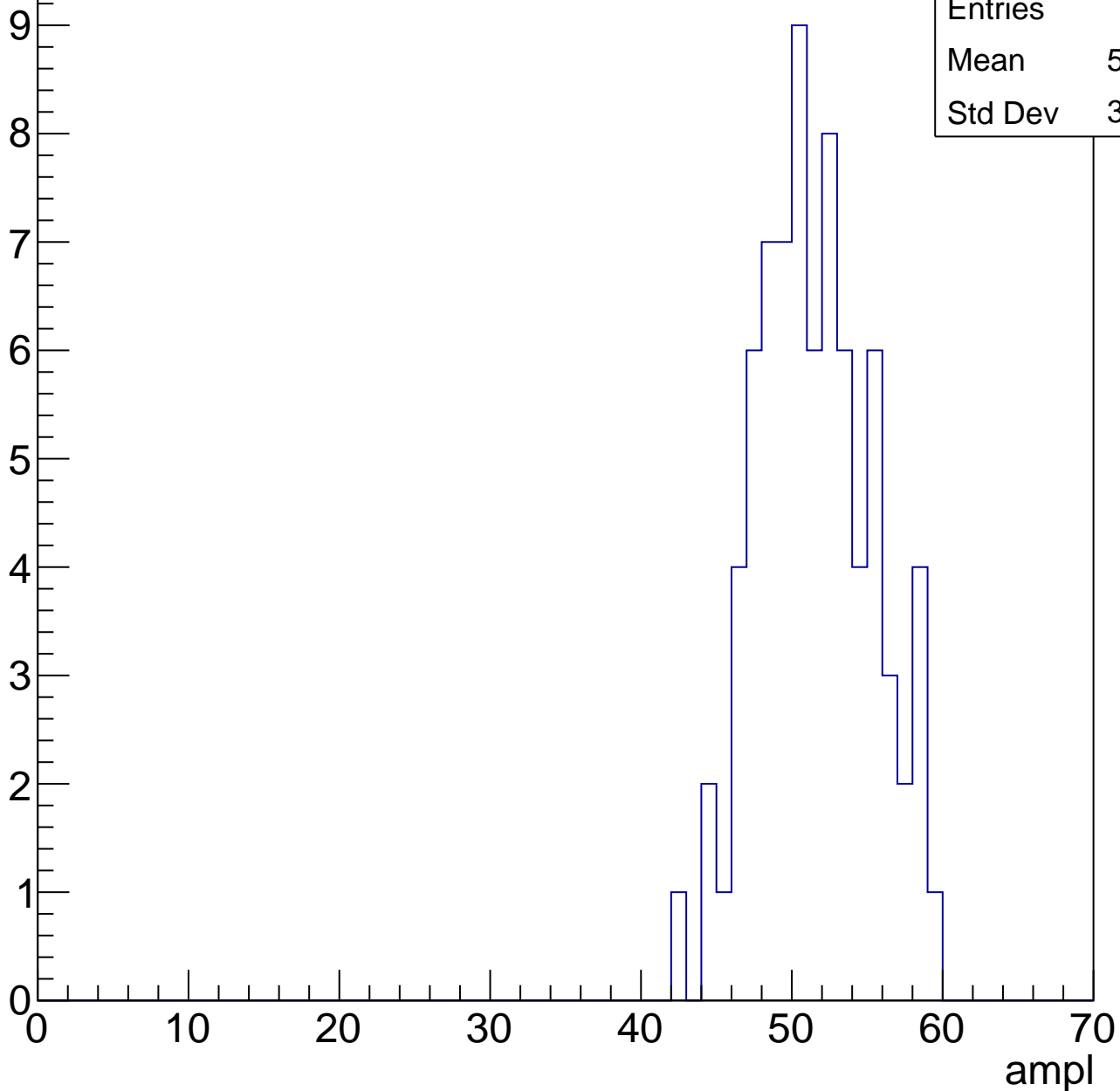


# B0L000S, U13-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	51.03
Std Dev	3.749

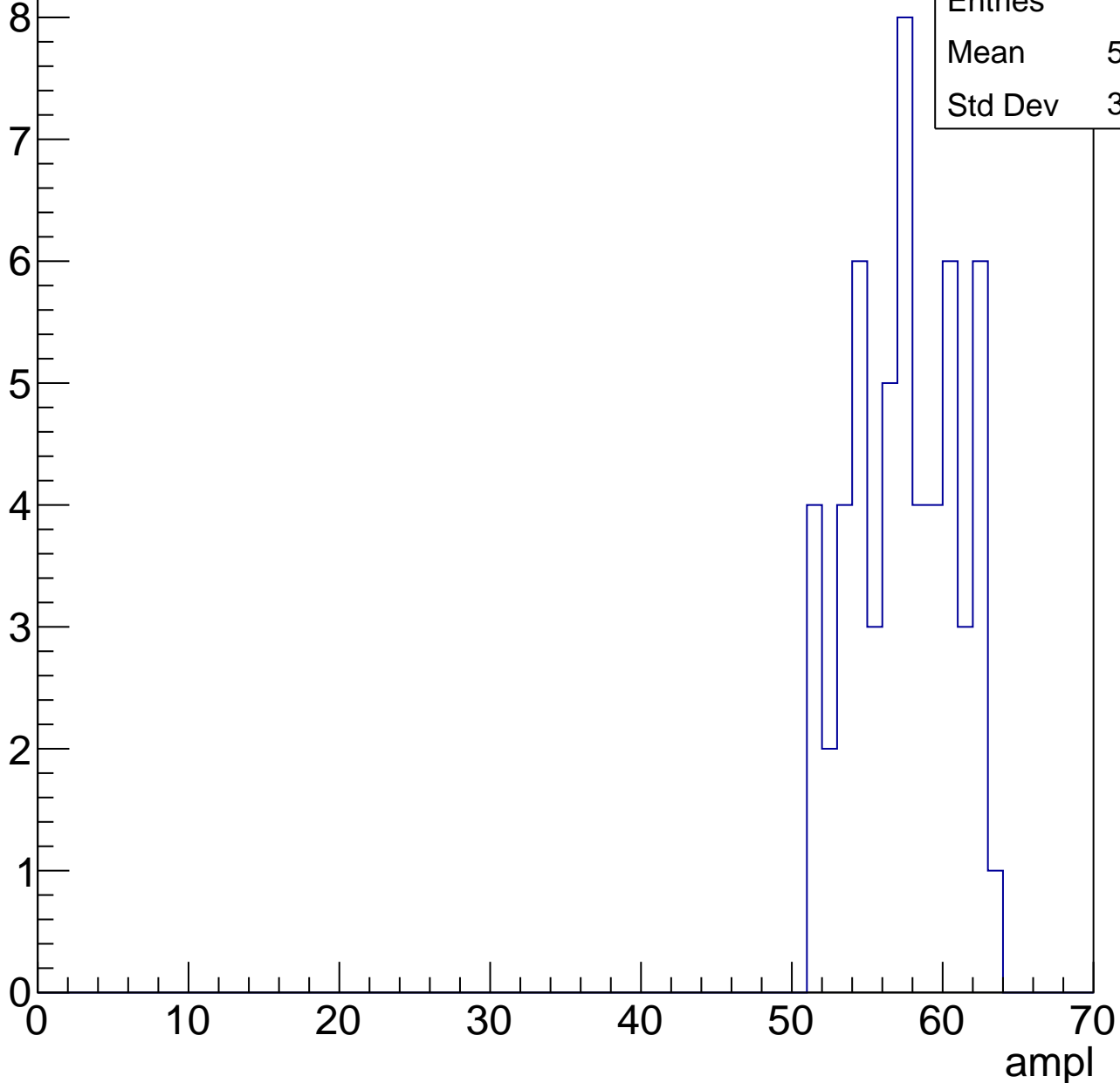


# B0L000S, U13-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	56.98
Std Dev	3.373

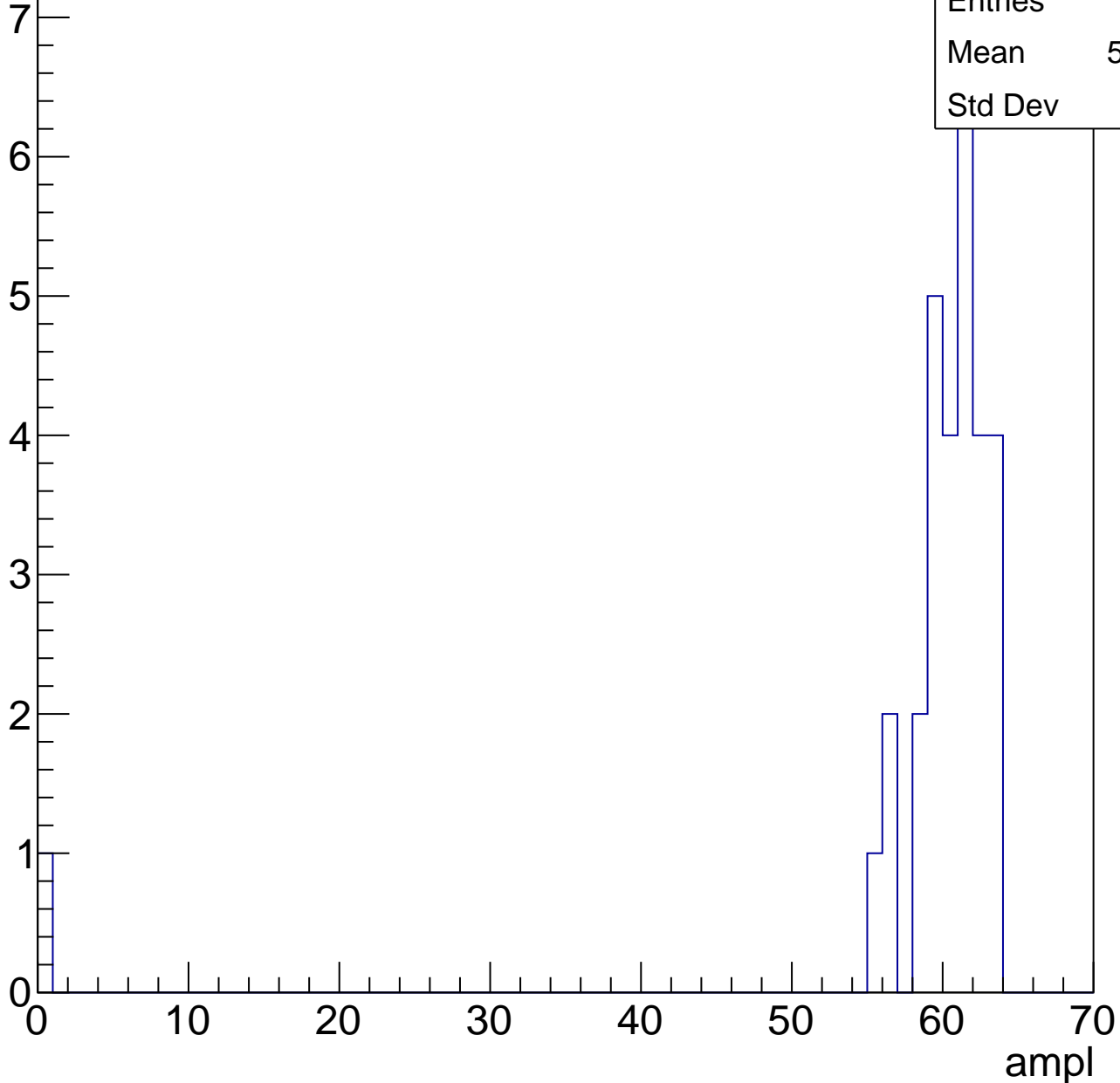


# B0L000S, U13-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	58.17
Std Dev	11



# B0L000S, U13-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch103, adc0

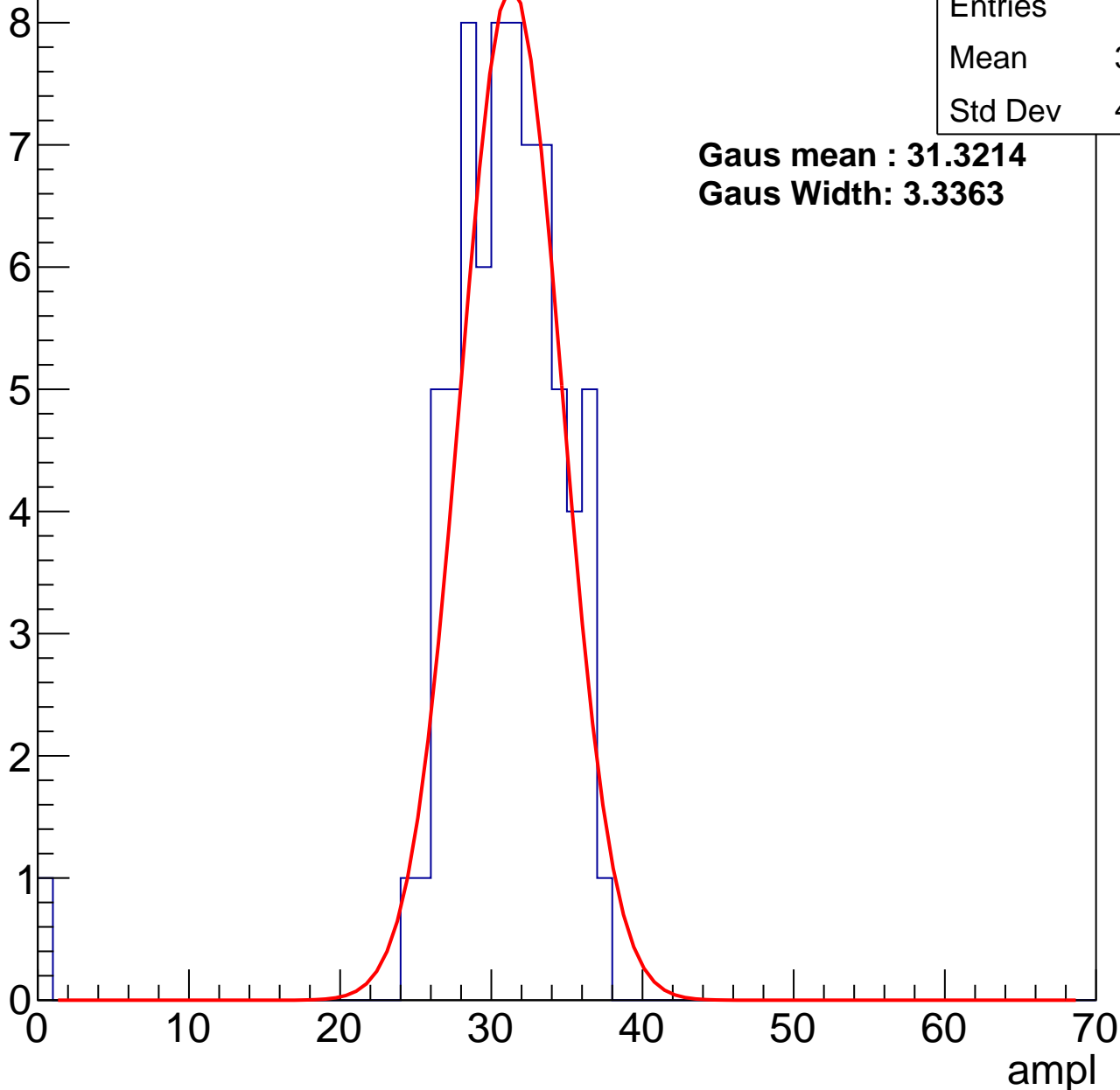
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	30.31
Std Dev	4.751

**Gaus mean : 31.3214**

**Gaus Width: 3.3363**



# B0L000S, U13-ch103, adc1

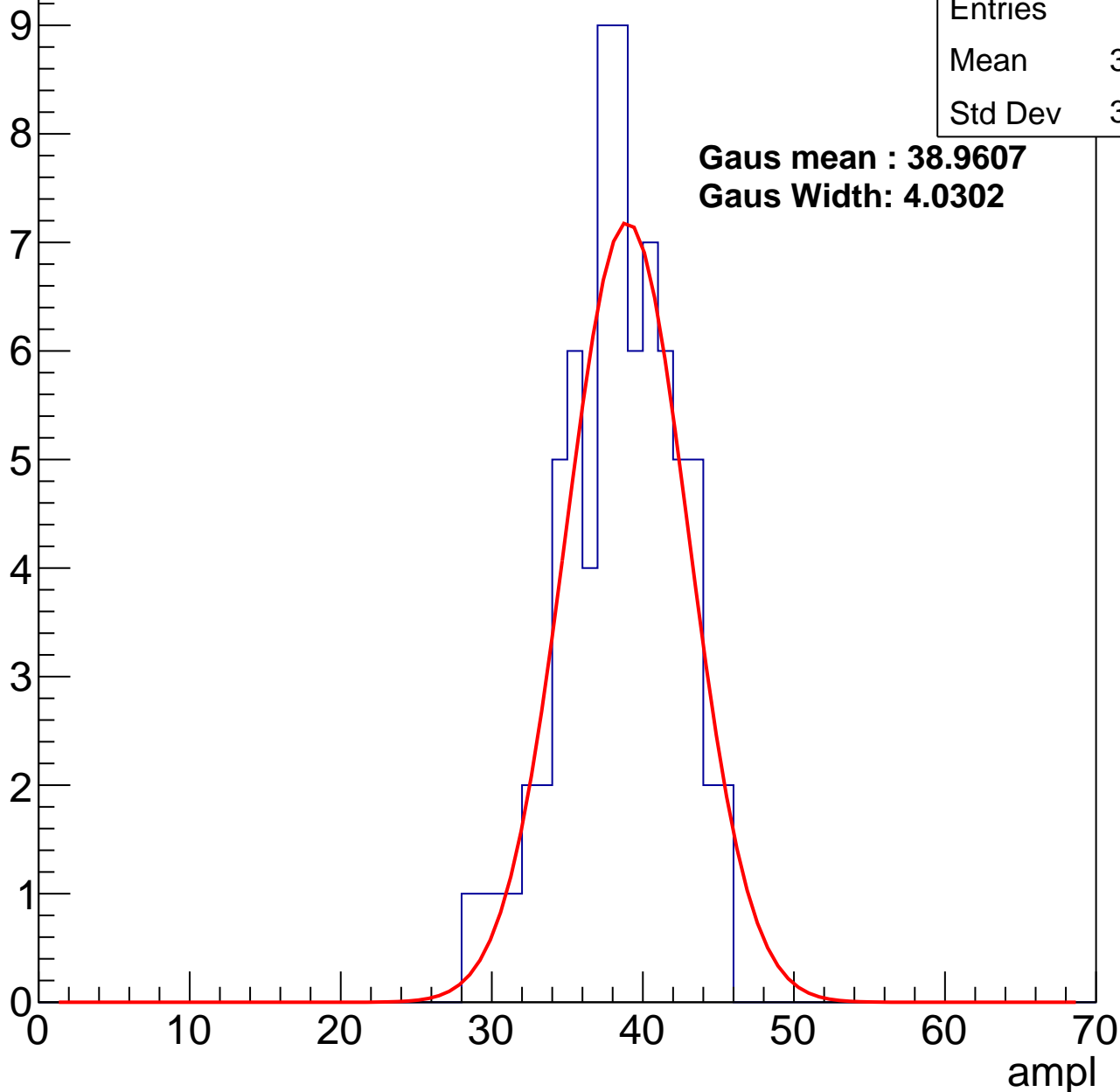
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.97
Std Dev	3.745

**Gaus mean : 38.9607**

**Gaus Width: 4.0302**



# B0L000S, U13-ch103, adc2

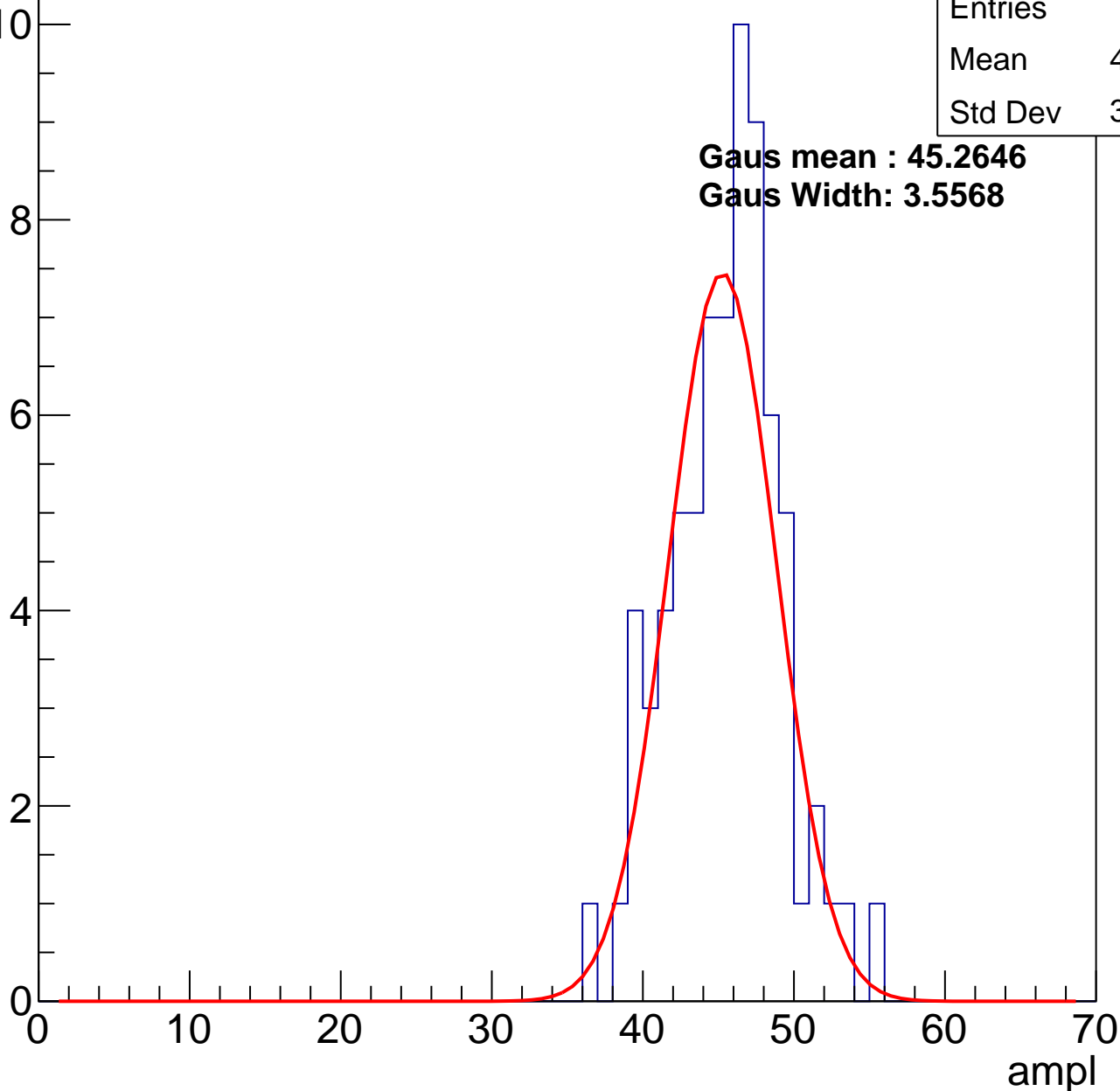
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	45.07
Std Dev	3.654

**Gaus mean : 45.2646**

**Gaus Width: 3.5568**

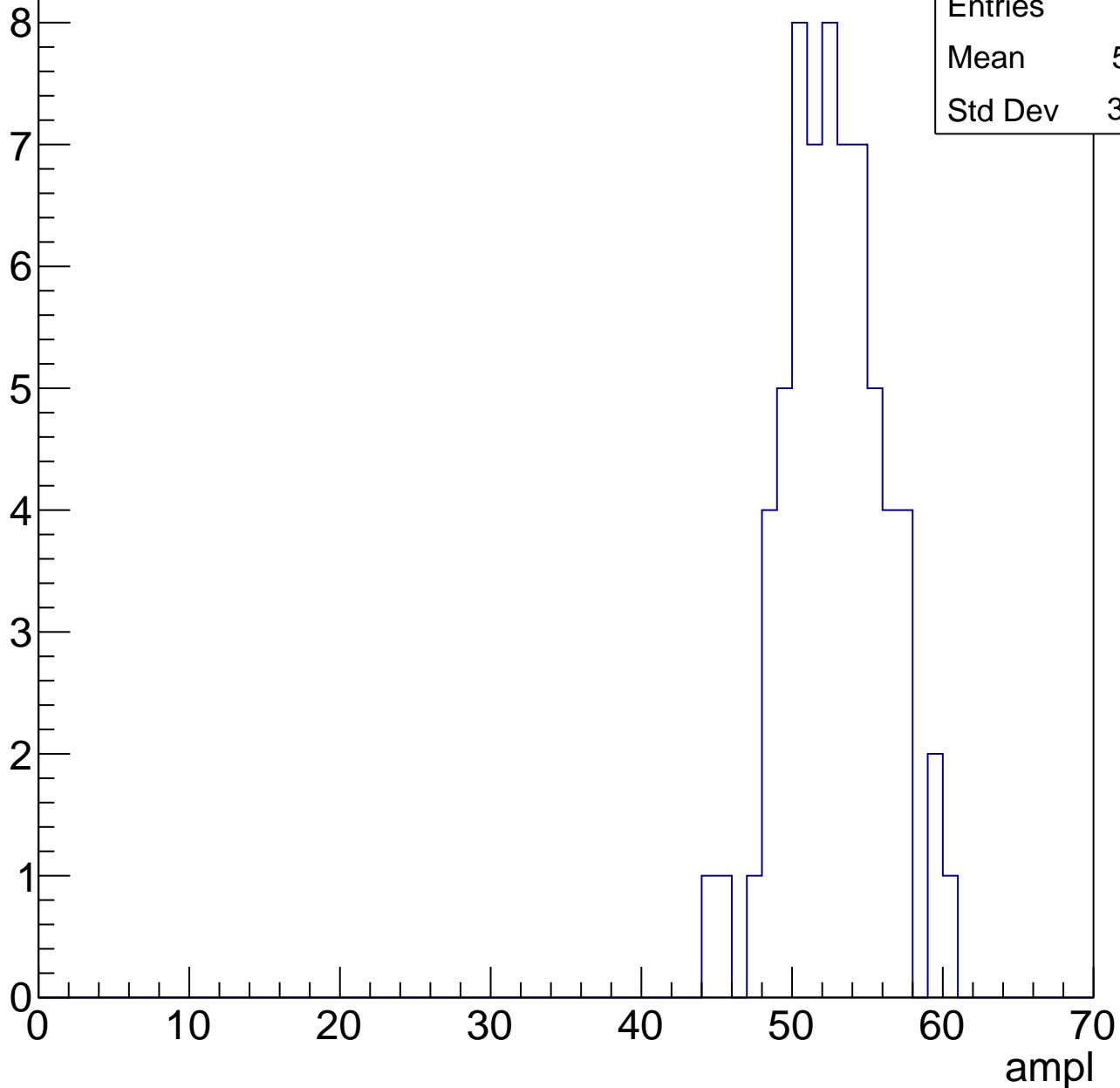


# B0L000S, U13-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	52.31
Std Dev	3.248

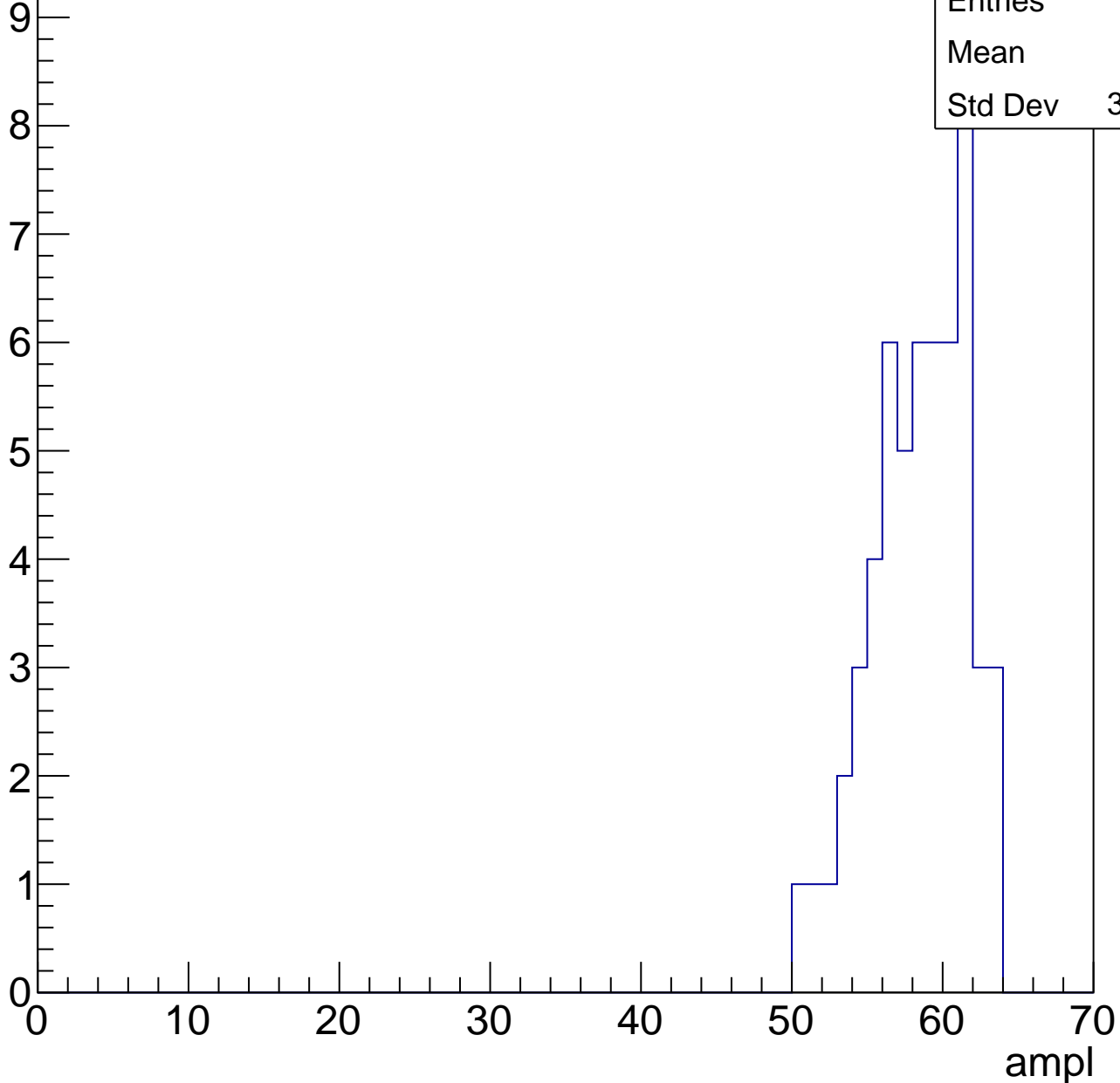


# B0L000S, U13-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	58
Std Dev	3.122

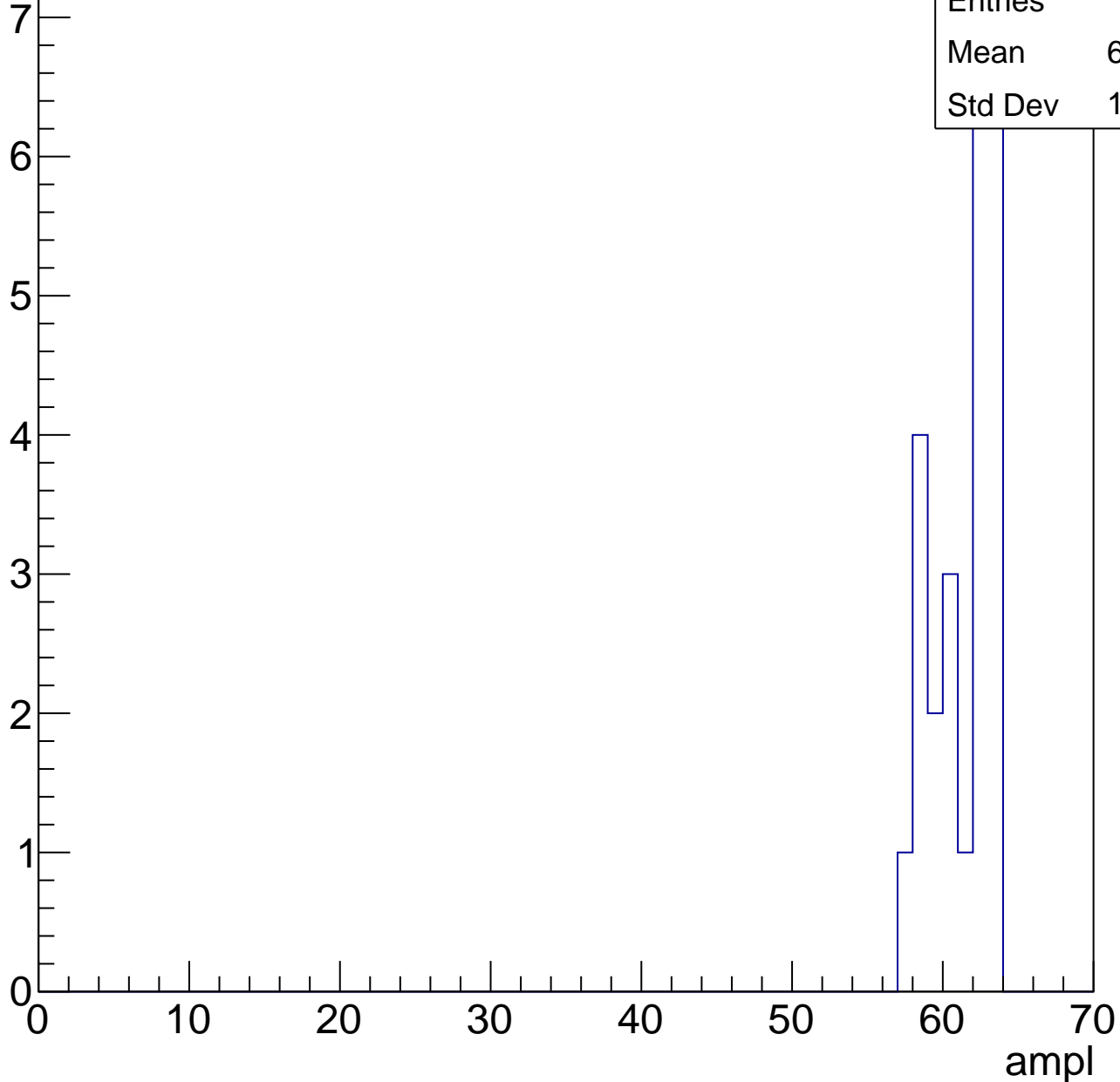


# B0L000S, U13-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	60.92
Std Dev	1.978



# B0L000S, U13-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch104, adc0

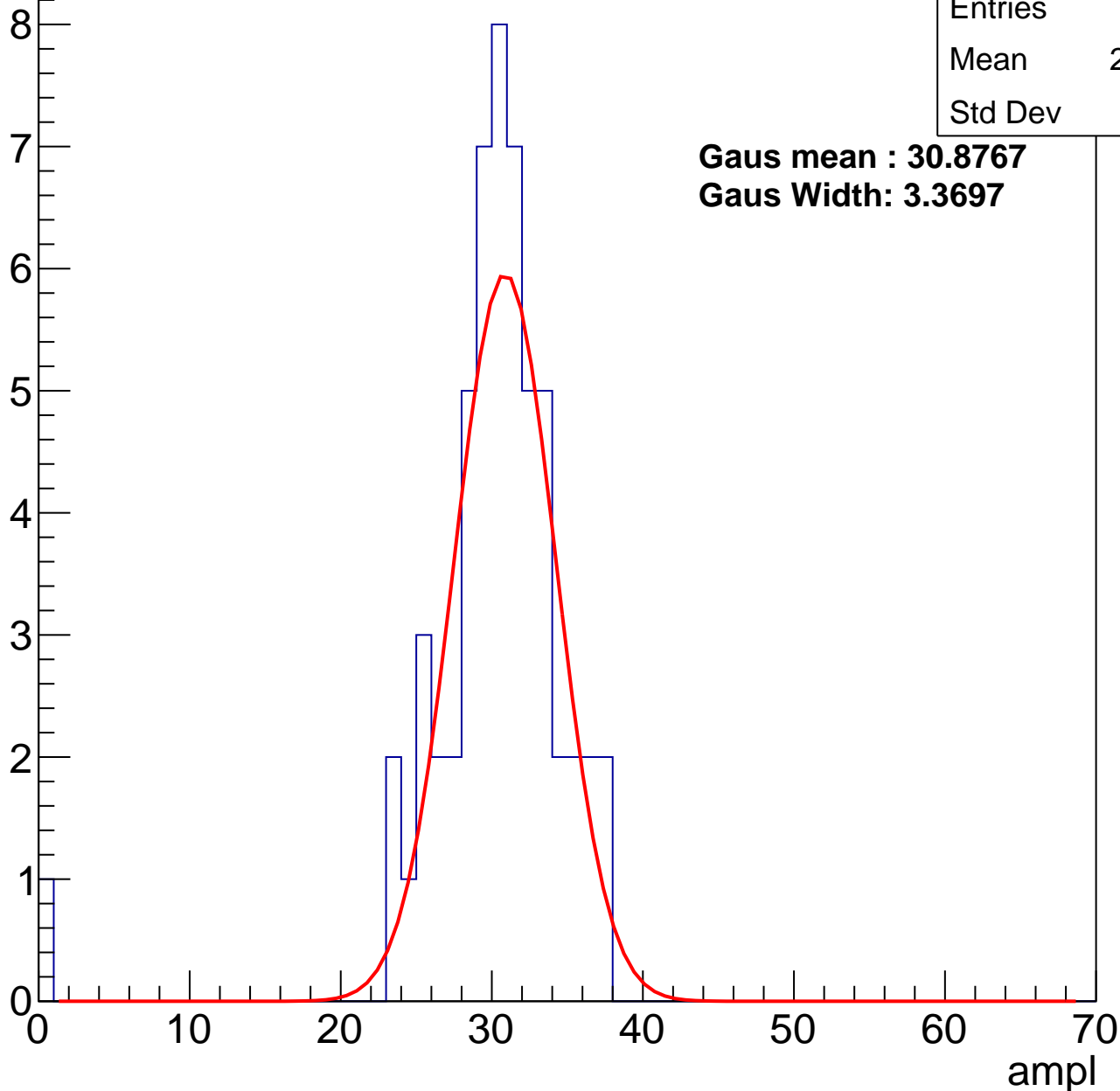
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.64
Std Dev	5.18

**Gaus mean : 30.8767**

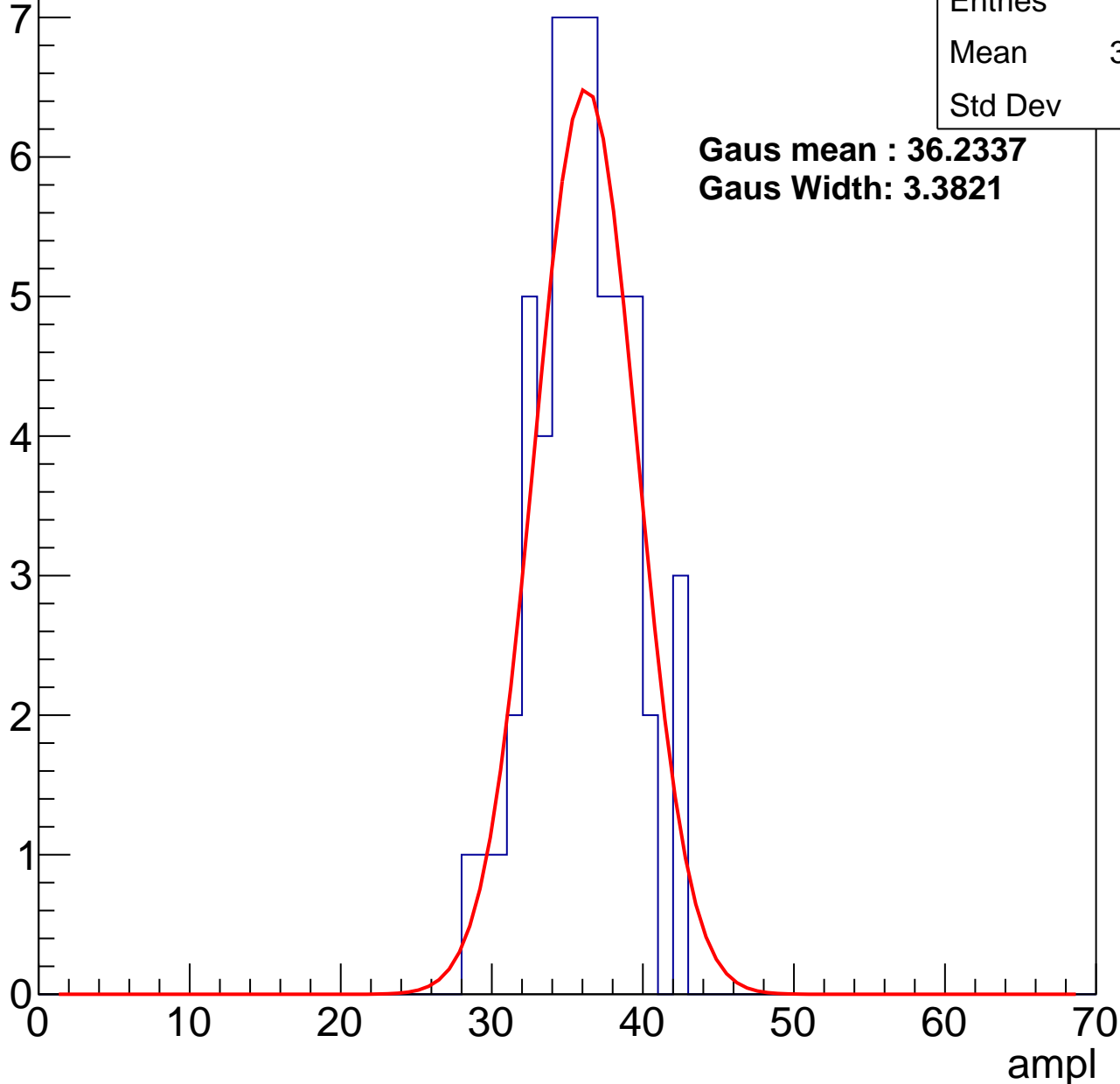
**Gaus Width: 3.3697**



# B0L000S, U13-ch104, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	55
Mean	35.49
Std Dev	3.15

**Gaus mean : 36.2337**  
**Gaus Width: 3.3821**

# B0L000S, U13-ch104, adc2

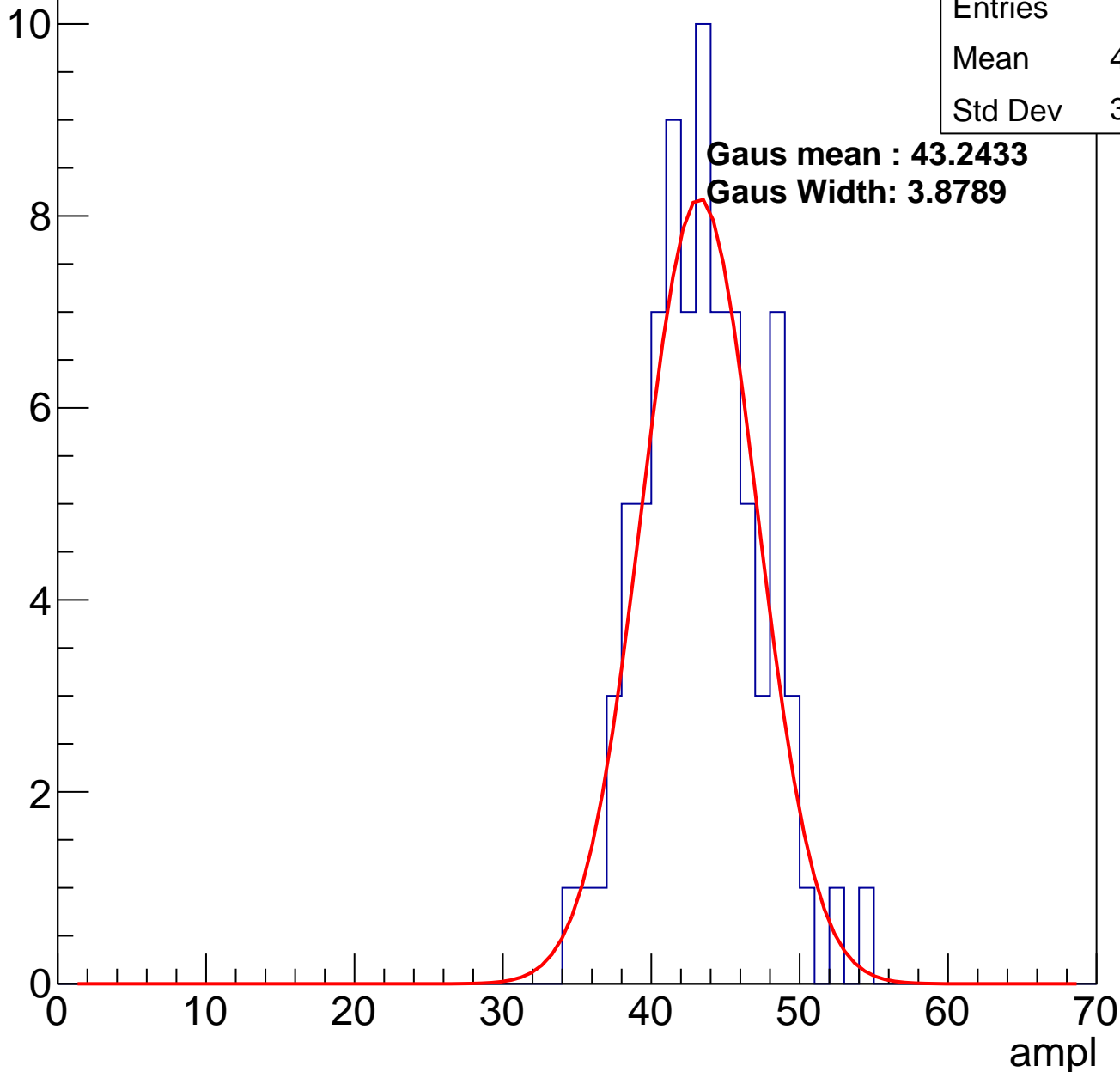
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	42.94
Std Dev	3.896

**Gaus mean : 43.2433**

**Gaus Width: 3.8789**

Entry



# B0L000S, U13-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

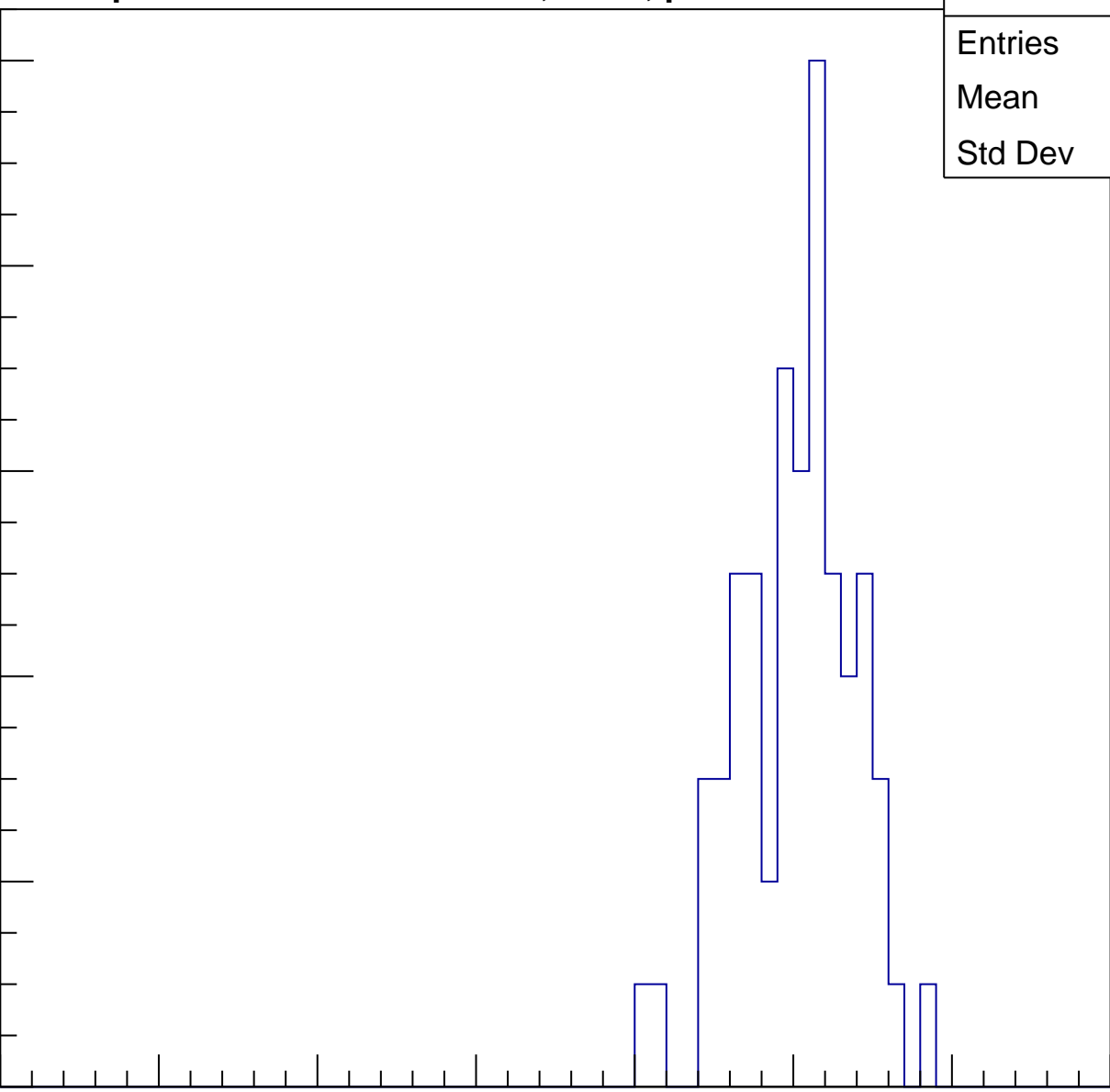
Entries	62
Mean	49.73
Std Dev	3.637

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

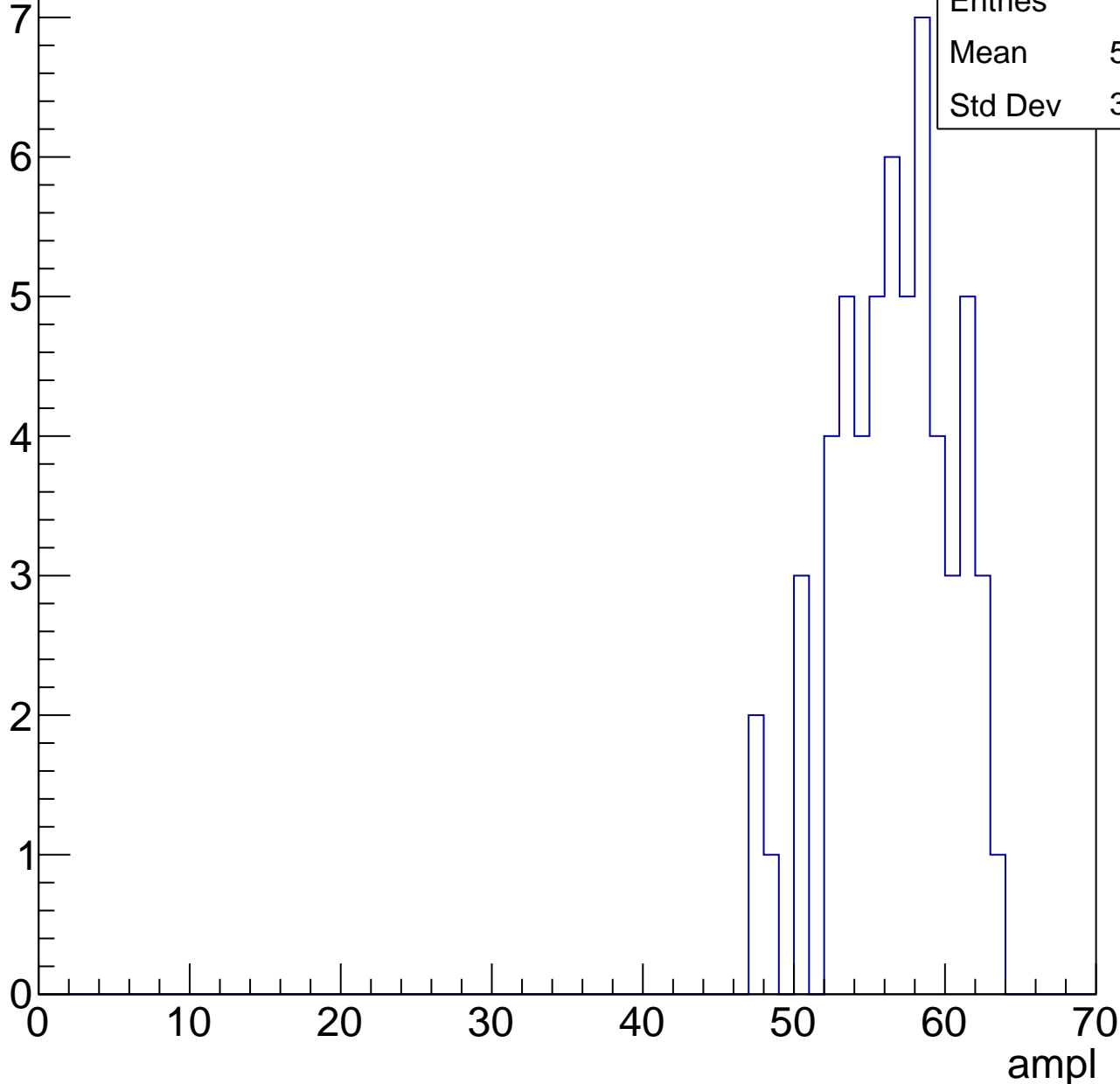


# B0L000S, U13-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	56.09
Std Dev	3.856

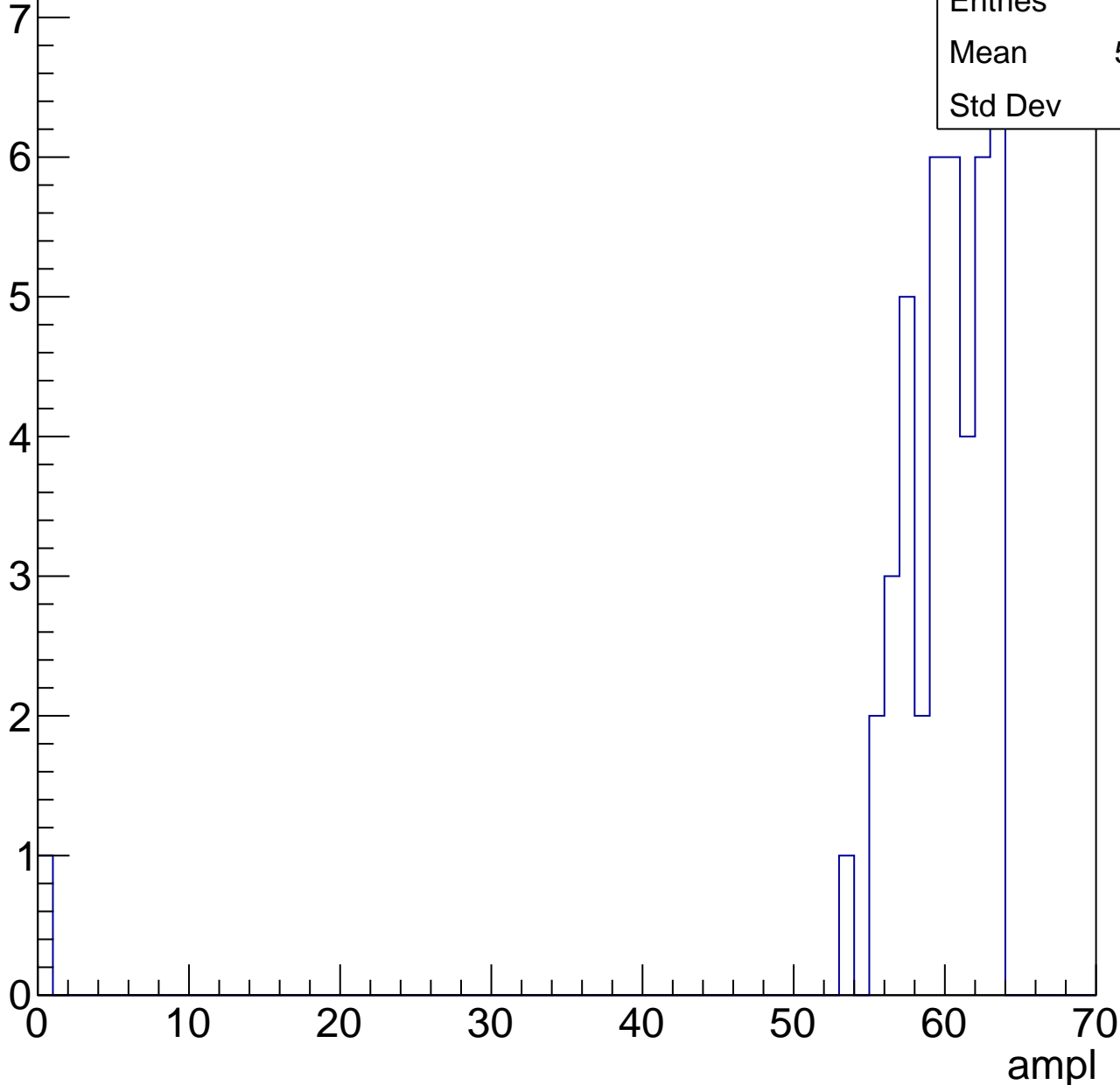


# B0L000S, U13-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.21
Std Dev	9.35



# B0L000S, U13-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

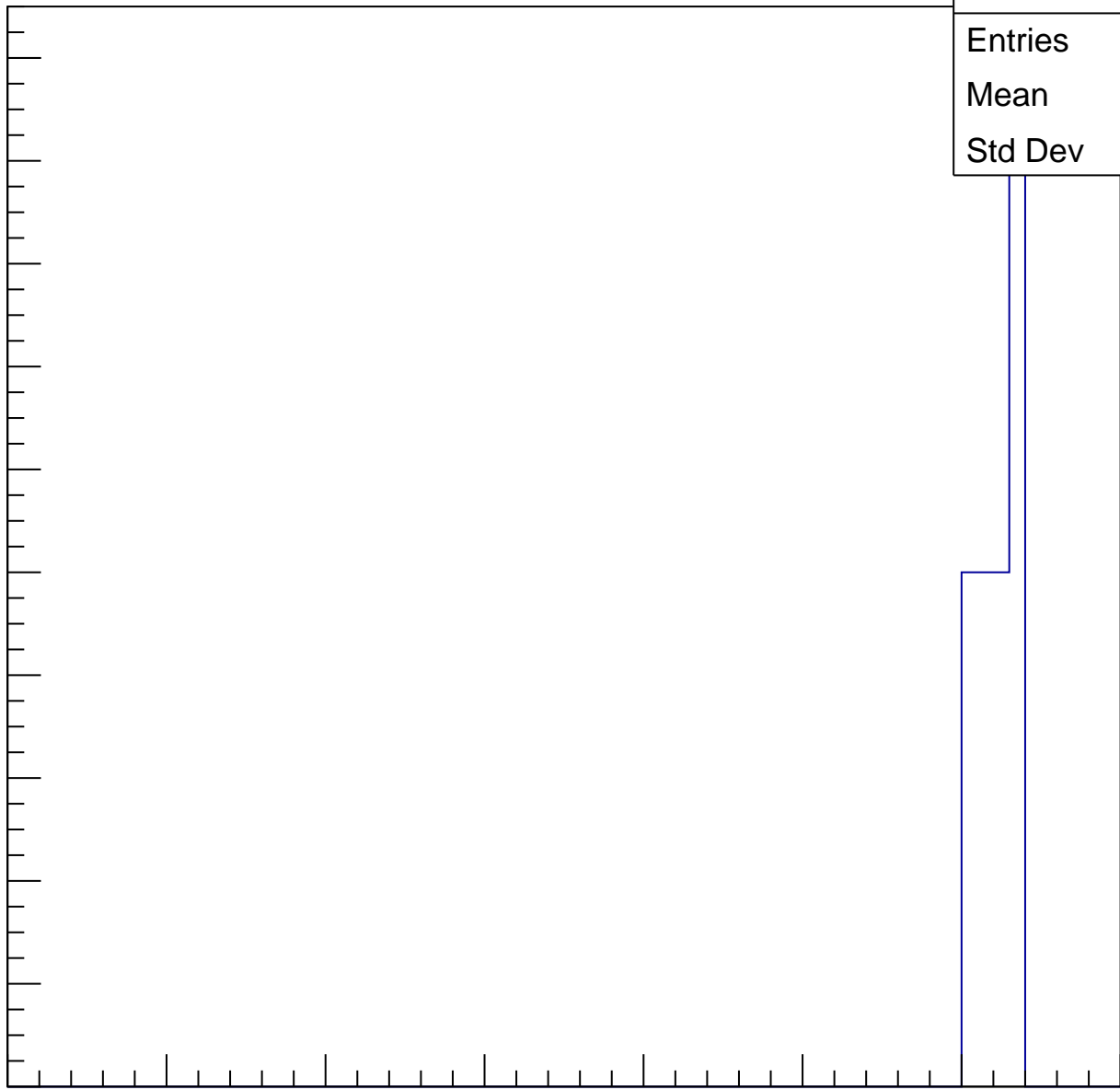
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70





# B0L000S, U13-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U13-ch105, adc0

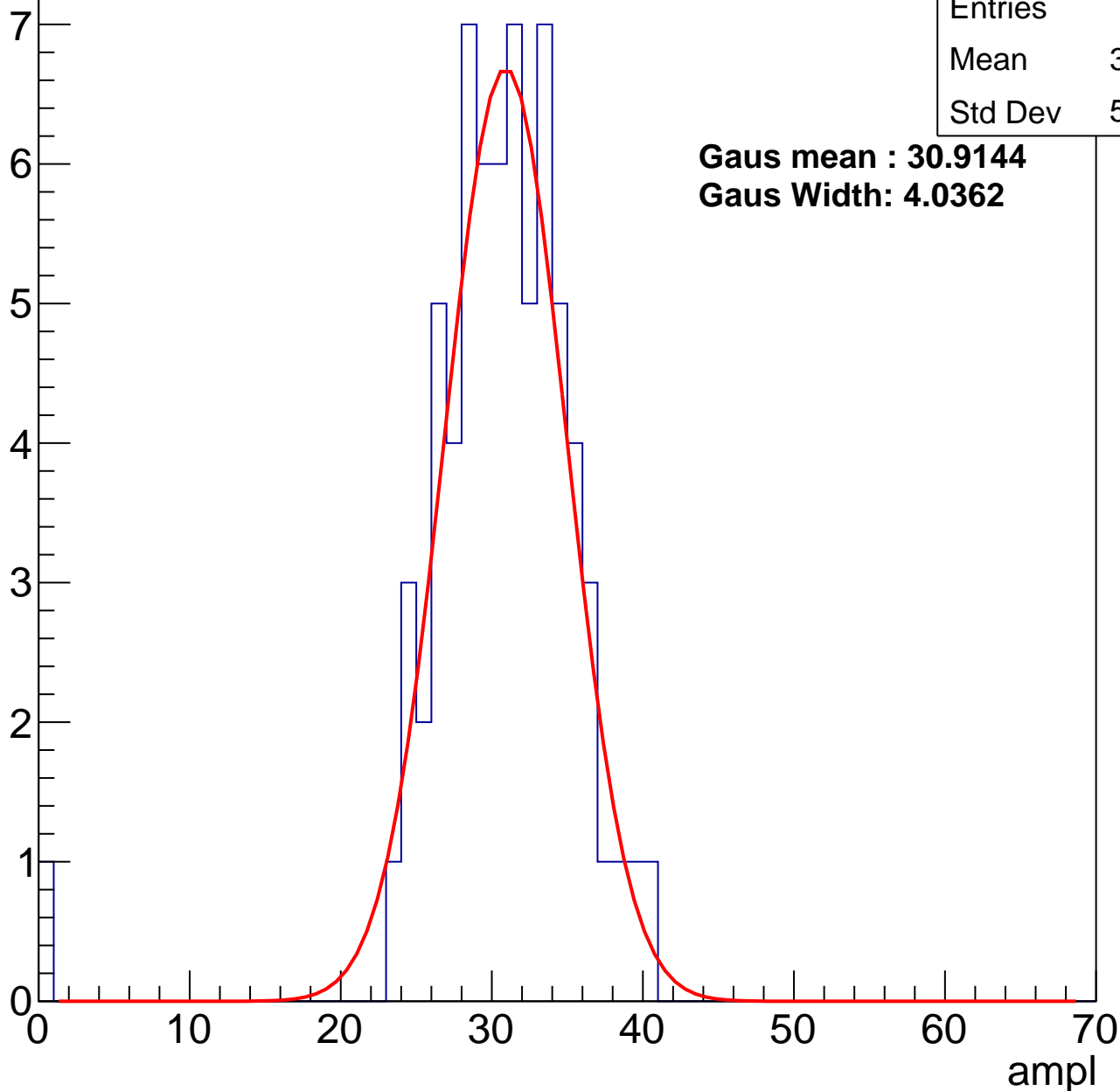
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.19
Std Dev	5.246

**Gaus mean : 30.9144**

**Gaus Width: 4.0362**



# B0L000S, U13-ch105, adc1

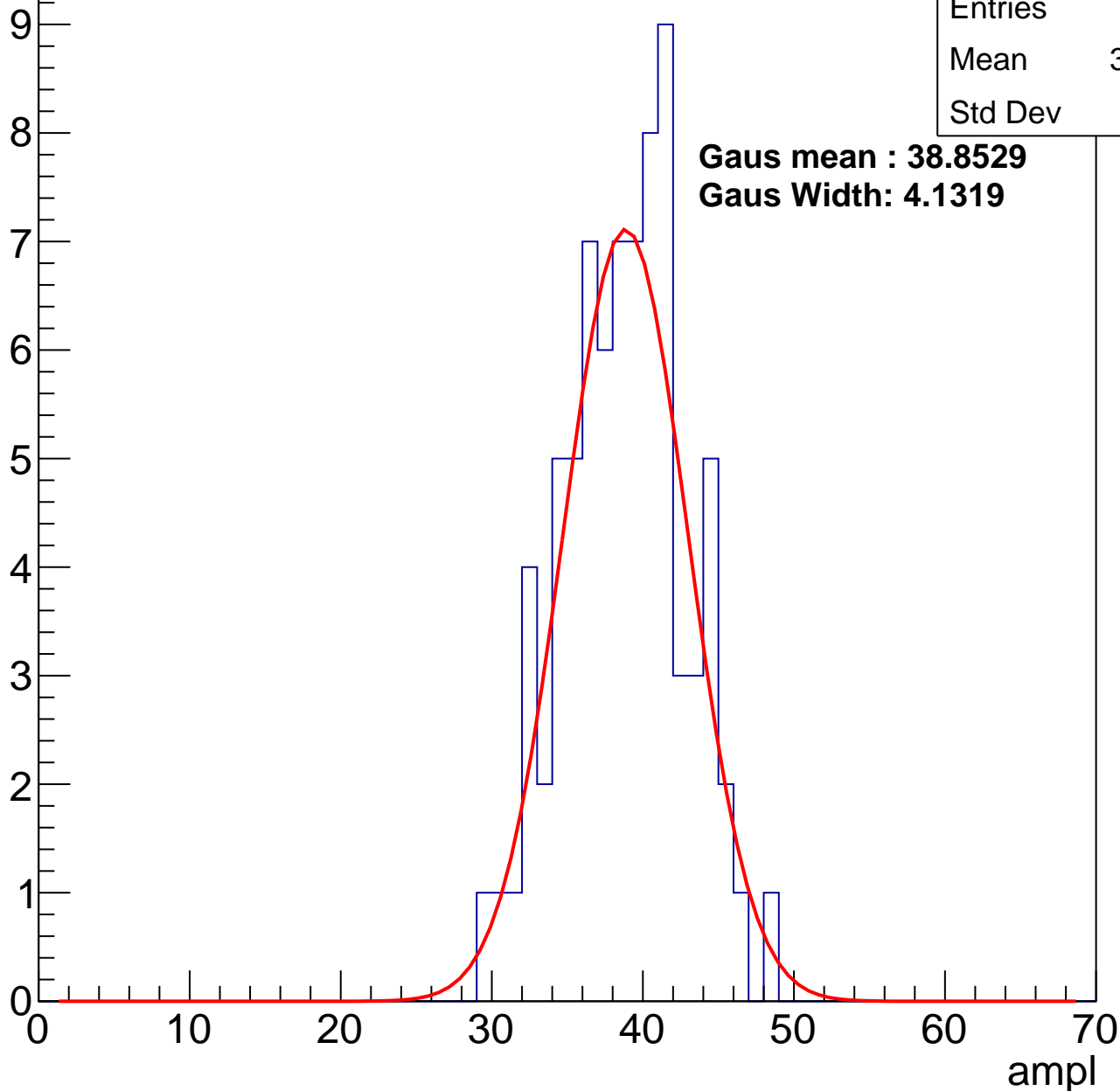
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	38.33
Std Dev	3.96

**Gaus mean : 38.8529**

**Gaus Width: 4.1319**



# B0L000S, U13-ch105, adc2

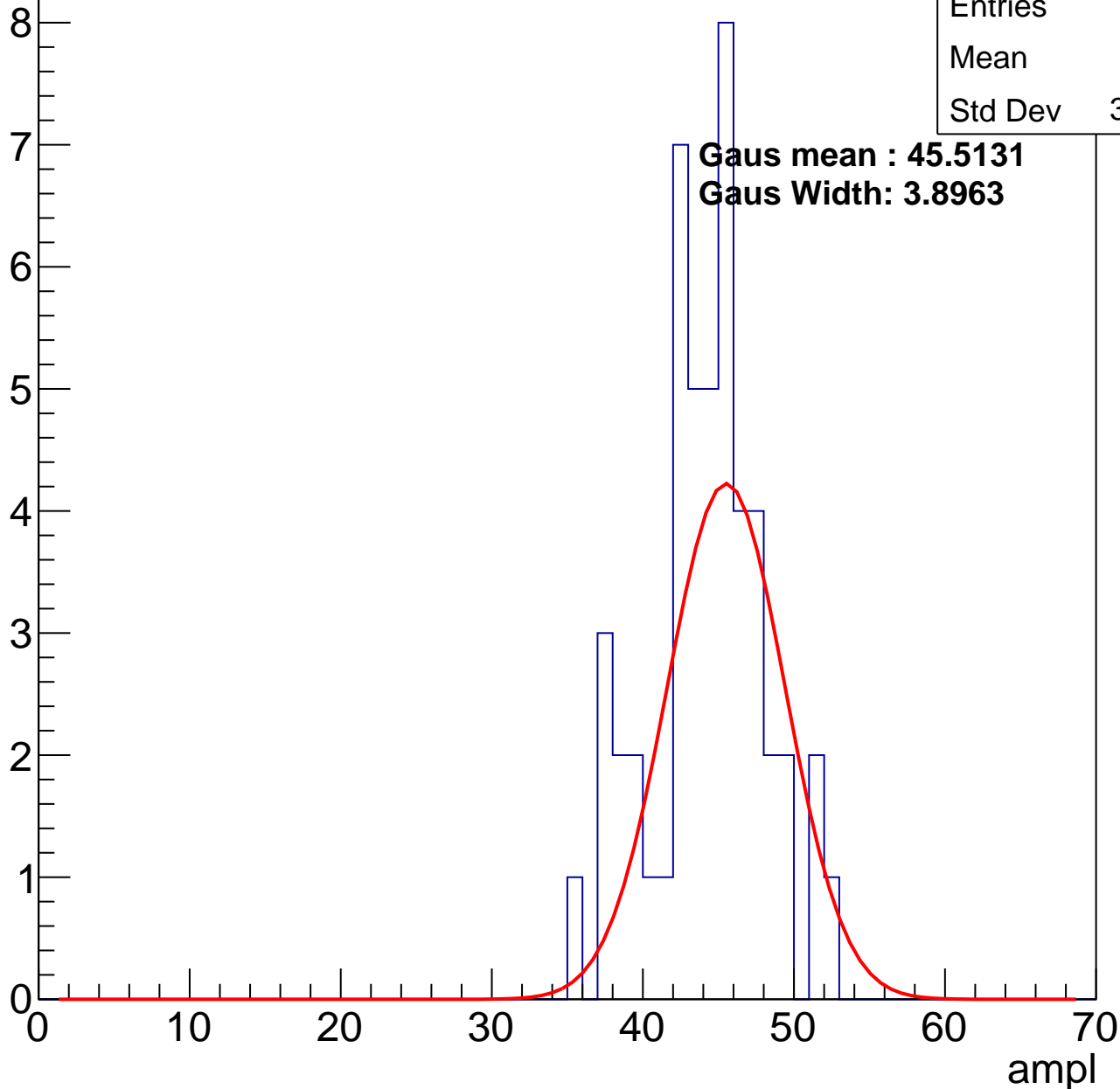
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	43.8
Std Dev	3.768

**Gaus mean : 45.5131**

**Gaus Width: 3.8963**

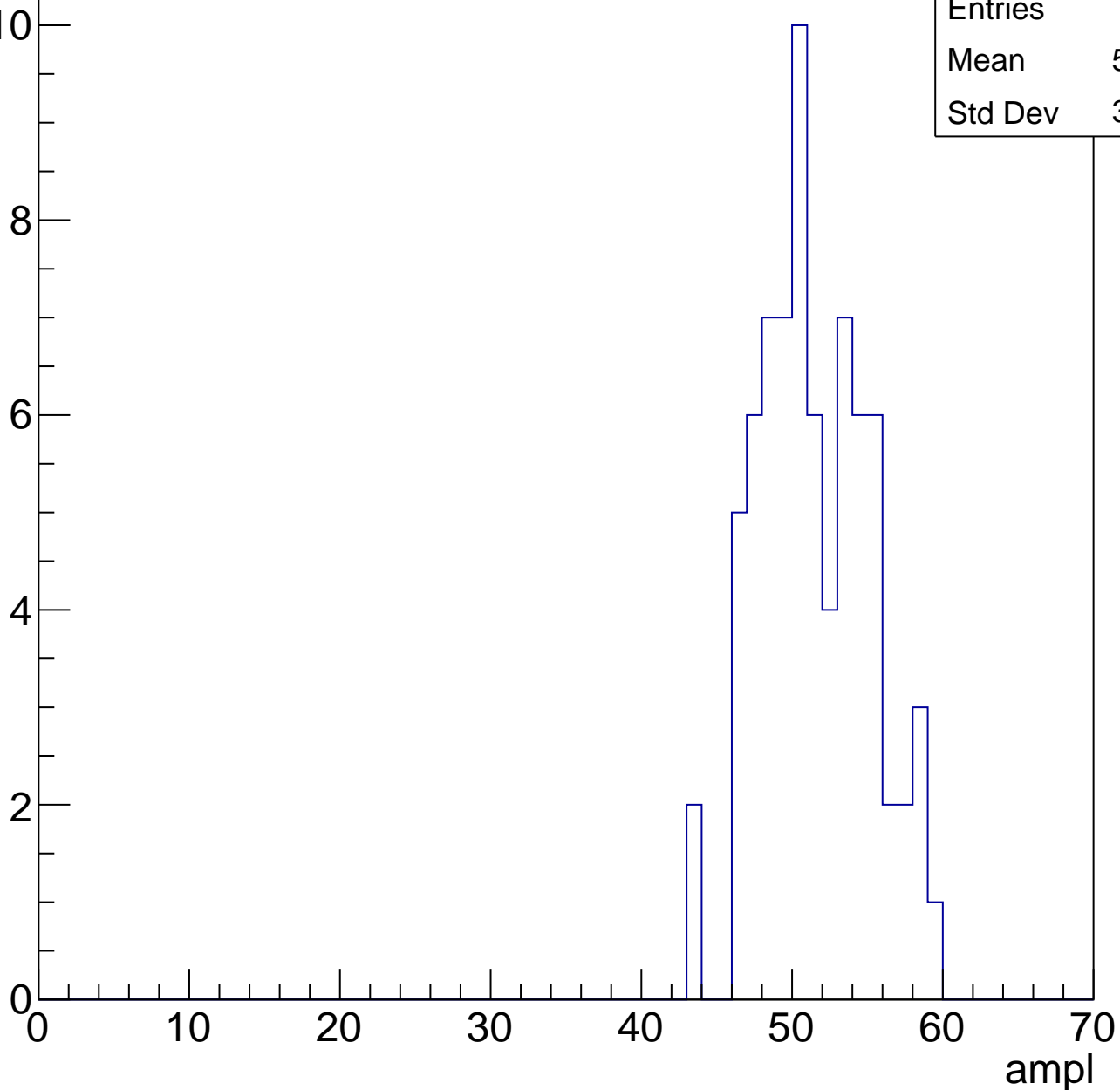


# B0L000S, U13-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	51.01
Std Dev	3.611

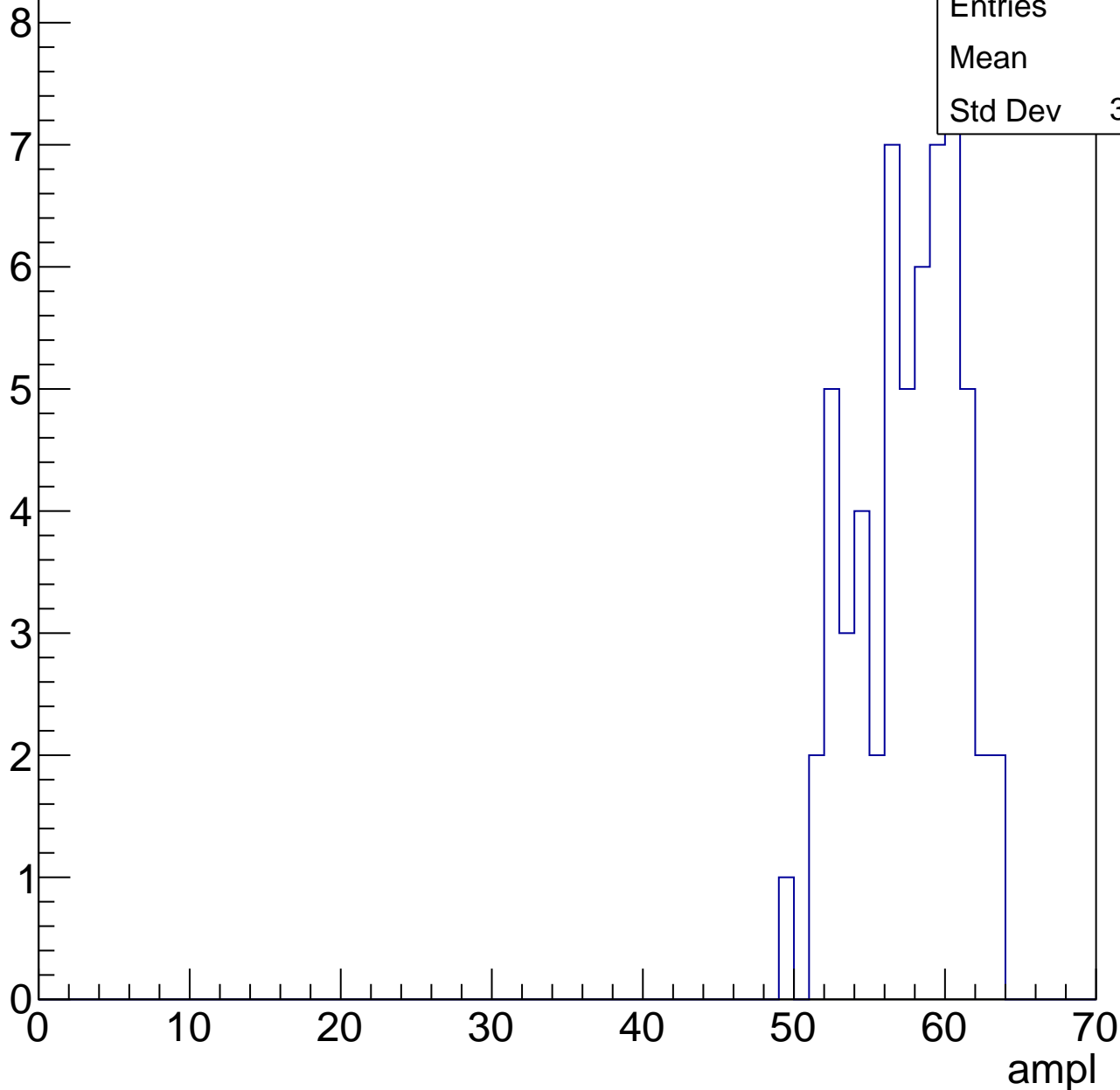


# B0L000S, U13-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	57.1
Std Dev	3.363

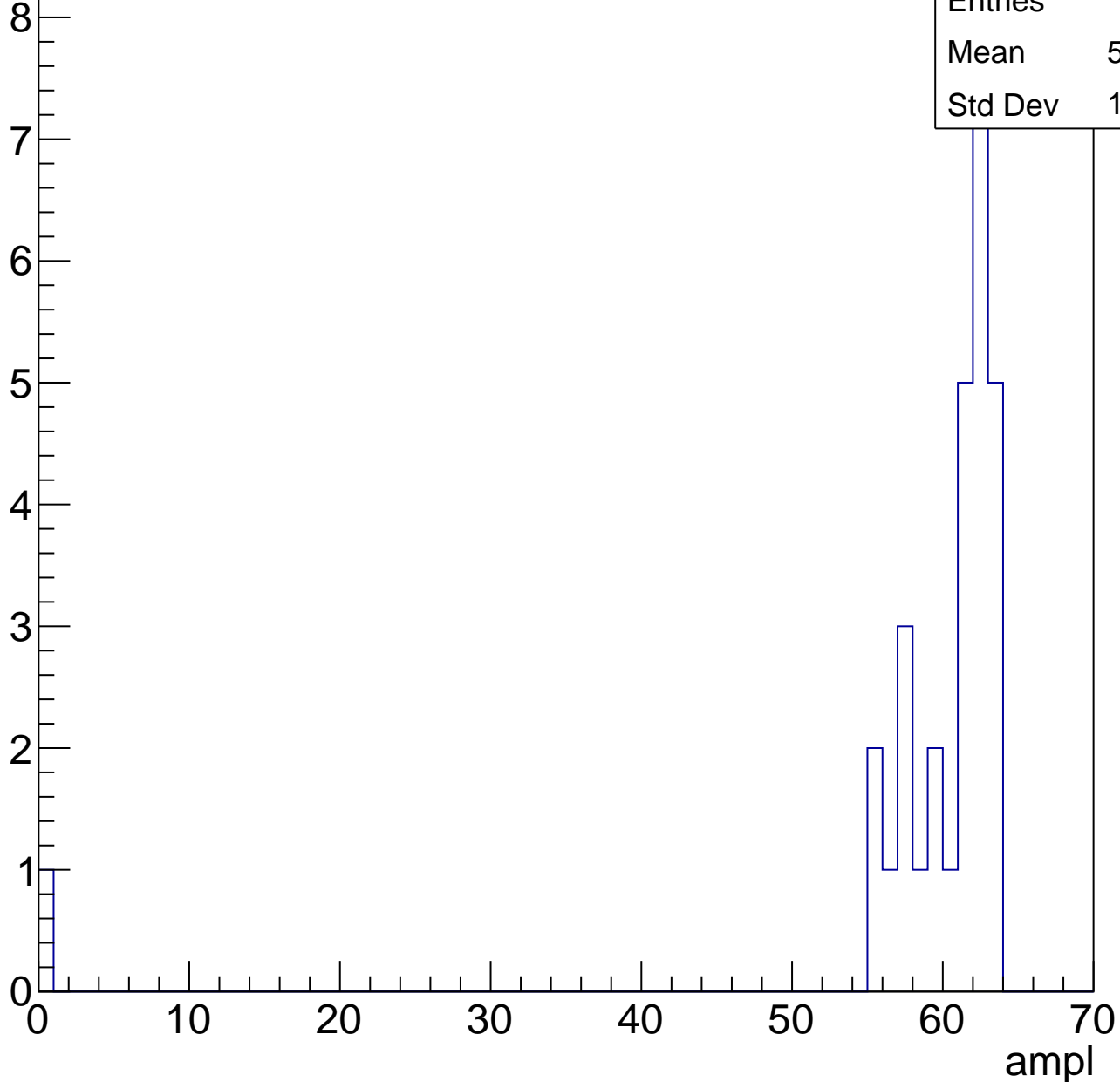


# B0L000S, U13-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	29
Mean	58.24
Std Dev	11.28



# B0L000S, U13-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U13-ch106, adc0

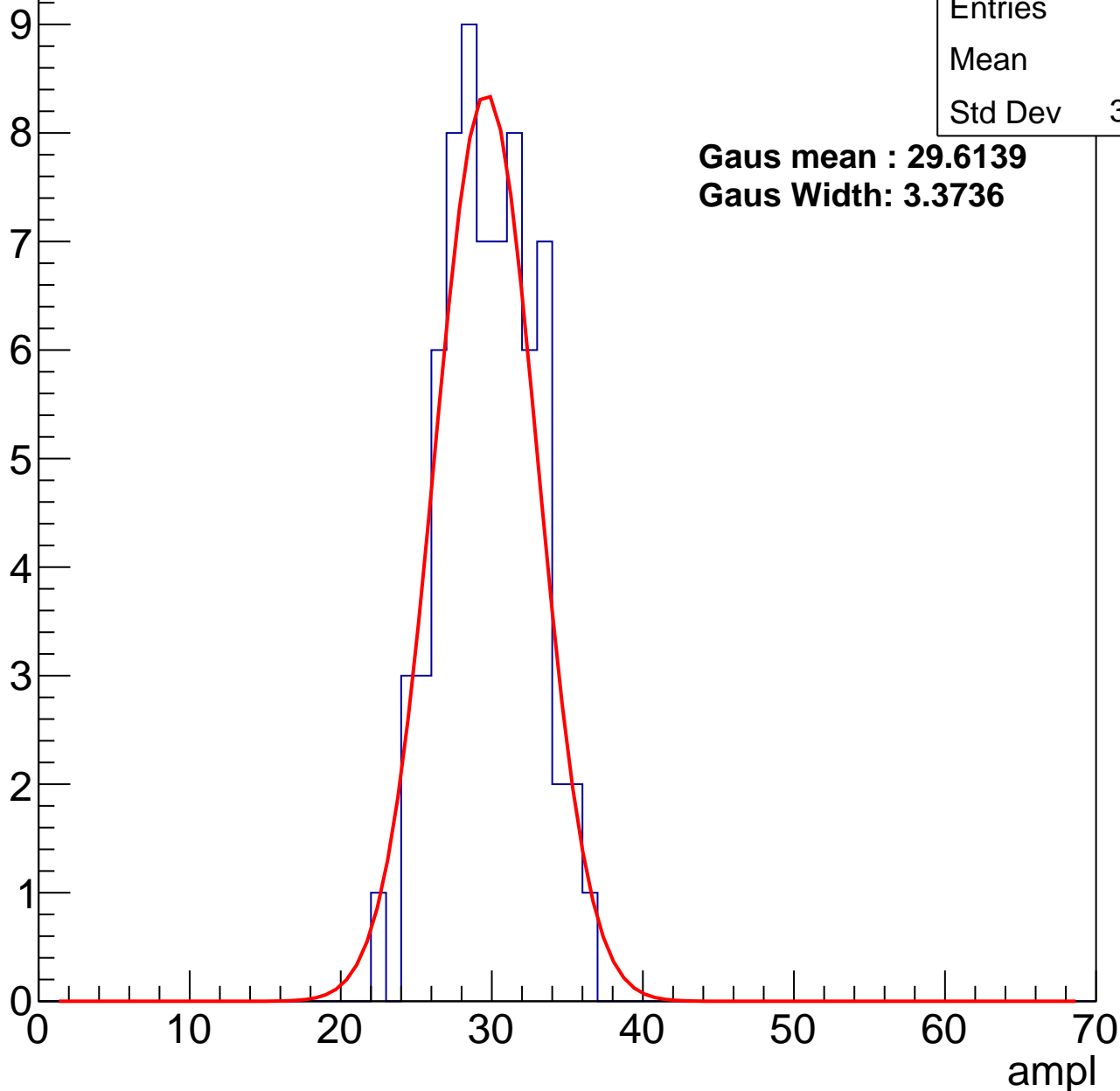
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.3
Std Dev	3.016

**Gaus mean : 29.6139**

**Gaus Width: 3.3736**



# B0L000S, U13-ch106, adc1

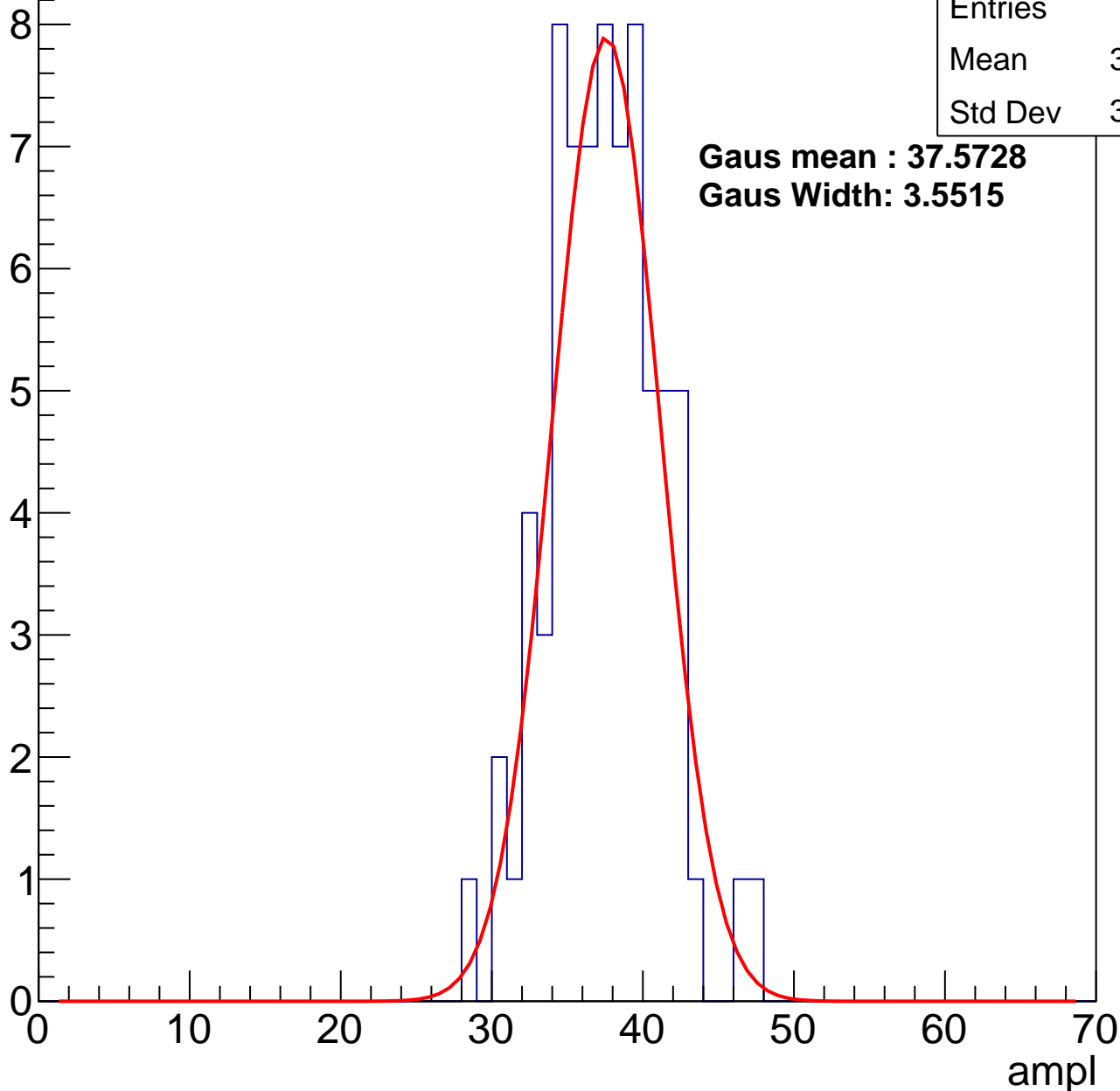
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.03
Std Dev	3.632

**Gaus mean : 37.5728**

**Gaus Width: 3.5515**



# B0L000S, U13-ch106, adc2

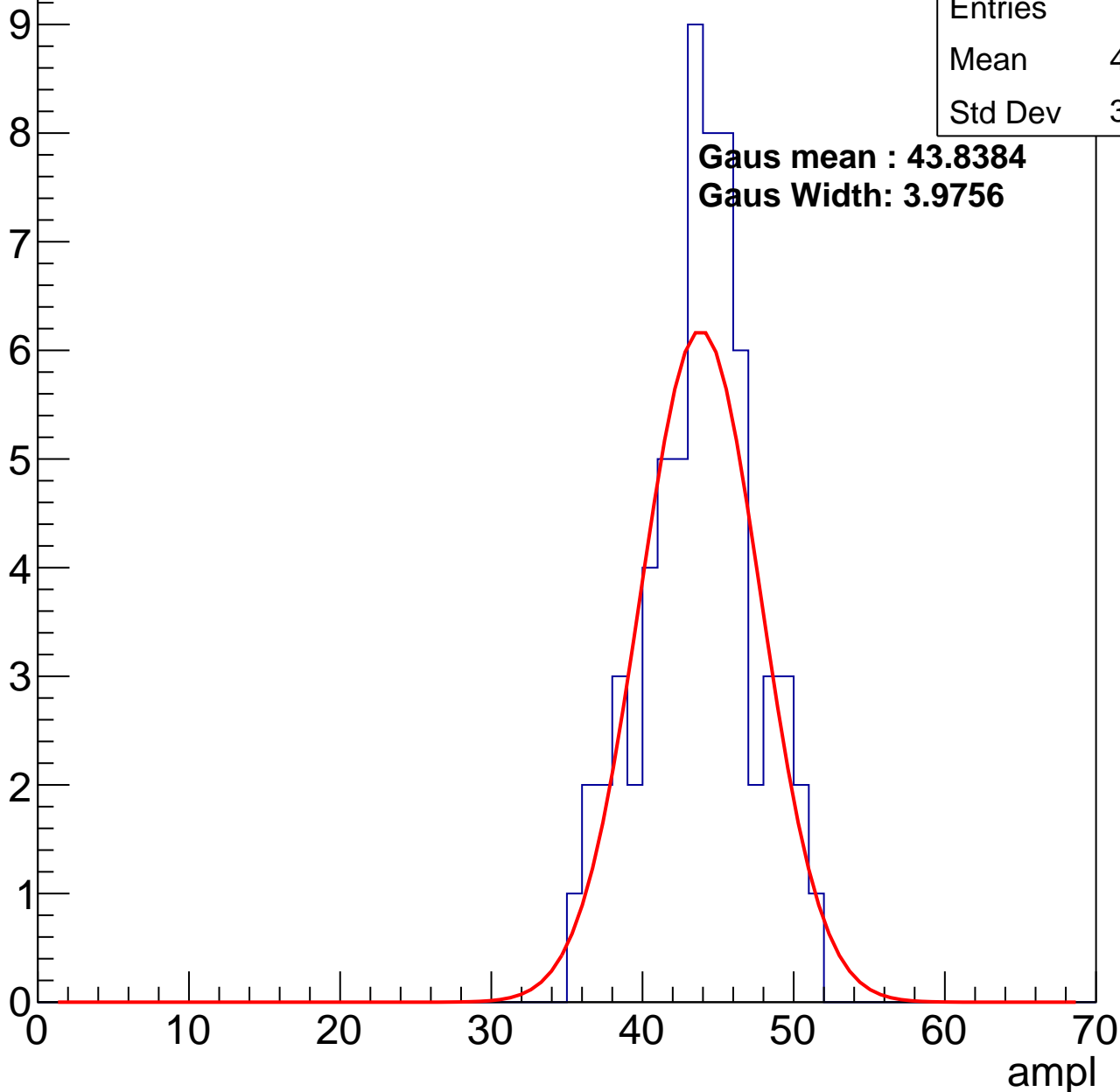
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.32
Std Dev	3.623

**Gaus mean : 43.8384**

**Gaus Width: 3.9756**

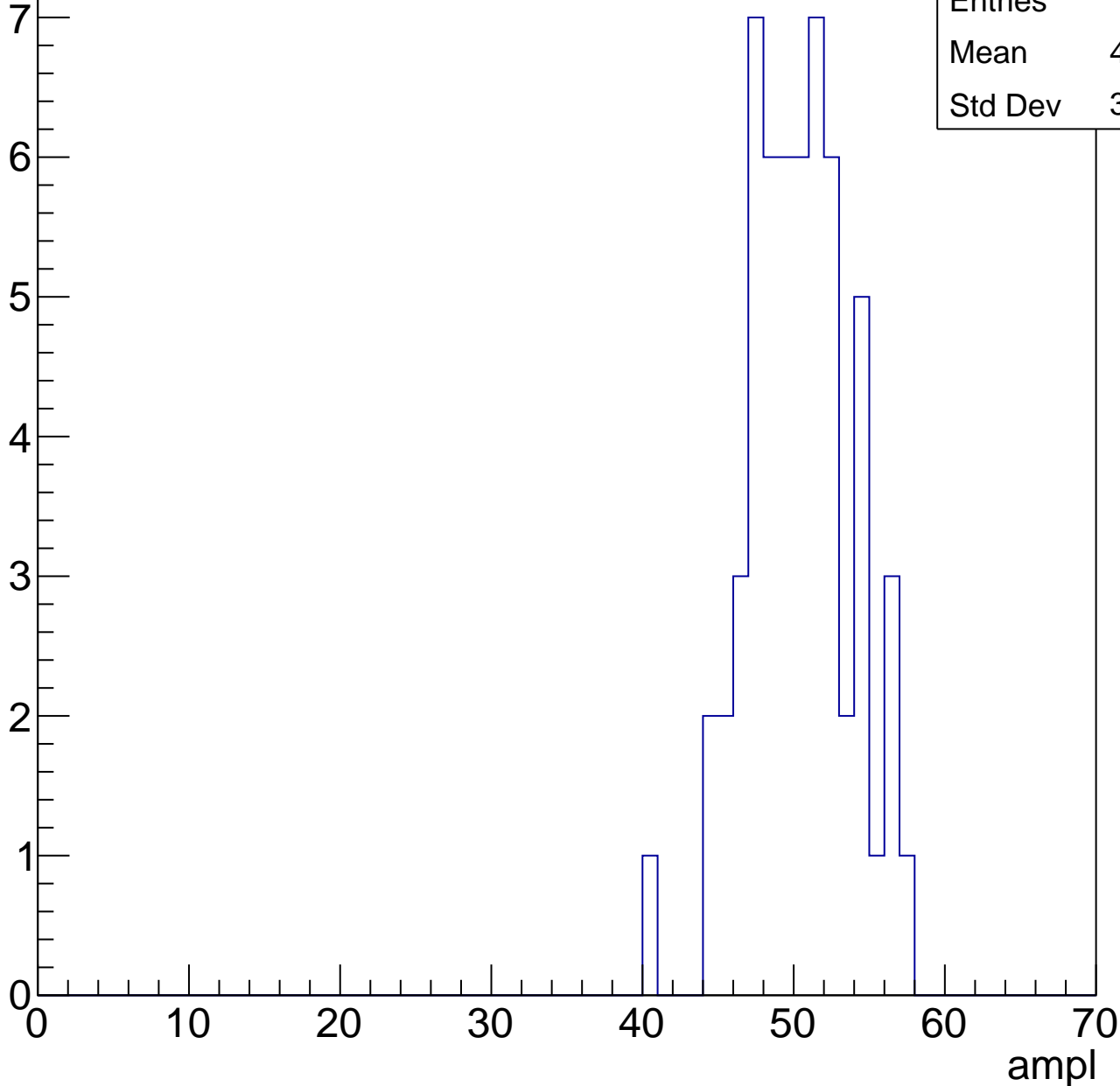


# B0L000S, U13-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

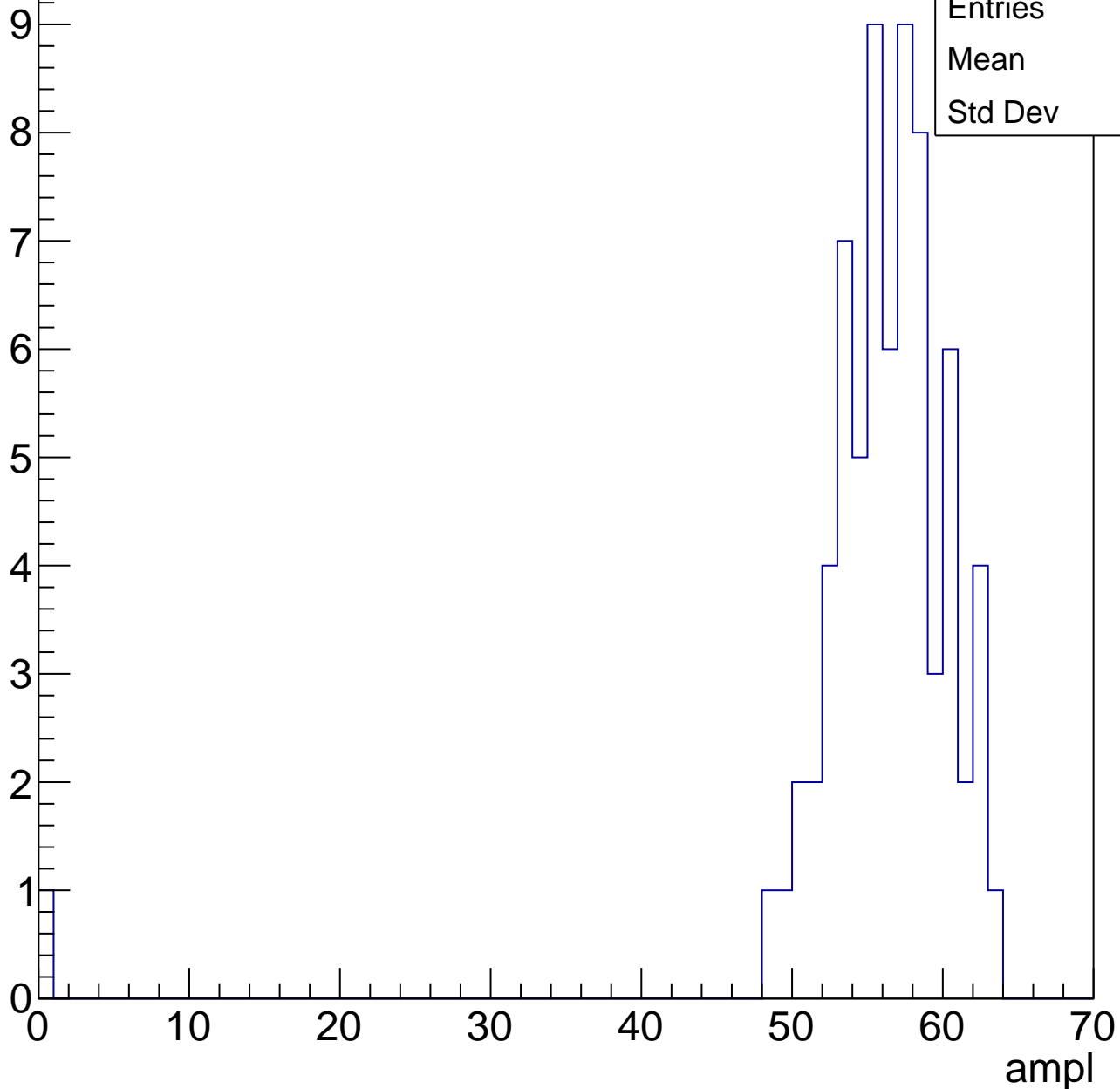
Entries	58
Mean	49.86
Std Dev	3.406



# B0L000S, U13-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



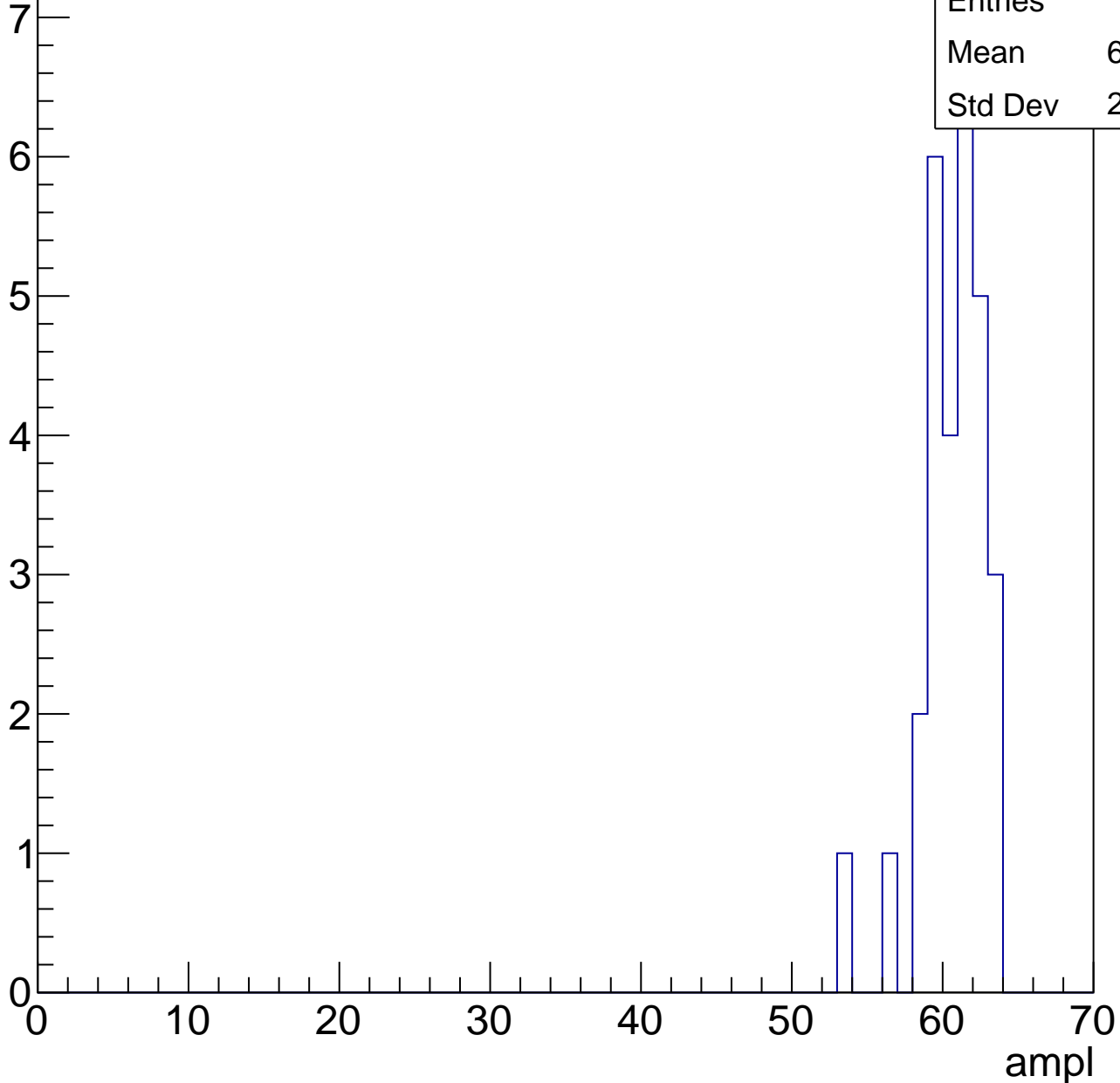
Entries	71
Mean	55.3
Std Dev	7.41

# B0L000S, U13-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

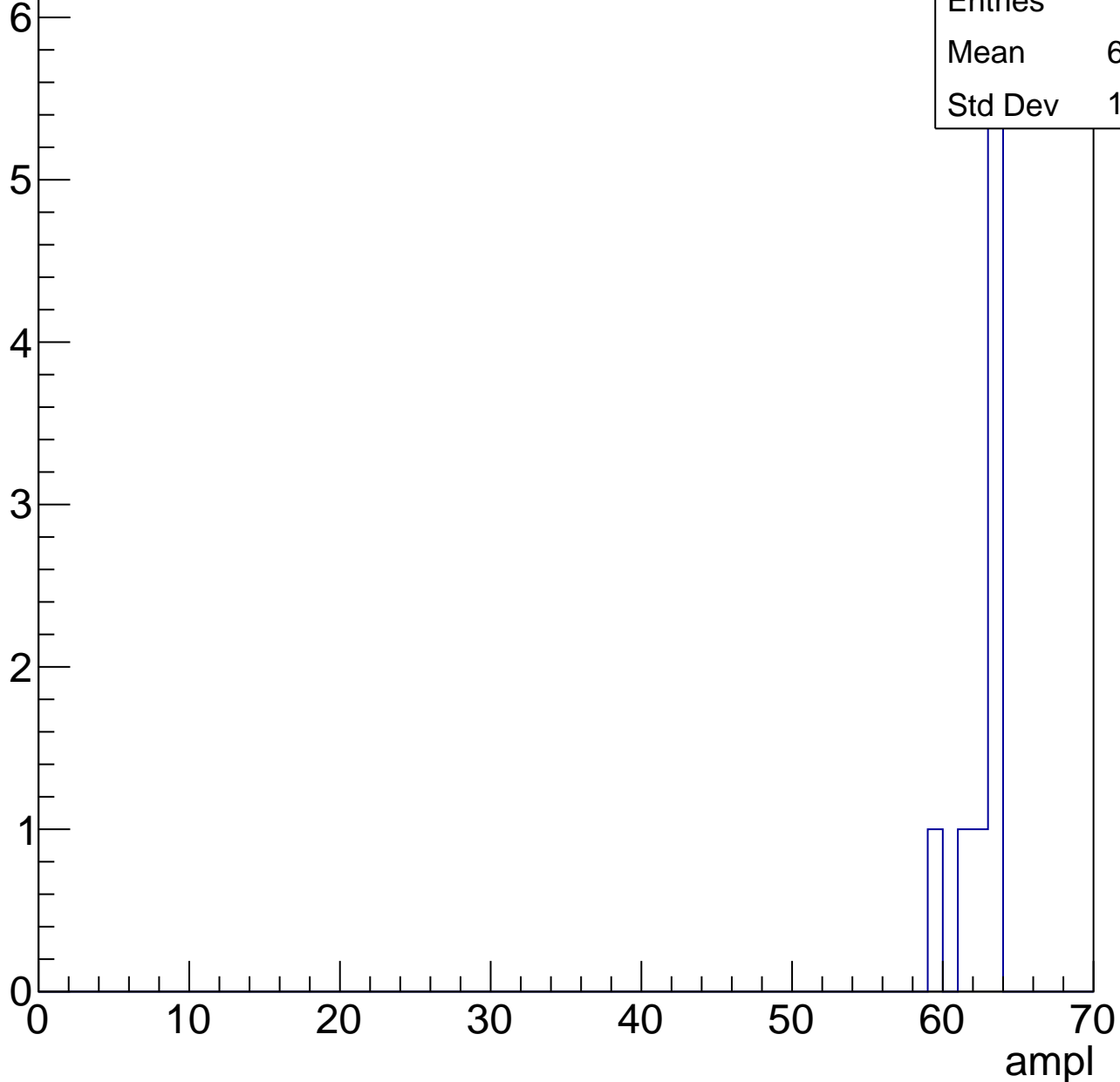
Entries	29
Mean	60.17
Std Dev	2.135



# B0L000S, U13-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	9
Mean	62.22
Std Dev	1.315



# B0L000S, U13-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch107, adc0

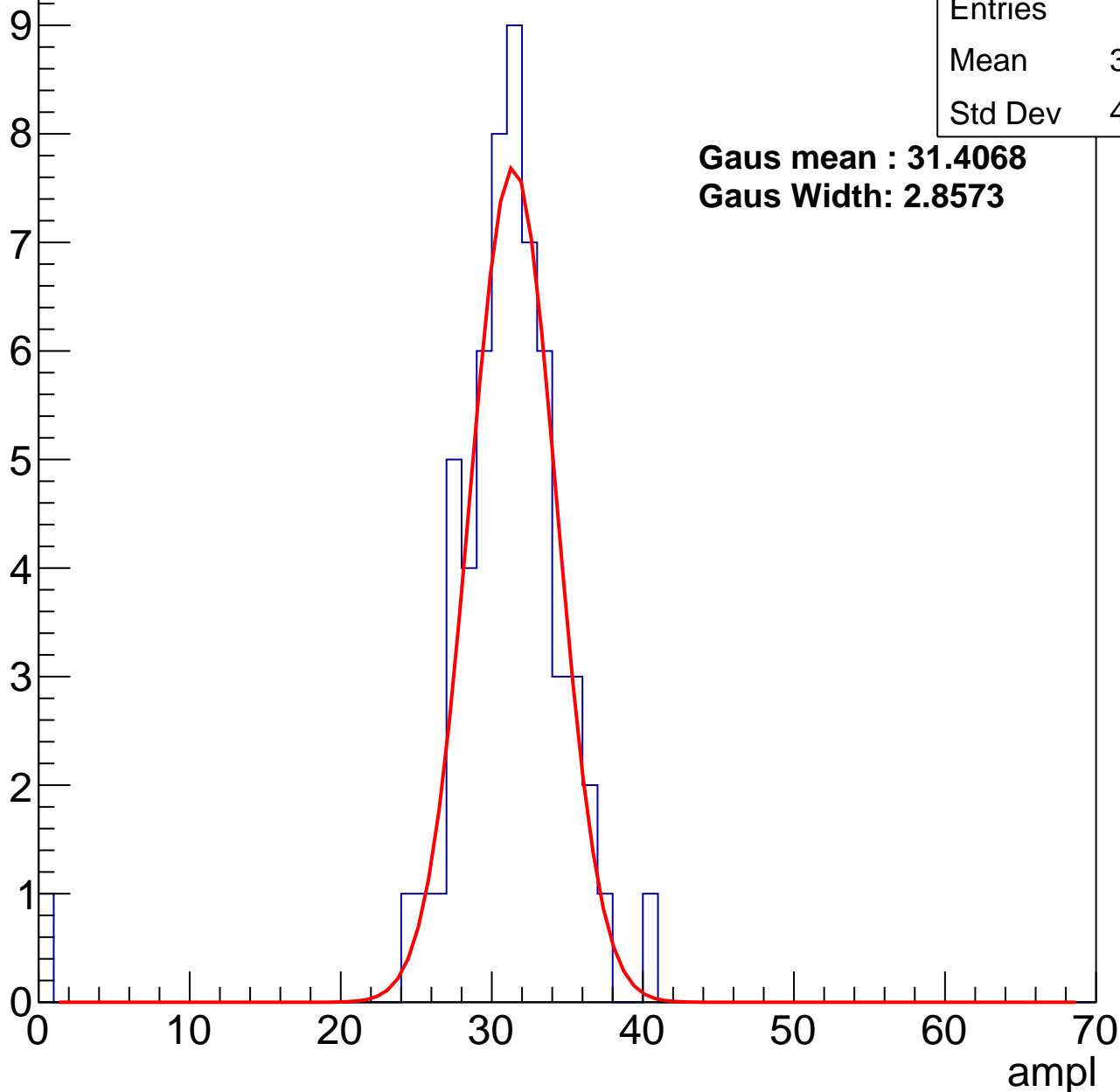
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.39
Std Dev	4.992

**Gaus mean : 31.4068**

**Gaus Width: 2.8573**



# B0L000S, U13-ch107, adc1

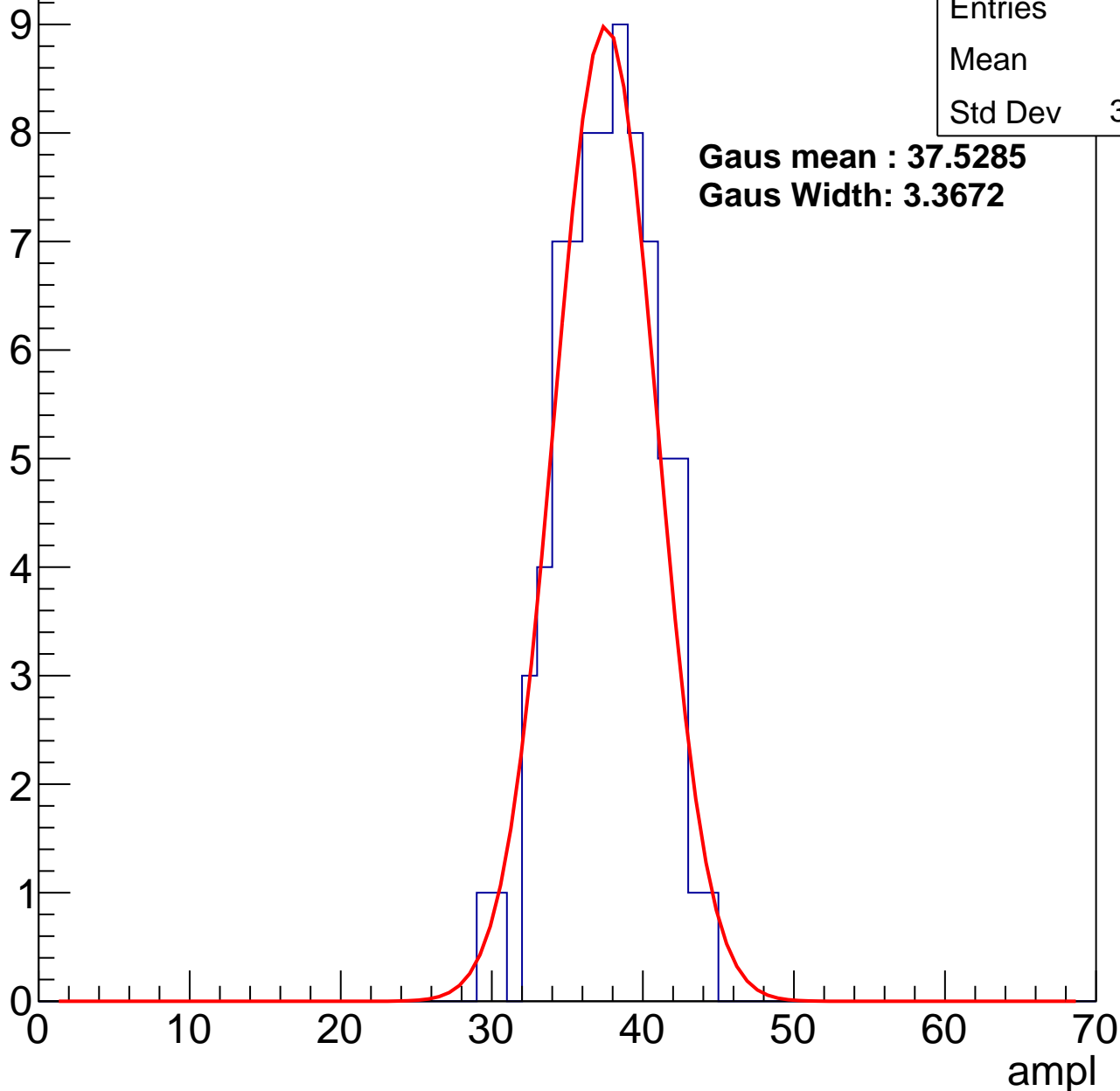
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.2
Std Dev	3.145

**Gaus mean : 37.5285**

**Gaus Width: 3.3672**



# B0L000S, U13-ch107, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.74
Std Dev	3.278

**Gaus mean : 44.8236**

**Gaus Width: 2.7101**

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

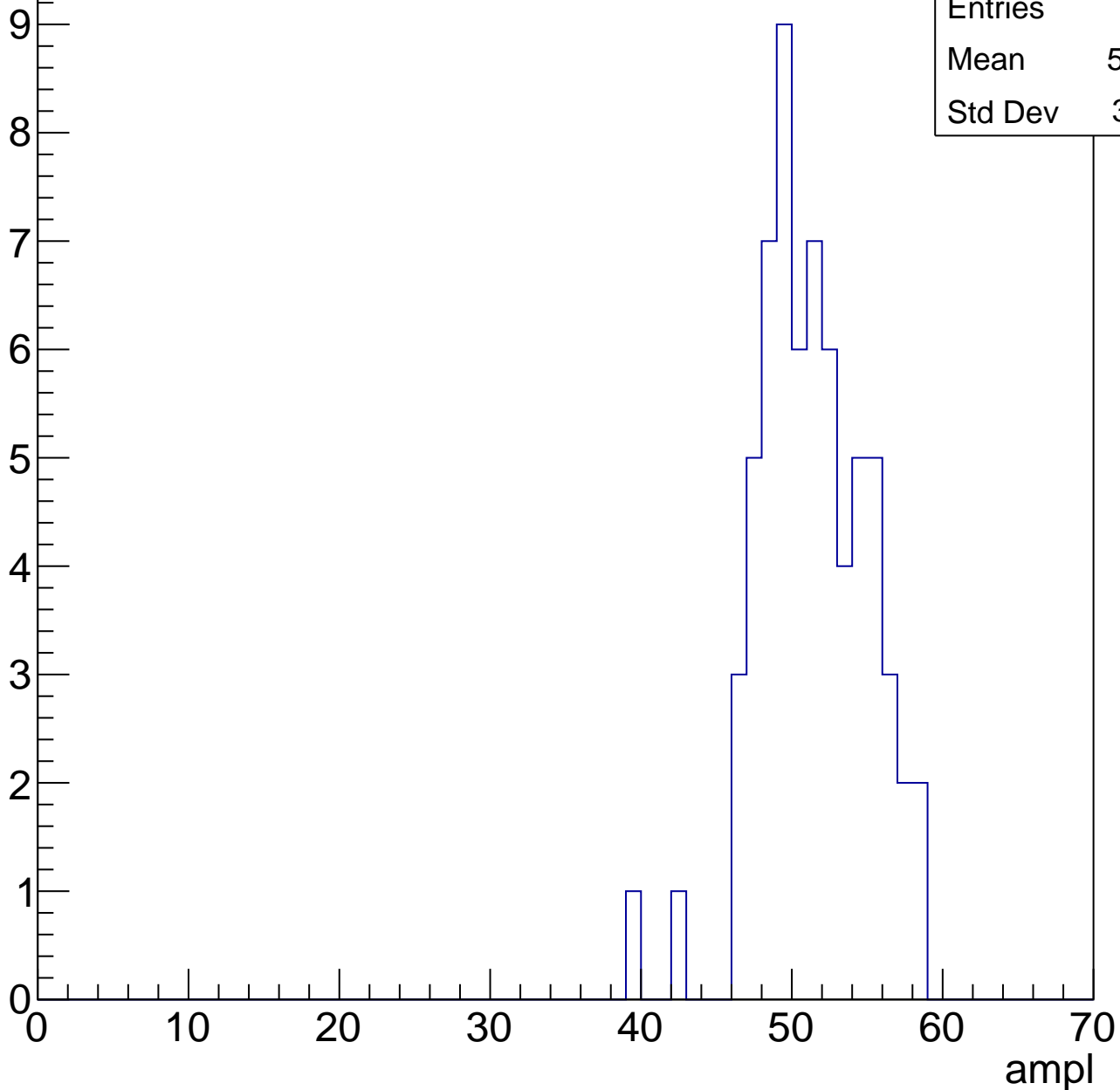
10

# B0L000S, U13-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.83
Std Dev	3.641

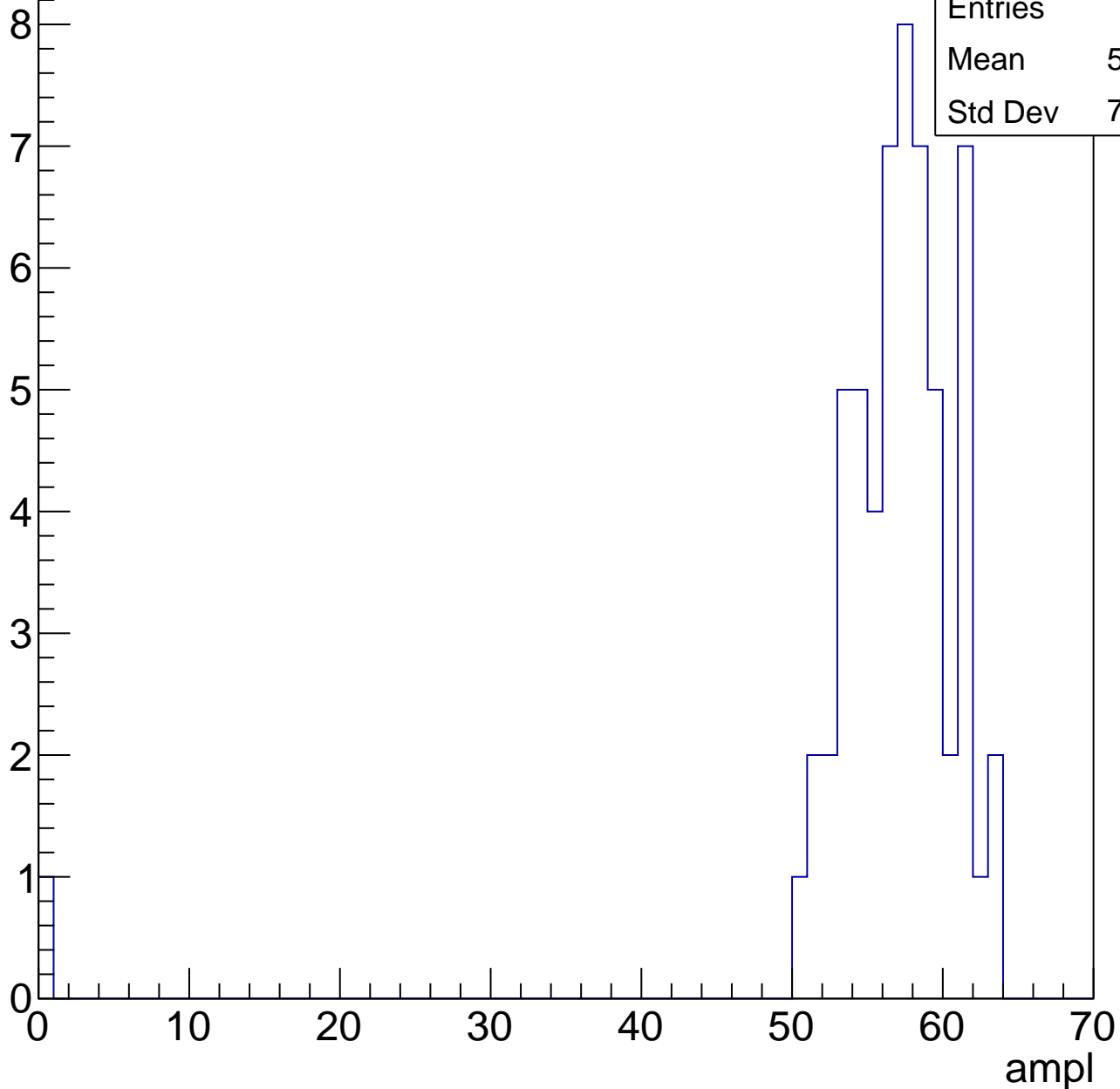


# B0L000S, U13-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

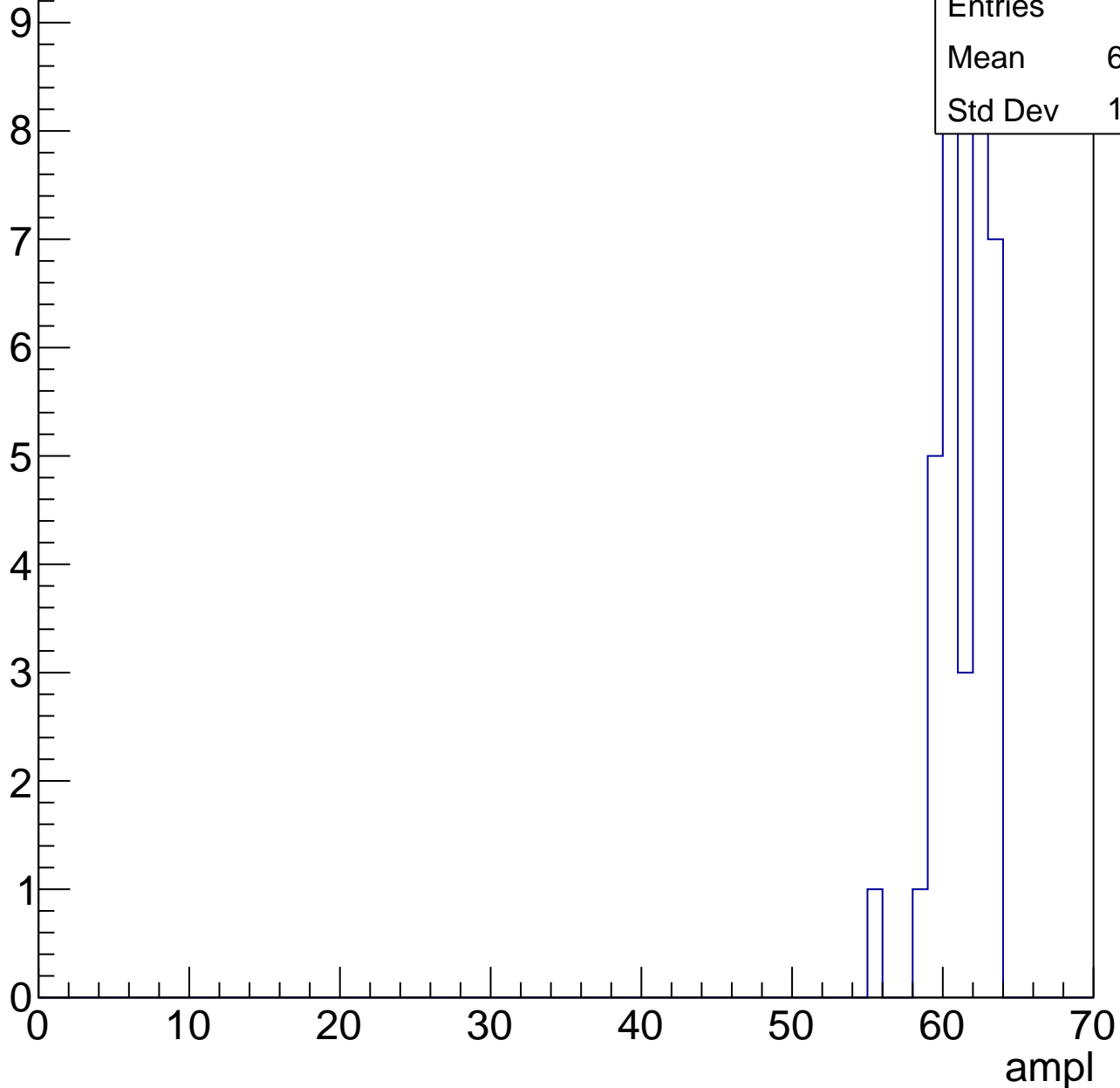
Entries	59
Mean	55.85
Std Dev	7.966



# B0L000S, U13-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

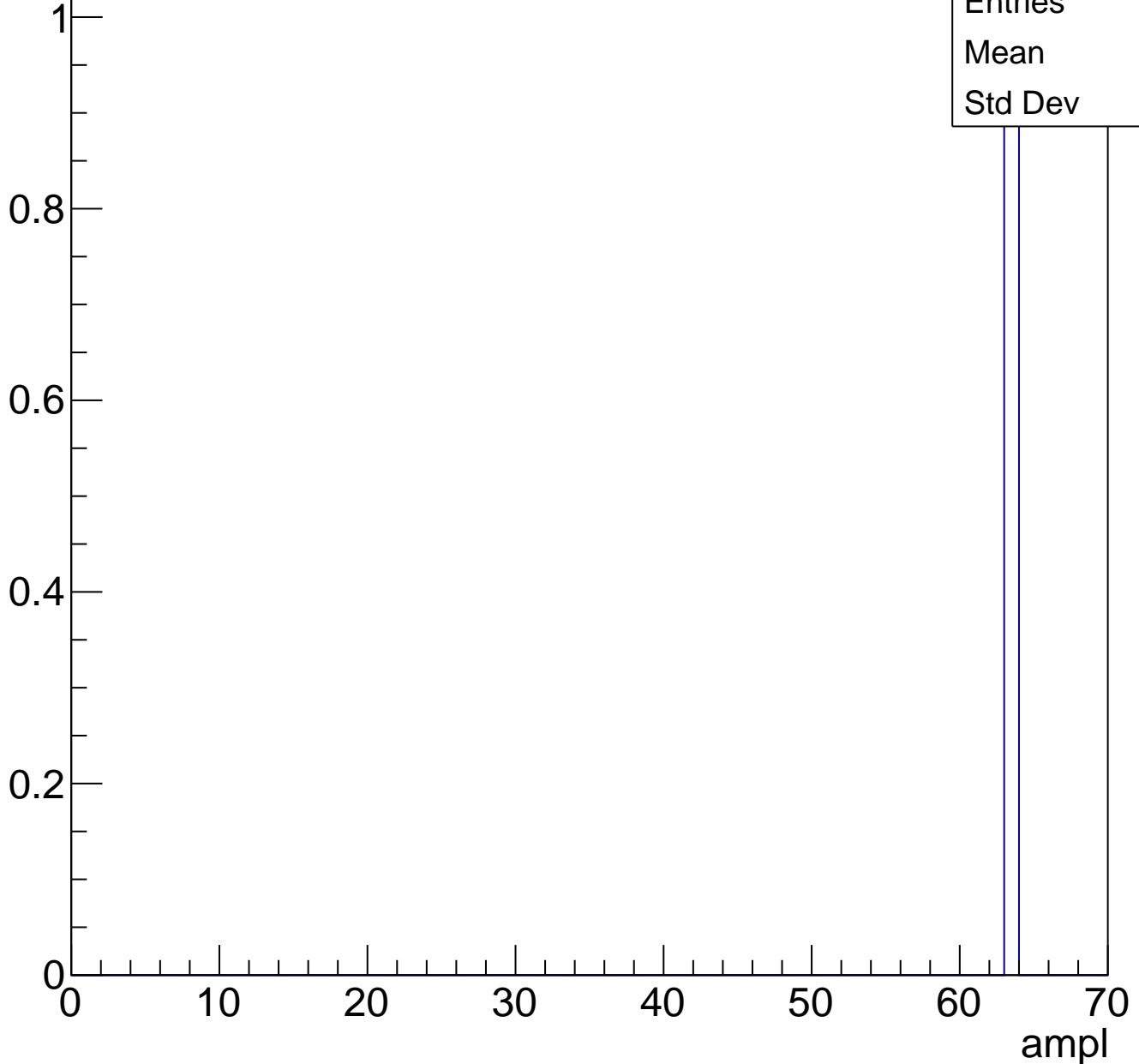
Entry



# B0L000S, U13-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch108, adc0

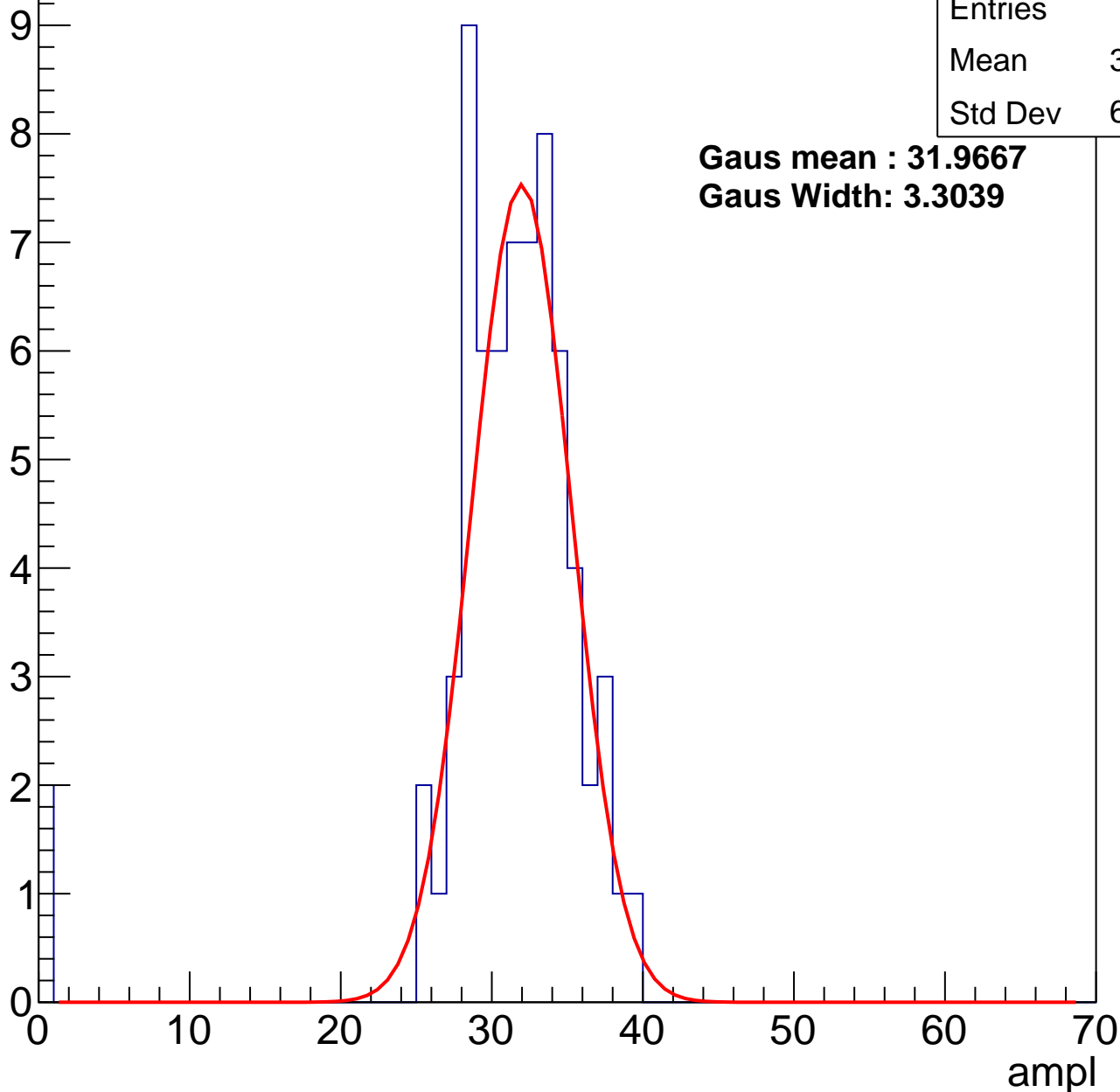
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.47
Std Dev	6.168

**Gaus mean : 31.9667**

**Gaus Width: 3.3039**



# B0L000S, U13-ch108, adc1

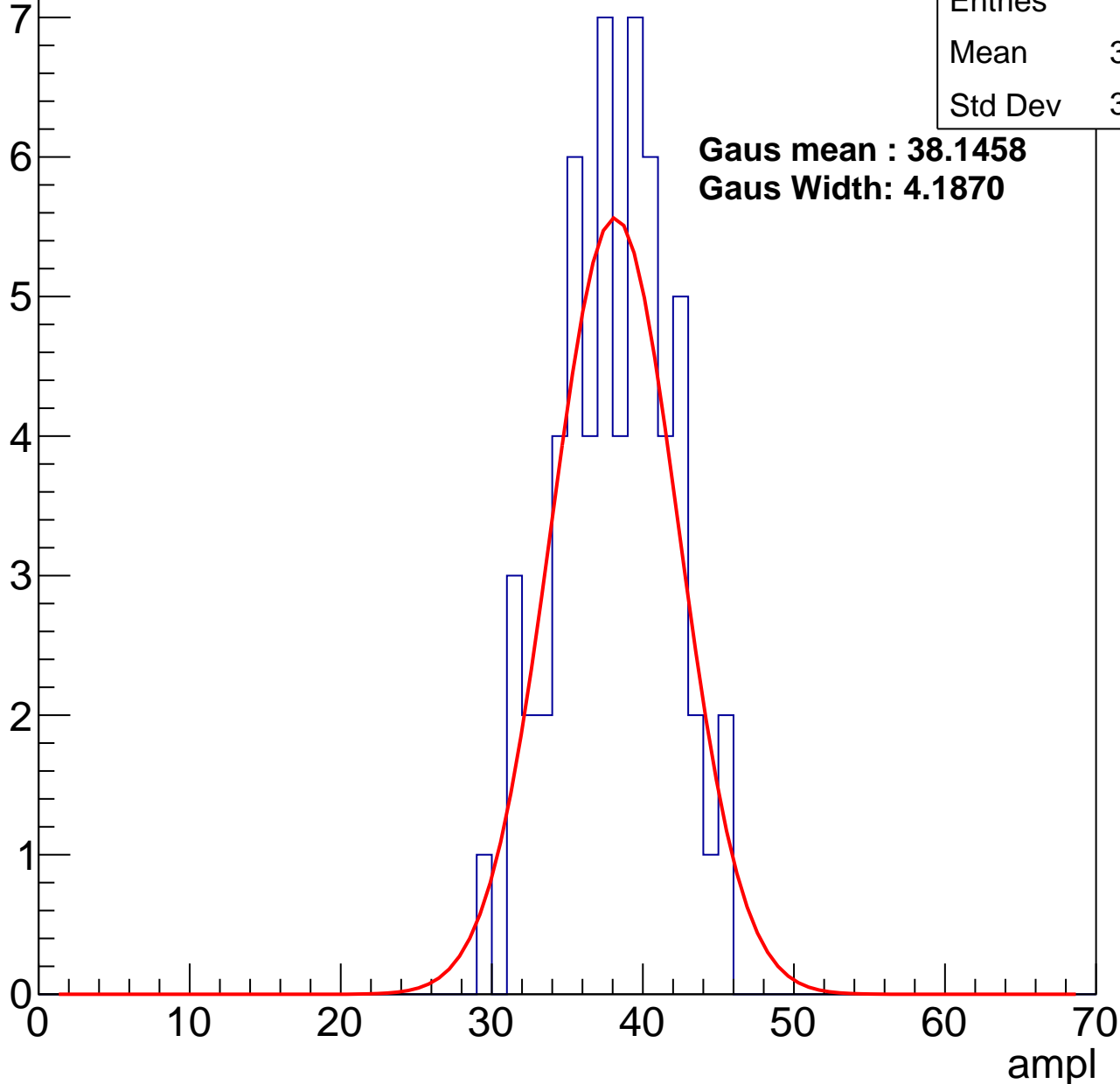
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	37.67
Std Dev	3.686

**Gaus mean : 38.1458**

**Gaus Width: 4.1870**



# B0L000S, U13-ch108, adc2

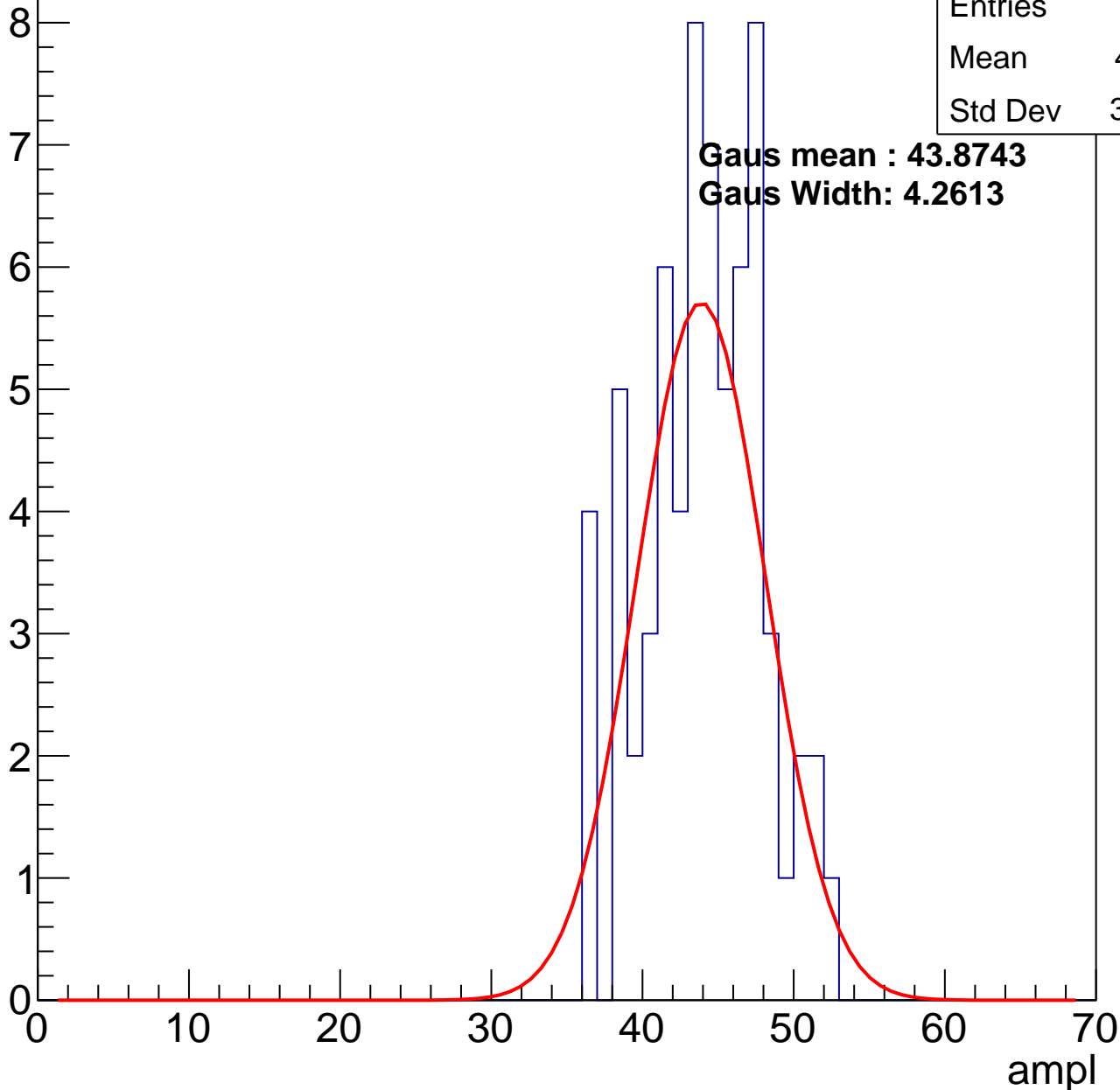
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.61
Std Dev	3.882

**Gaus mean : 43.8743**

**Gaus Width: 4.2613**

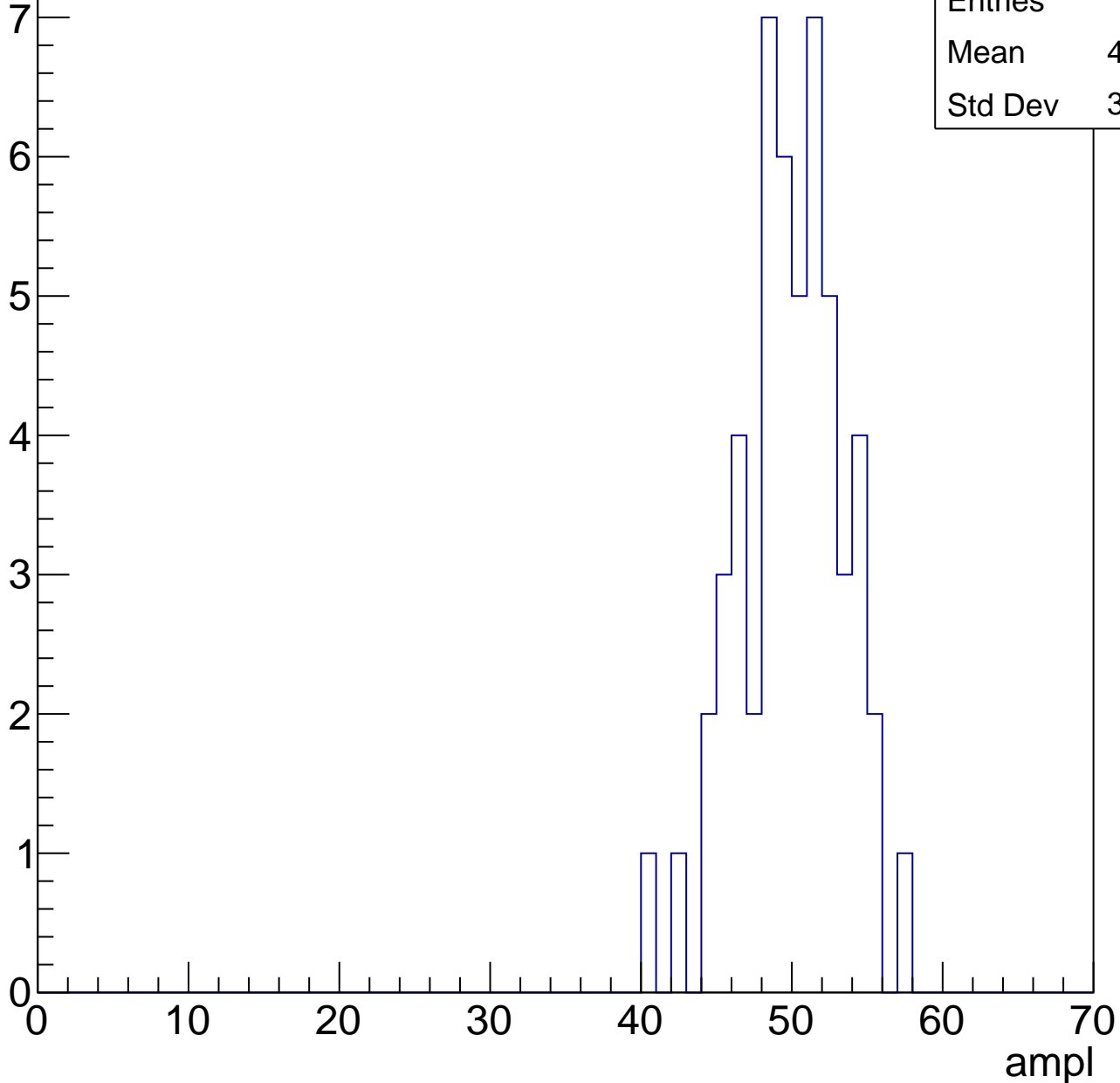


# B0L000S, U13-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	49.47
Std Dev	3.446

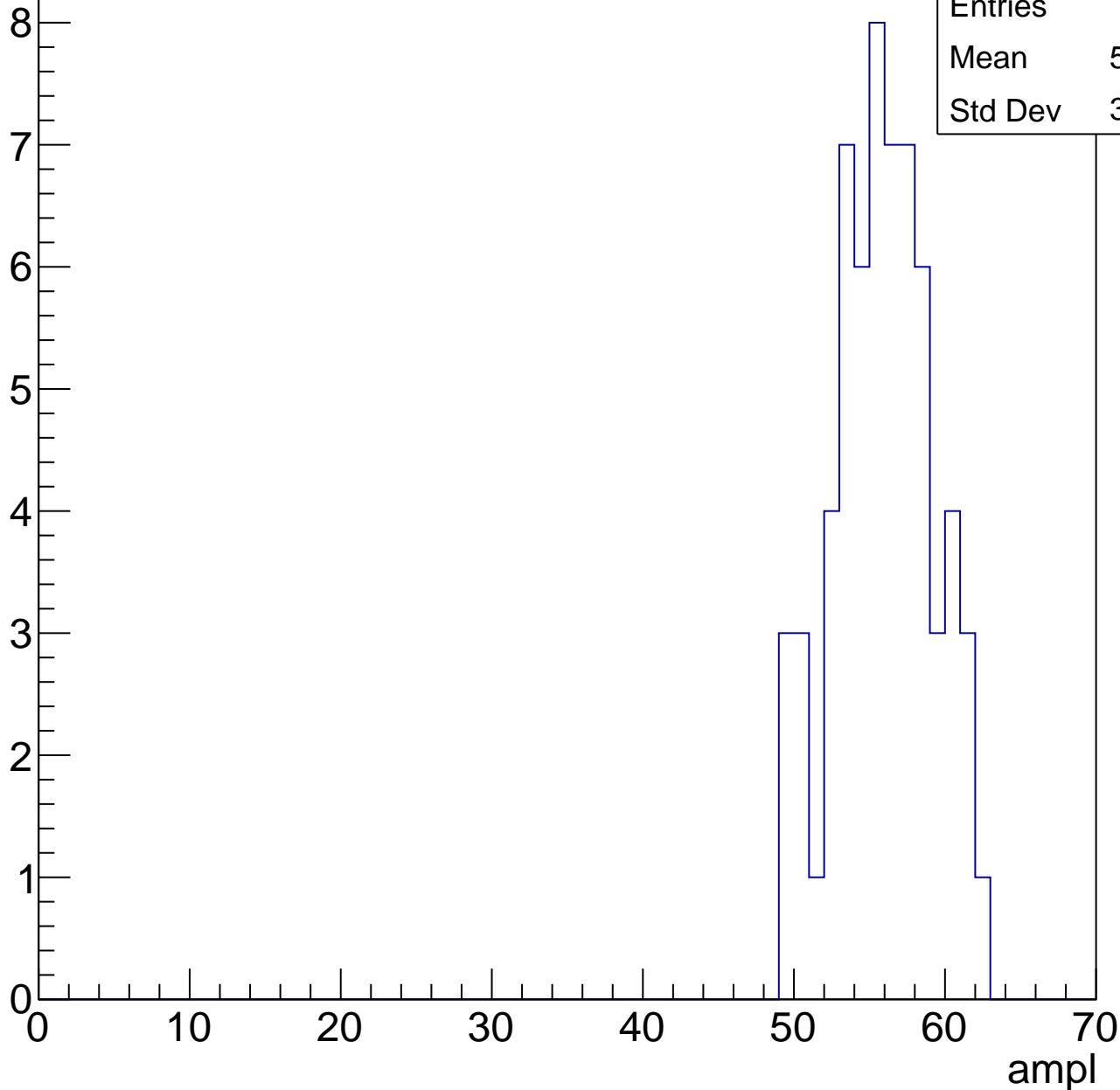


# B0L000S, U13-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	55.43
Std Dev	3.216

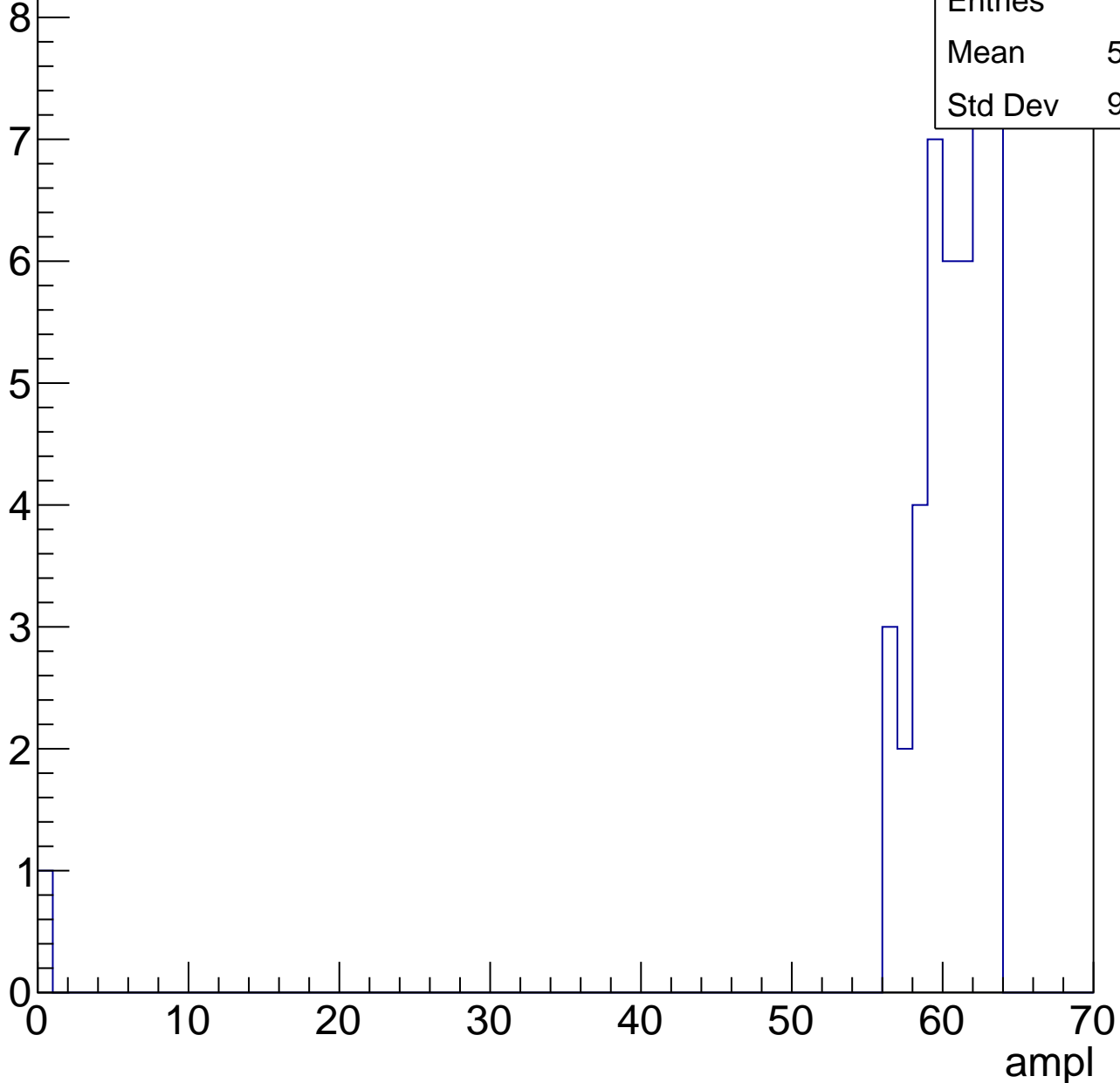


# B0L000S, U13-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.96
Std Dev	9.129



# B0L000S, U13-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

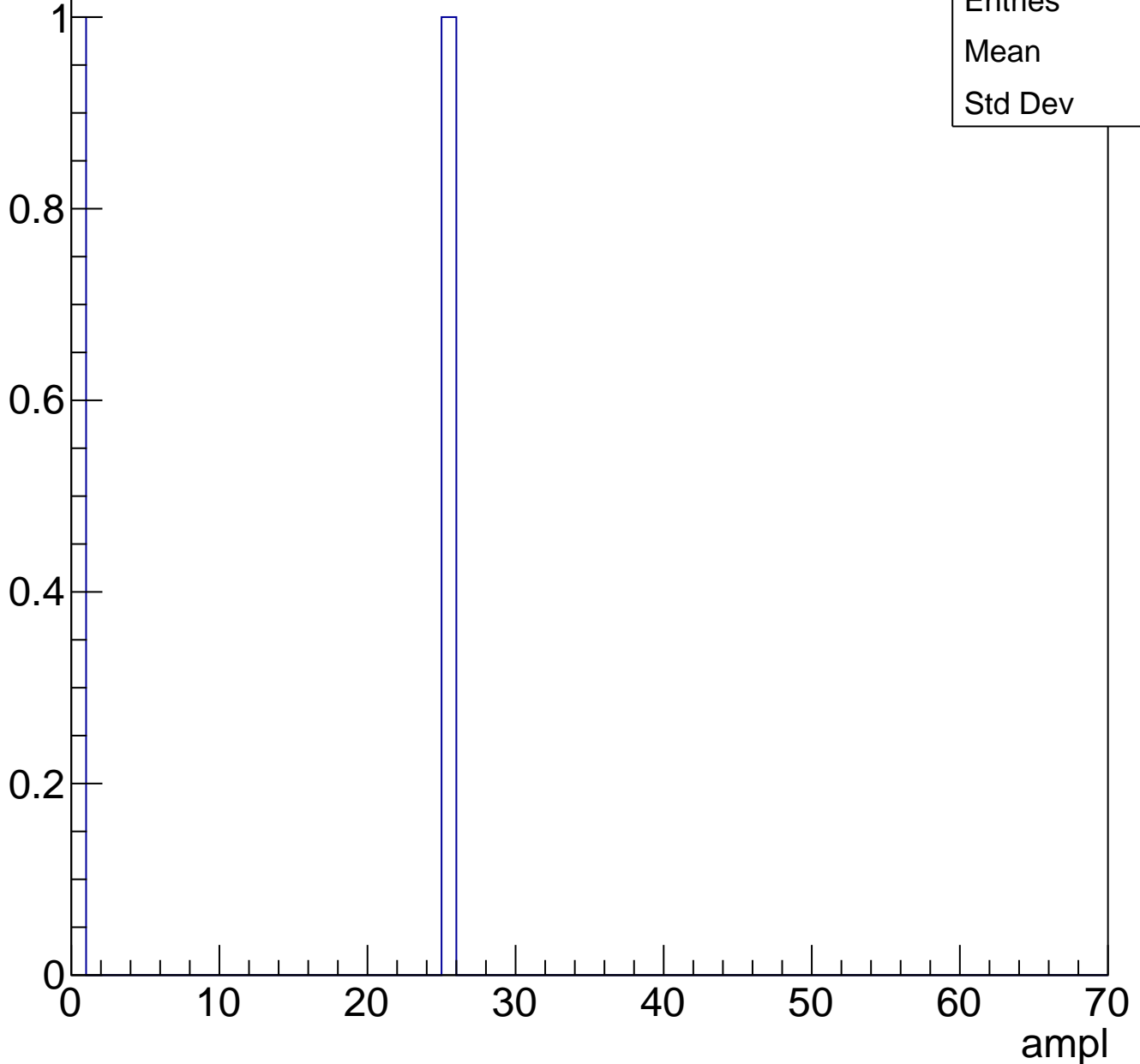
0 10 20 30 40 50 60 70



# B0L000S, U13-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	12.5
Std Dev	12.5

# B0L000S, U13-ch109, adc0

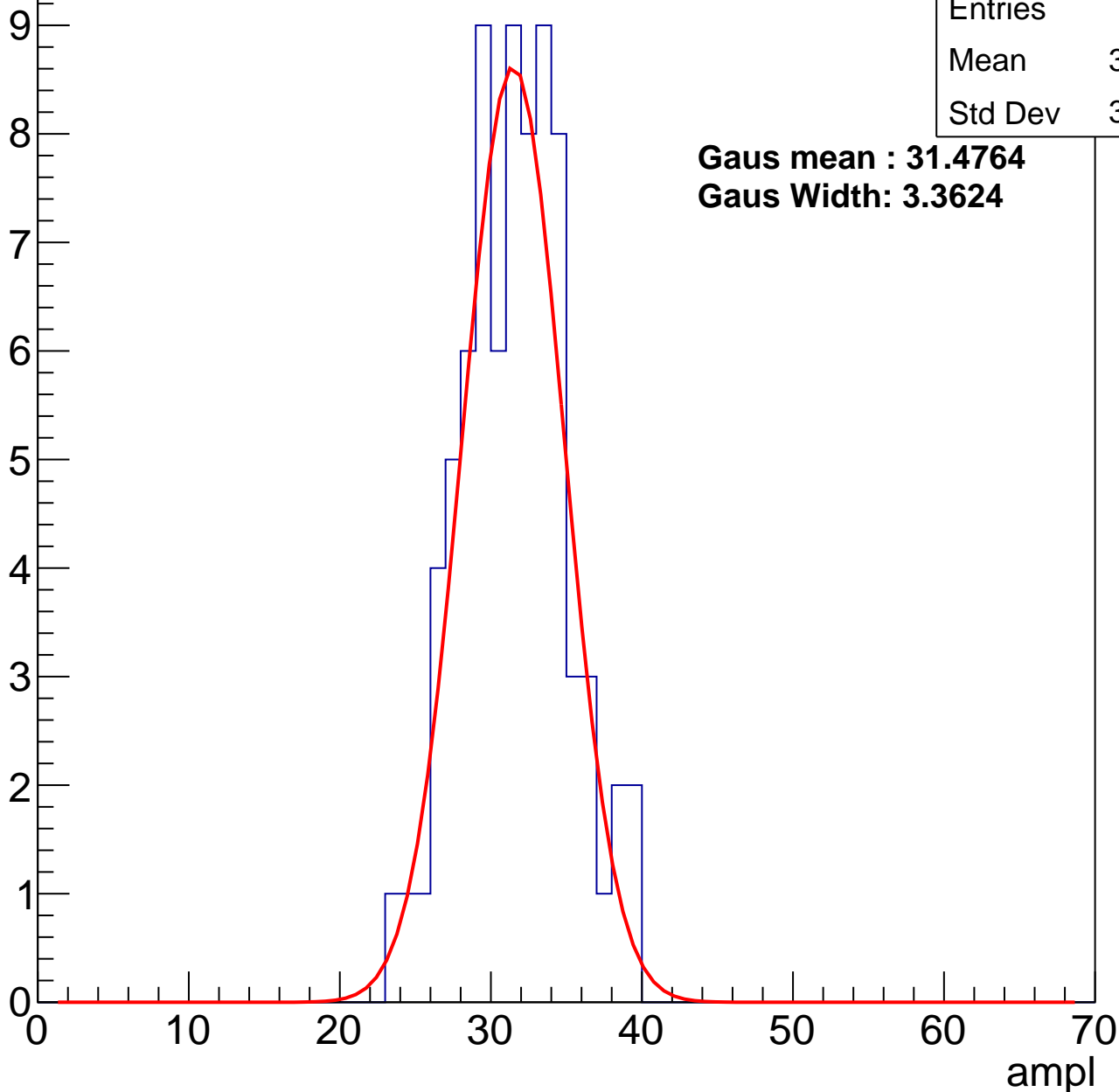
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	31.13
Std Dev	3.443

**Gaus mean : 31.4764**

**Gaus Width: 3.3624**



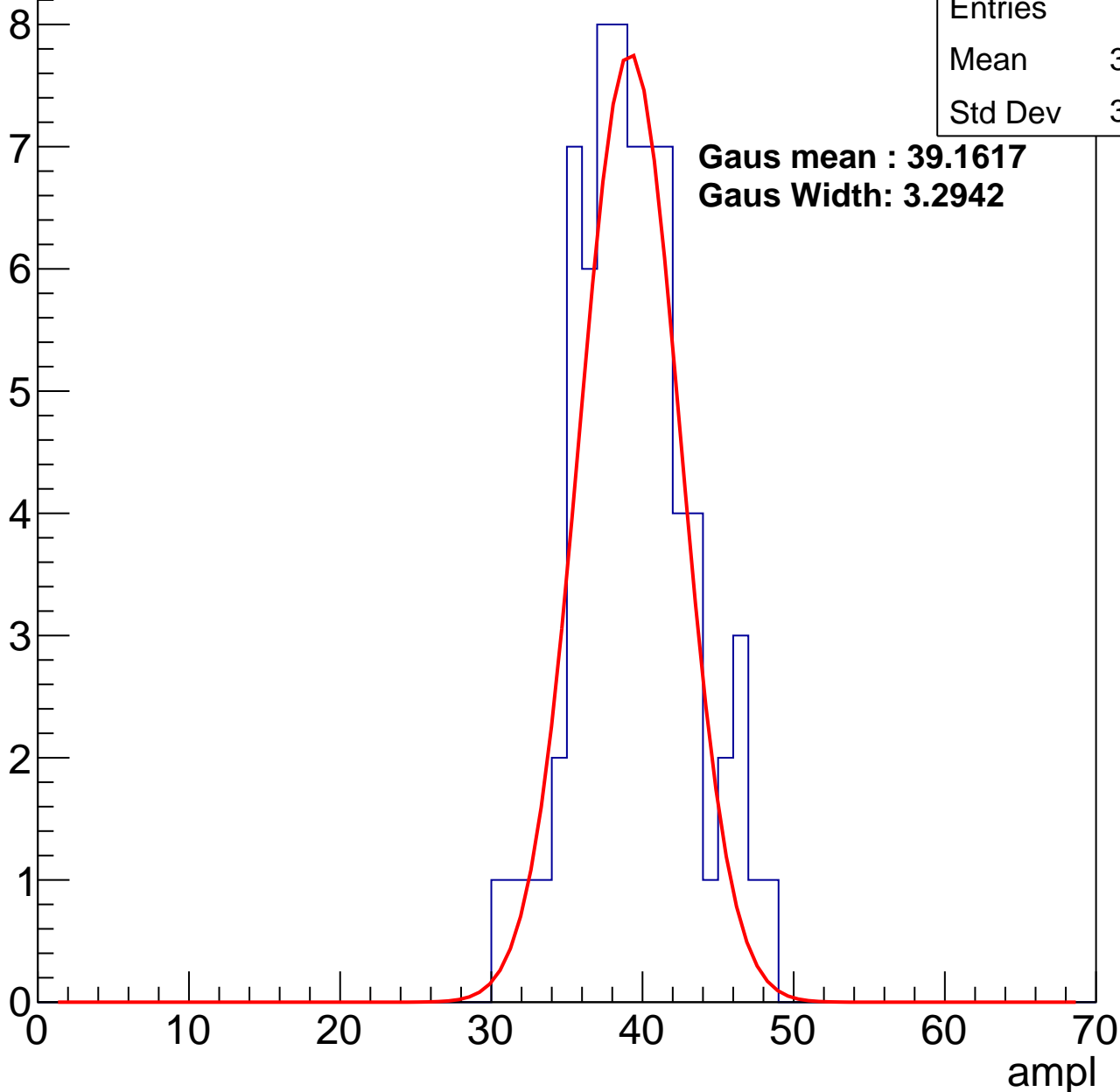
# B0L000S, U13-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	38.92
Std Dev	3.744

**Gaus mean : 39.1617**  
**Gaus Width: 3.2942**



# B0L000S, U13-ch109, adc2

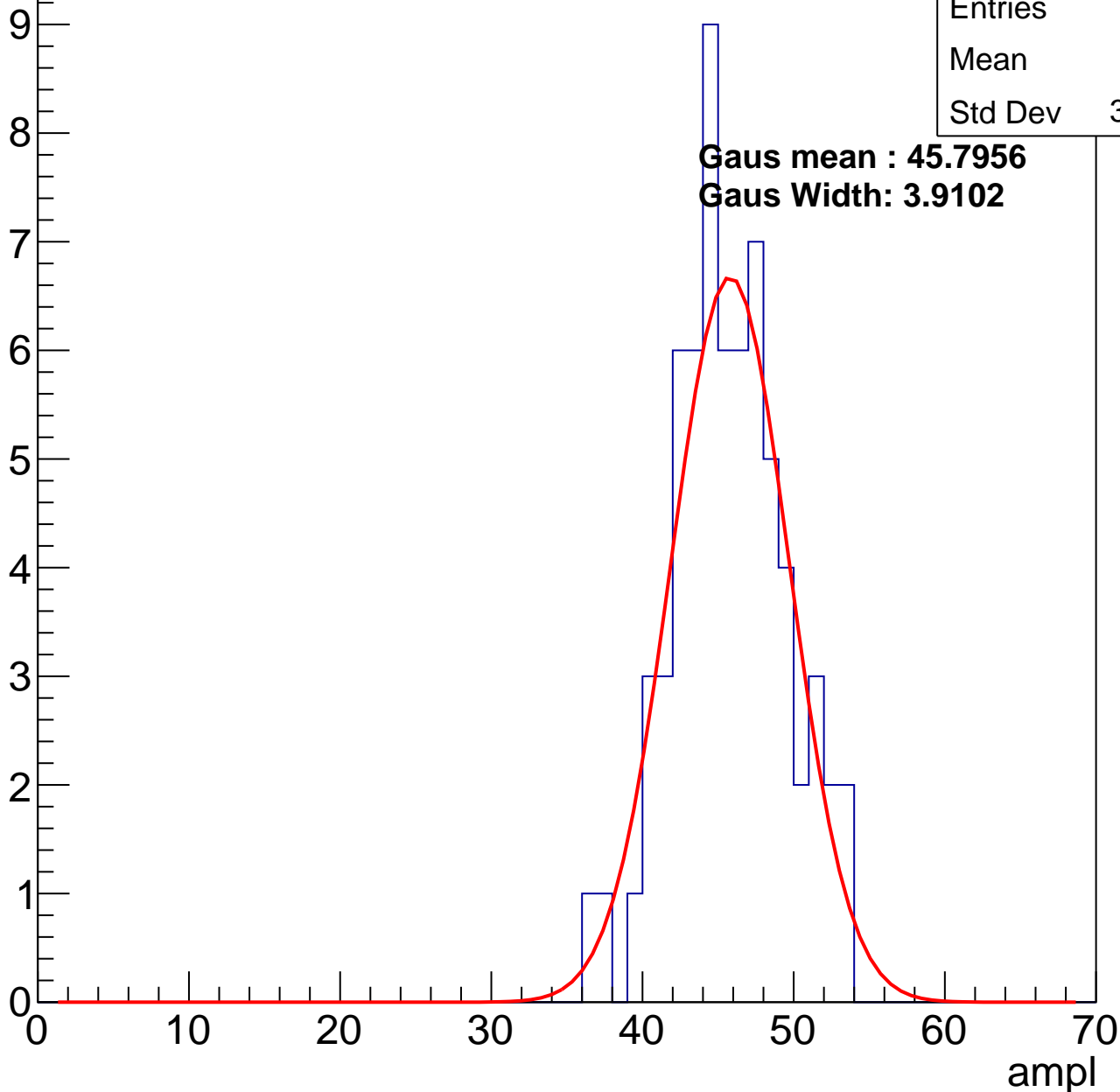
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	45.3
Std Dev	3.689

**Gaus mean : 45.7956**

**Gaus Width: 3.9102**

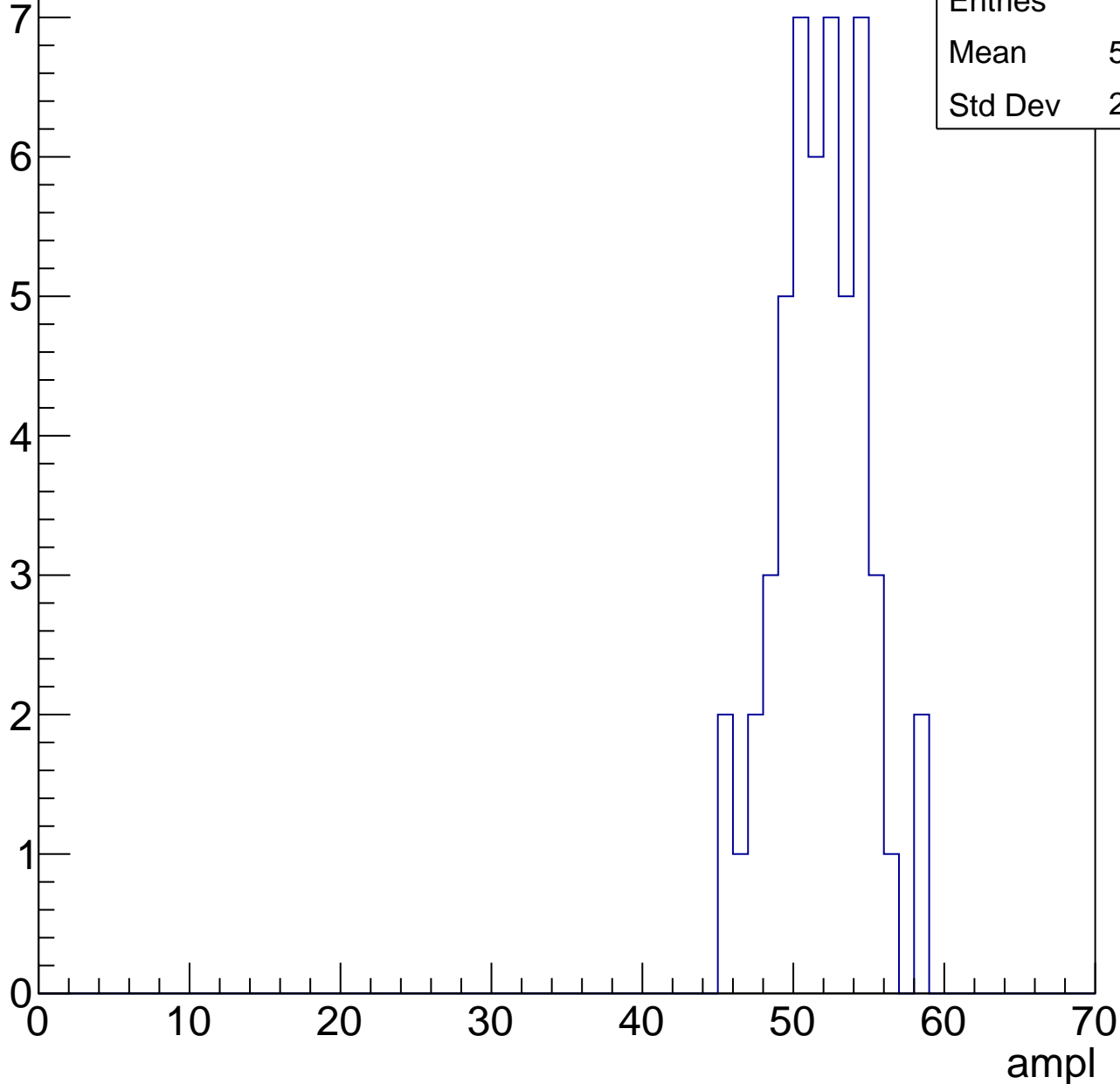


# B0L000S, U13-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	51.35
Std Dev	2.929



# B0L000S, U13-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	57.15
Std Dev	3.22

Entry

10

8

6

4

2

0

0

10

20

30

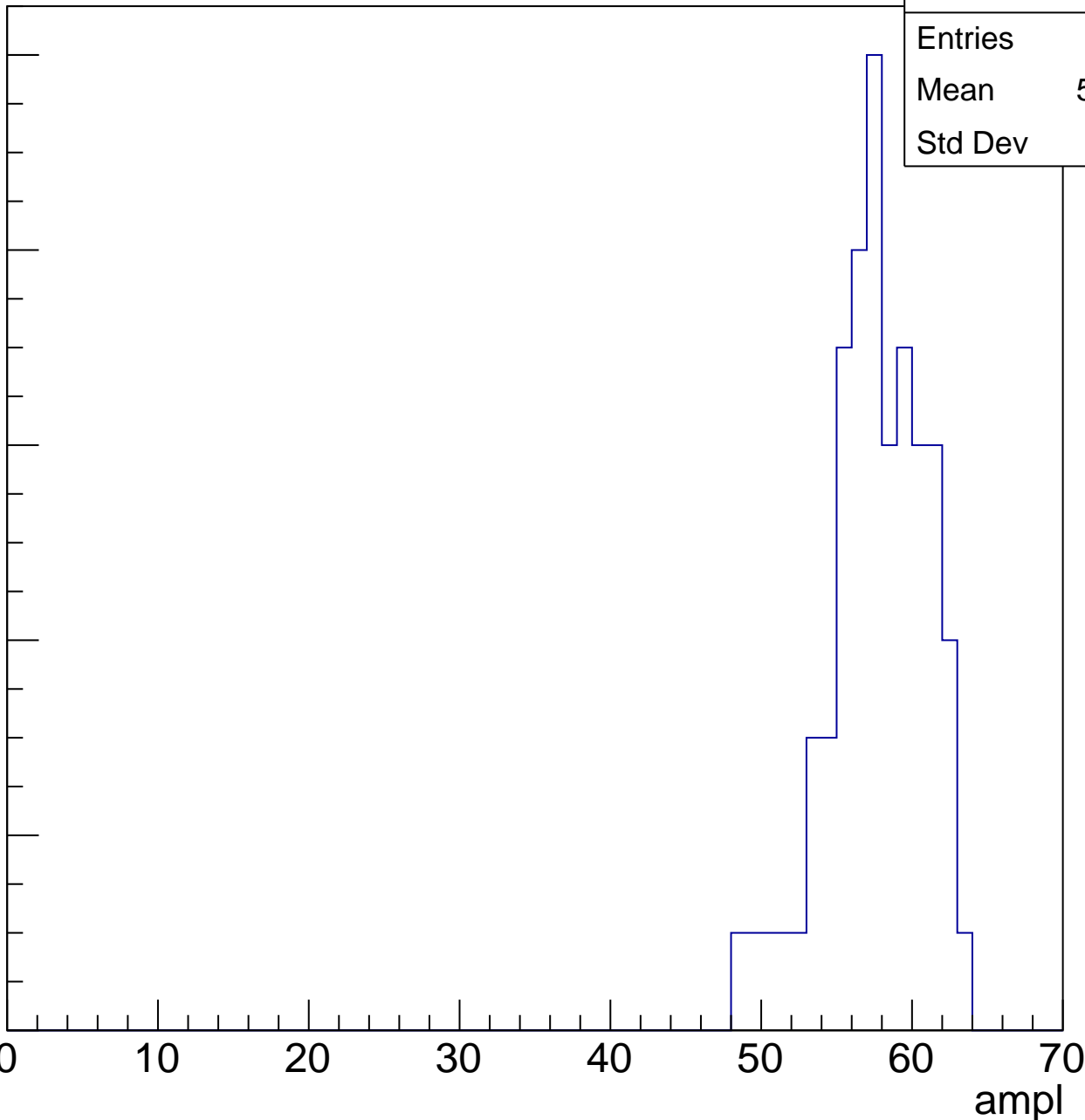
40

50

60

70

ampl

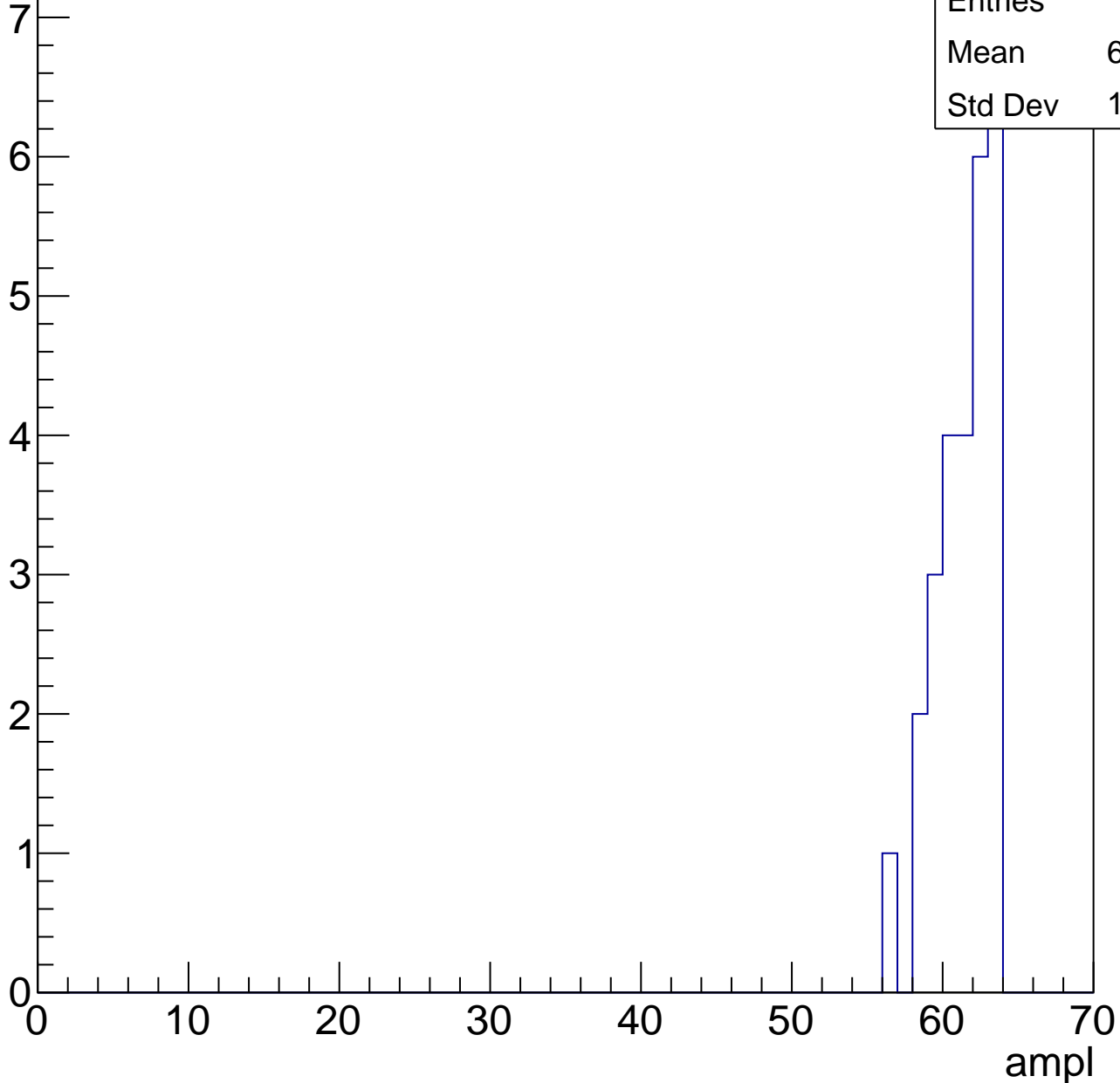


# B0L000S, U13-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	60.96
Std Dev	1.856



# B0L000S, U13-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U13-ch110, adc0

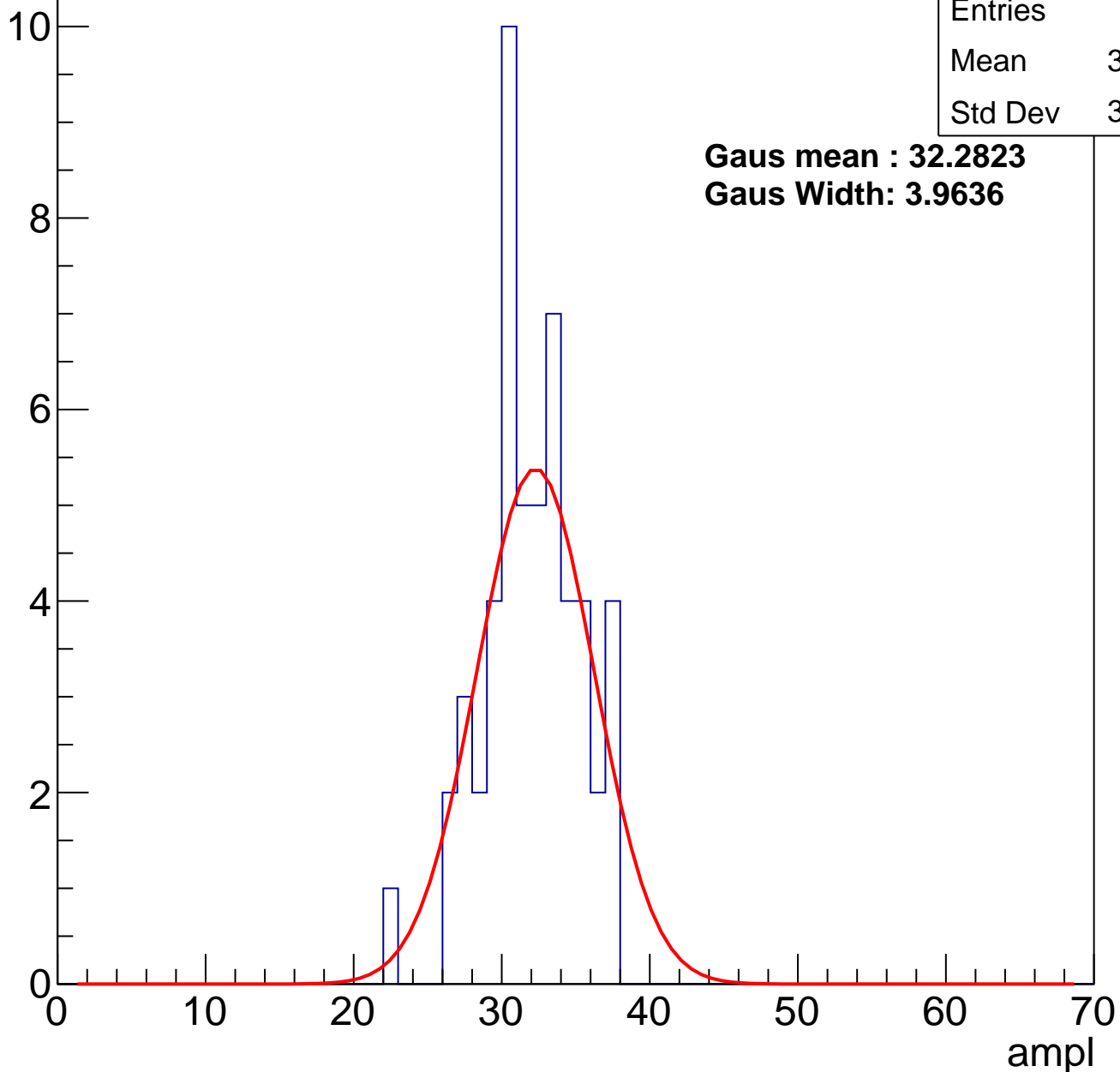
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	53
Mean	31.49
Std Dev	3.184

**Gaus mean : 32.2823**

**Gaus Width: 3.9636**

Entry



# B0L000S, U13-ch110, adc1

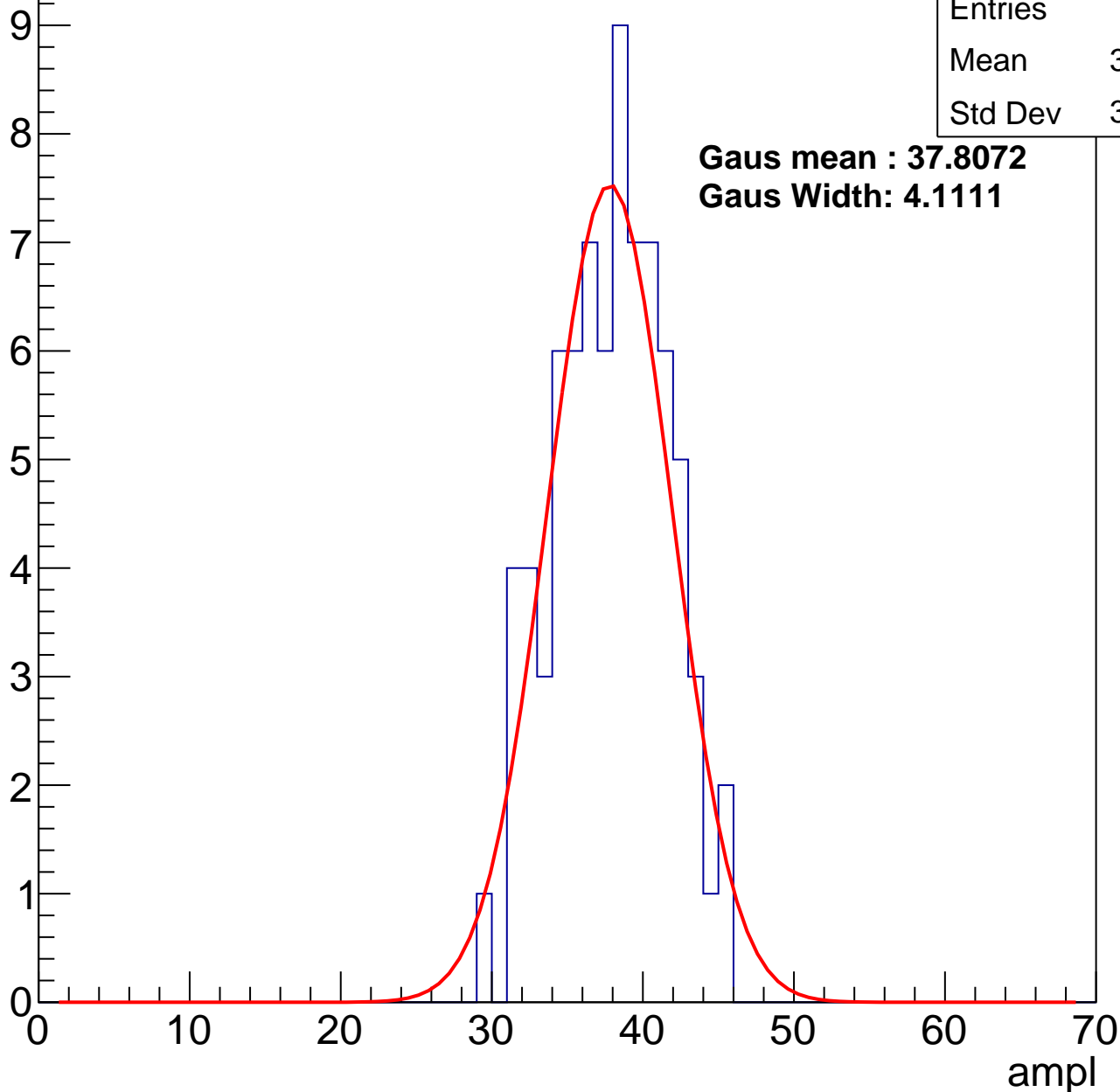
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	37.43
Std Dev	3.662

**Gaus mean : 37.8072**

**Gaus Width: 4.1111**



# B0L000S, U13-ch110, adc2

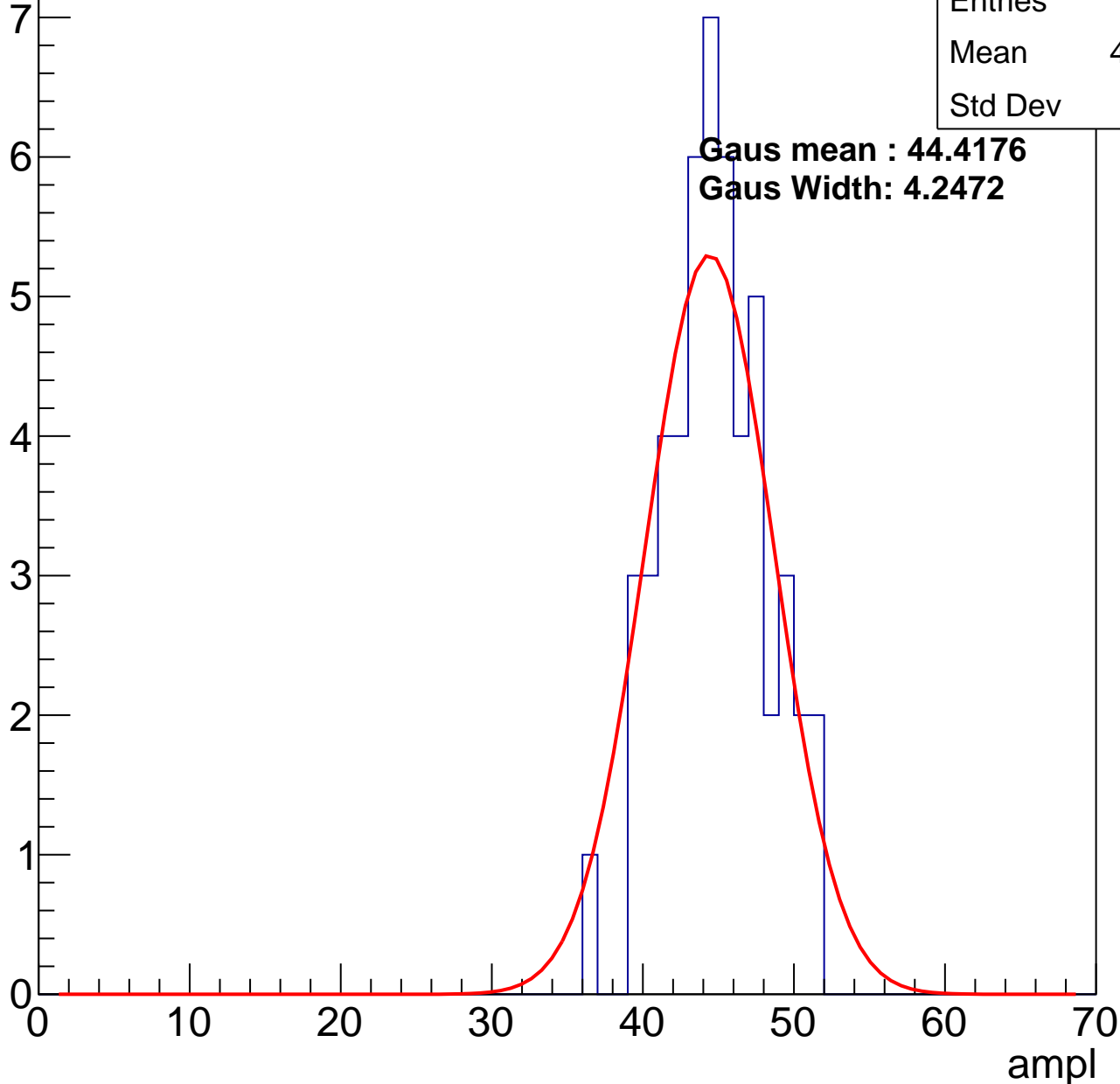
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.33
Std Dev	3.35

**Gaus mean : 44.4176**

**Gaus Width: 4.2472**

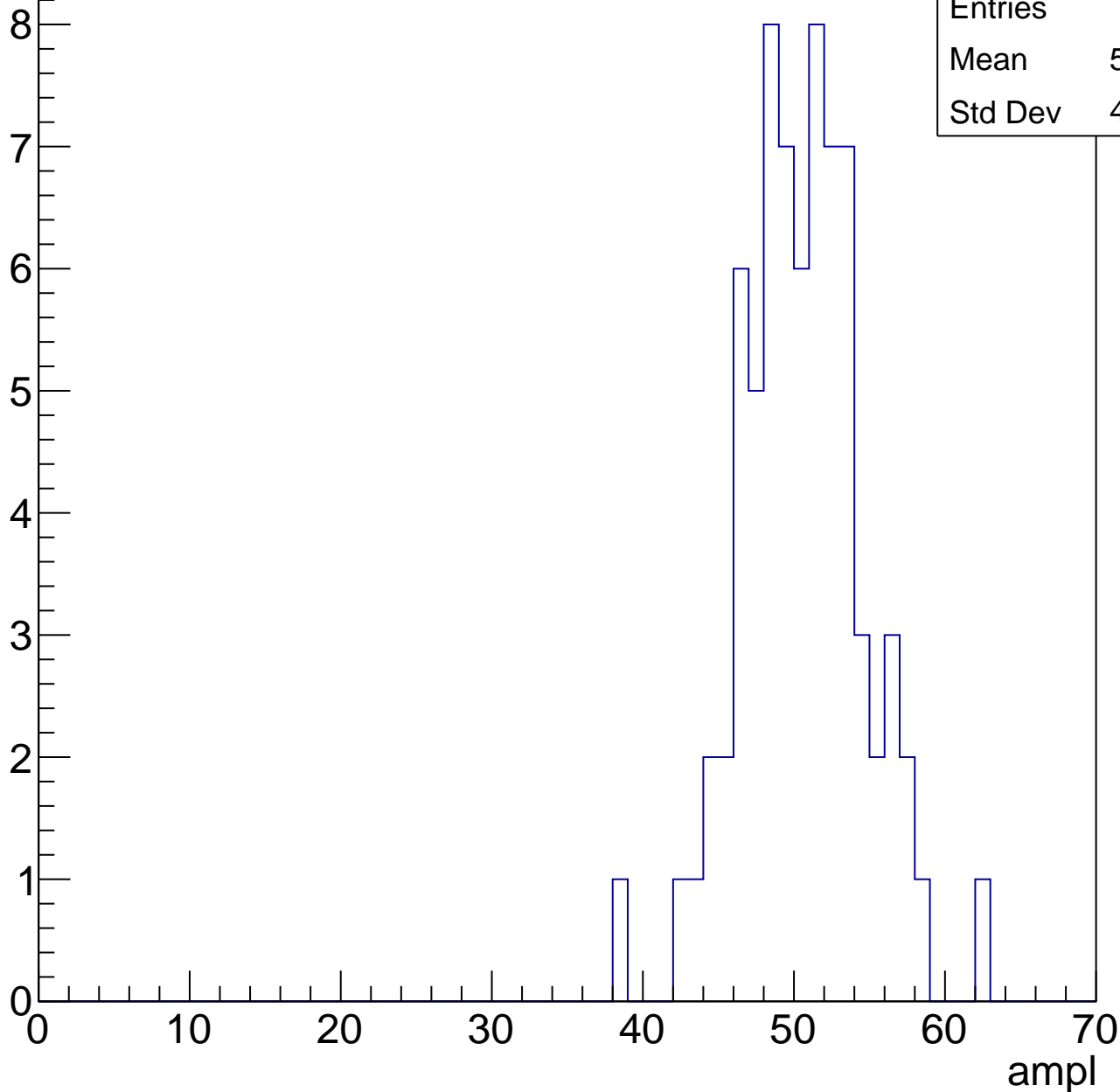


# B0L000S, U13-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

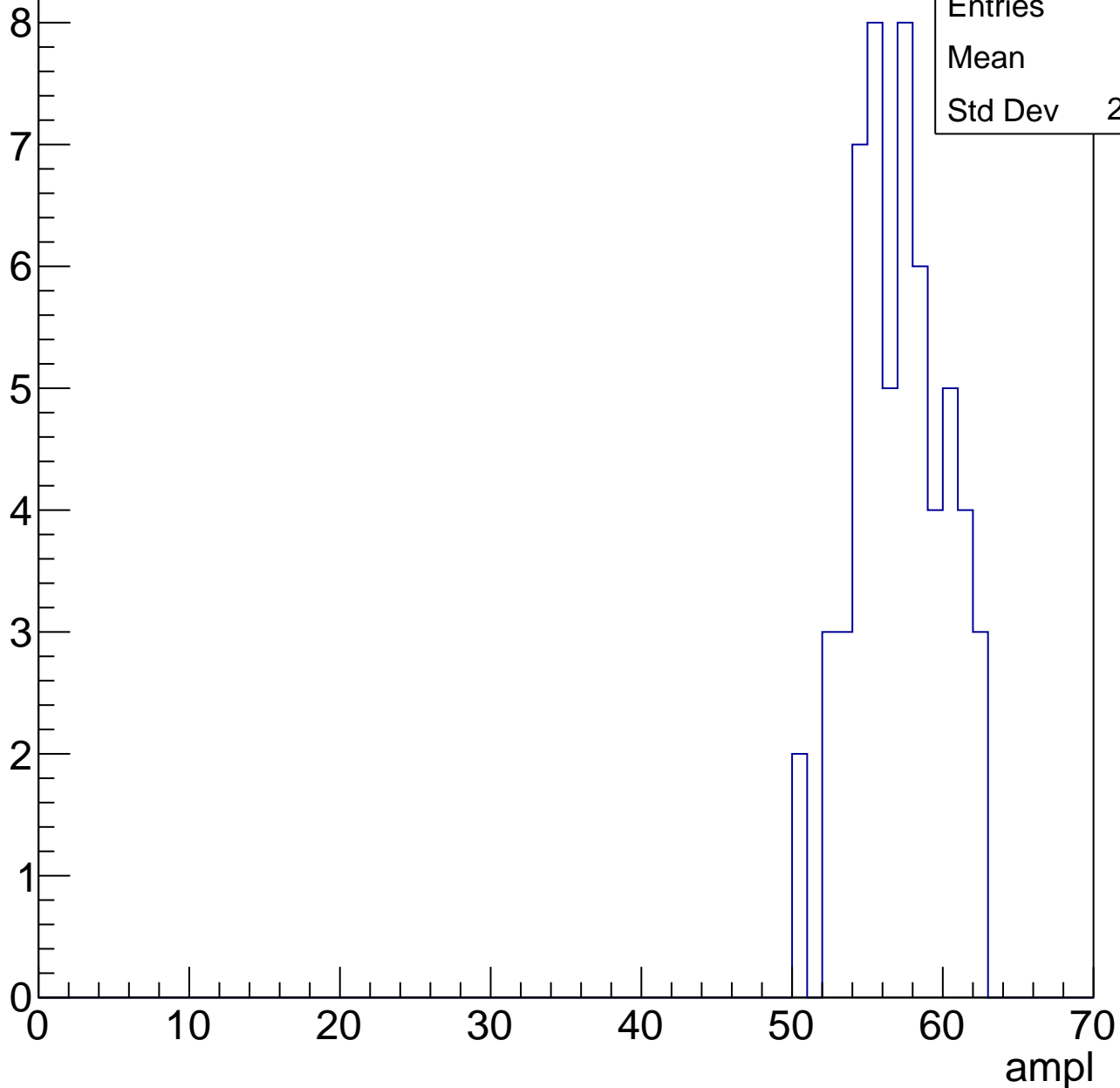
Entries	73
Mean	50.08
Std Dev	4.003



# B0L000S, U13-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

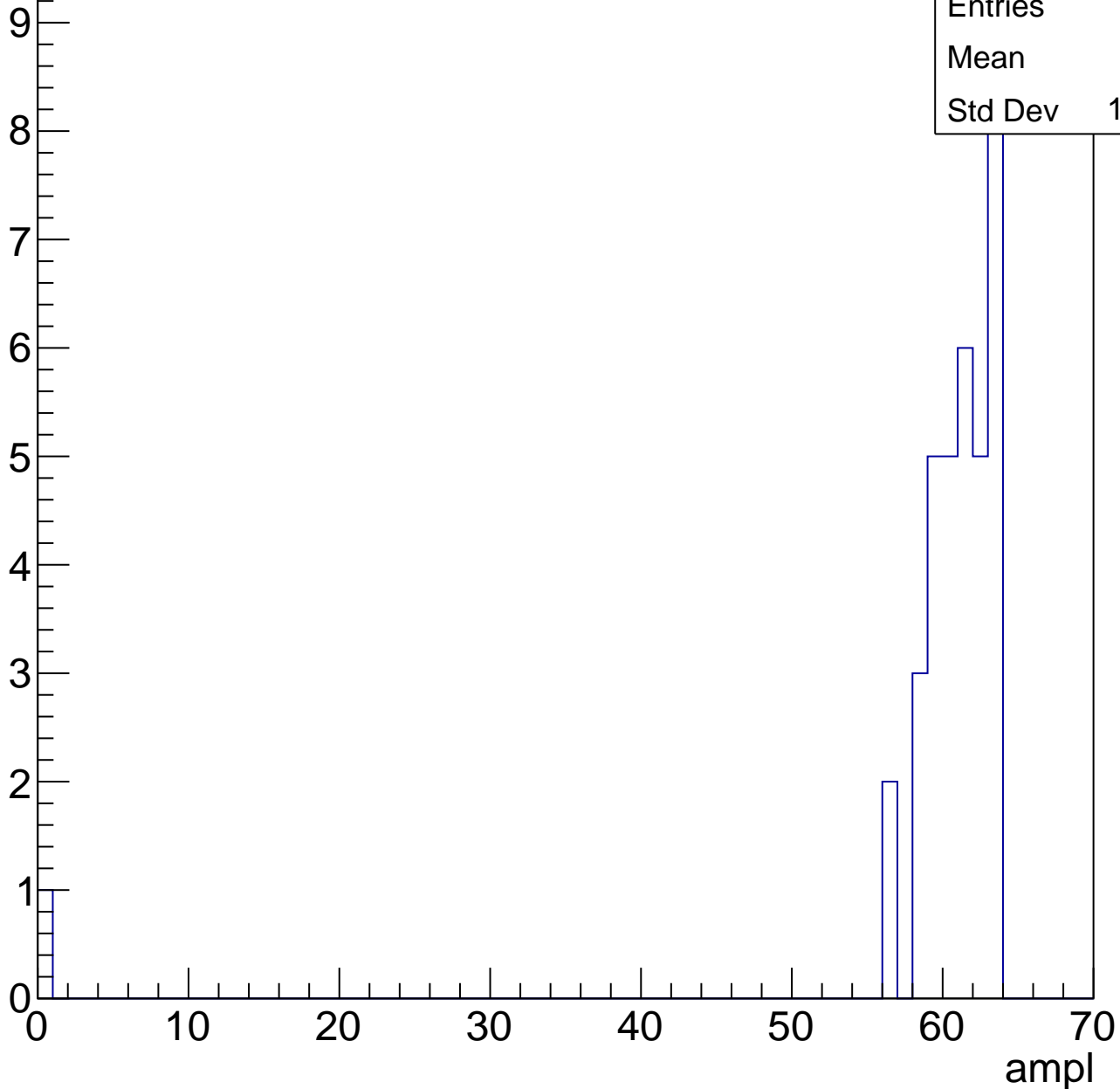


# B0L000S, U13-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

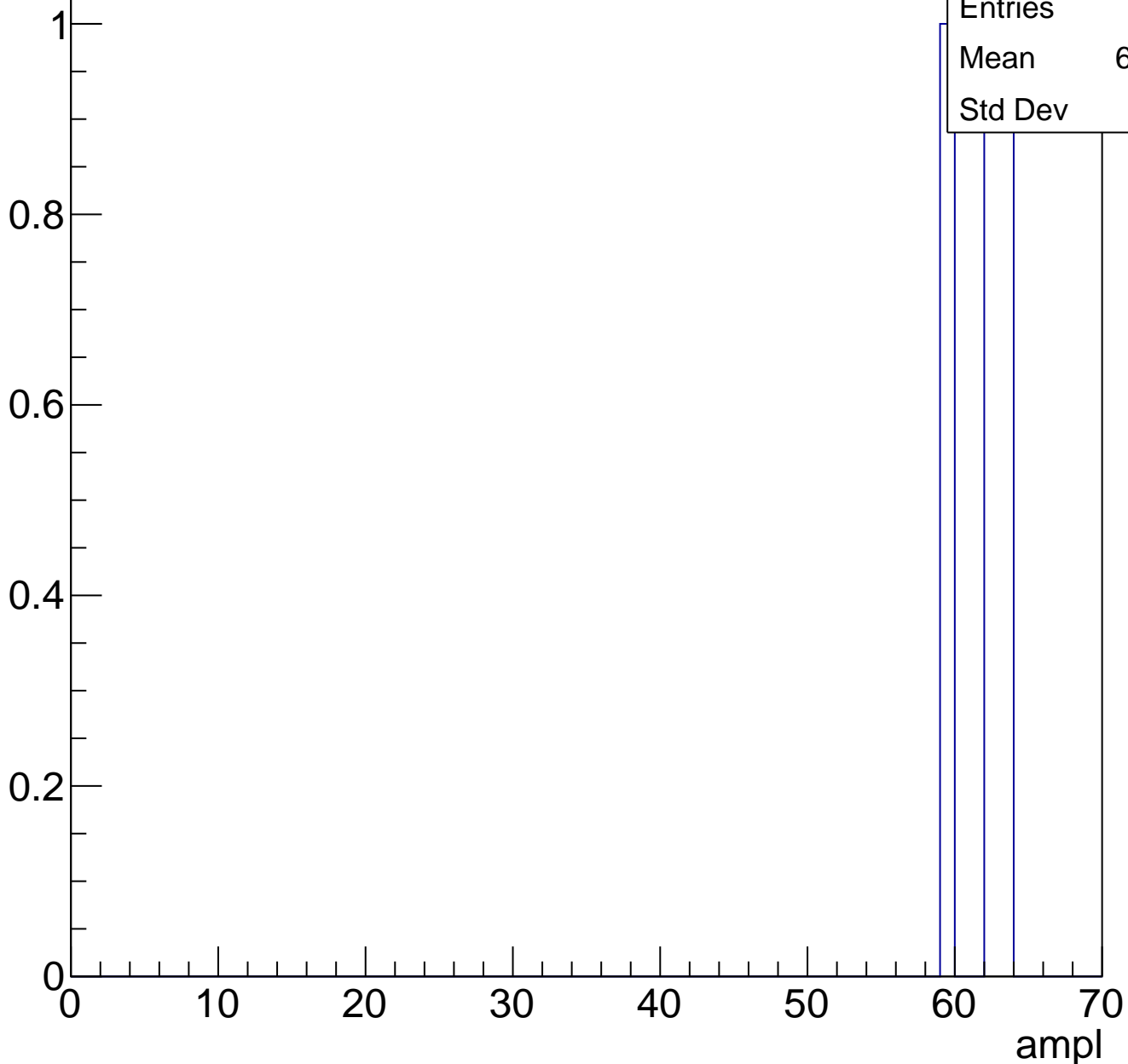
Entries	36
Mean	59
Std Dev	10.17



# B0L000S, U13-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch111, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	30.91
Std Dev	5.092

**Gaus mean : 31.7409**

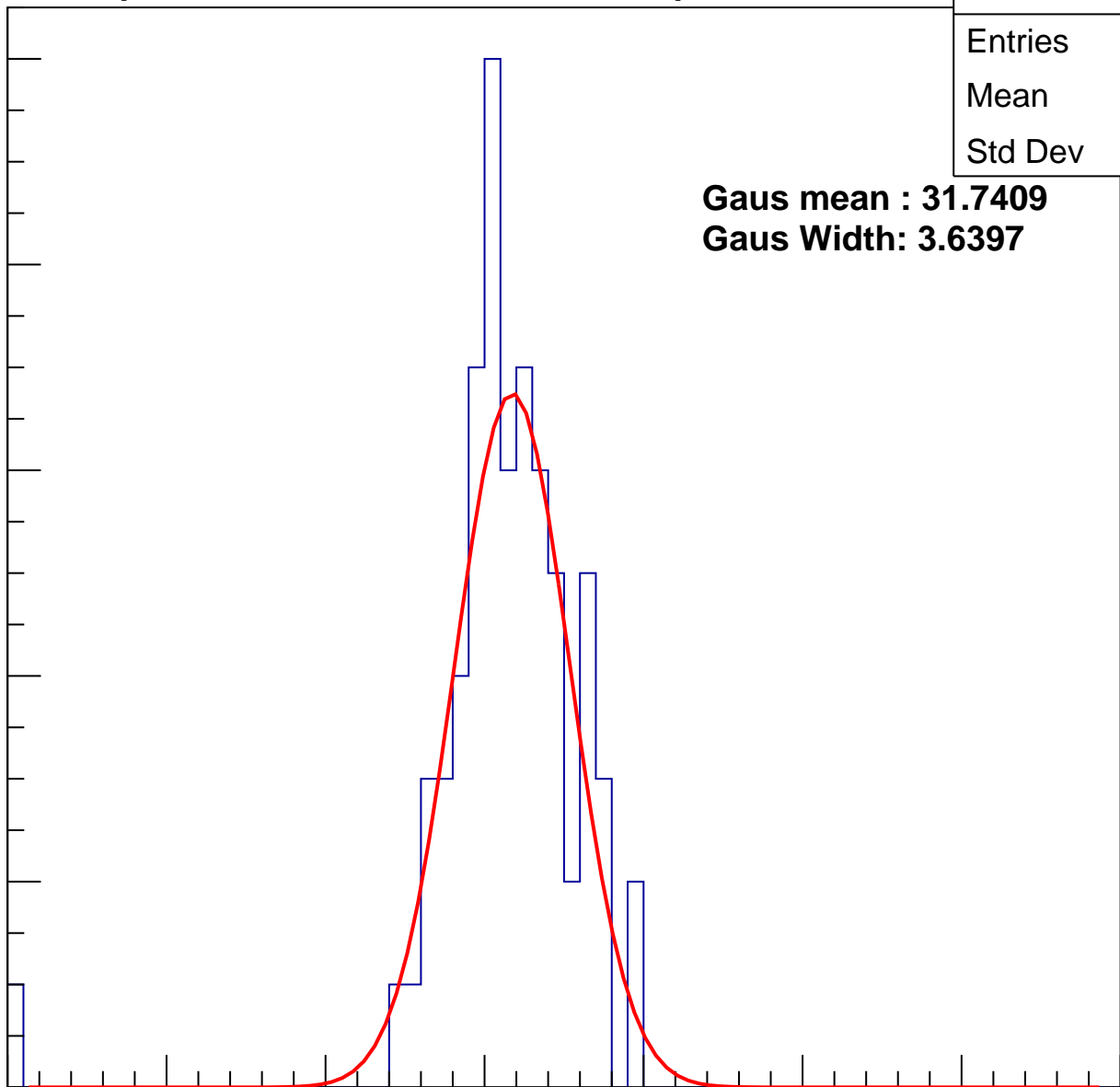
**Gaus Width: 3.6397**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch111, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	38.17
Std Dev	3.568

**Gaus mean : 38.7838**

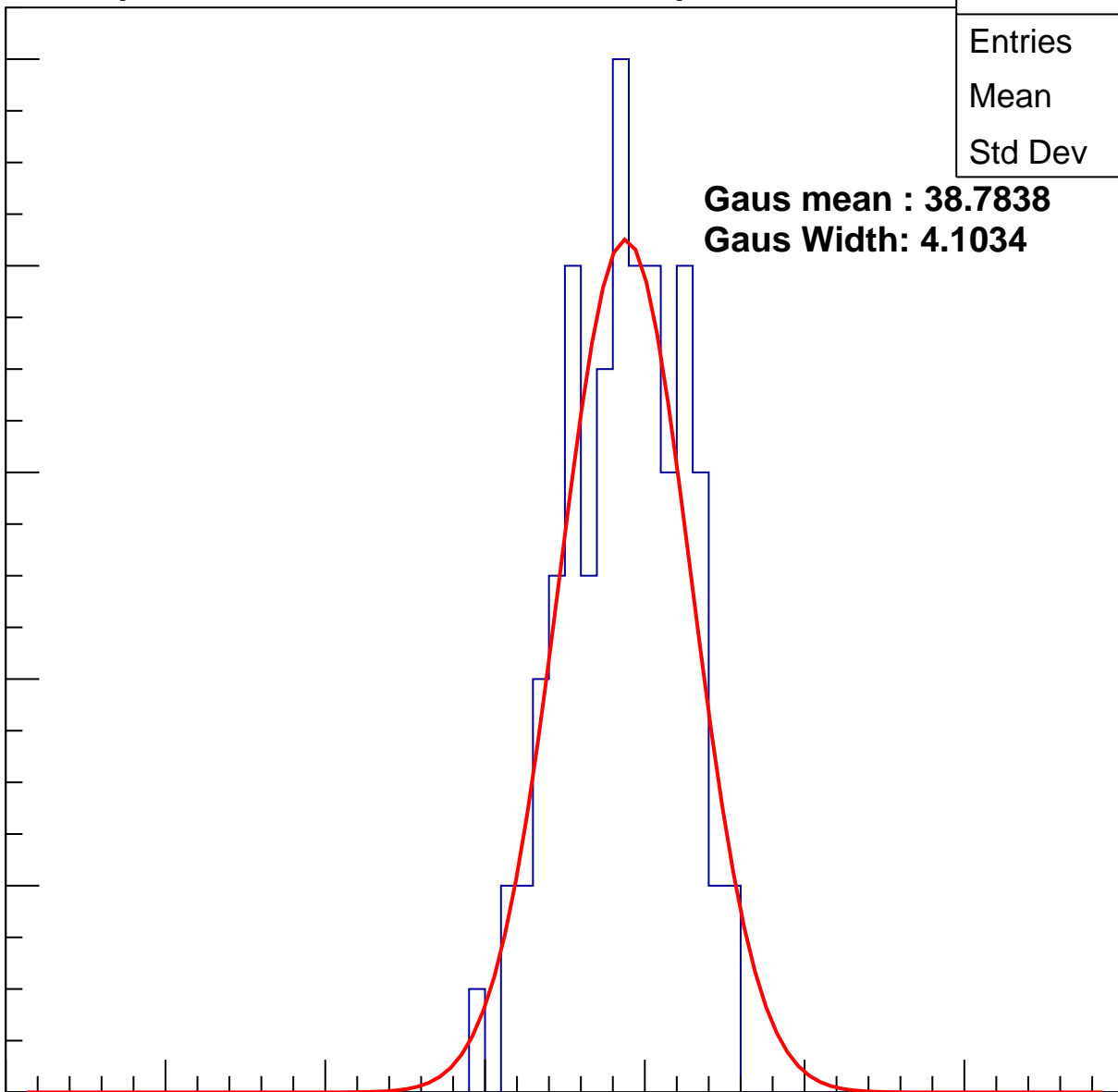
**Gaus Width: 4.1034**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch111, adc2

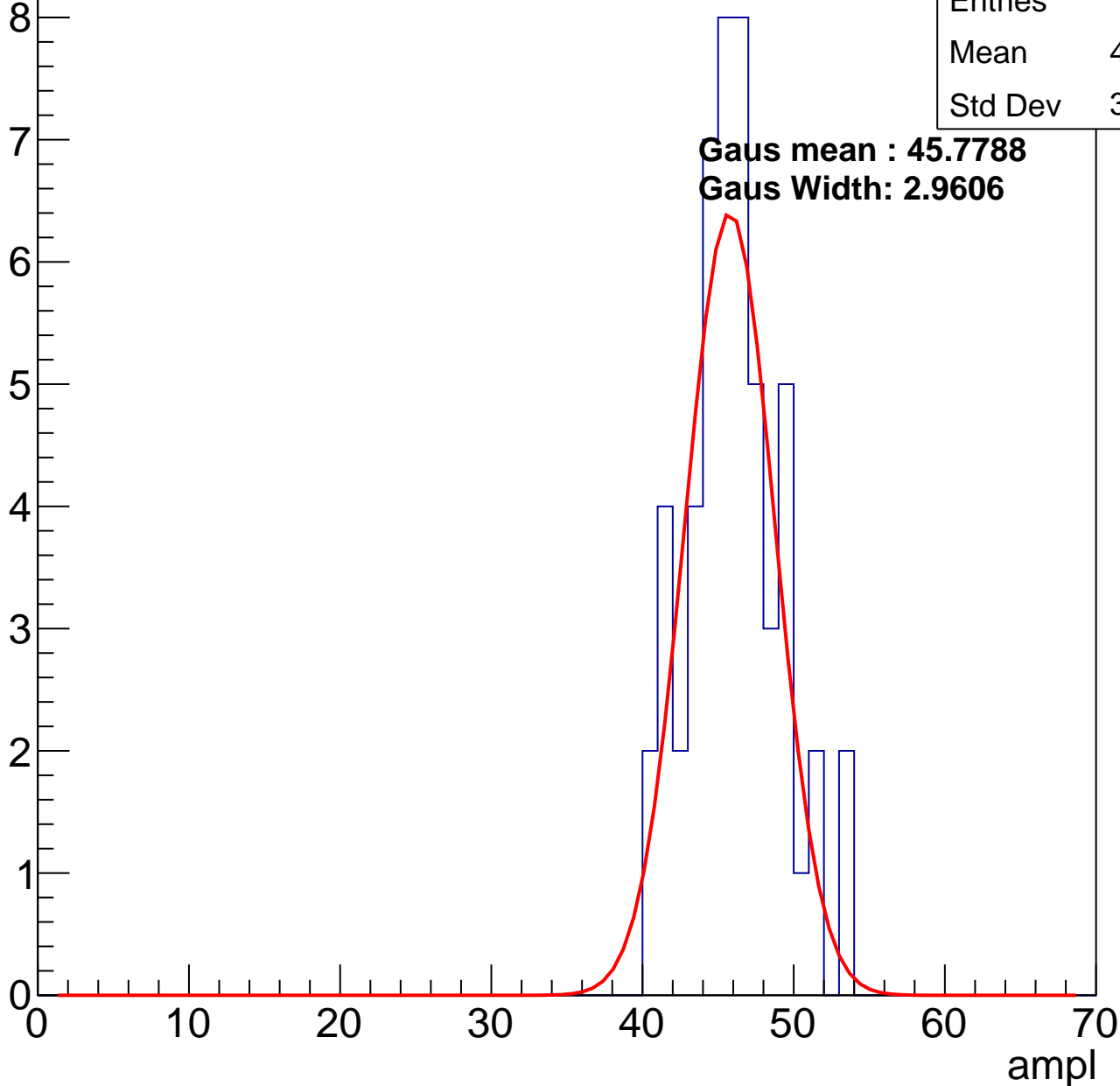
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	45.62
Std Dev	3.054

**Gaus mean : 45.7788**

**Gaus Width: 2.9606**

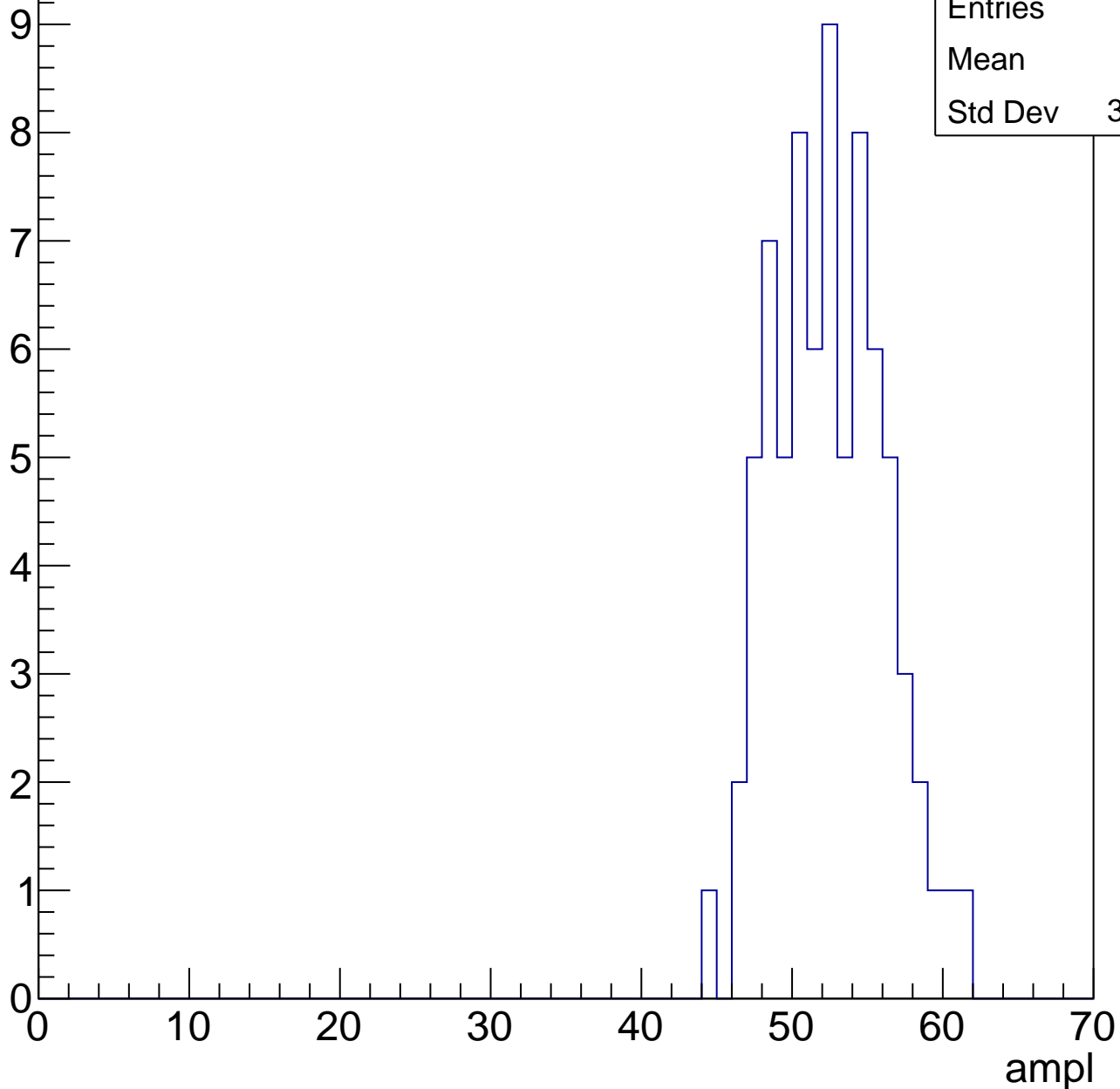


# B0L000S, U13-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	52
Std Dev	3.593

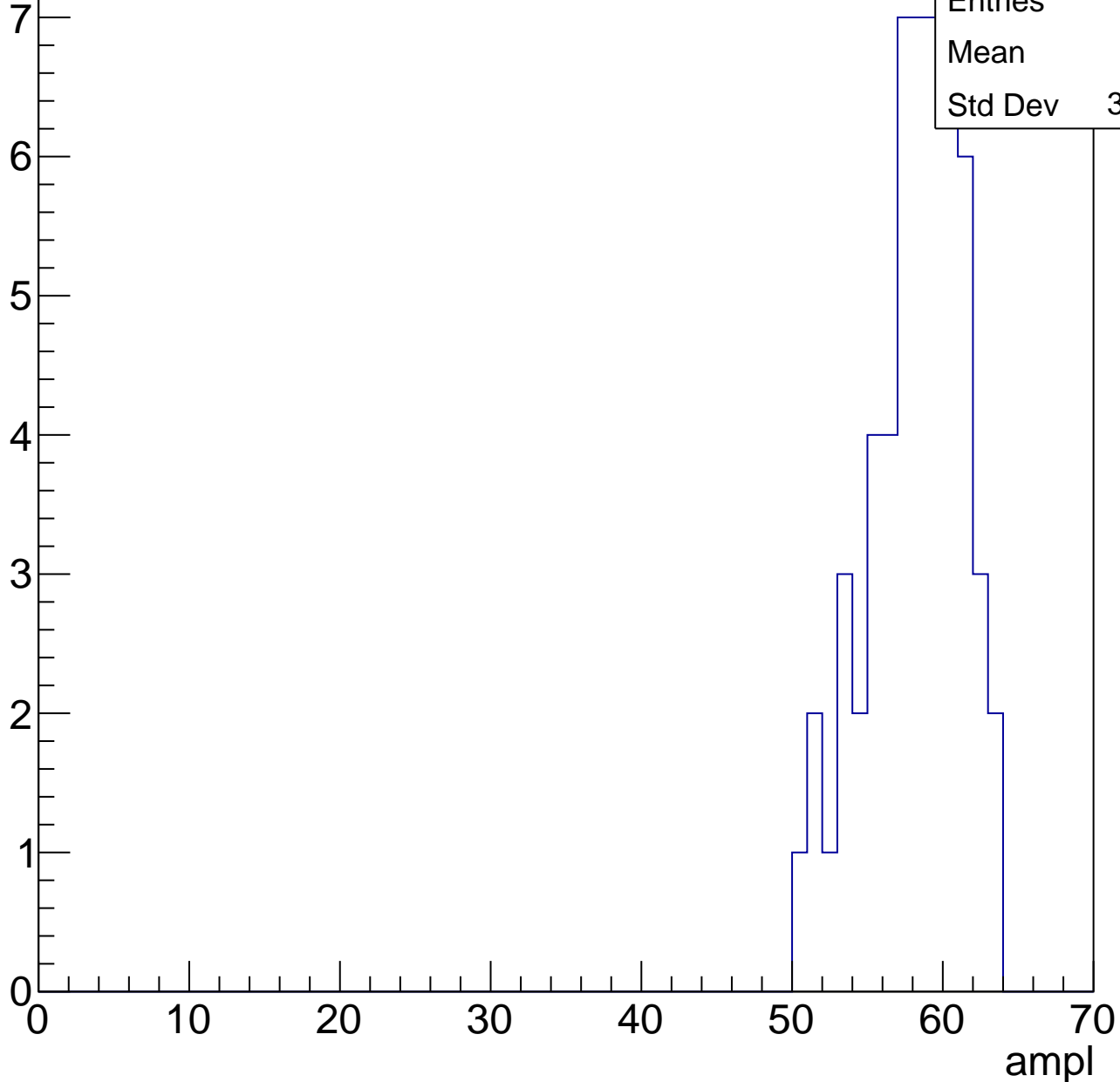


# B0L000S, U13-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	57.7
Std Dev	3.122

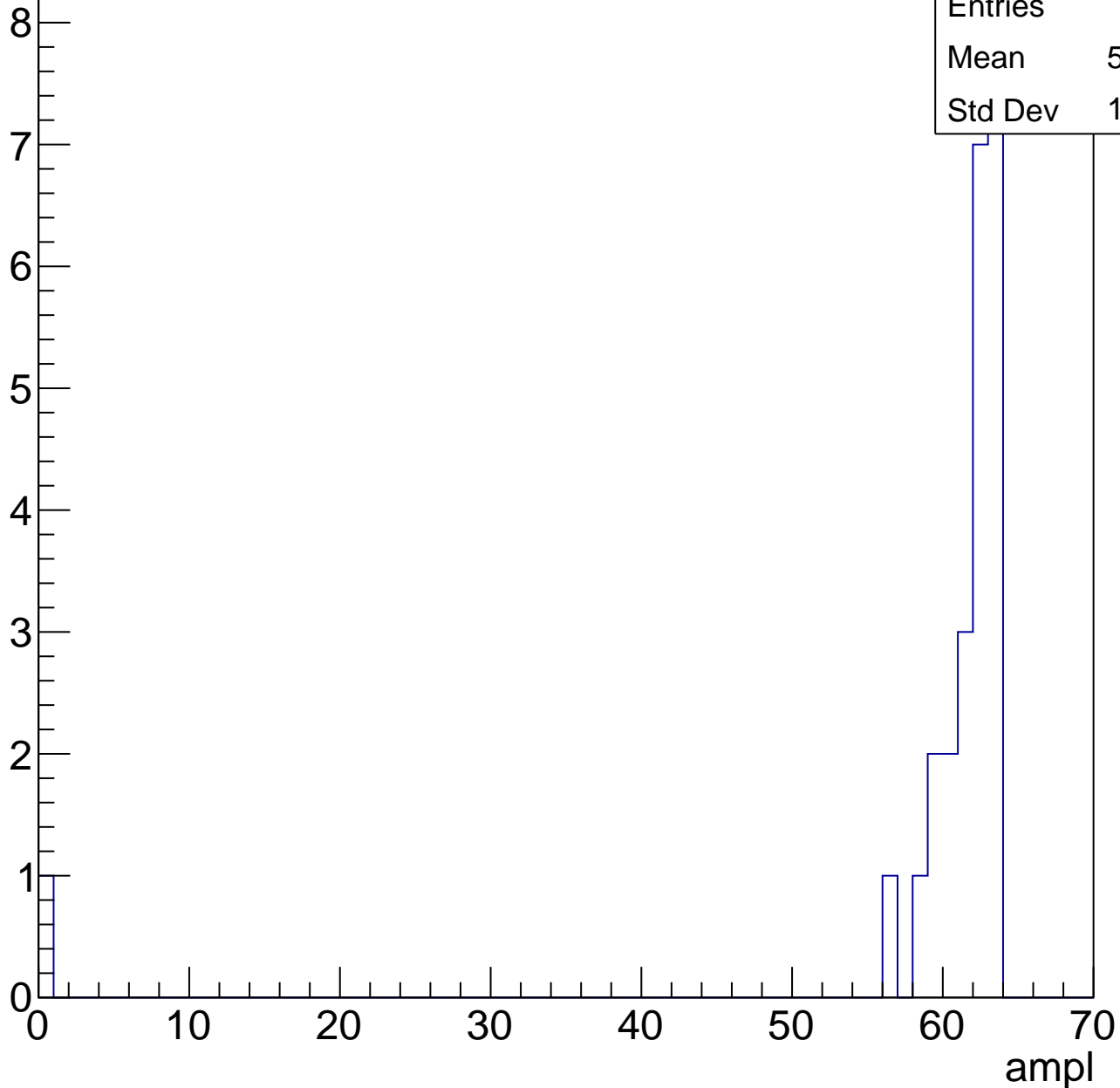


# B0L000S, U13-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	58.92
Std Dev	12.16



# B0L000S, U13-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



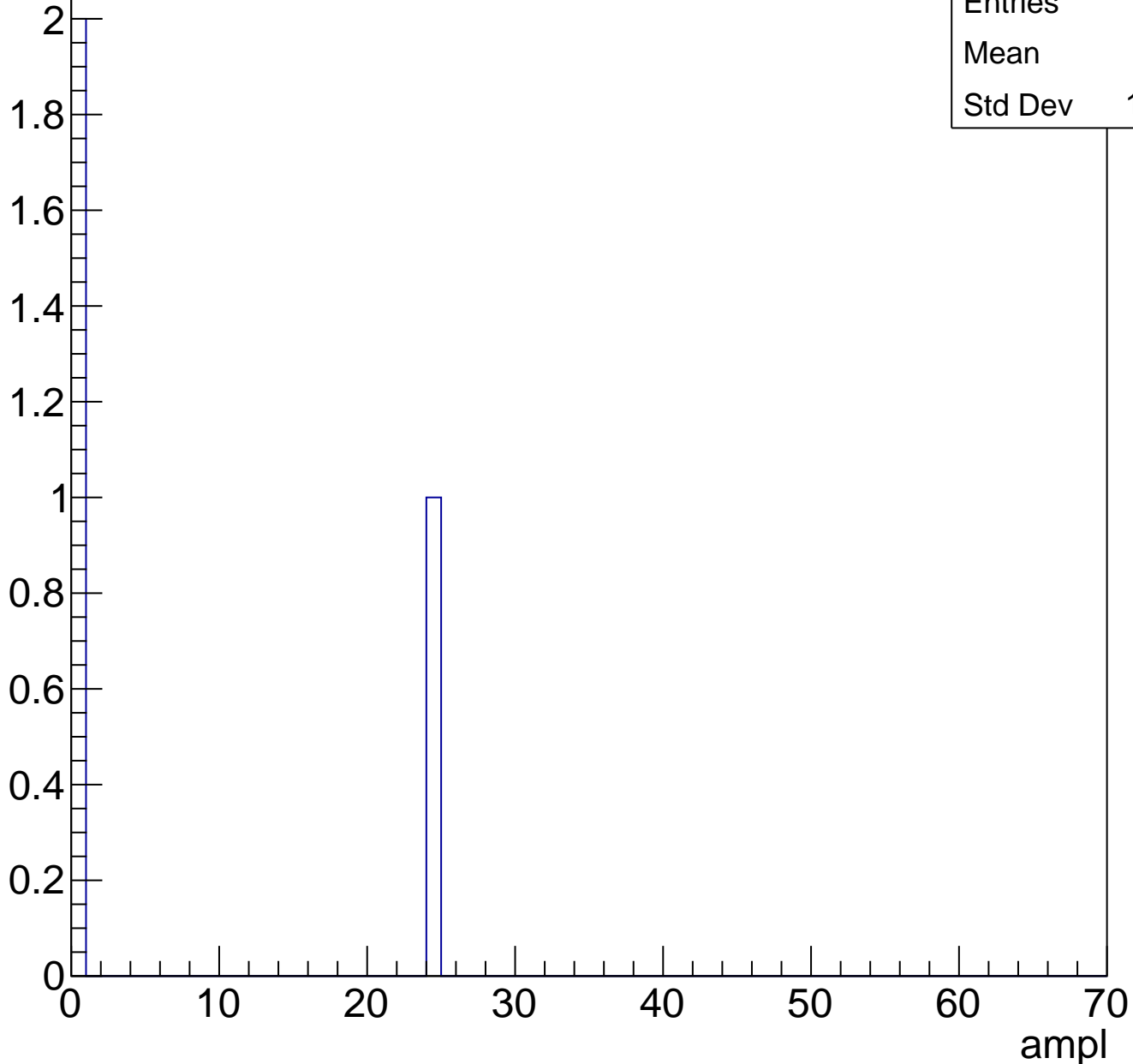
Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U13-ch112, adc0

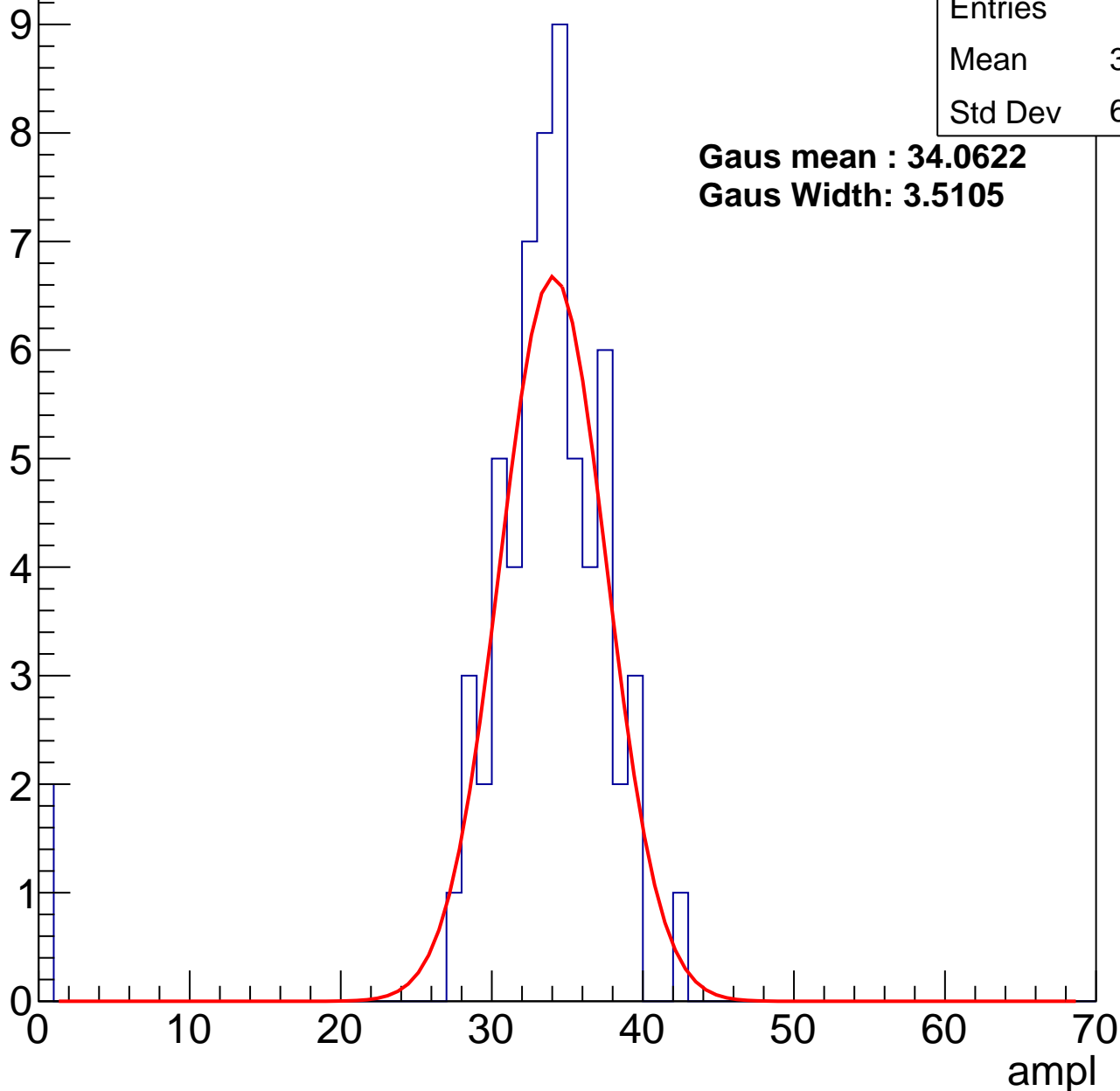
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	32.47
Std Dev	6.686

**Gaus mean : 34.0622**

**Gaus Width: 3.5105**



# B0L000S, U13-ch112, adc1

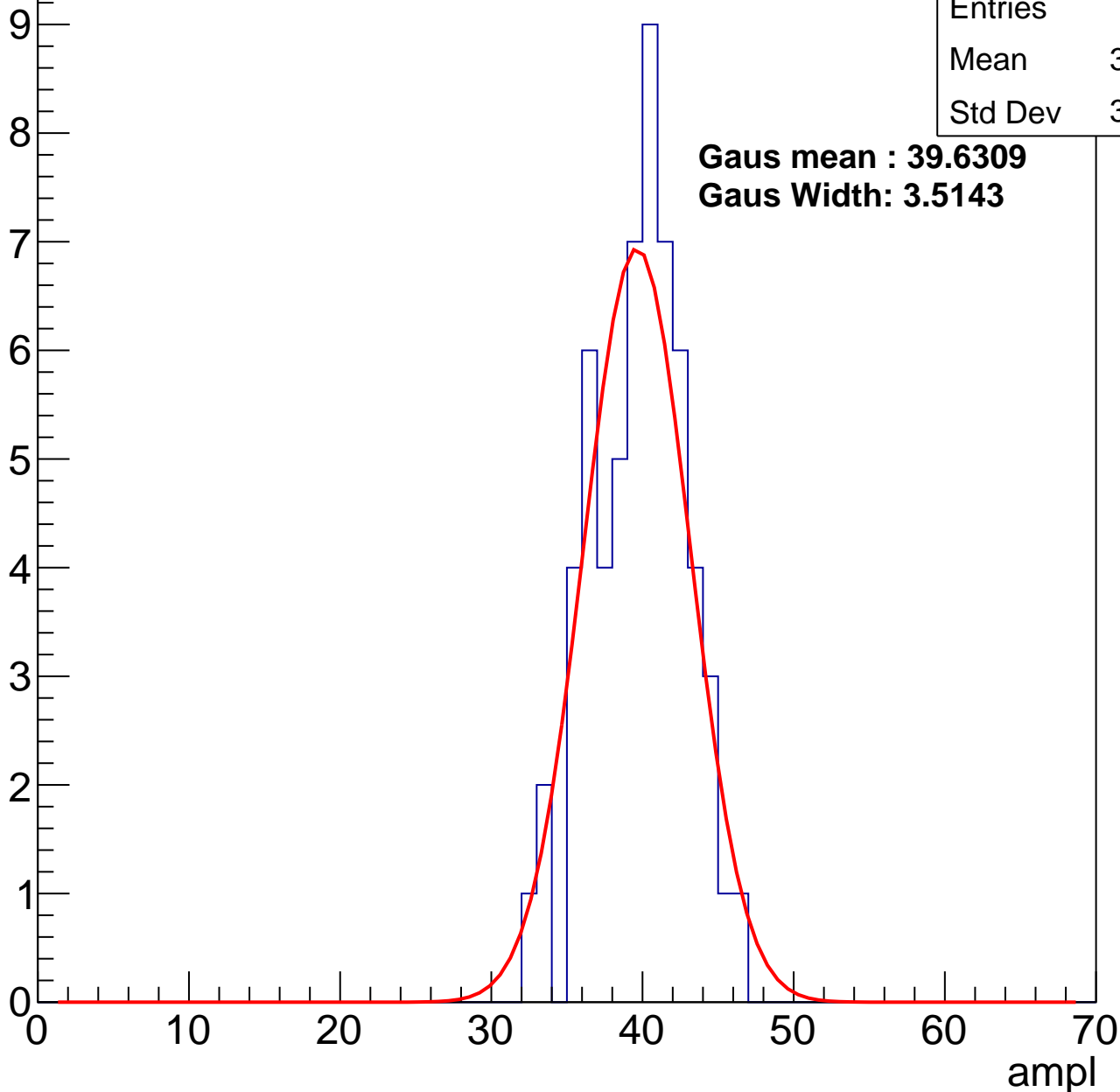
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	39.32
Std Dev	3.079

**Gaus mean : 39.6309**

**Gaus Width: 3.5143**



# B0L000S, U13-ch112, adc2

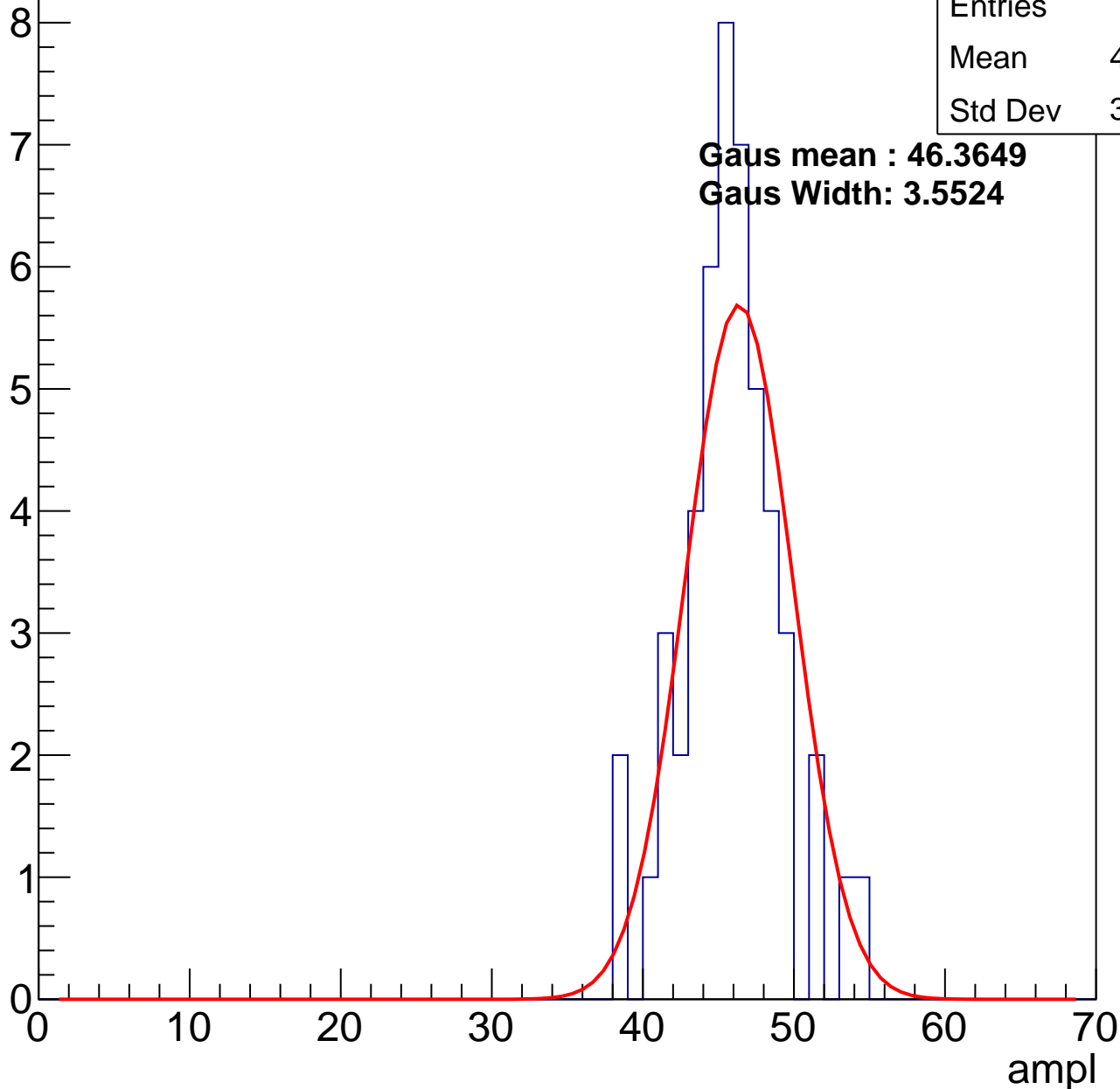
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	45.39
Std Dev	3.294

**Gaus mean : 46.3649**

**Gaus Width: 3.5524**



# B0L000S, U13-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

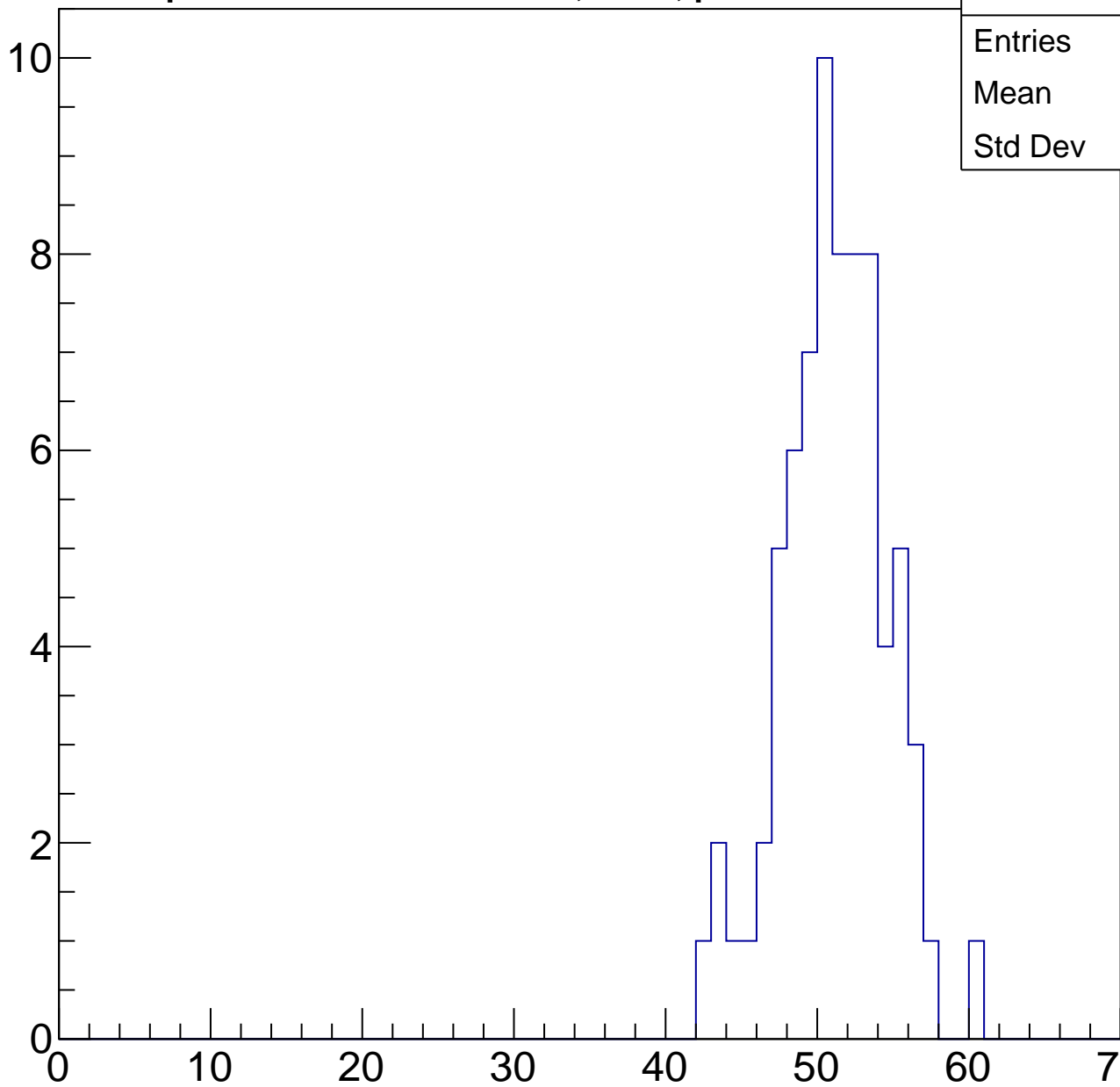
Entries	73
Mean	50.67
Std Dev	3.425

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

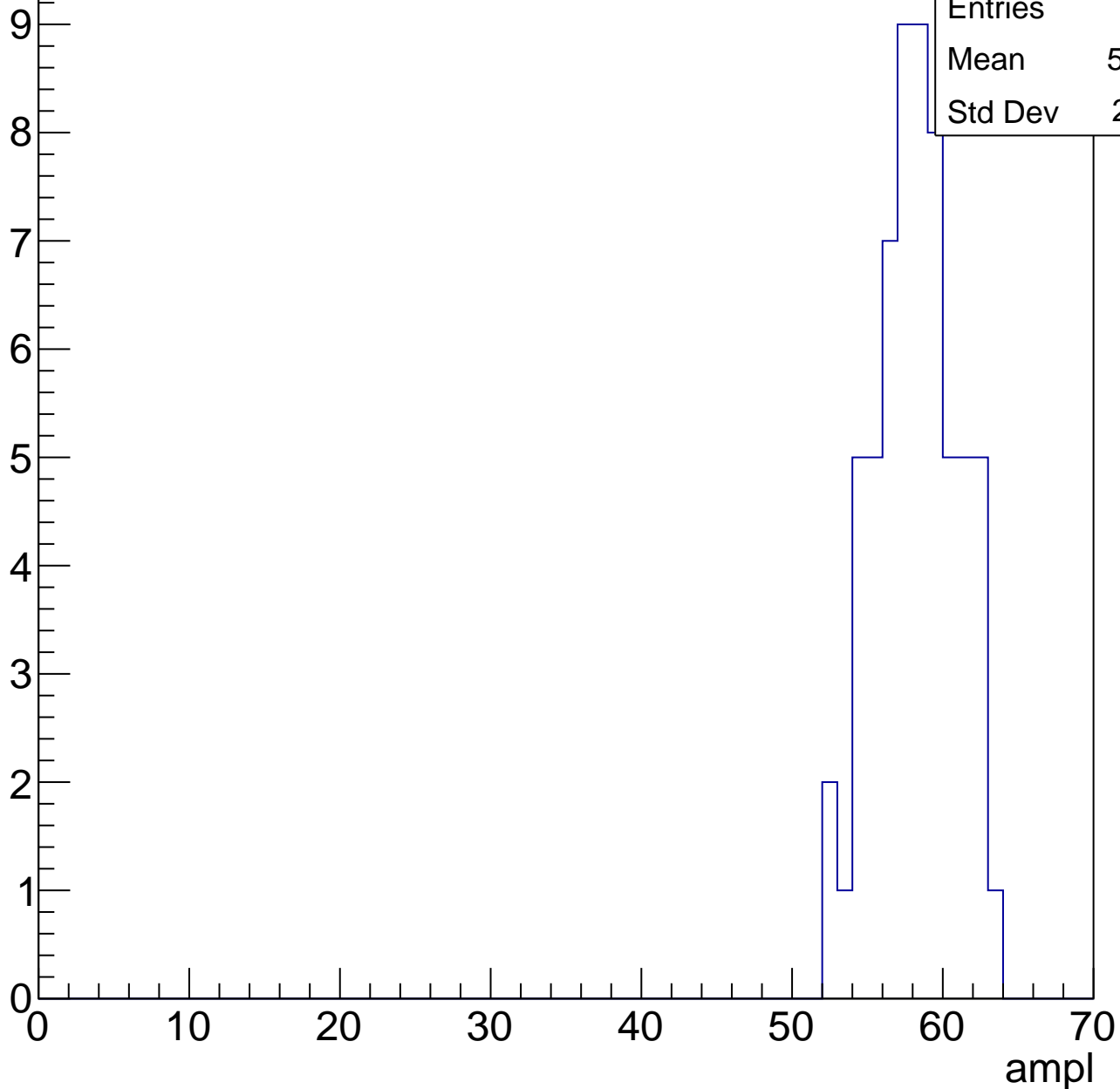
ampl



# B0L000S, U13-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

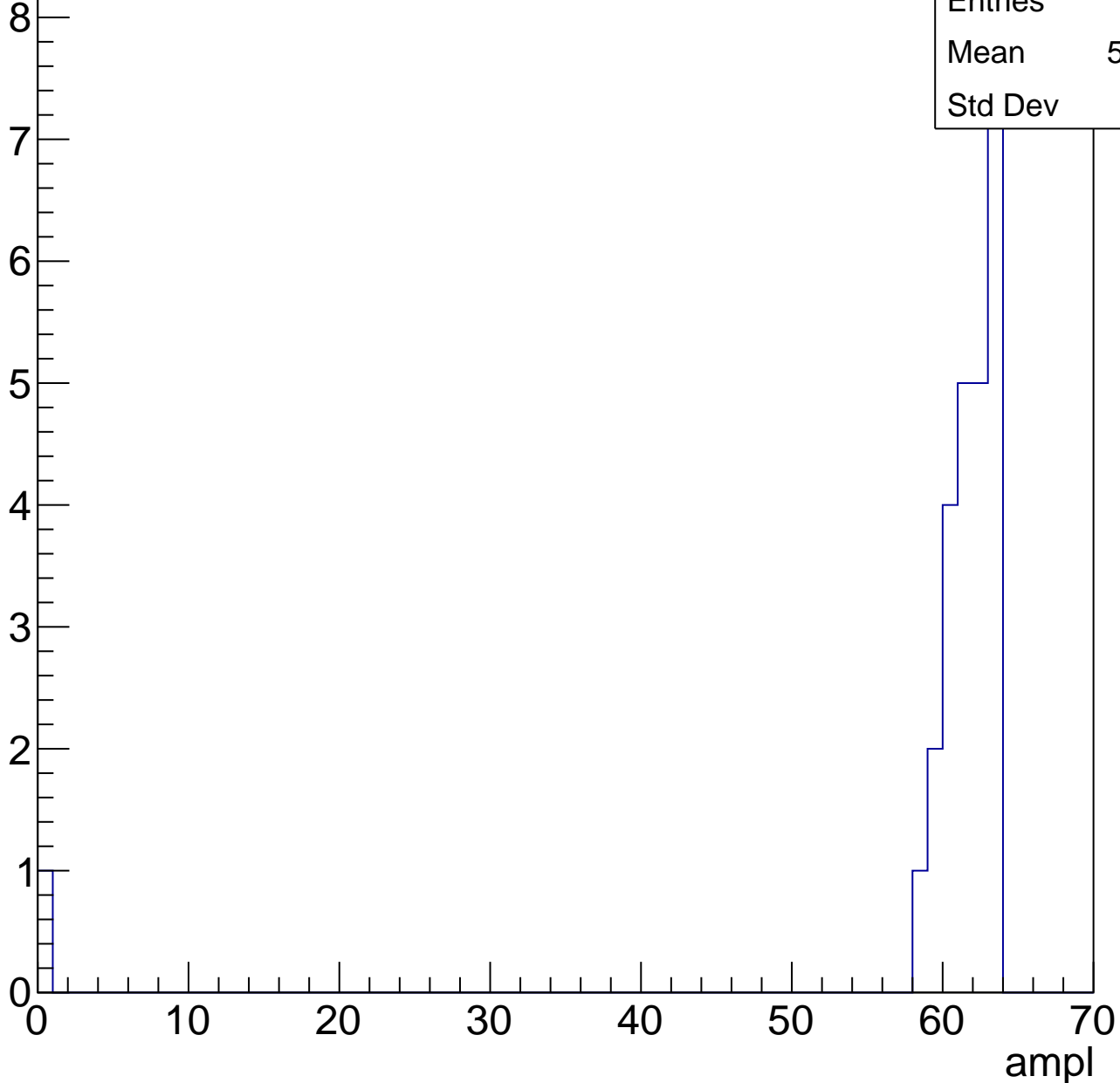


# B0L000S, U13-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	59.04
Std Dev	11.9



# B0L000S, U13-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U13-ch113, adc0

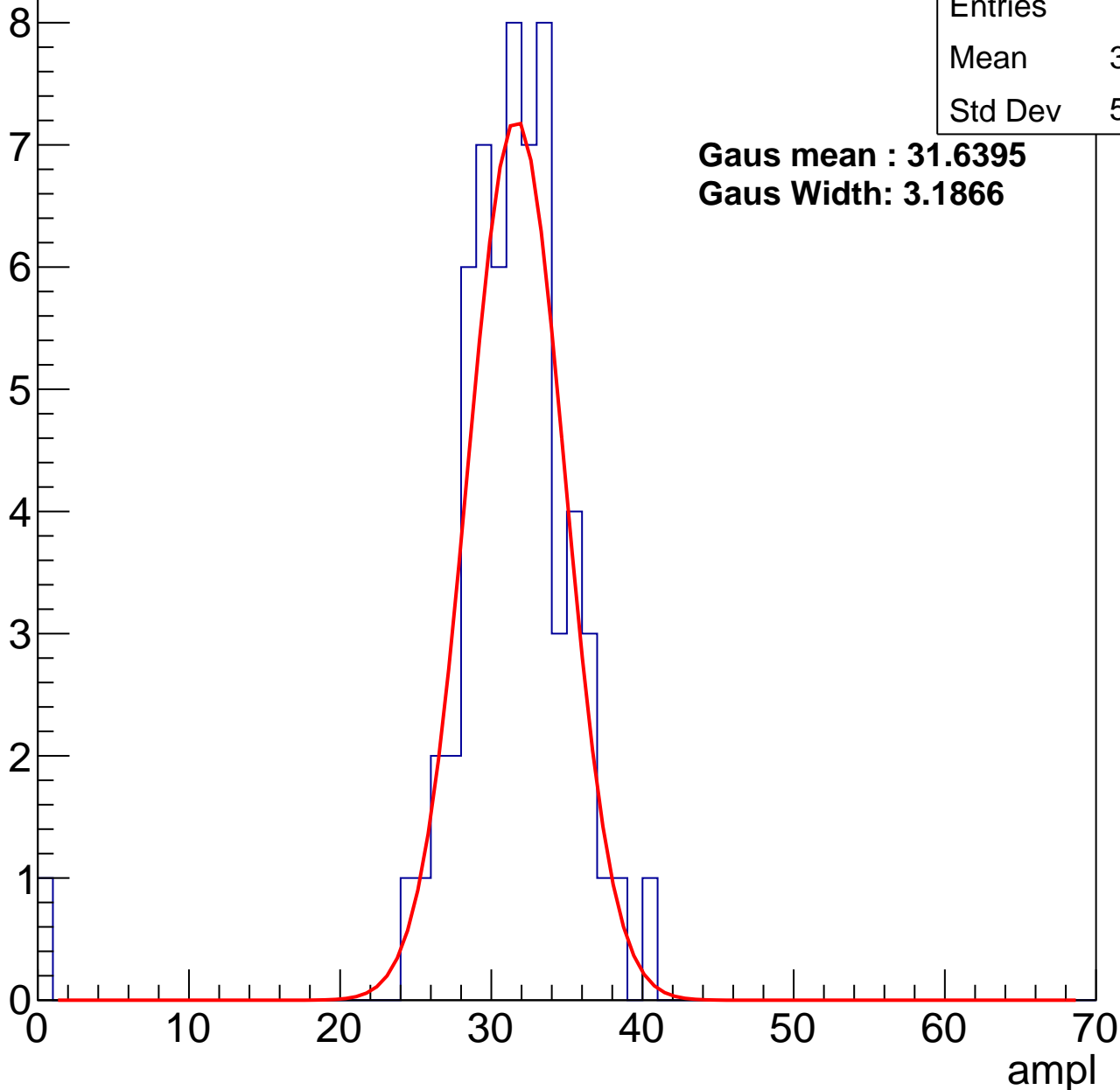
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.76
Std Dev	5.054

**Gaus mean : 31.6395**

**Gaus Width: 3.1866**



# B0L000S, U13-ch113, adc1

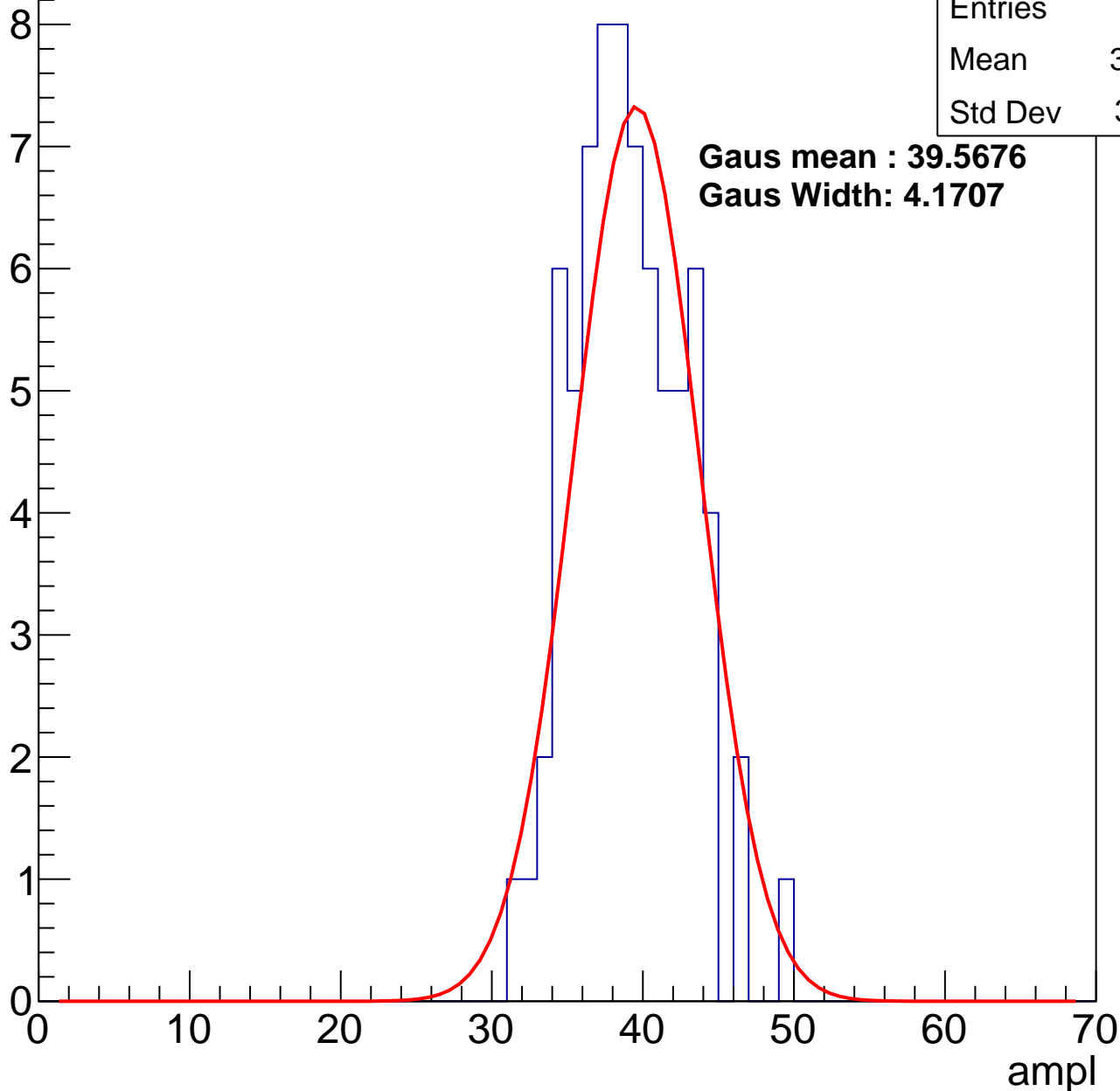
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	38.69
Std Dev	3.631

**Gaus mean : 39.5676**

**Gaus Width: 4.1707**



# B0L000S, U13-ch113, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	44.58
Std Dev	3.353

**Gaus mean : 45.1709**

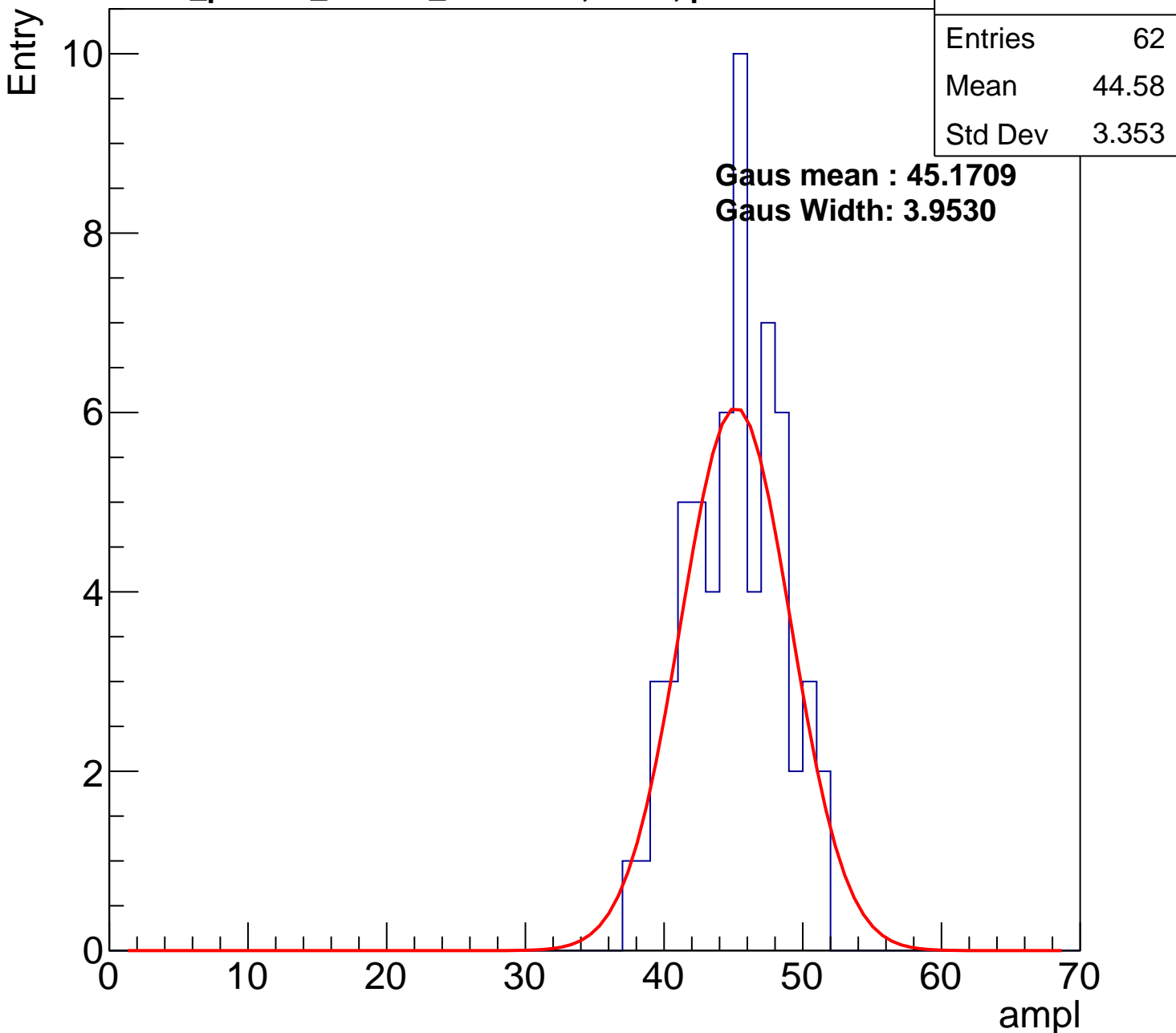
**Gaus Width: 3.9530**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

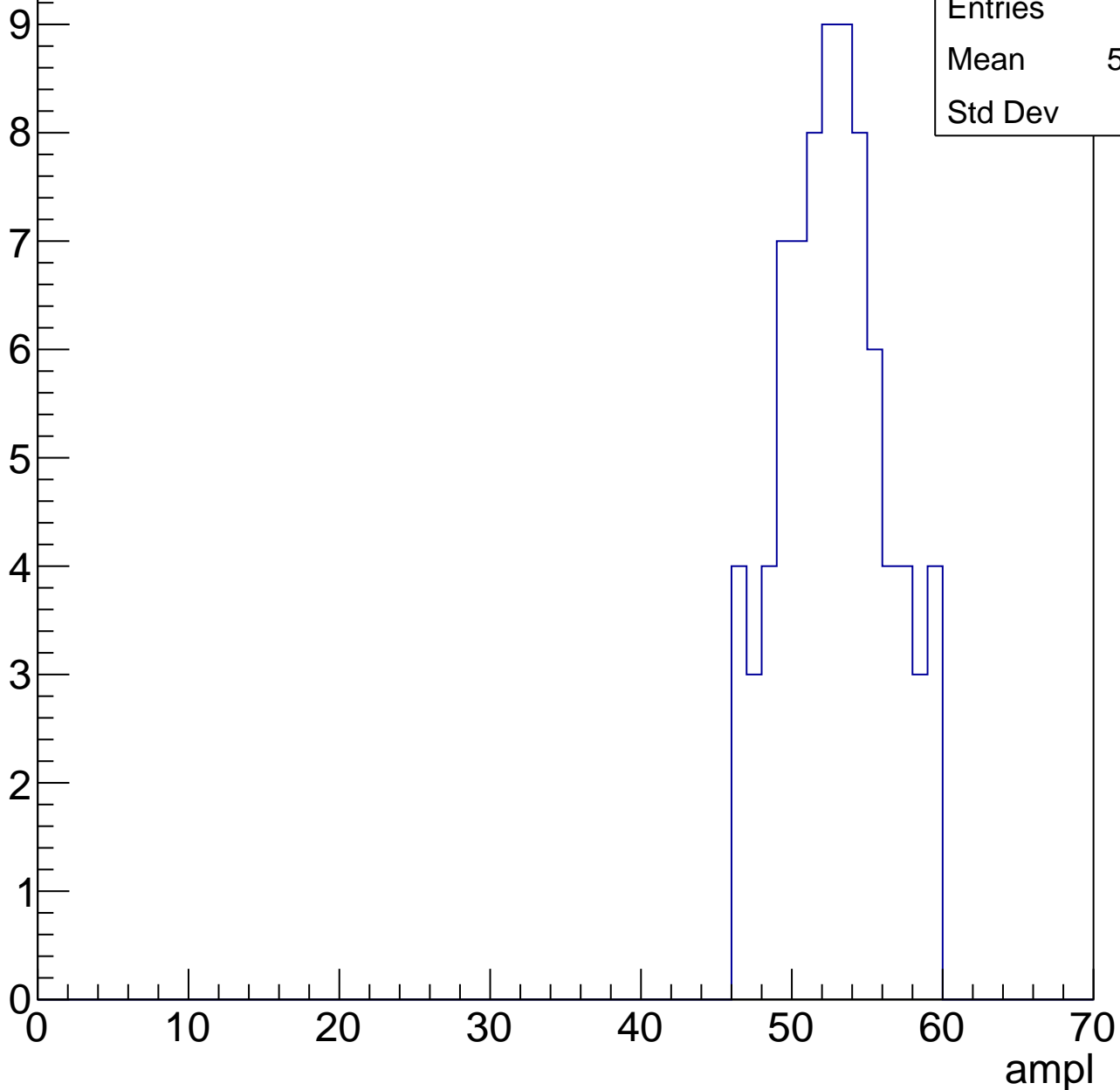


# B0L000S, U13-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	52.34
Std Dev	3.42

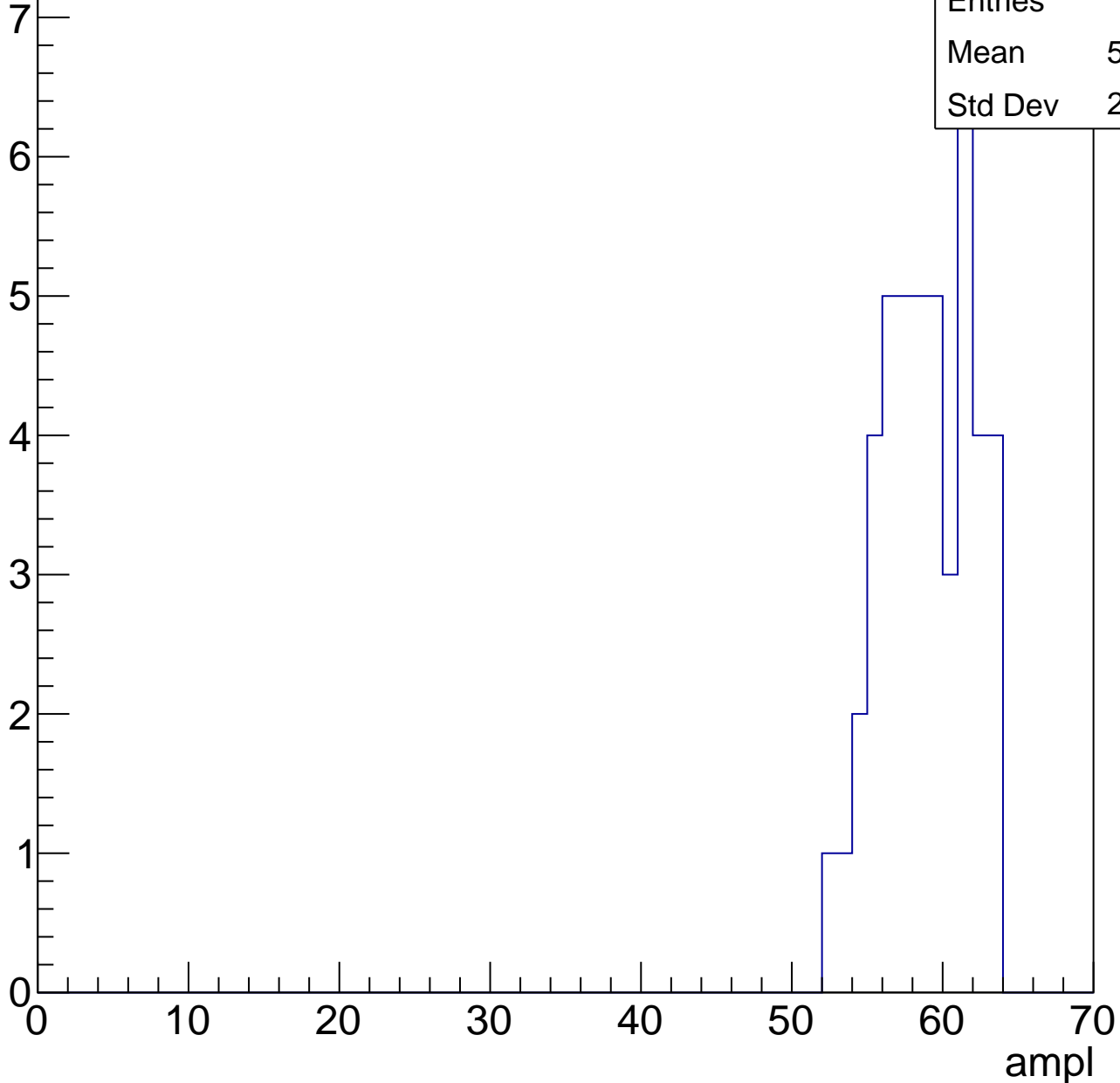


# B0L000S, U13-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.48
Std Dev	2.902

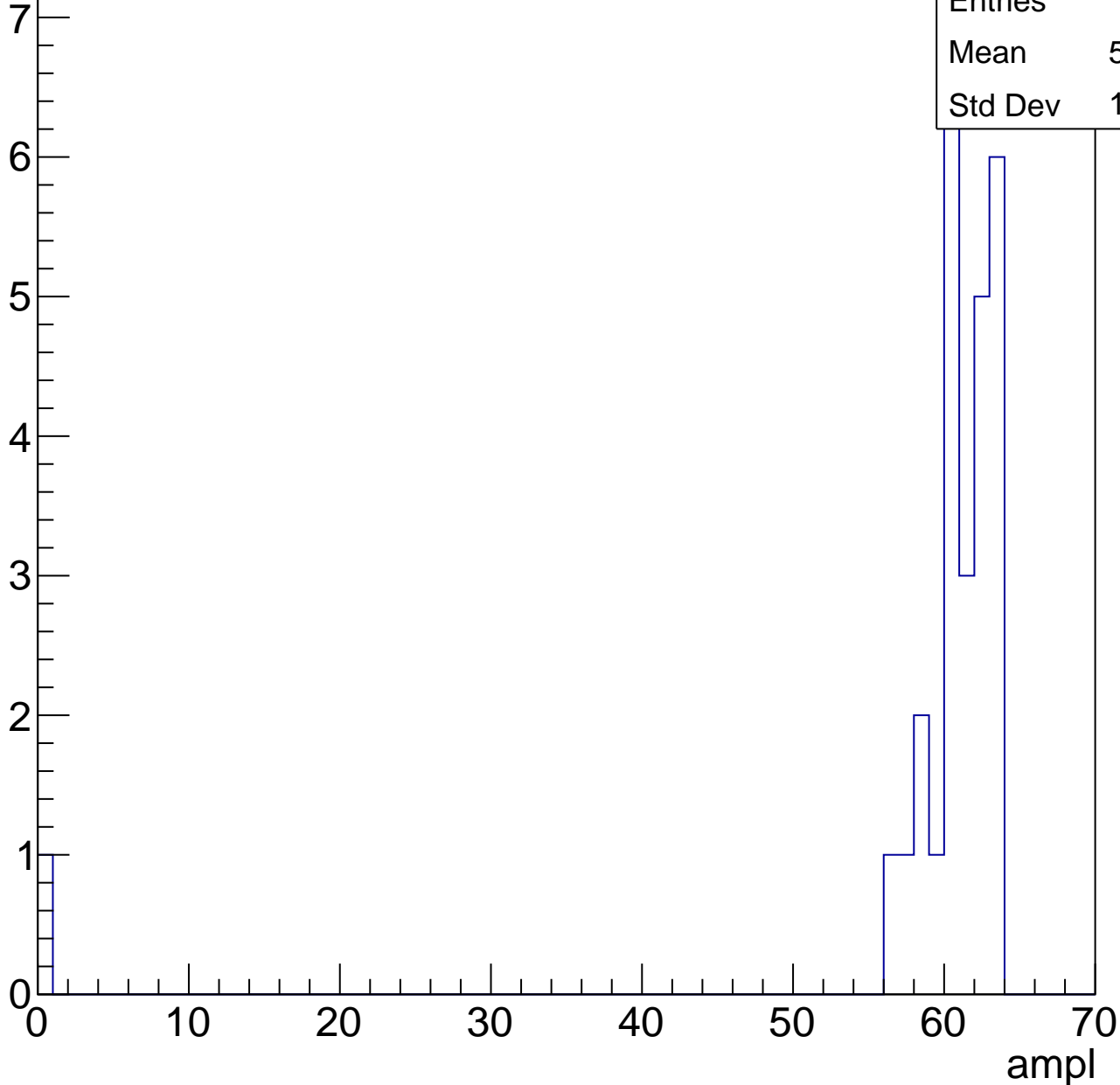


# B0L000S, U13-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	58.48
Std Dev	11.62



# B0L000S, U13-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch114, adc0

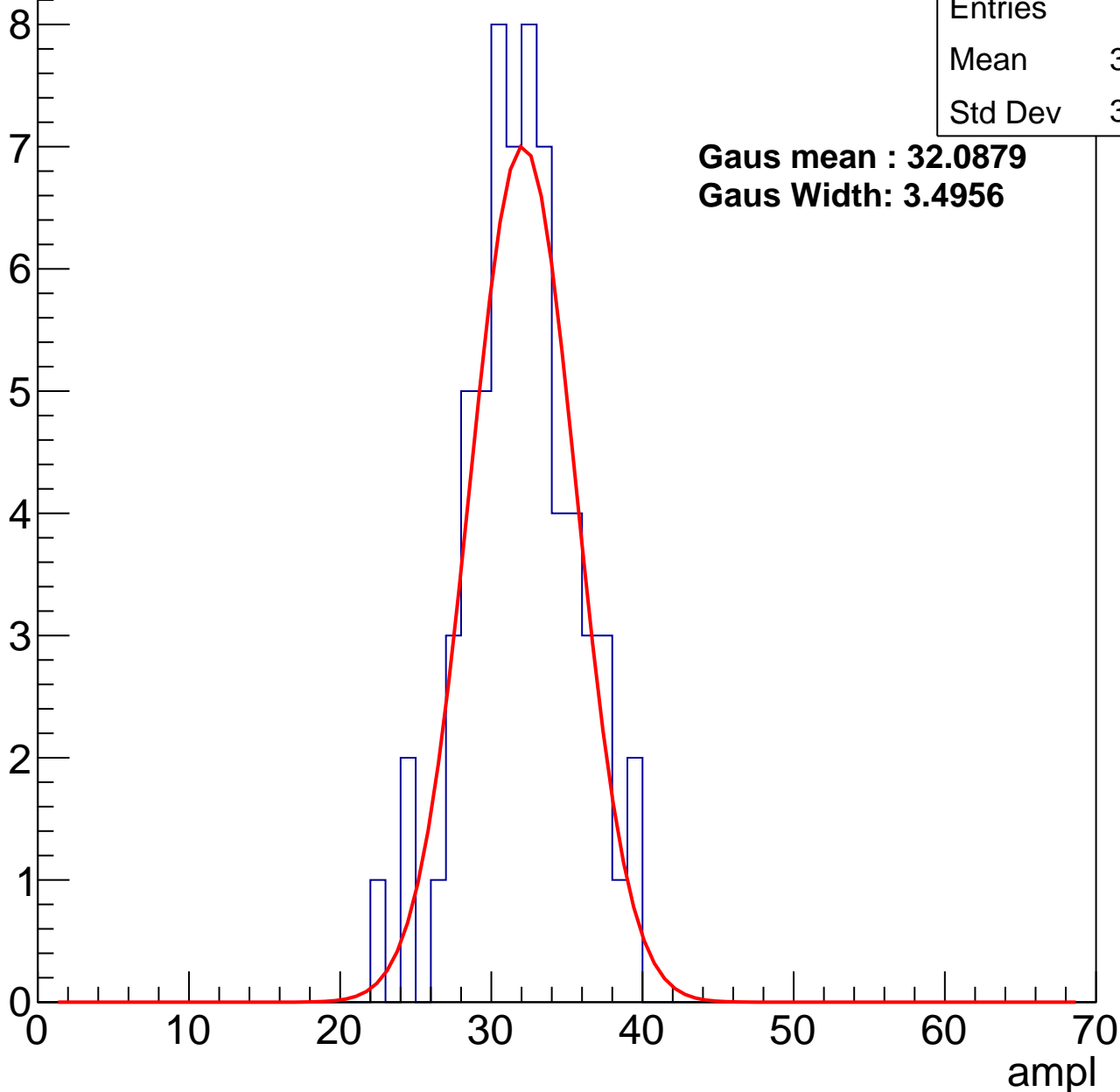
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.52
Std Dev	3.544

**Gaus mean : 32.0879**

**Gaus Width: 3.4956**



# B0L000S, U13-ch114, adc1

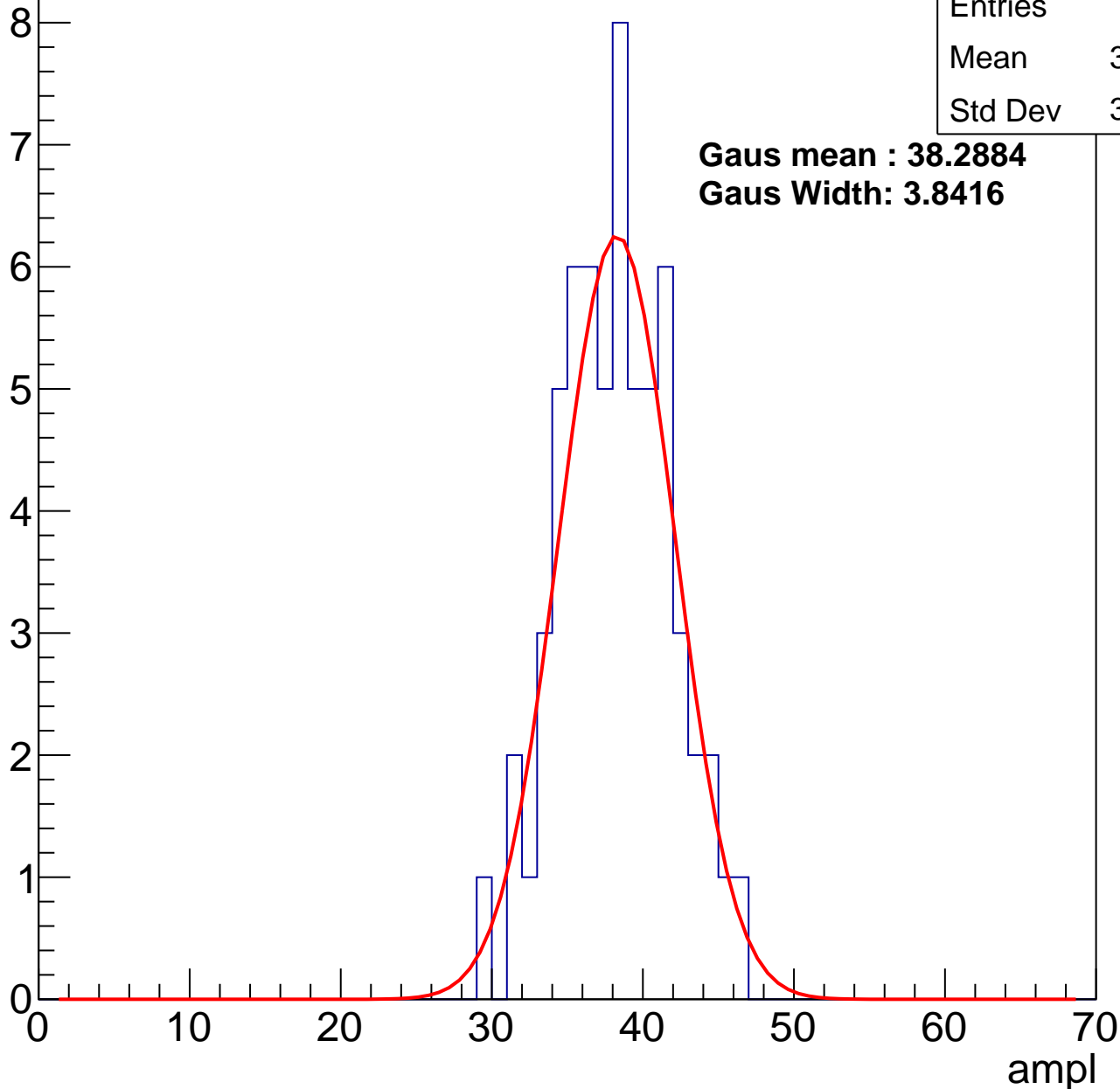
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.73
Std Dev	3.615

**Gaus mean : 38.2884**

**Gaus Width: 3.8416**



# B0L000S, U13-ch114, adc2

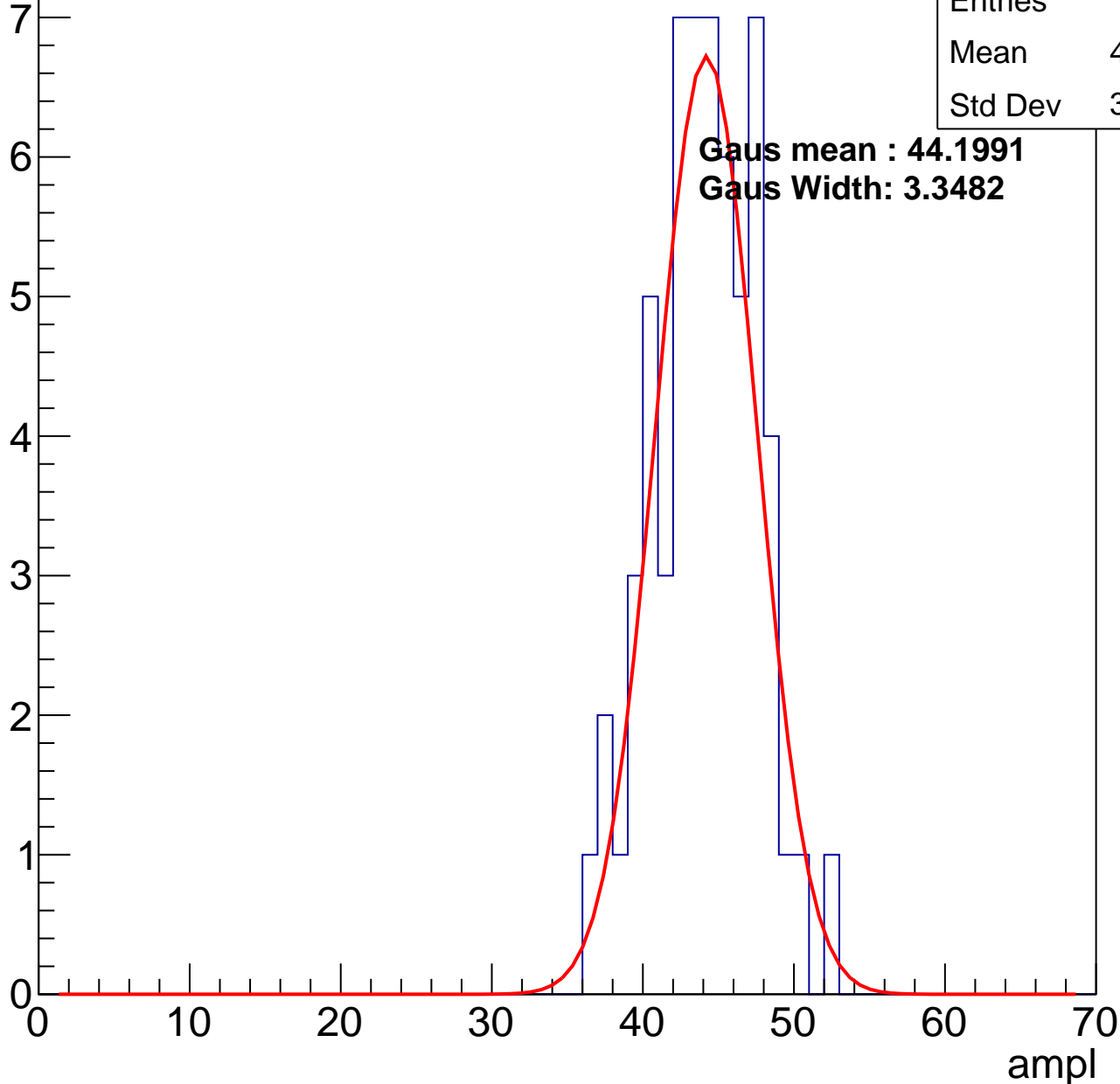
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.66
Std Dev	3.353

**Gaus mean : 44.1991**

**Gaus Width: 3.3482**

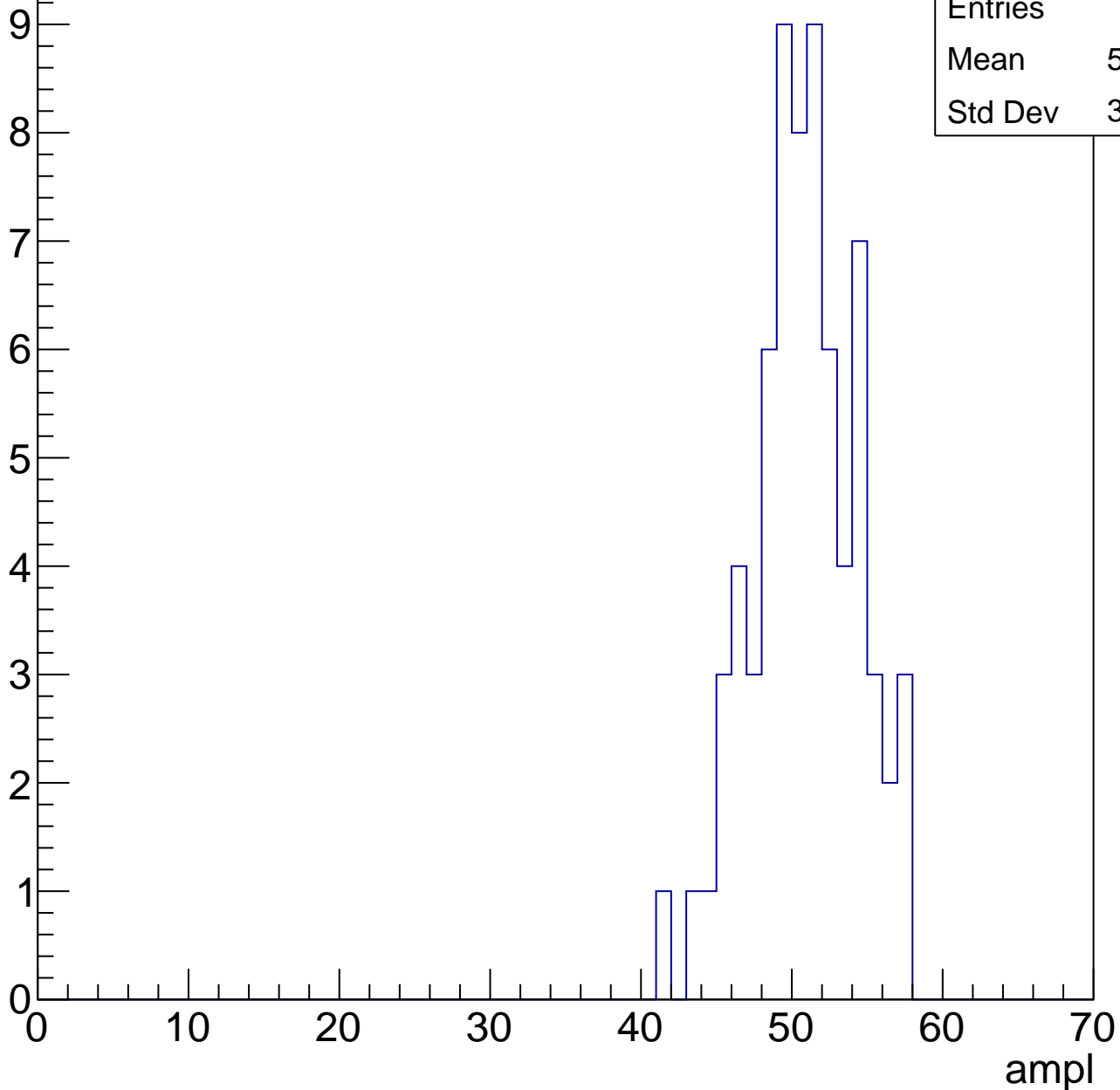


# B0L000S, U13-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.37
Std Dev	3.444

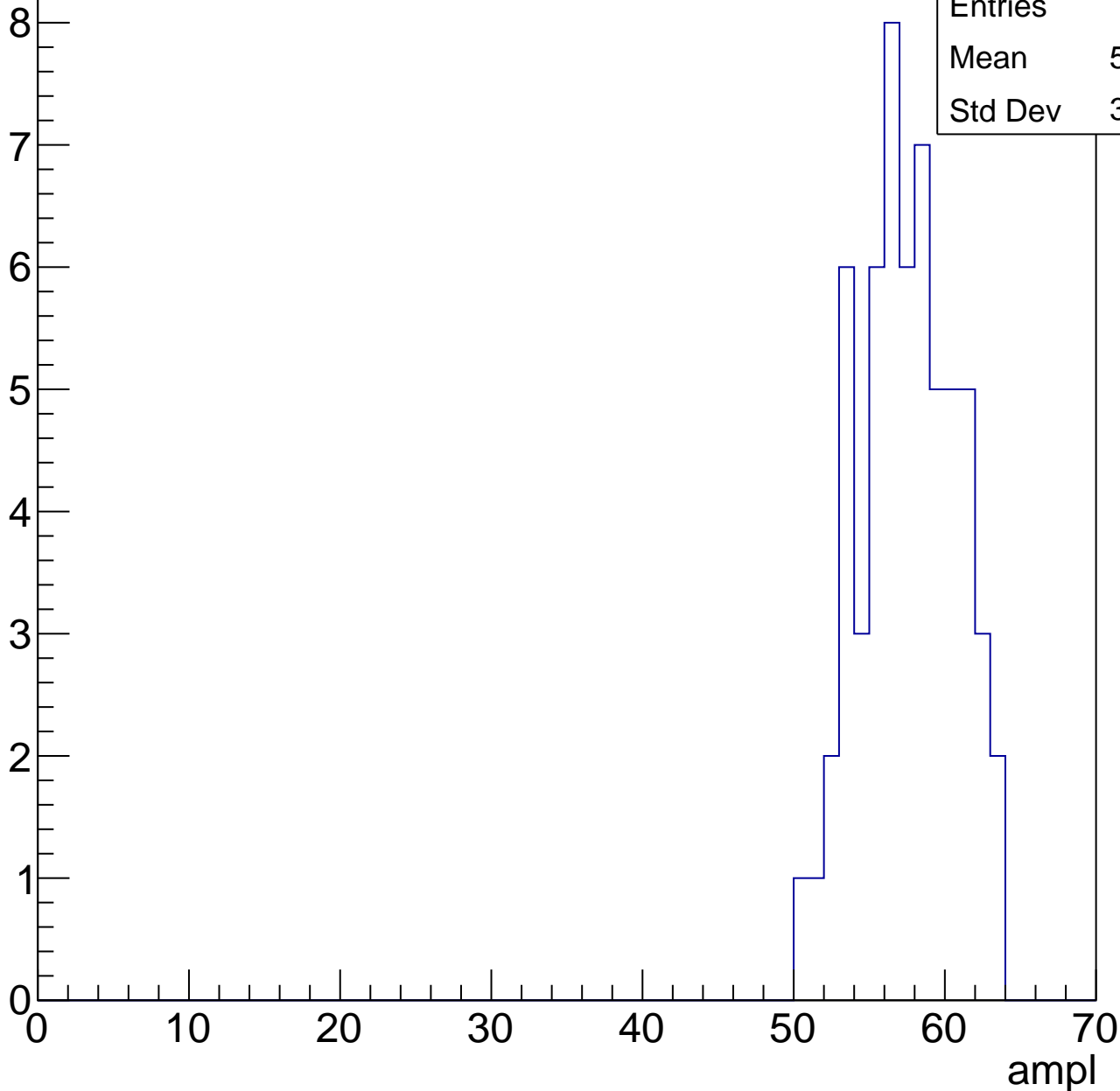


# B0L000S, U13-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.05
Std Dev	3.133

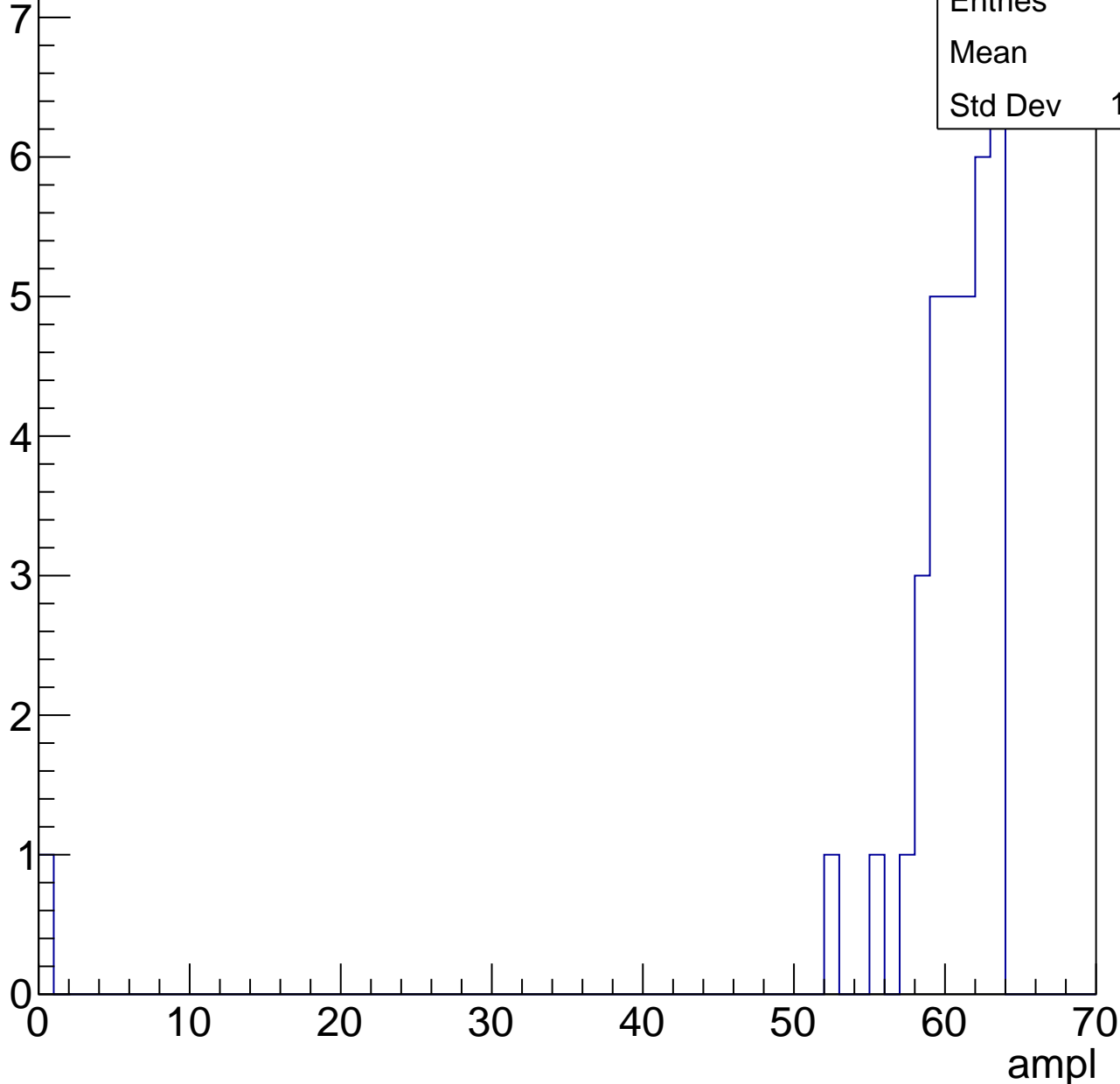


# B0L000S, U13-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

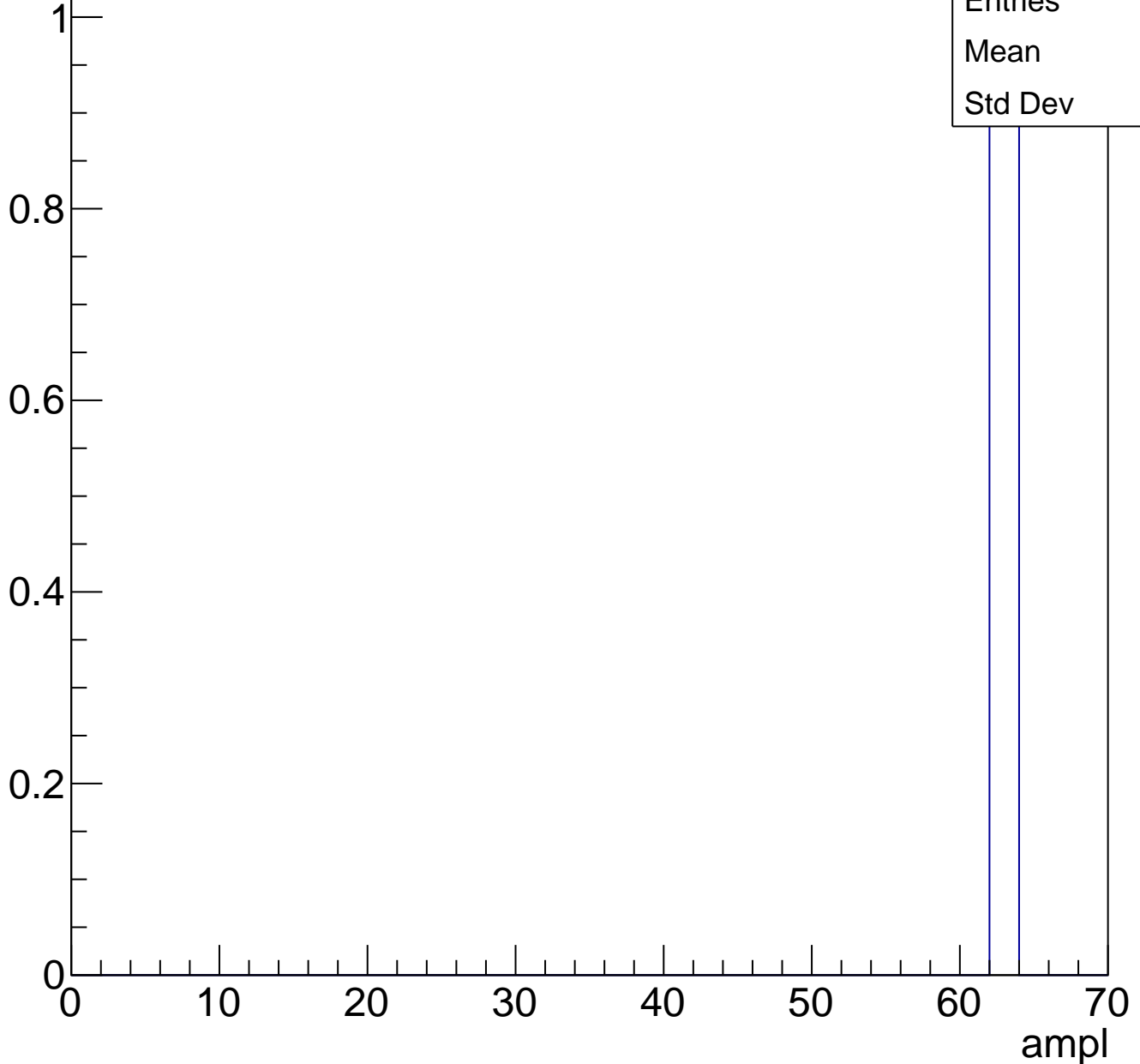
Entries	35
Mean	58.6
Std Dev	10.33



# B0L000S, U13-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch115, adc0

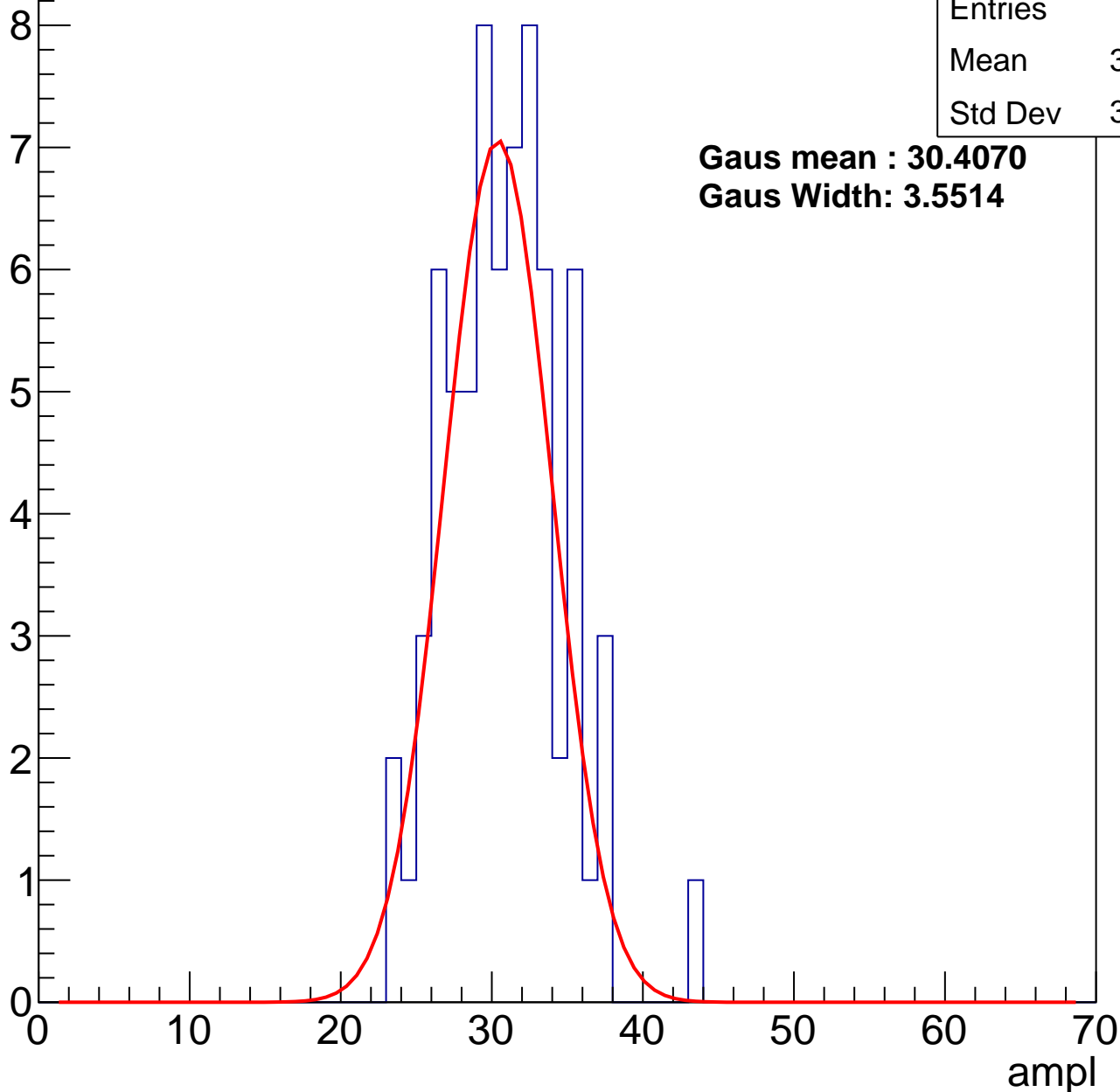
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.39
Std Dev	3.769

**Gaus mean : 30.4070**

**Gaus Width: 3.5514**



# B0L000S, U13-ch115, adc1

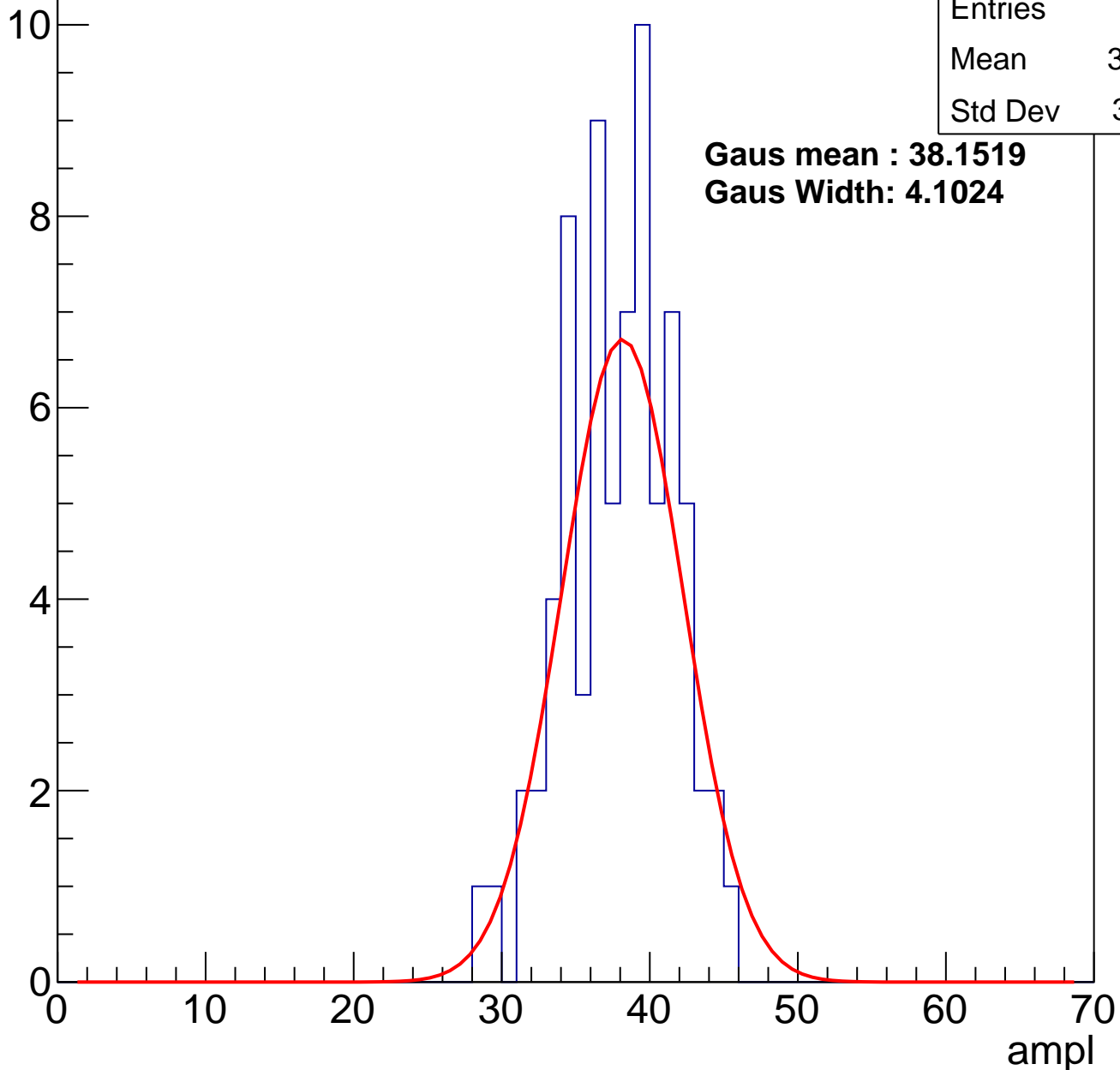
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	37.47
Std Dev	3.621

**Gaus mean : 38.1519**

**Gaus Width: 4.1024**

Entry



# B0L000S, U13-ch115, adc2

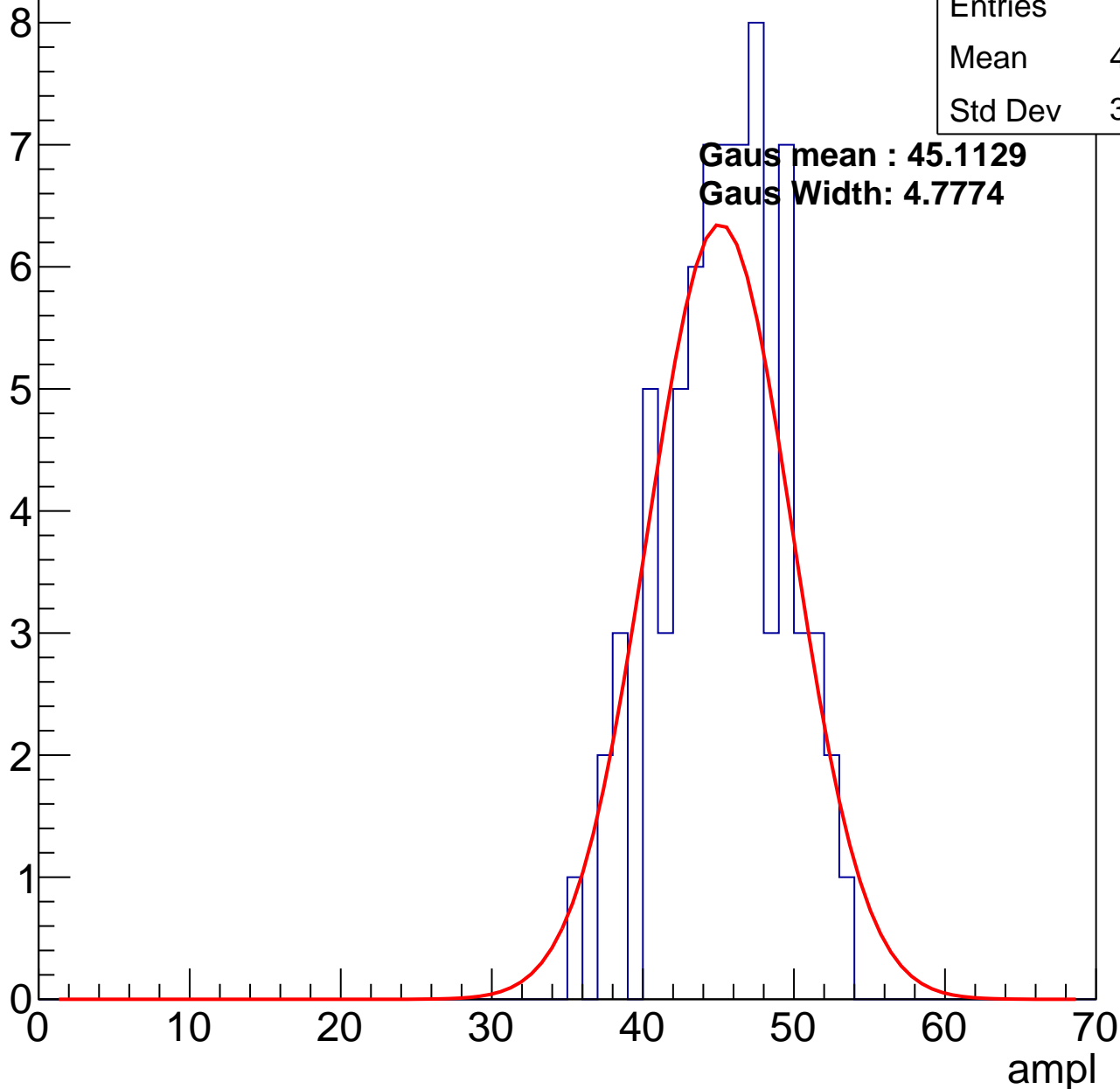
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	44.96
Std Dev	3.964

Gaus mean : 45.1129

Gaus Width: 4.7774

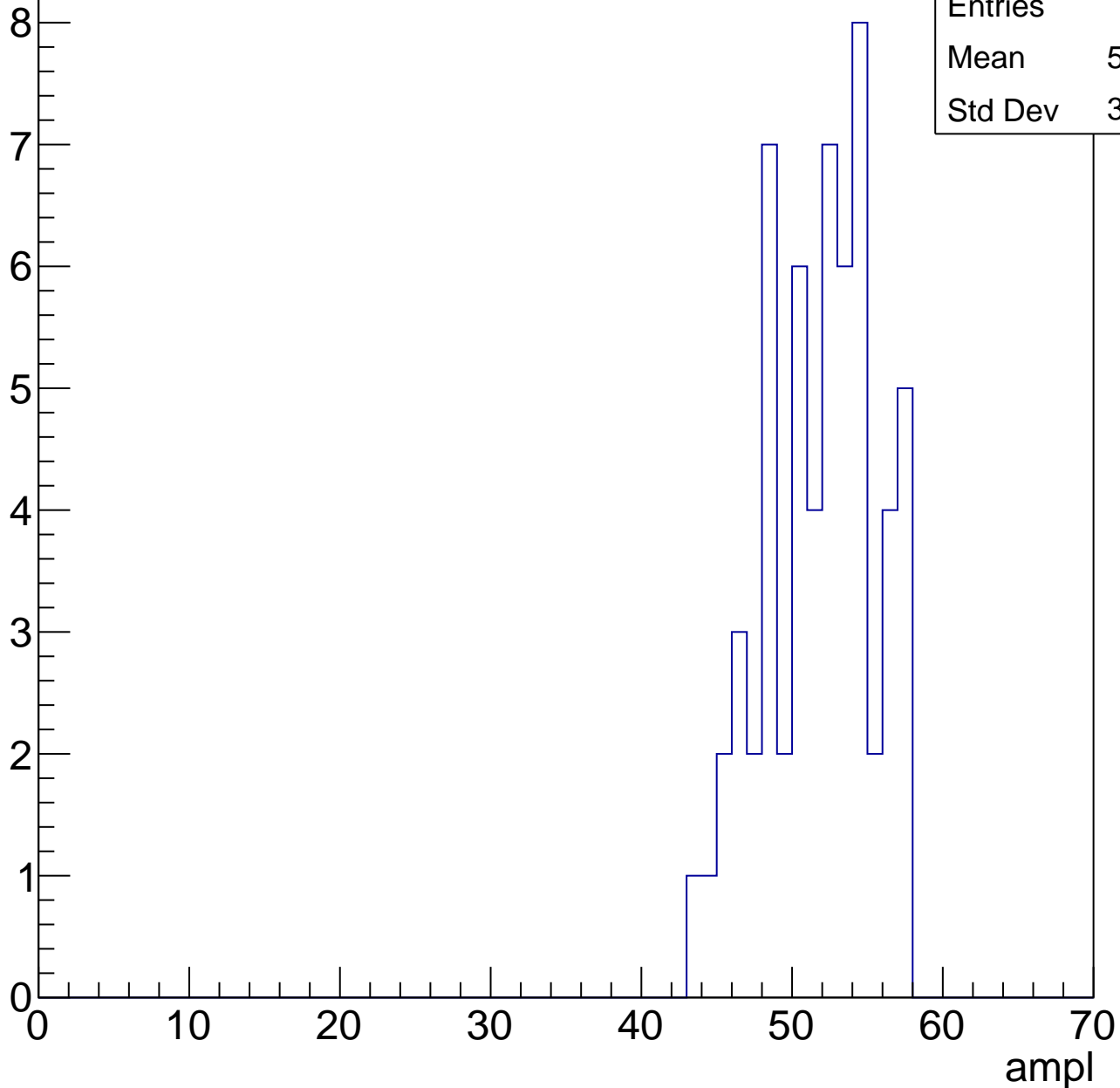


# B0L000S, U13-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.33
Std Dev	3.599



# B0L000S, U13-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

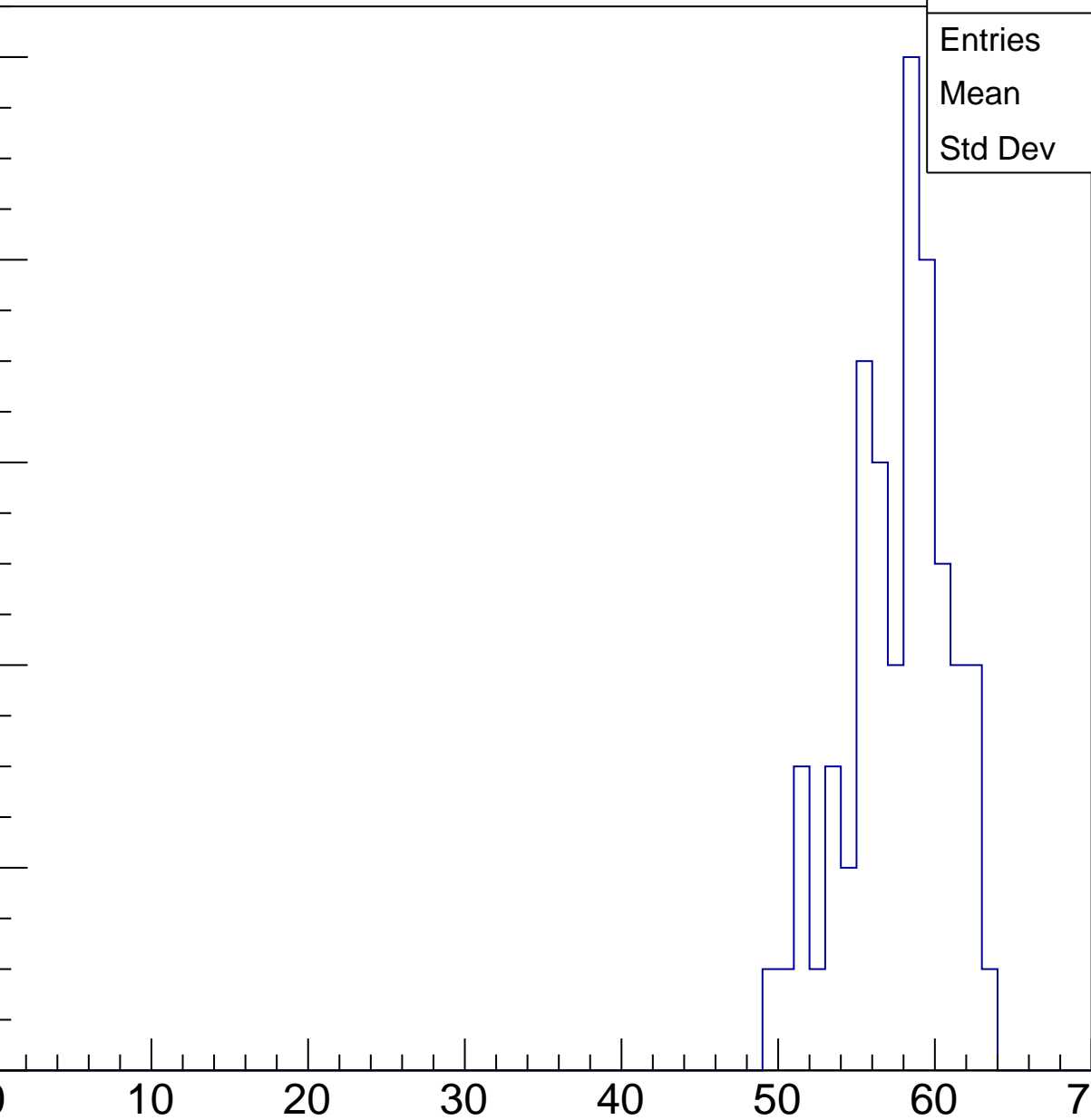
Entries	60
Mean	57.12
Std Dev	3.246

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

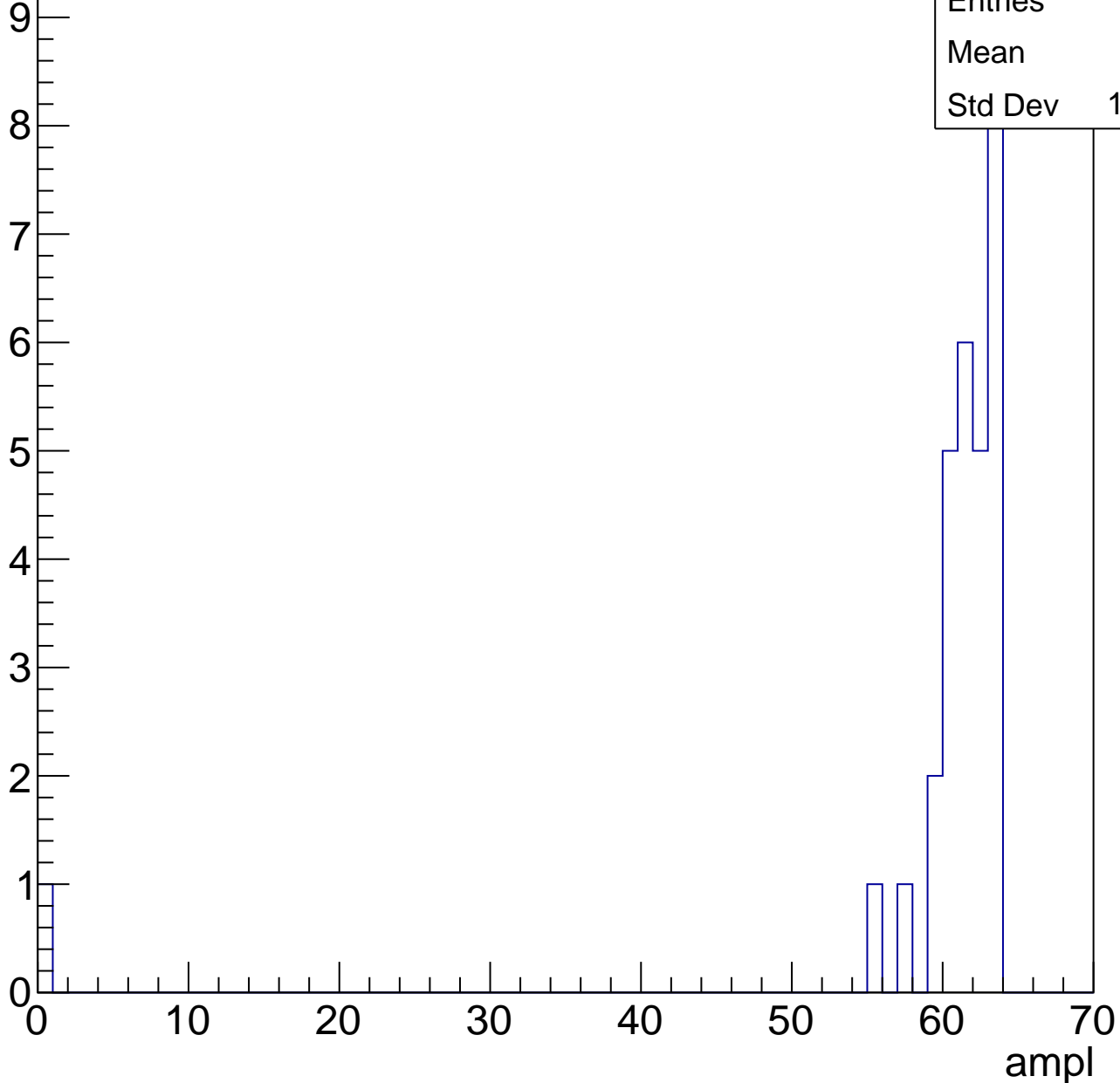


# B0L000S, U13-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	59.1
Std Dev	11.13



# B0L000S, U13-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch116, adc0

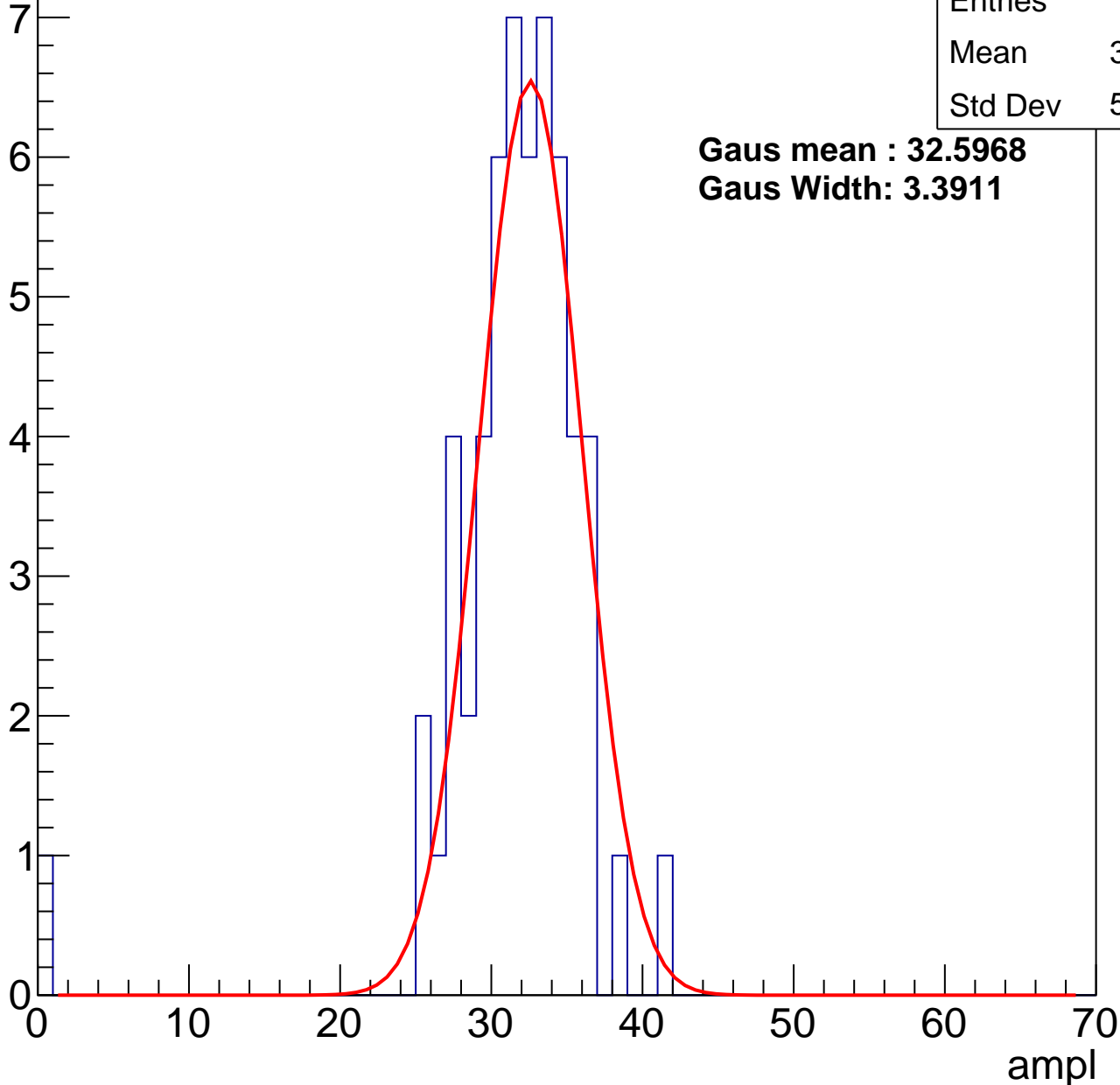
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	31.12
Std Dev	5.282

**Gaus mean : 32.5968**

**Gaus Width: 3.3911**



# B0L000S, U13-ch116, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	39.64
Std Dev	3.591

**Gaus mean : 40.3581**

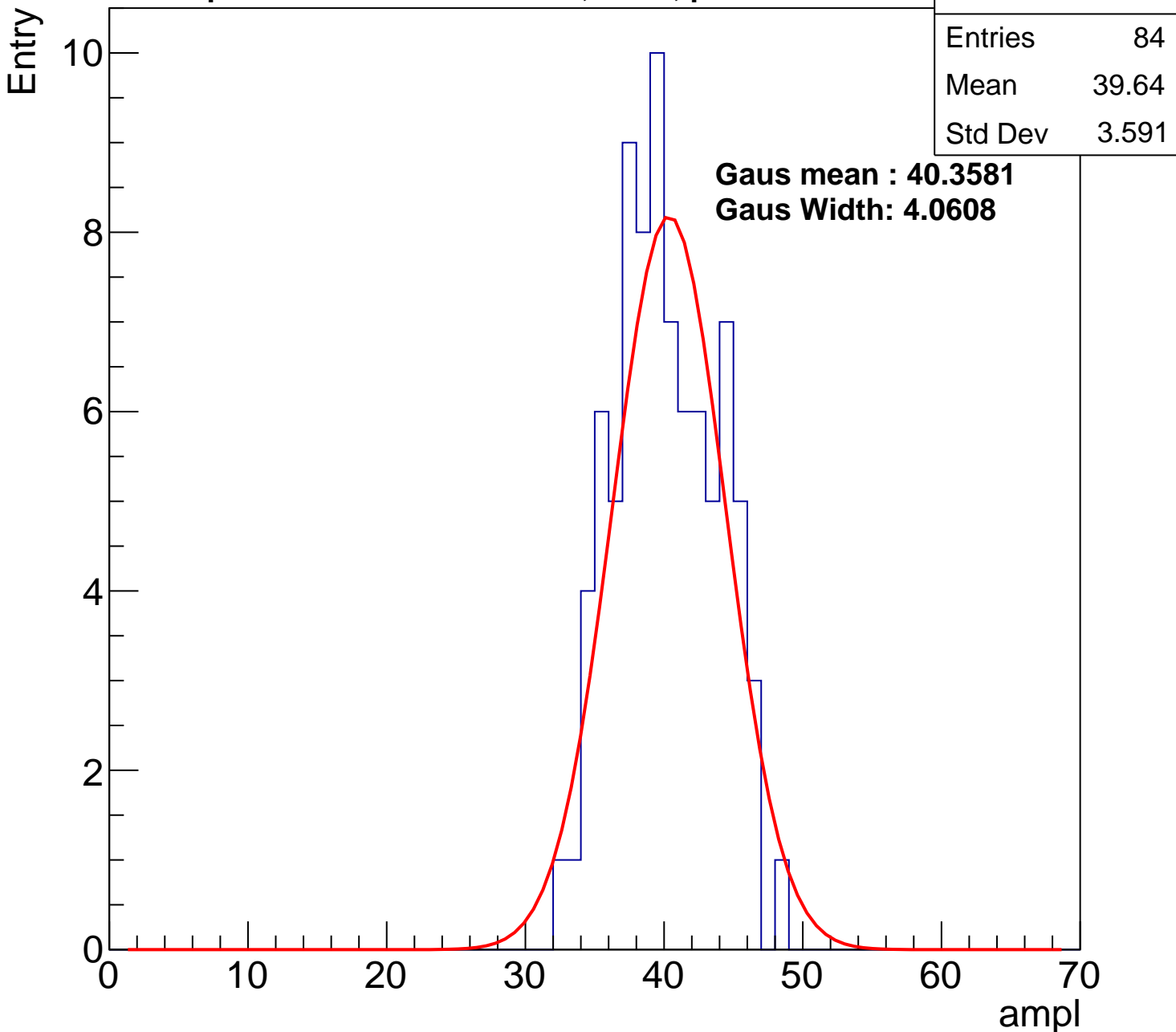
**Gaus Width: 4.0608**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch116, adc2

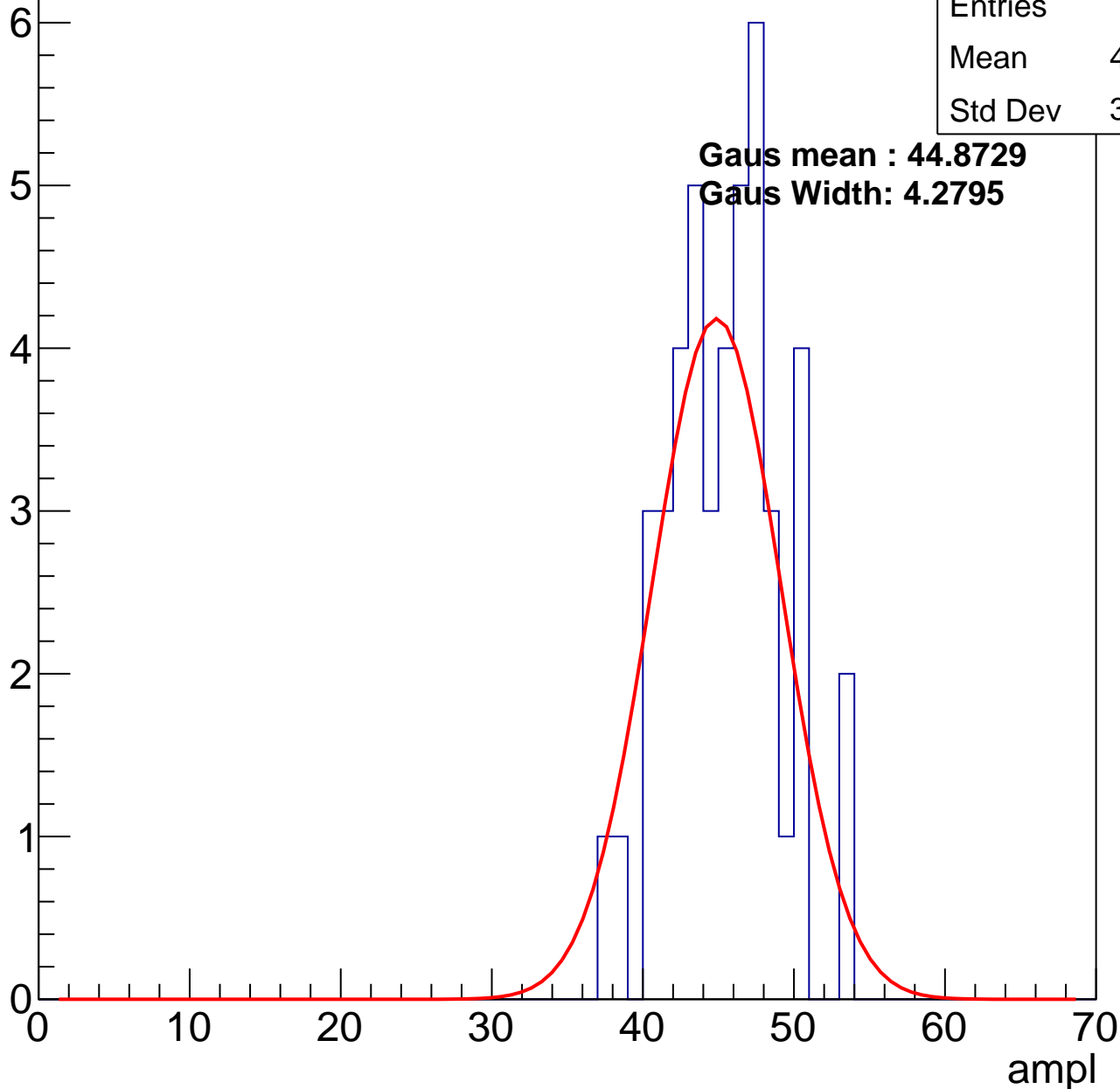
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	44.98
Std Dev	3.636

**Gaus mean : 44.8729**

**Gaus Width: 4.2795**

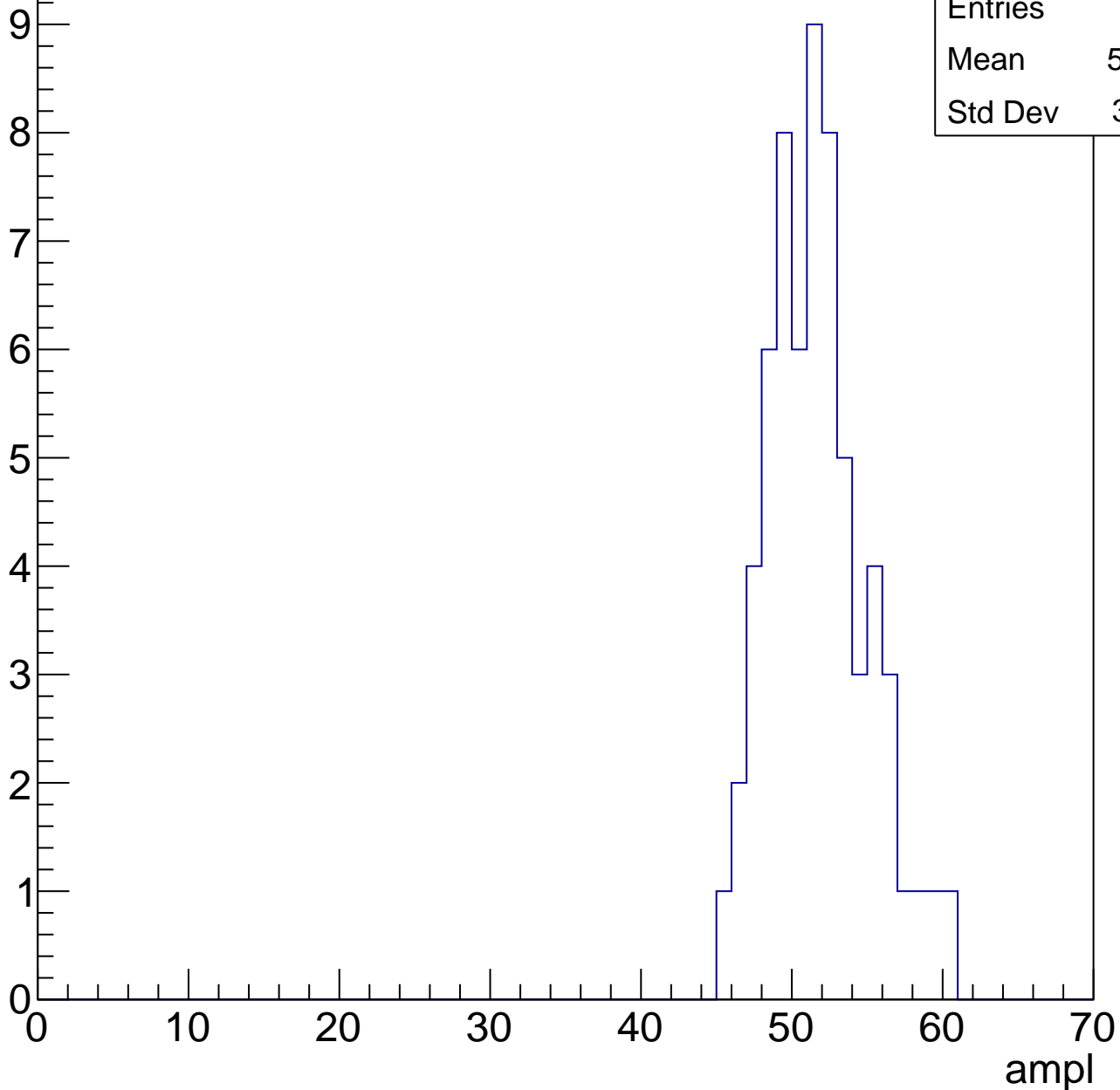


# B0L000S, U13-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	51.25
Std Dev	3.241

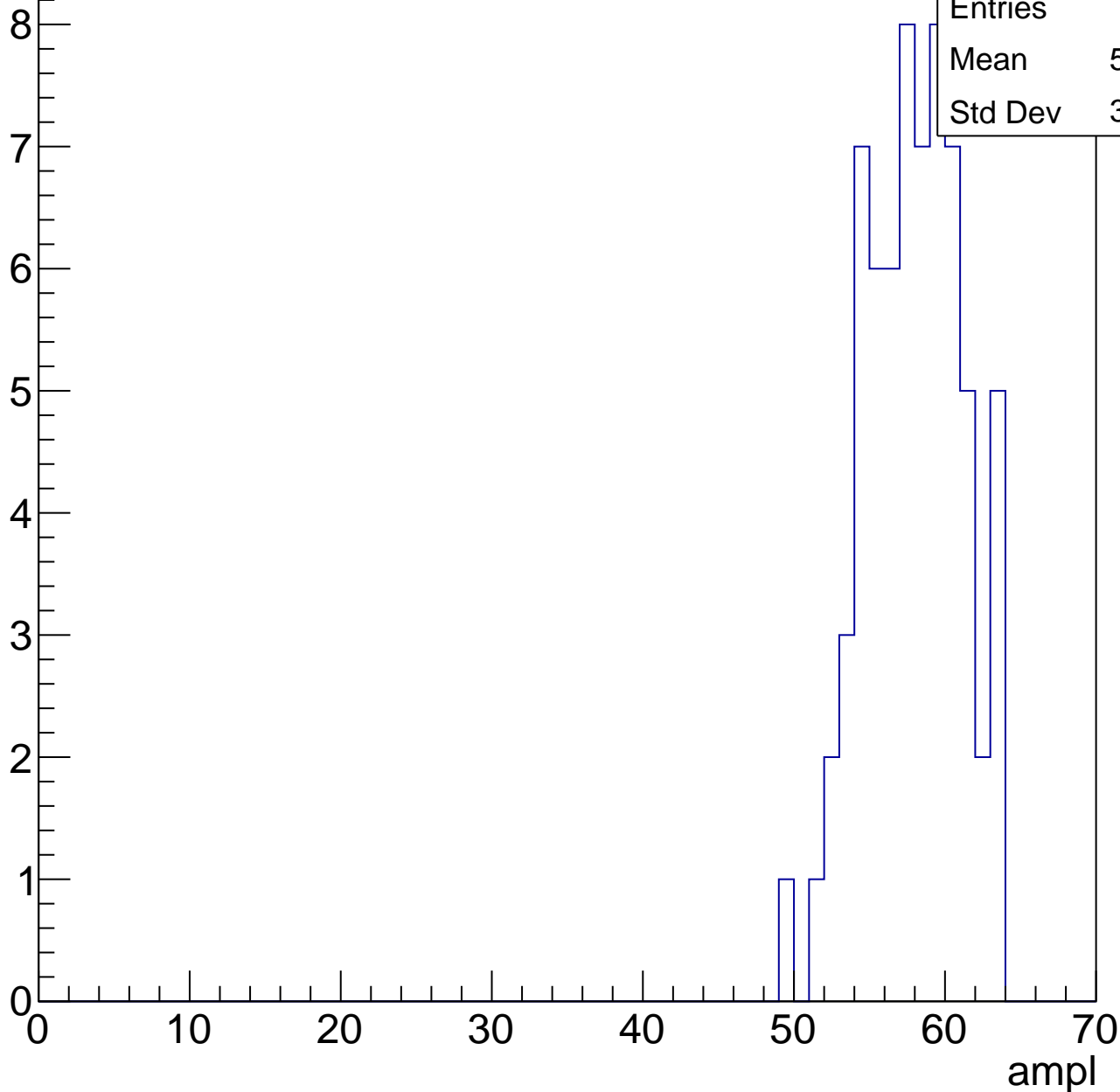


# B0L000S, U13-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	57.43
Std Dev	3.187

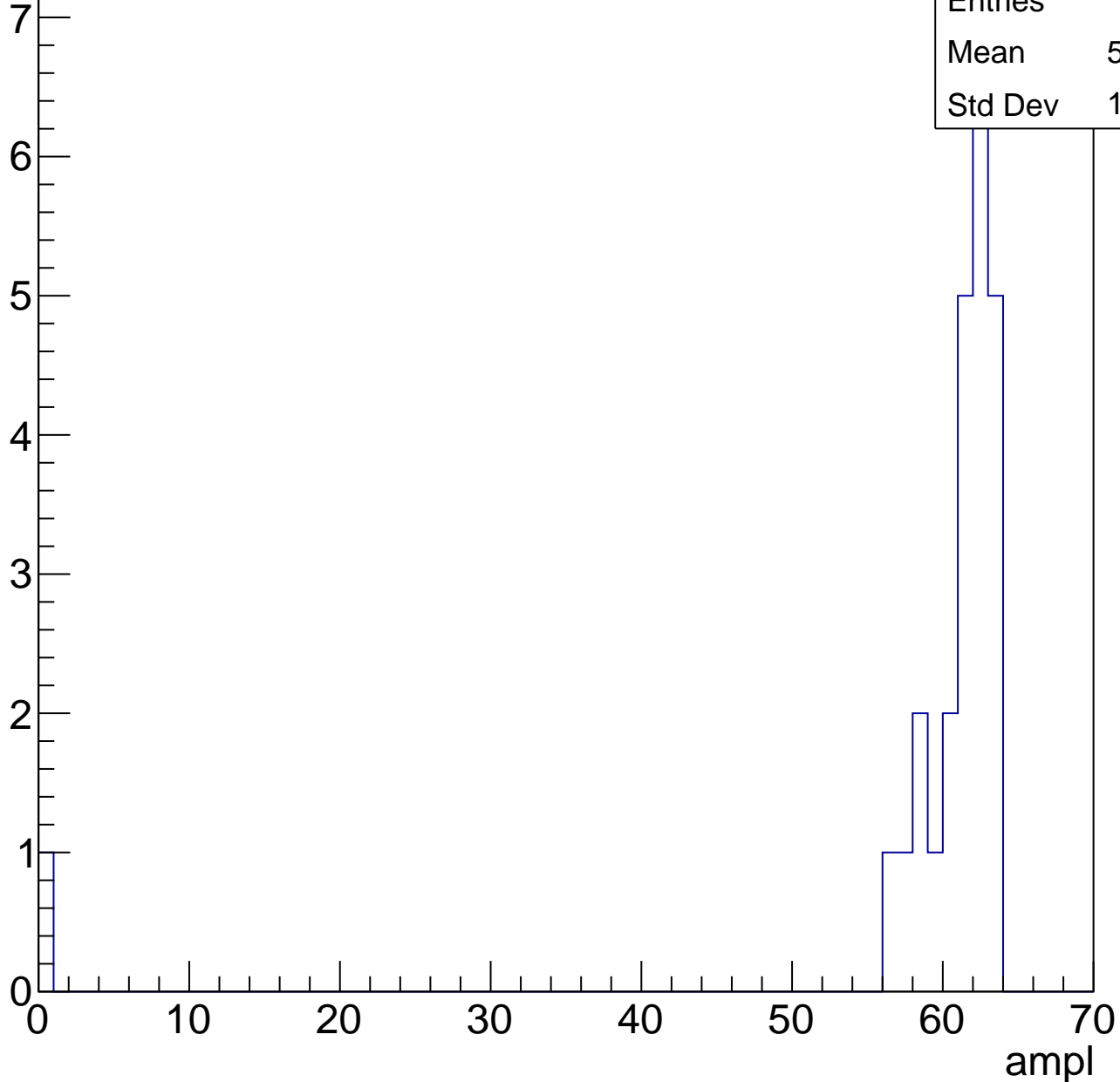


# B0L000S, U13-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	58.48
Std Dev	12.09



# B0L000S, U13-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0



# B0L000S, U13-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch117, adc0

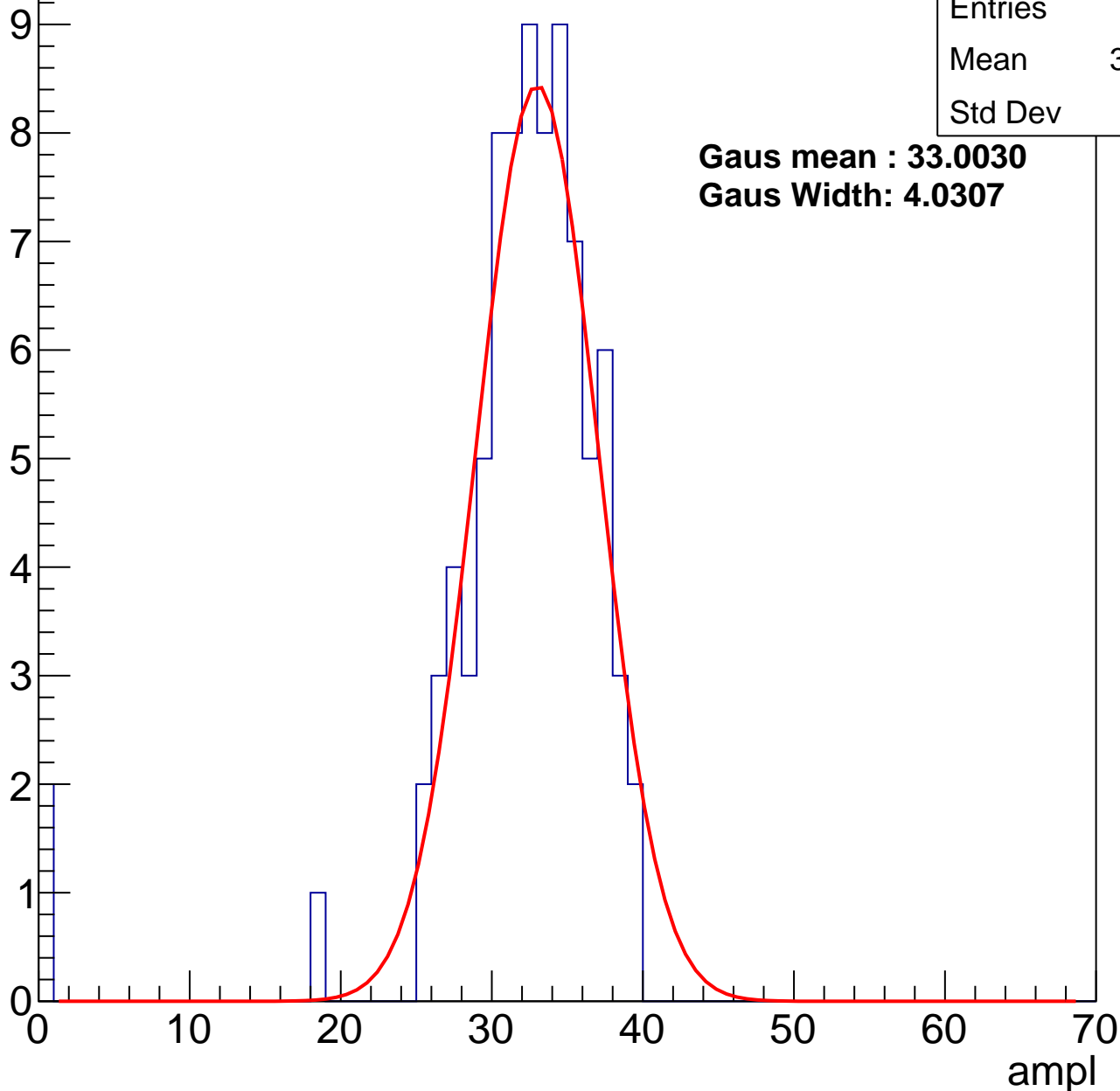
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	31.39
Std Dev	6.13

**Gaus mean : 33.0030**

**Gaus Width: 4.0307**



# B0L000S, U13-ch117, adc1

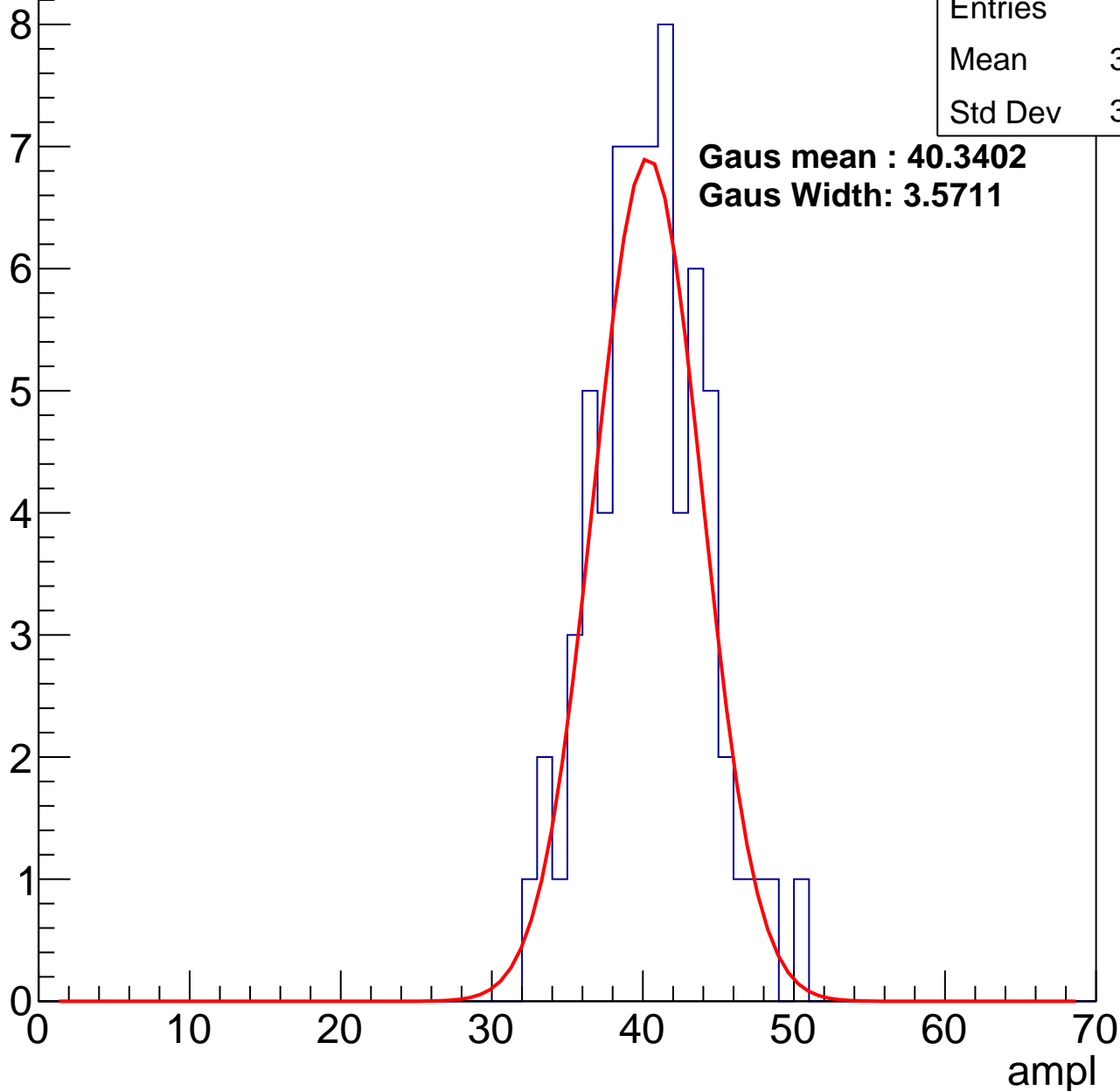
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	39.98
Std Dev	3.654

**Gaus mean : 40.3402**

**Gaus Width: 3.5711**



# B0L000S, U13-ch117, adc2

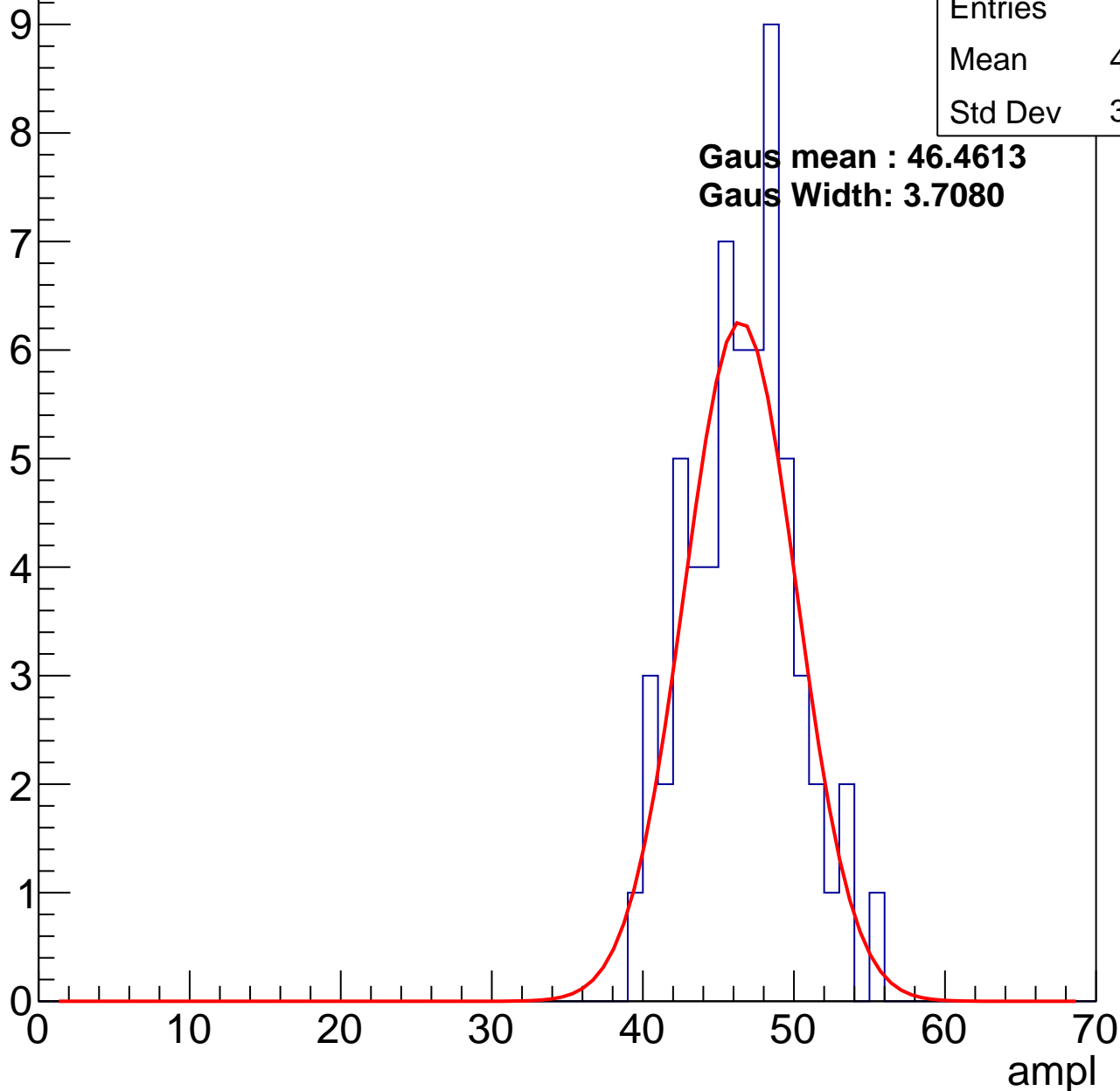
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	46.13
Std Dev	3.495

**Gaus mean : 46.4613**

**Gaus Width: 3.7080**

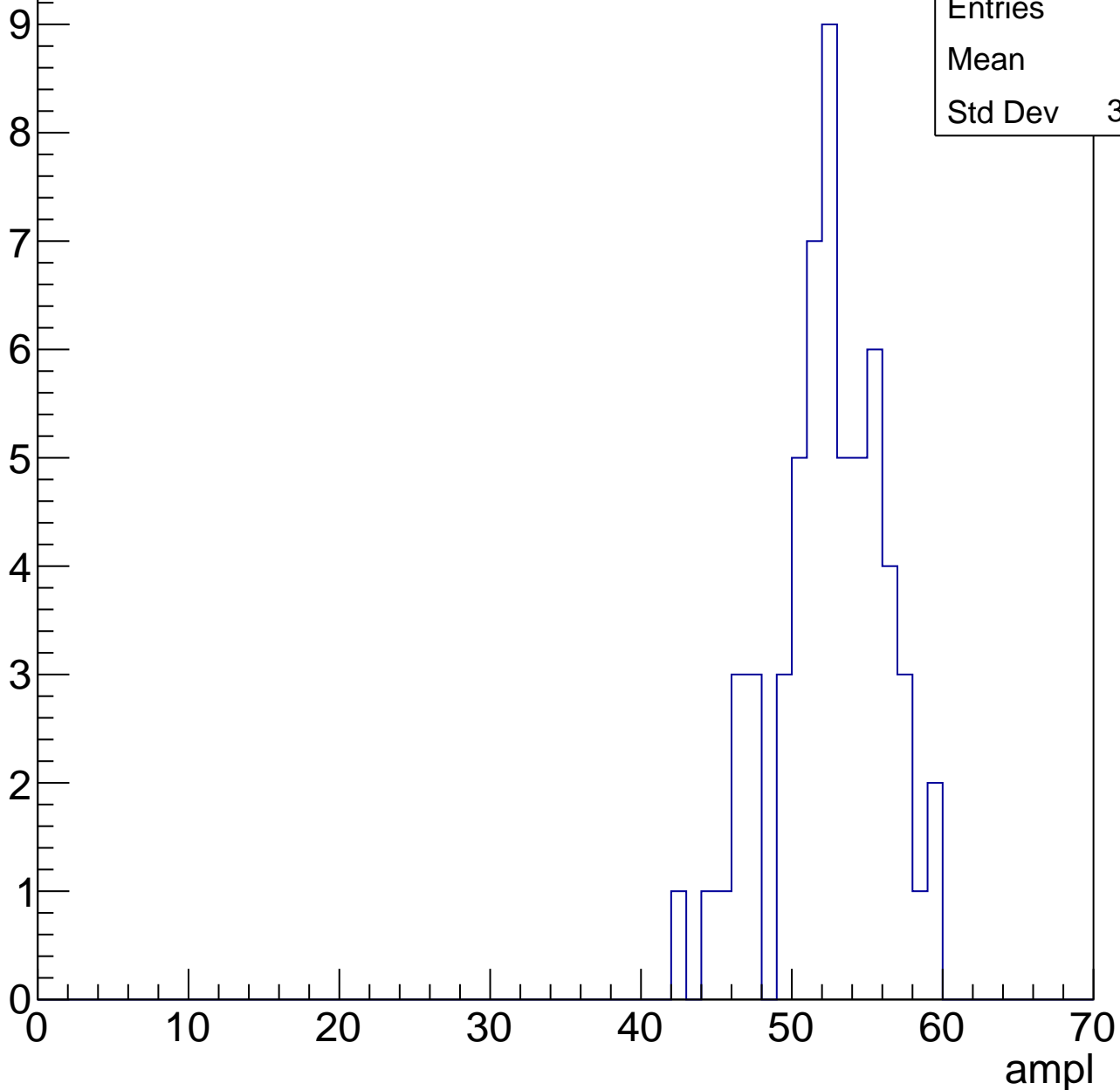


# B0L000S, U13-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	52
Std Dev	3.687

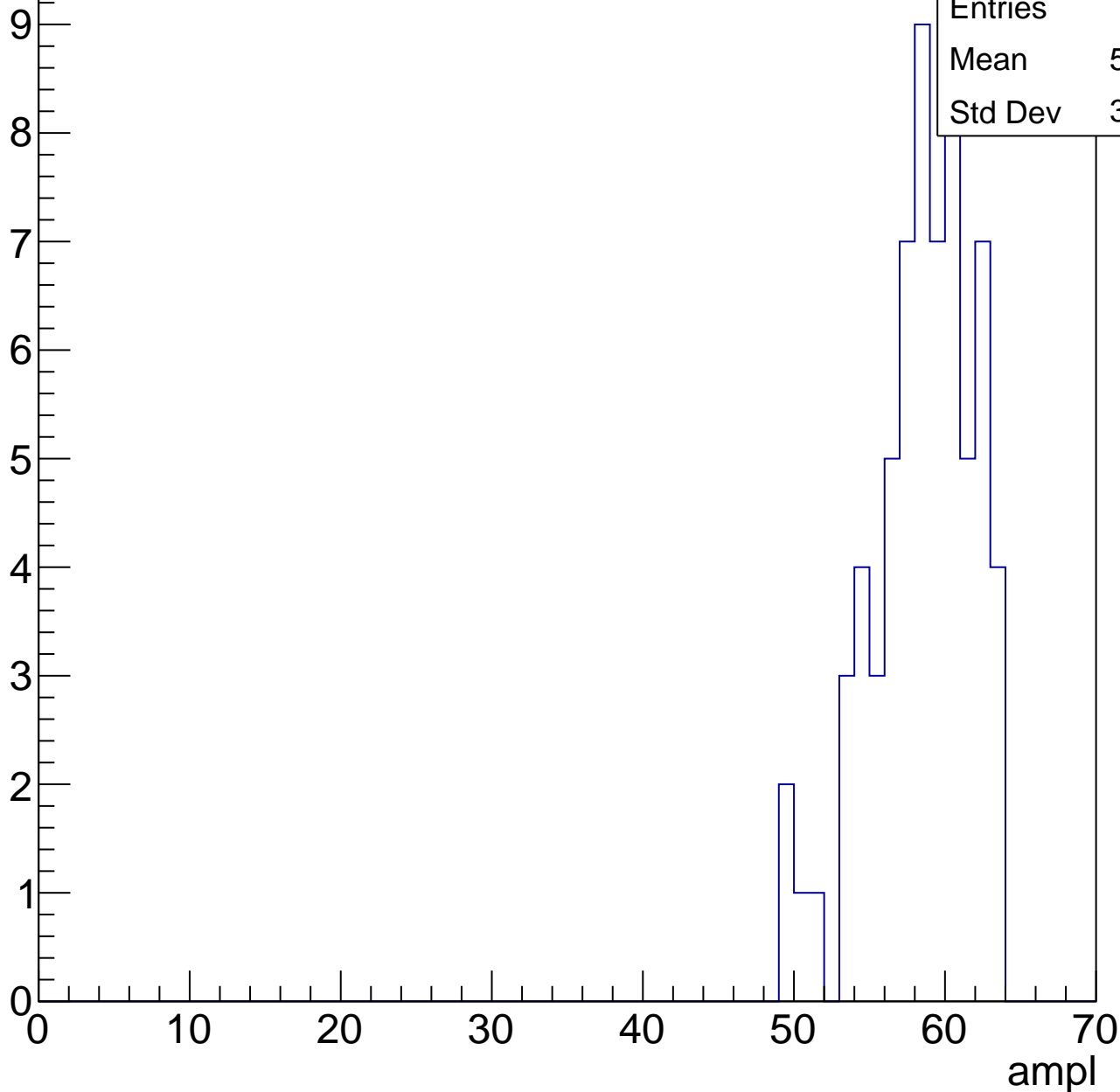


# B0L000S, U13-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	57.94
Std Dev	3.402

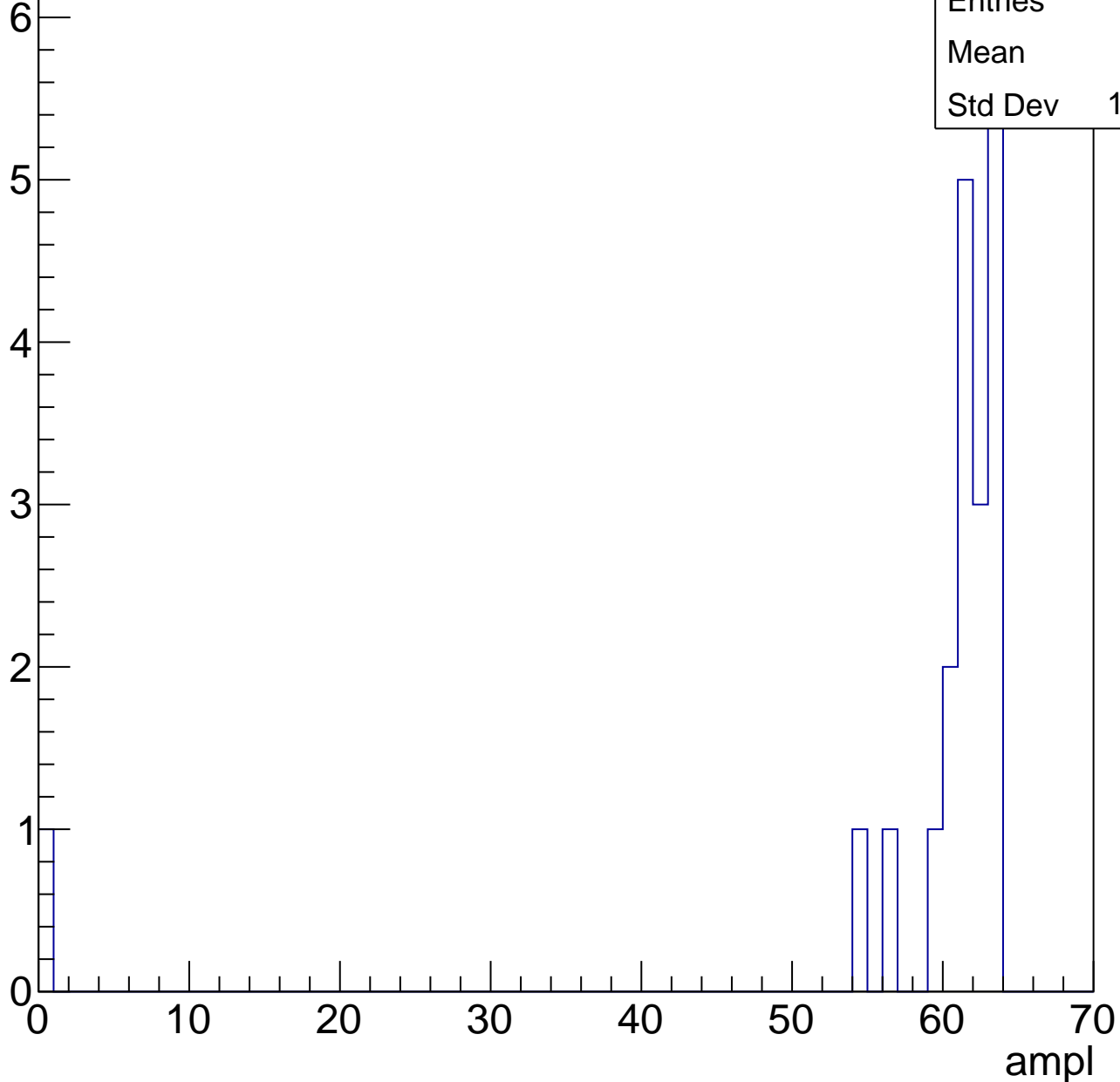


# B0L000S, U13-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	57.9
Std Dev	13.48



# B0L000S, U13-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch118, adc0

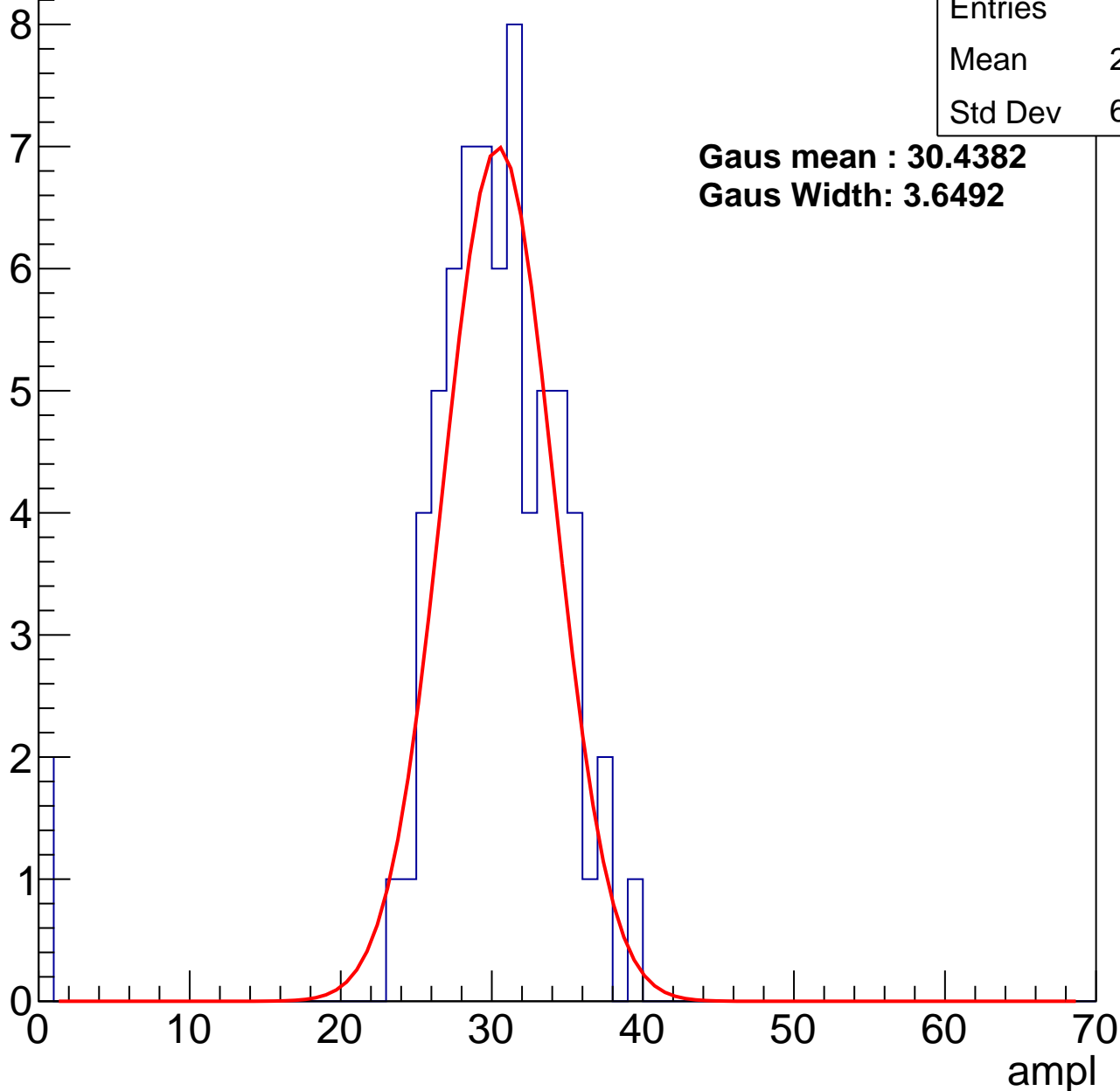
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.25
Std Dev	6.113

**Gaus mean : 30.4382**

**Gaus Width: 3.6492**



# B0L000S, U13-ch118, adc1

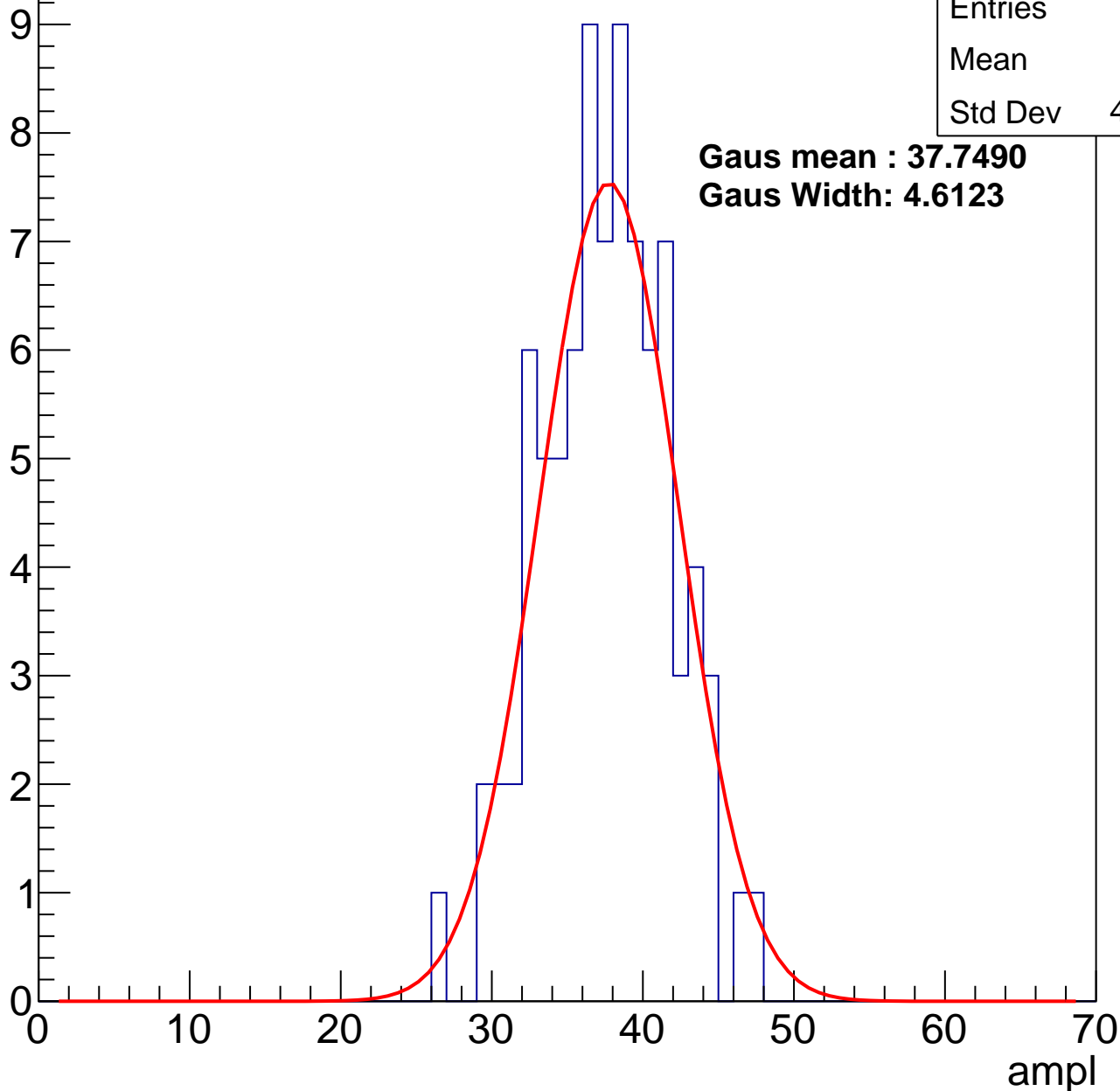
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	37.1
Std Dev	4.143

**Gaus mean : 37.7490**

**Gaus Width: 4.6123**



# B0L000S, U13-ch118, adc2

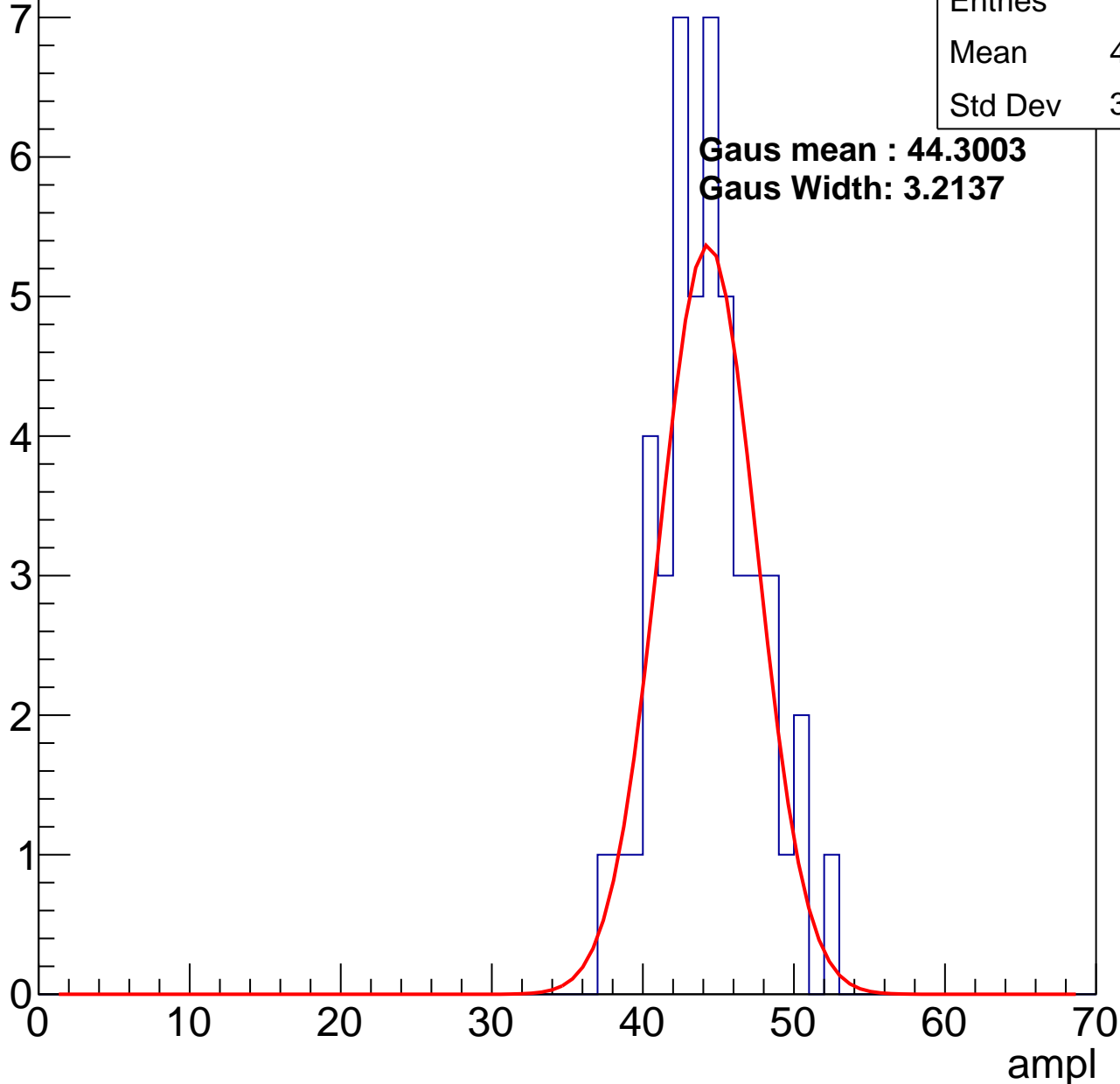
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	43.89
Std Dev	3.217

**Gaus mean : 44.3003**

**Gaus Width: 3.2137**

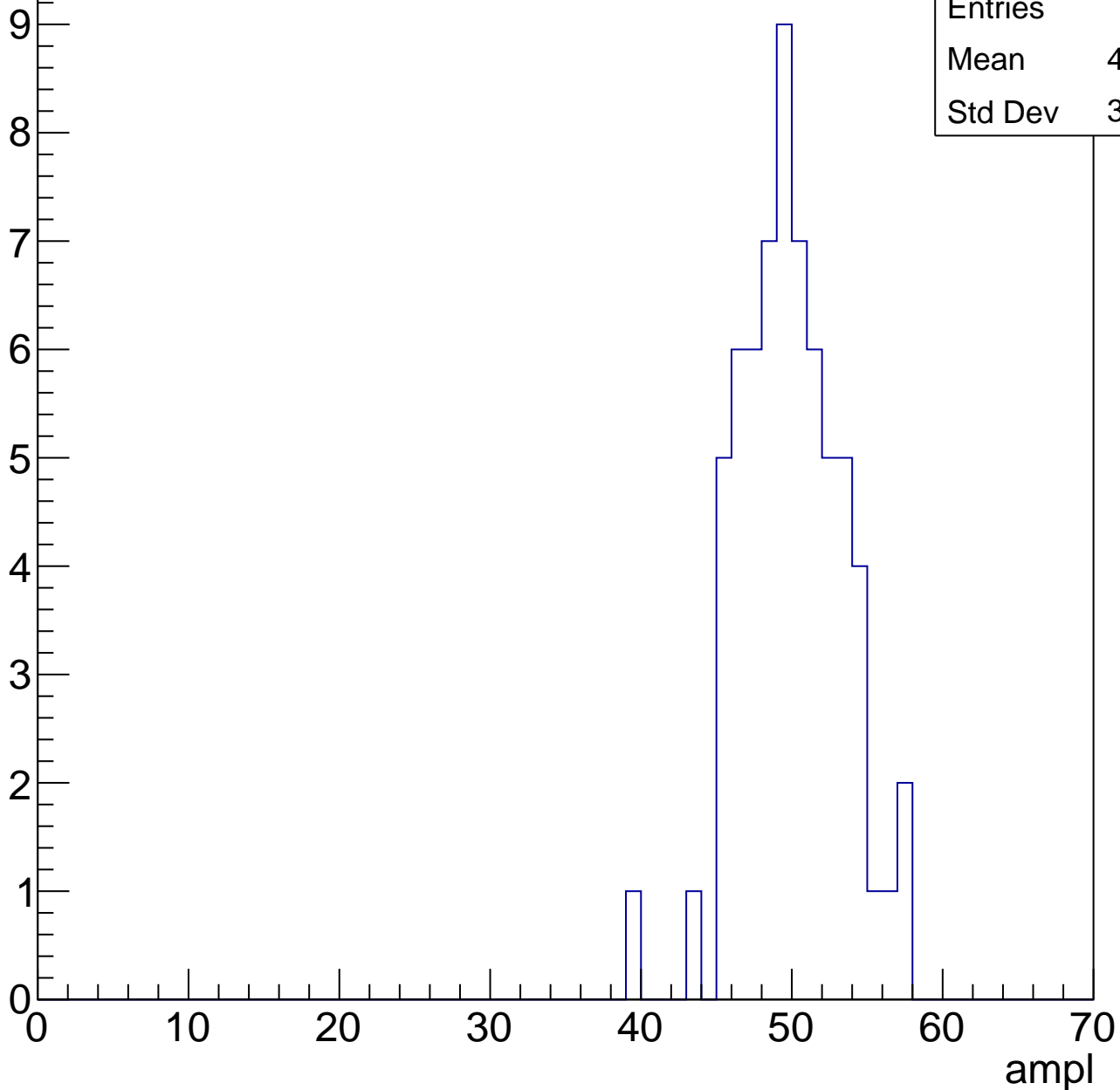


# B0L000S, U13-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	49.45
Std Dev	3.385

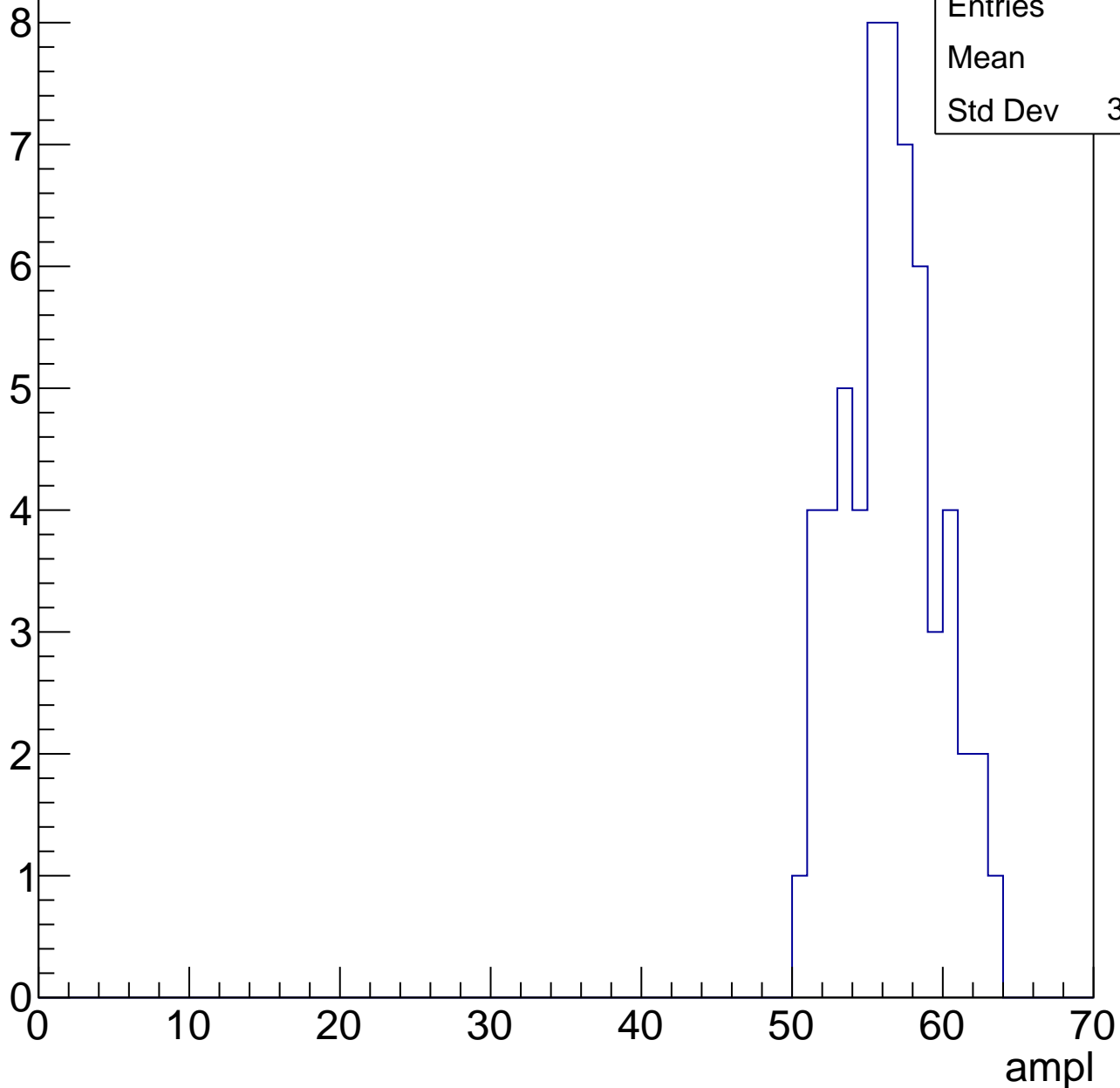


# B0L000S, U13-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

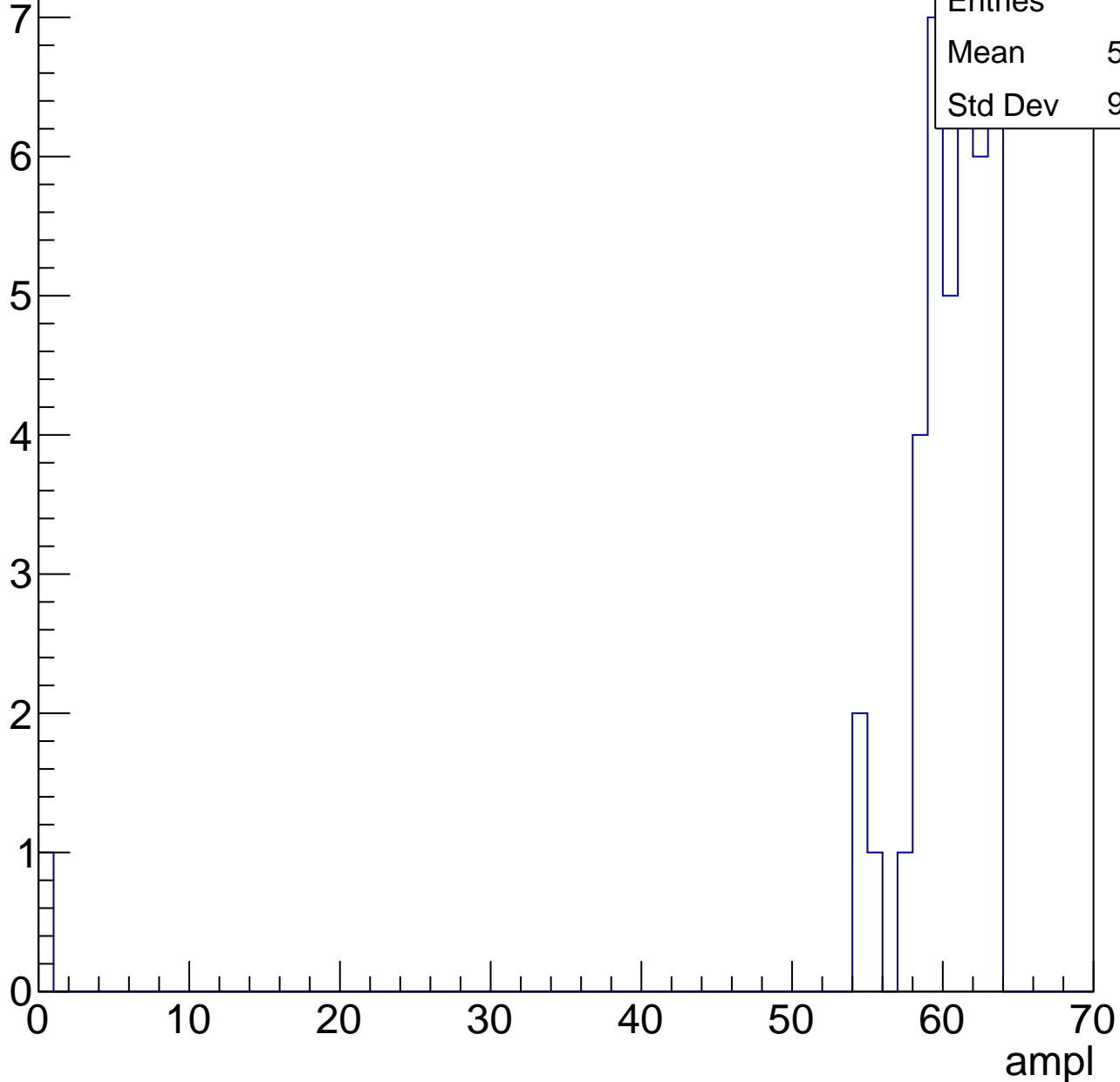
Entries	59
Mean	56
Std Dev	3.086



# B0L000S, U13-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

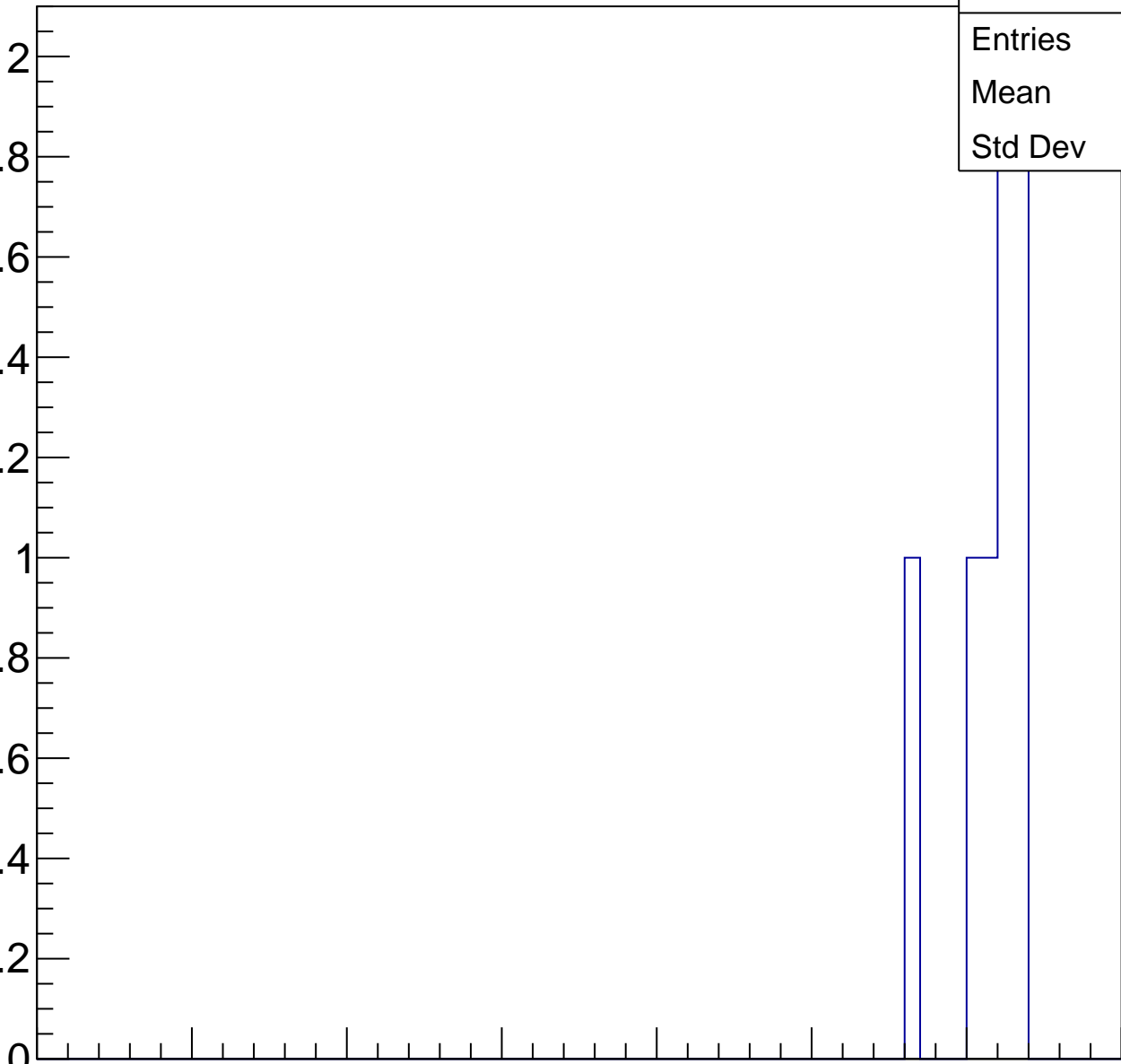
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61
Std Dev	2.268

0 10 20 30 40 50 60 70

ampl





# B0L000S, U13-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch119, adc0

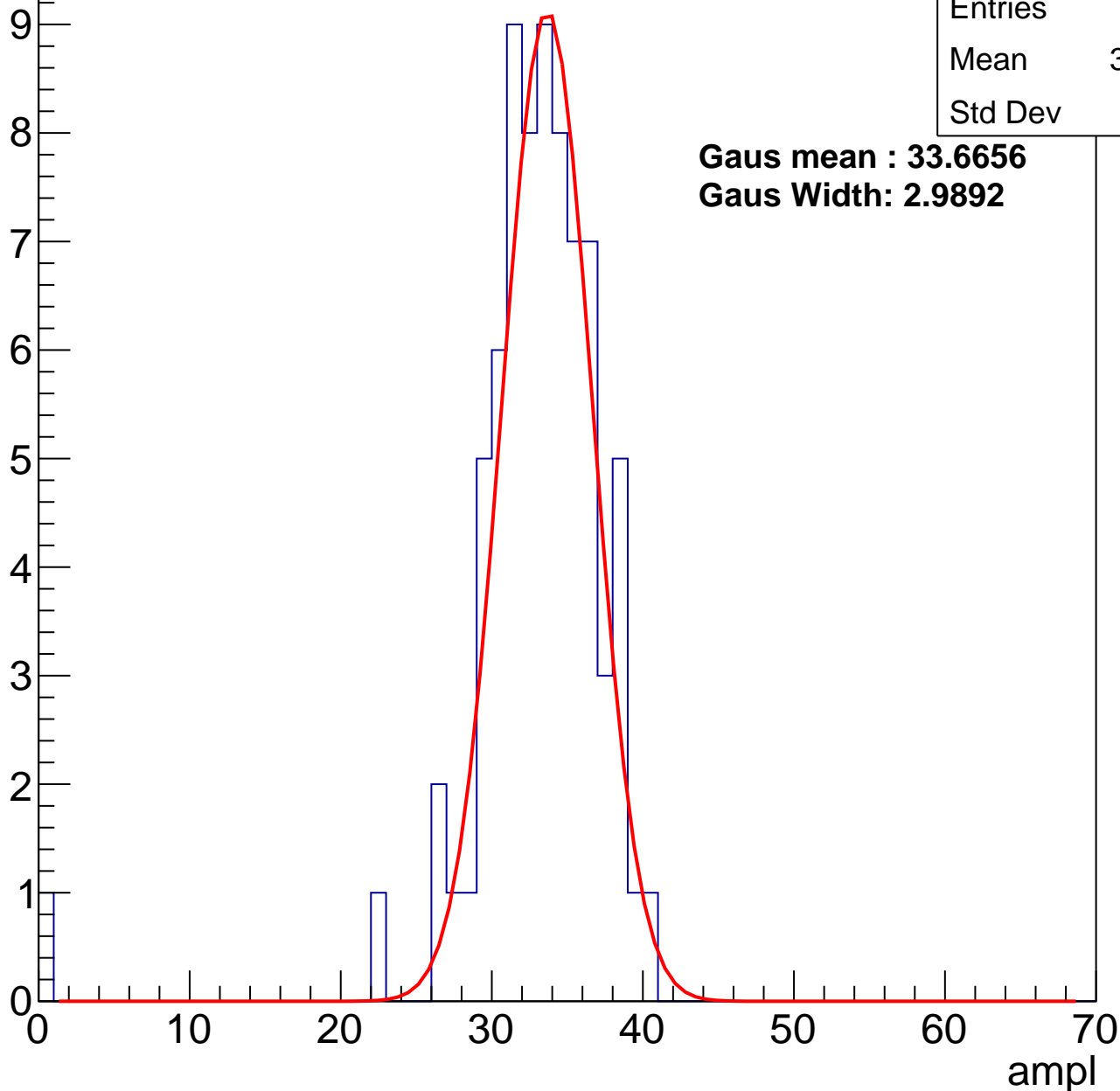
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	32.47
Std Dev	5.01

**Gaus mean : 33.6656**

**Gaus Width: 2.9892**



# B0L000S, U13-ch119, adc1

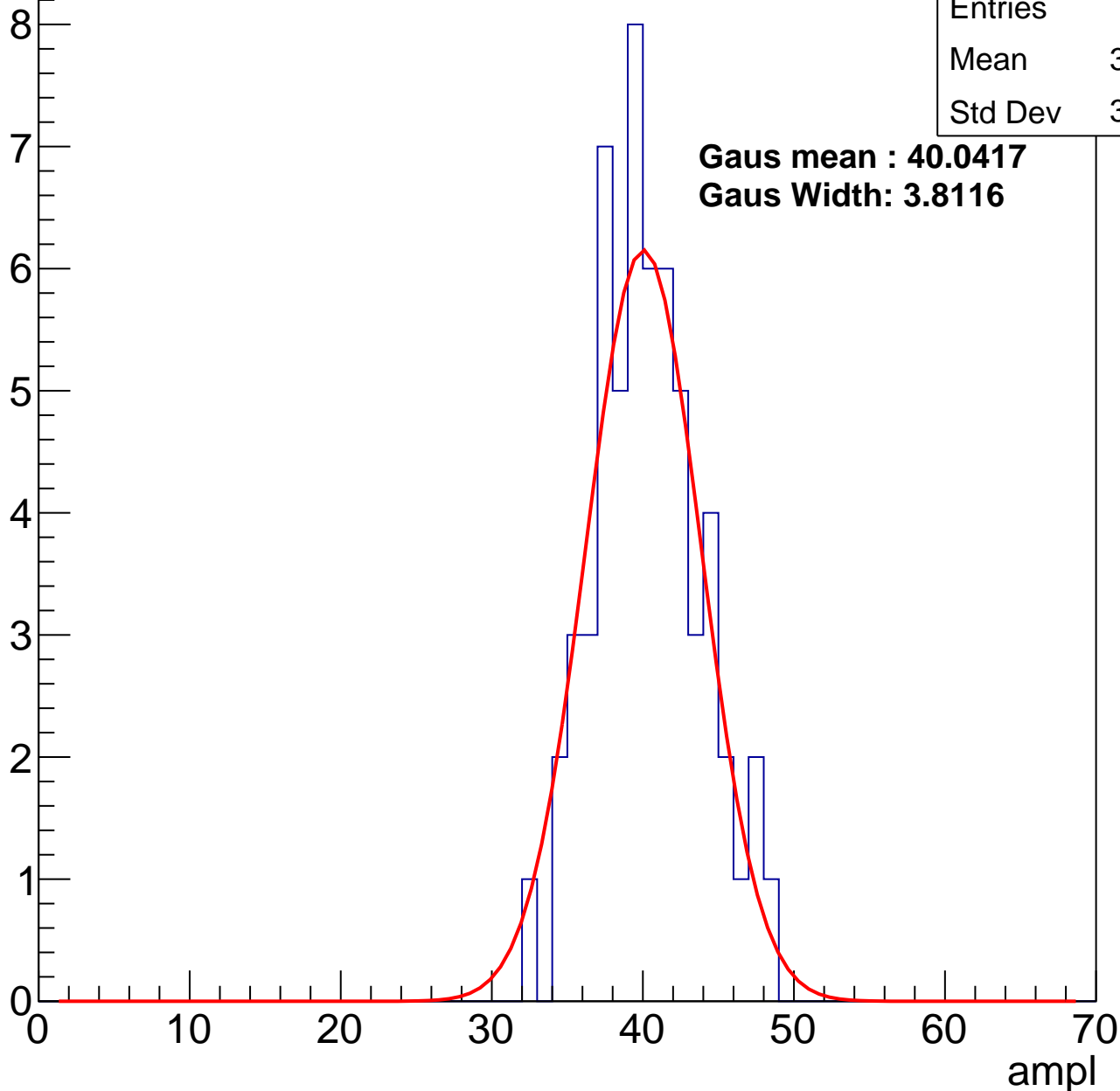
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	39.88
Std Dev	3.479

**Gaus mean : 40.0417**

**Gaus Width: 3.8116**



# B0L000S, U13-ch119, adc2

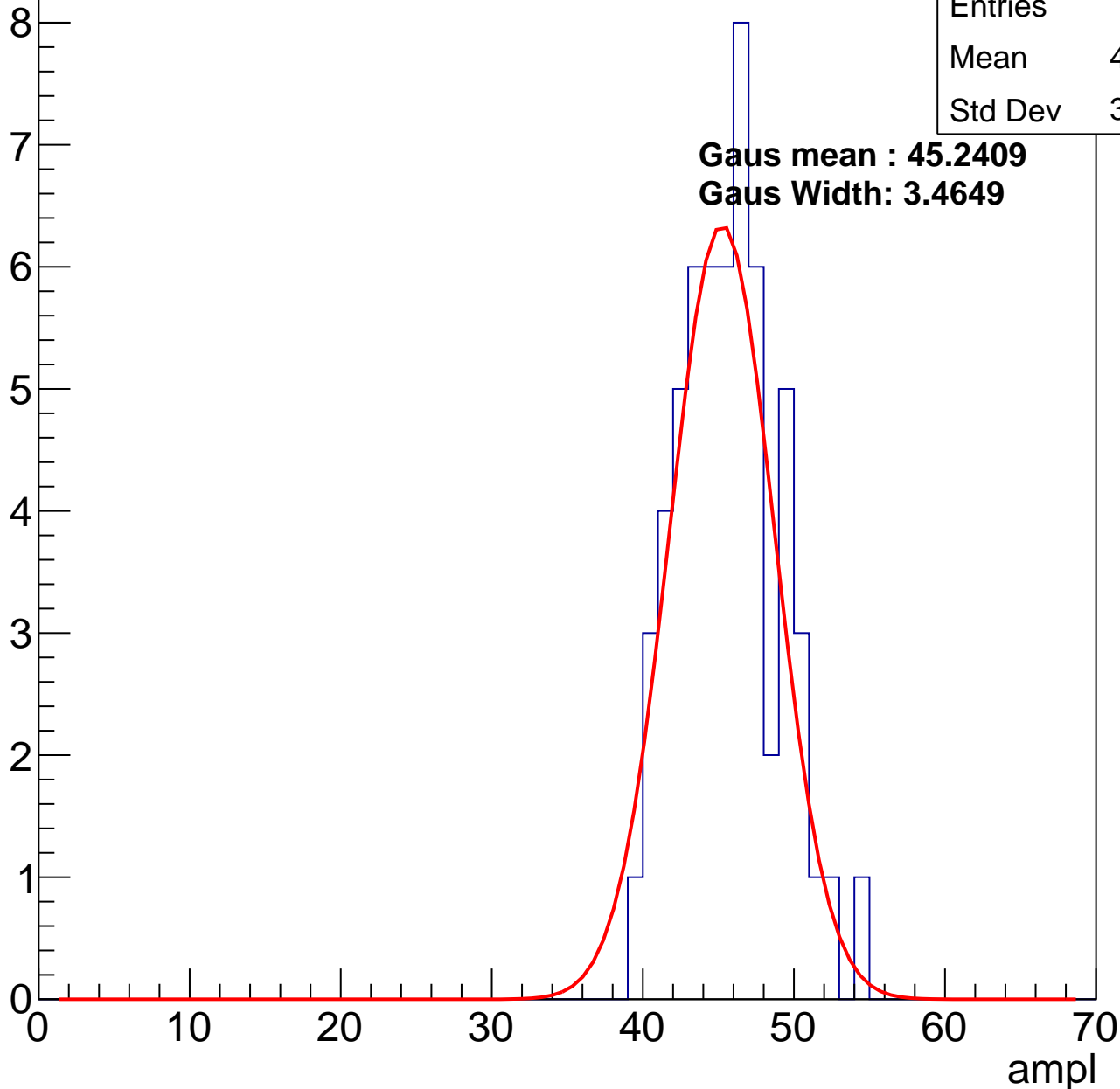
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	45.22
Std Dev	3.264

**Gaus mean : 45.2409**

**Gaus Width: 3.4649**



# B0L000S, U13-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

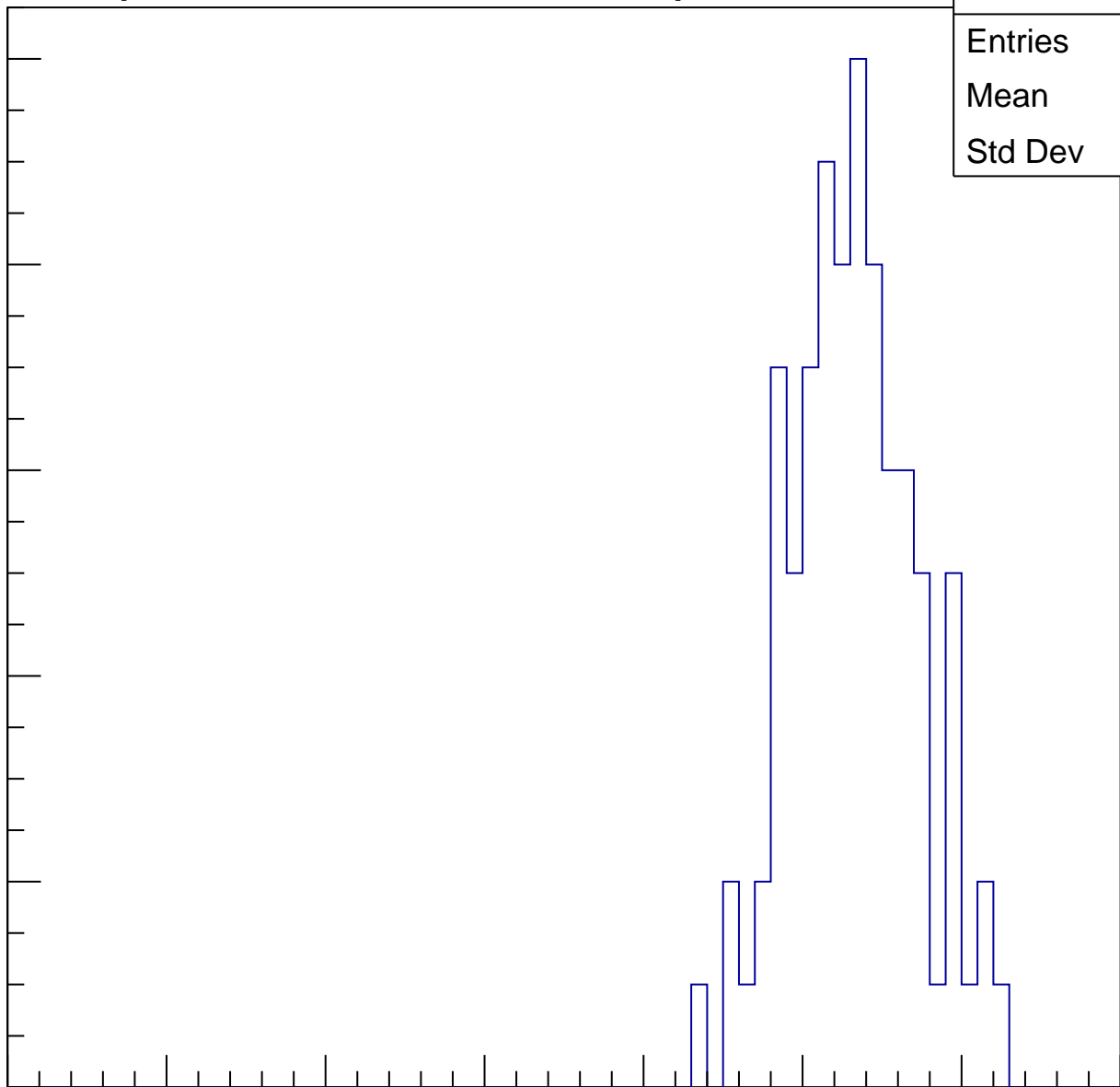
Entries	87
Mean	52.75
Std Dev	3.916

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U13-ch119, adc4

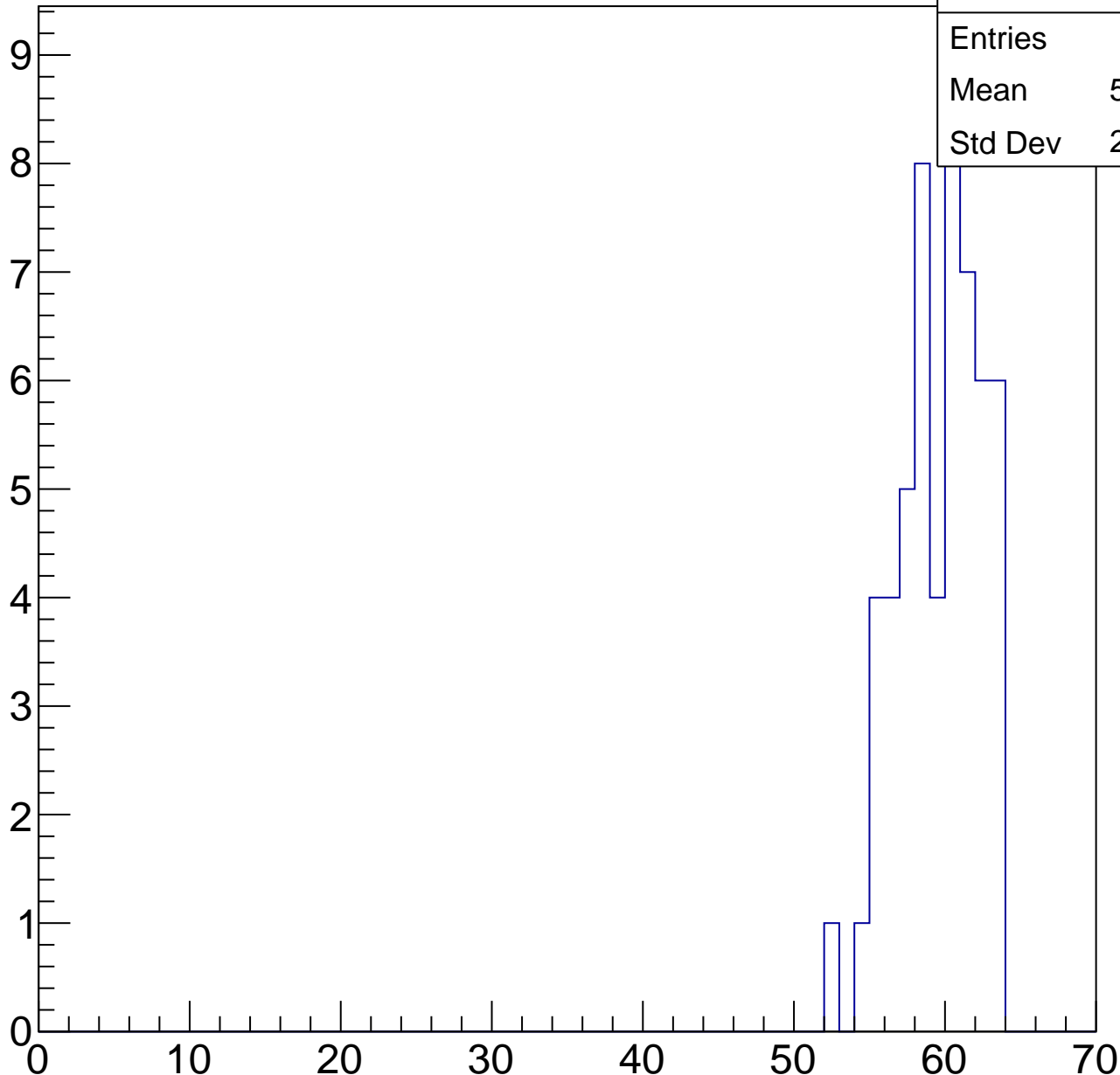
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	59.13
Std Dev	2.656

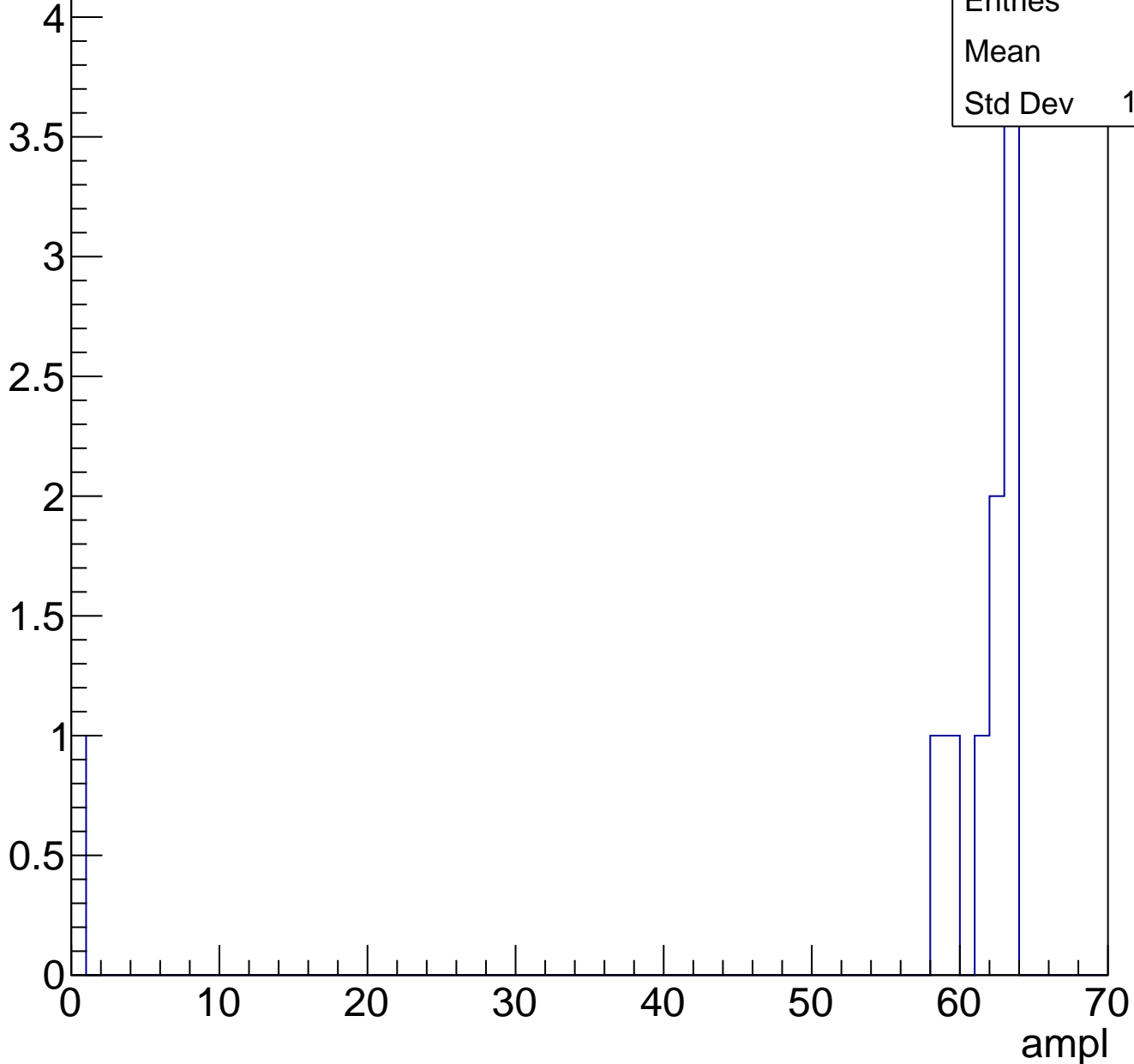
ampl



# B0L000S, U13-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0



# B0L000S, U13-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch120, adc0

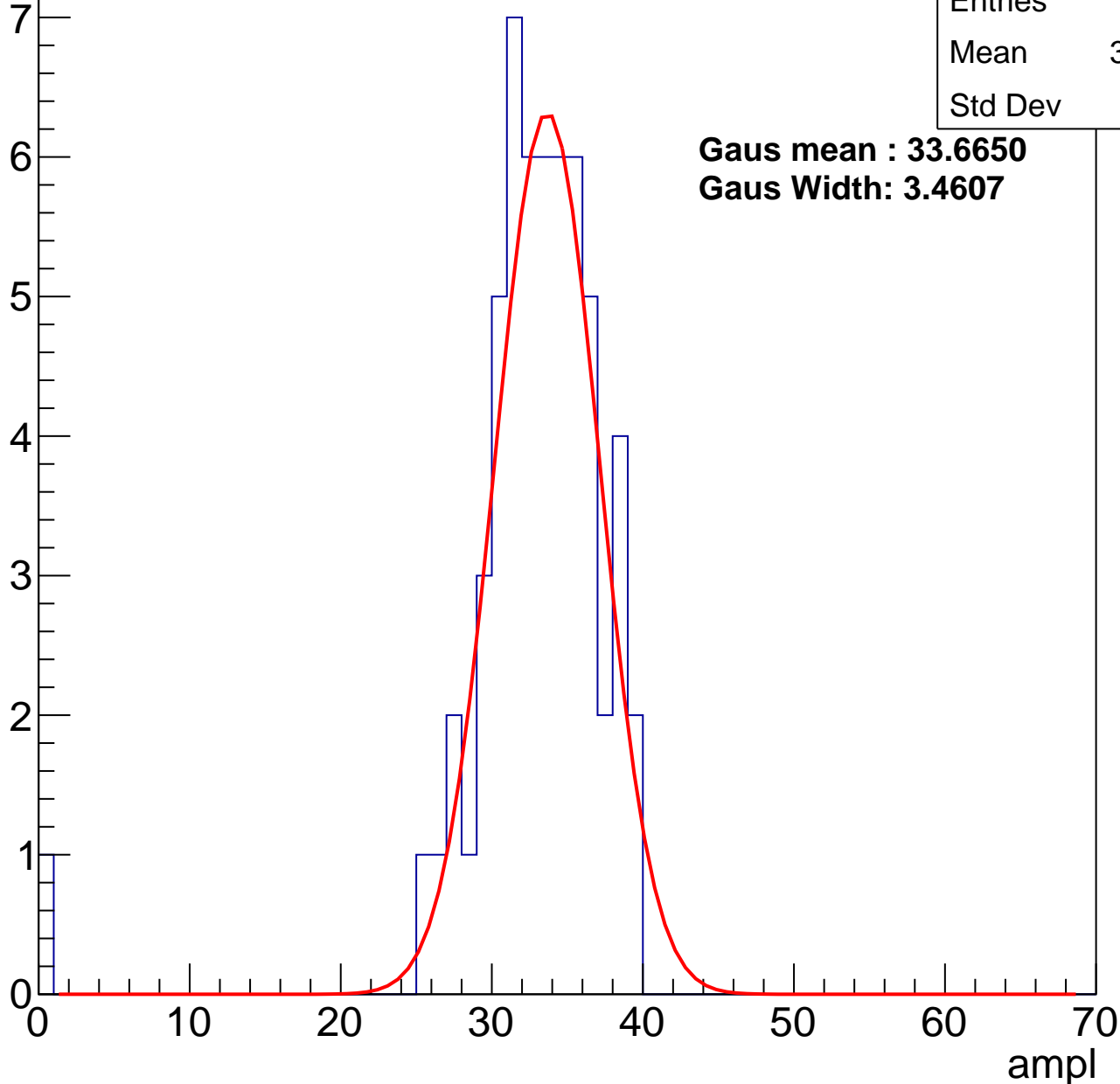
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	32.33
Std Dev	5.38

**Gaus mean : 33.6650**

**Gaus Width: 3.4607**



# B0L000S, U13-ch120, adc1

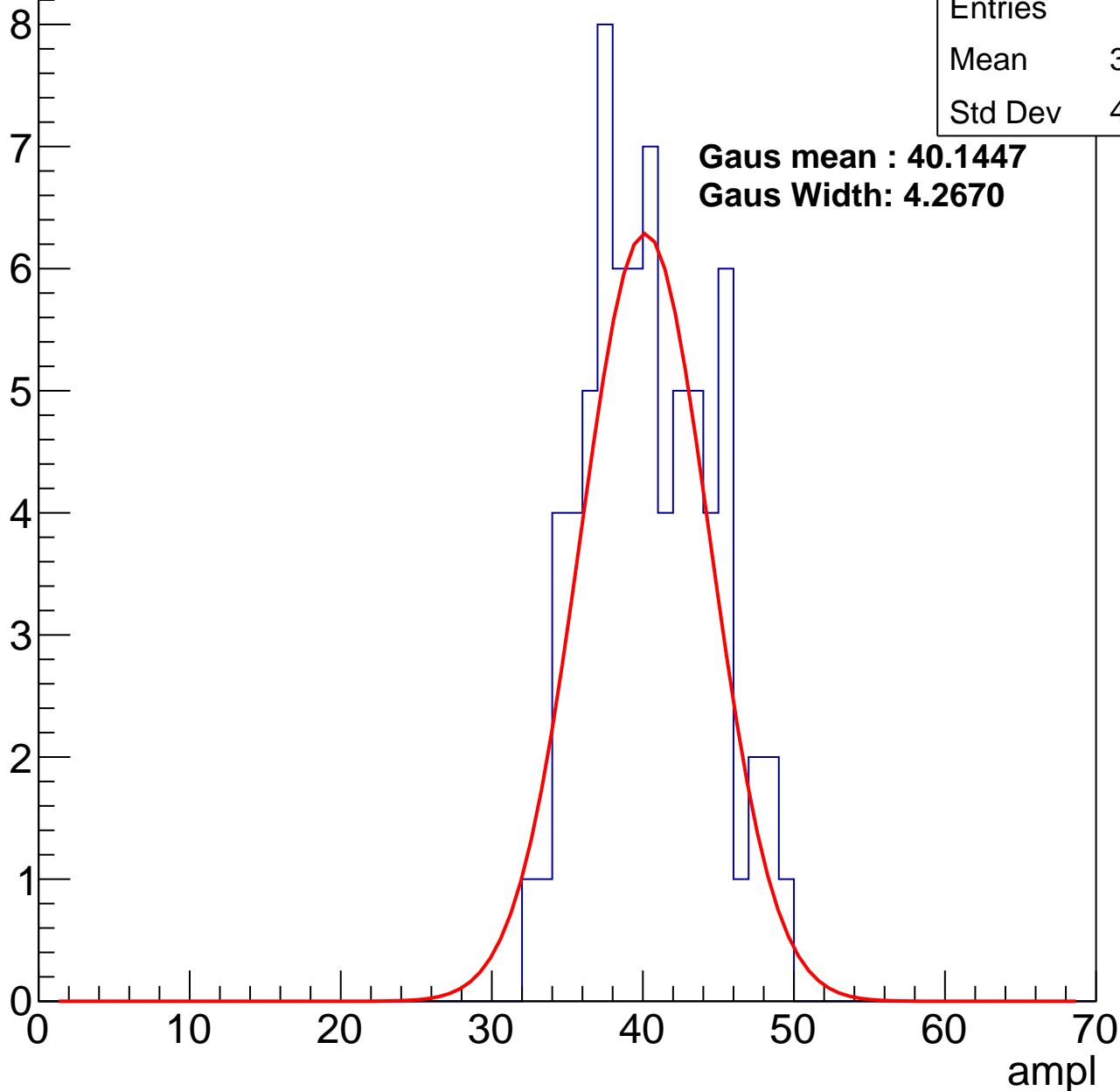
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	39.99
Std Dev	4.022

**Gaus mean : 40.1447**

**Gaus Width: 4.2670**



# B0L000S, U13-ch120, adc2

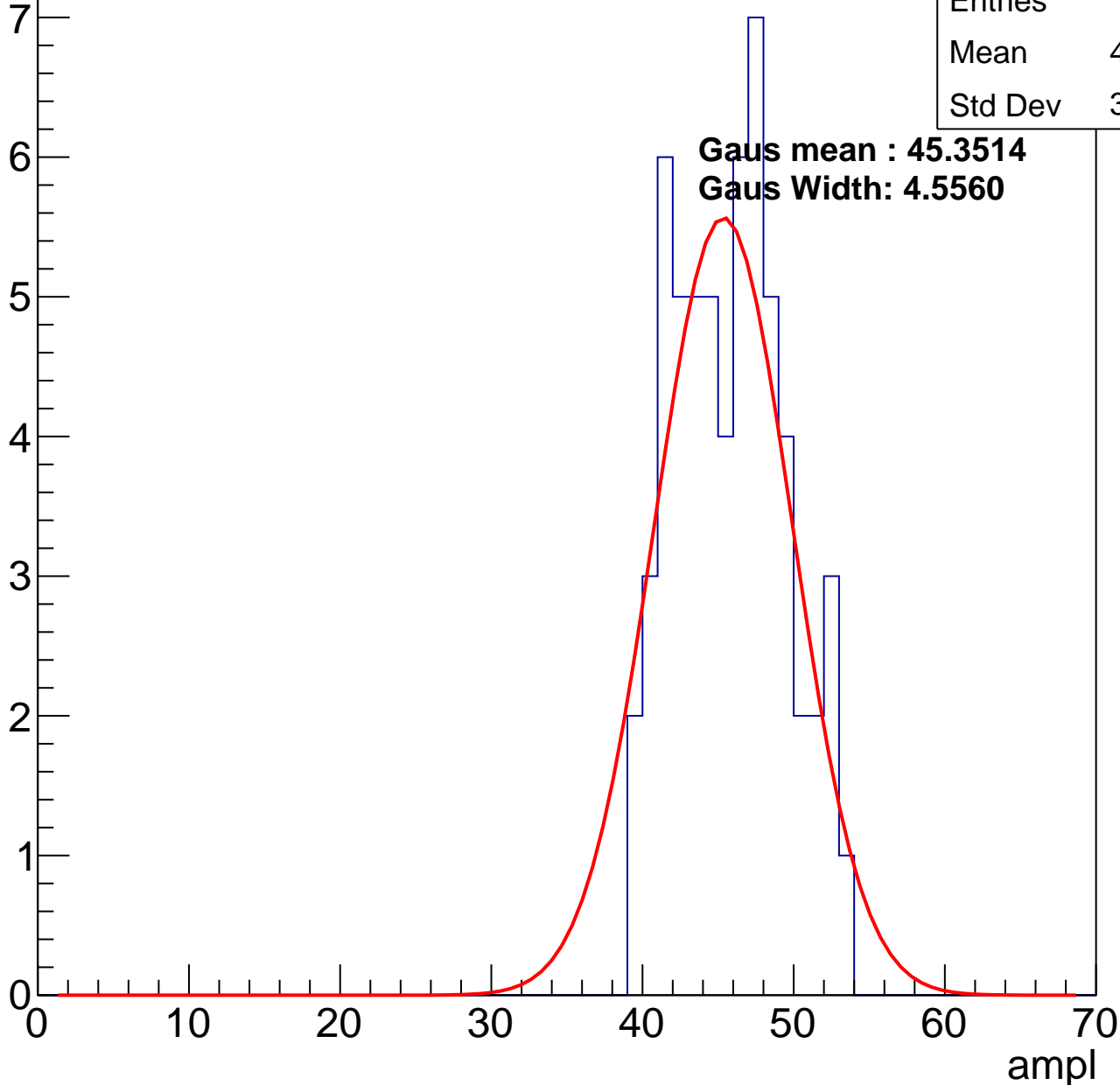
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	45.35
Std Dev	3.609

**Gaus mean : 45.3514**

**Gaus Width: 4.5560**

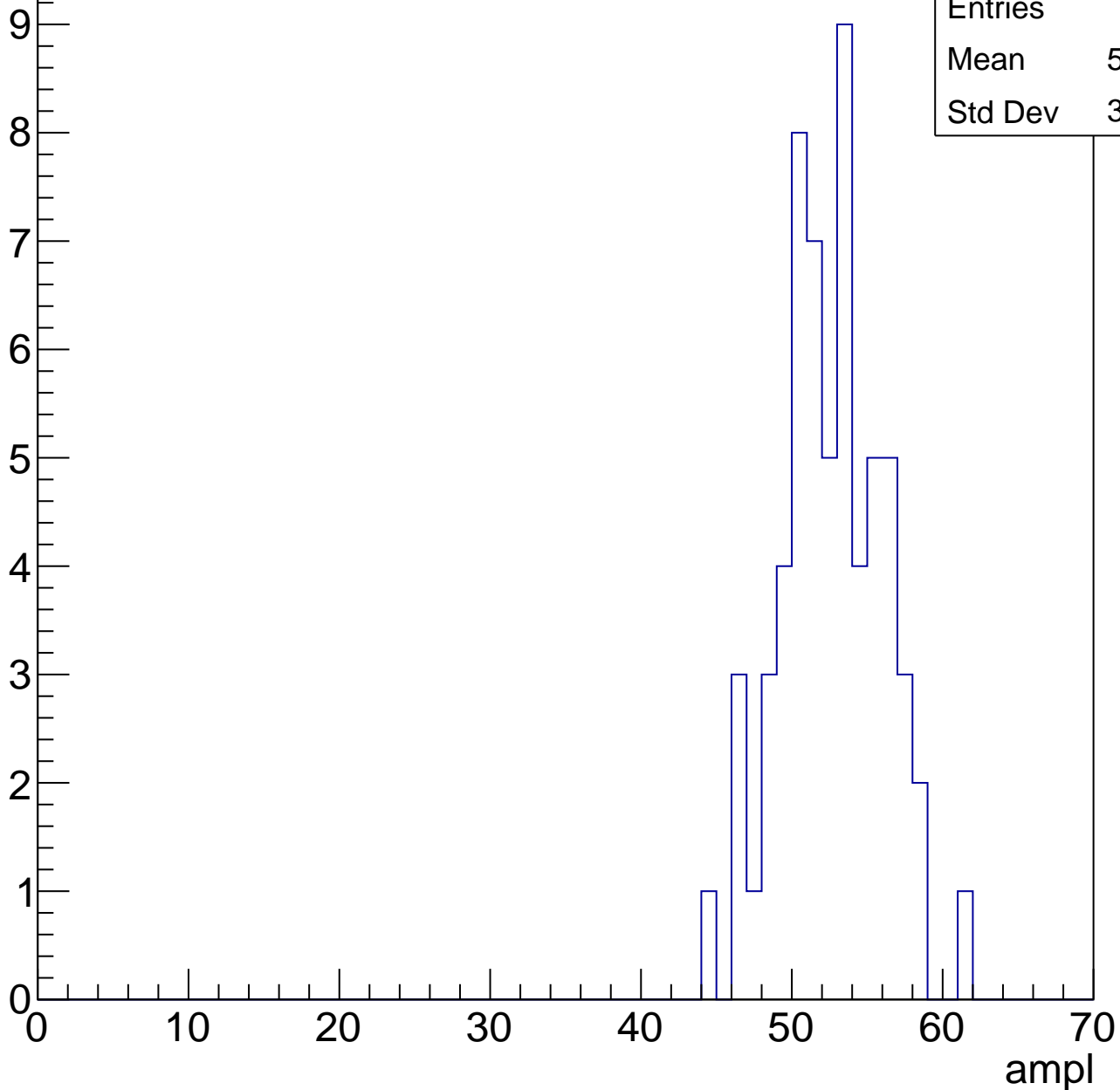


# B0L000S, U13-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	52.16
Std Dev	3.379



# B0L000S, U13-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	58.22
Std Dev	3.146

Entry

10

8

6

4

2

0

0

10

20

30

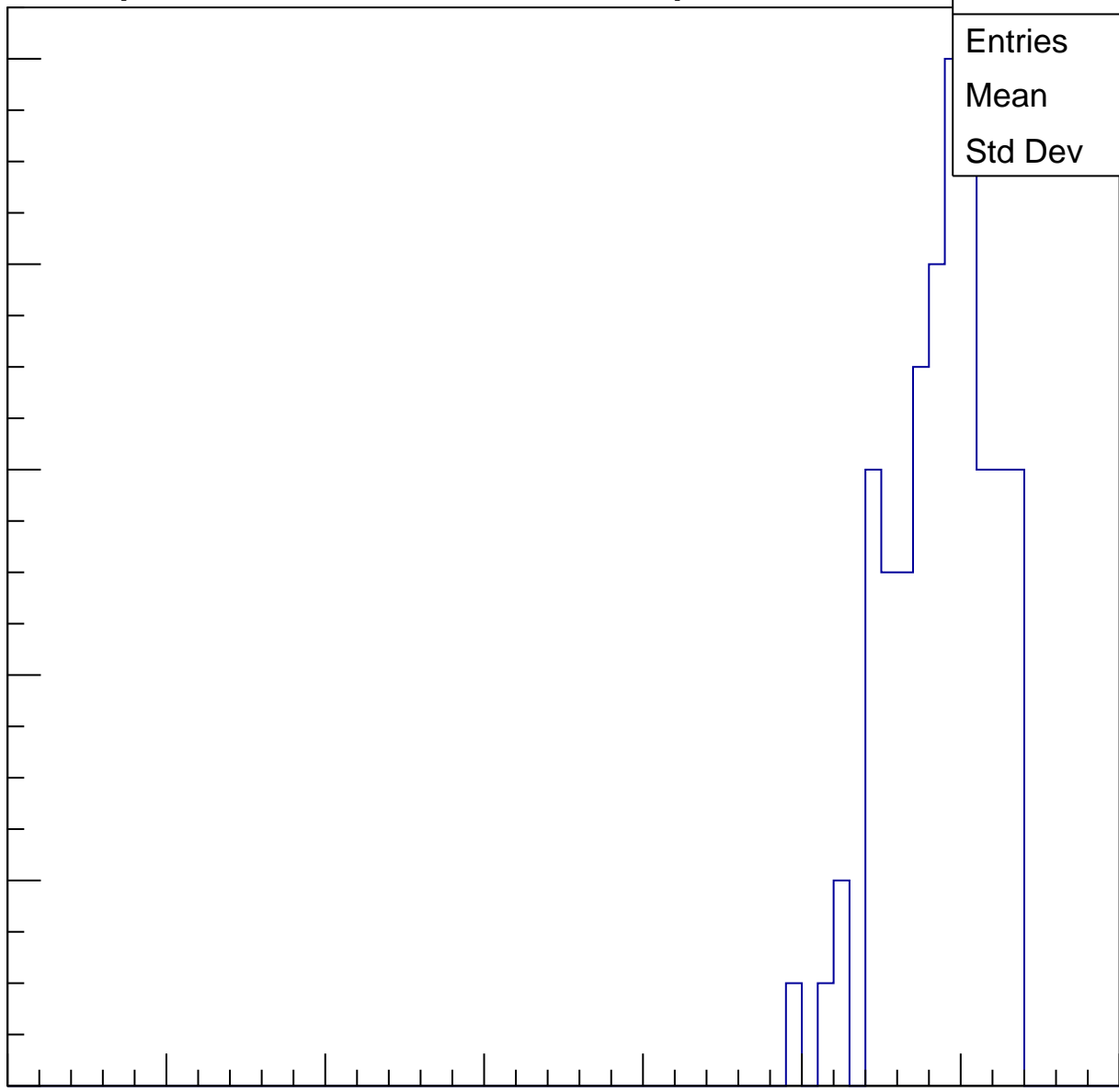
40

50

60

70

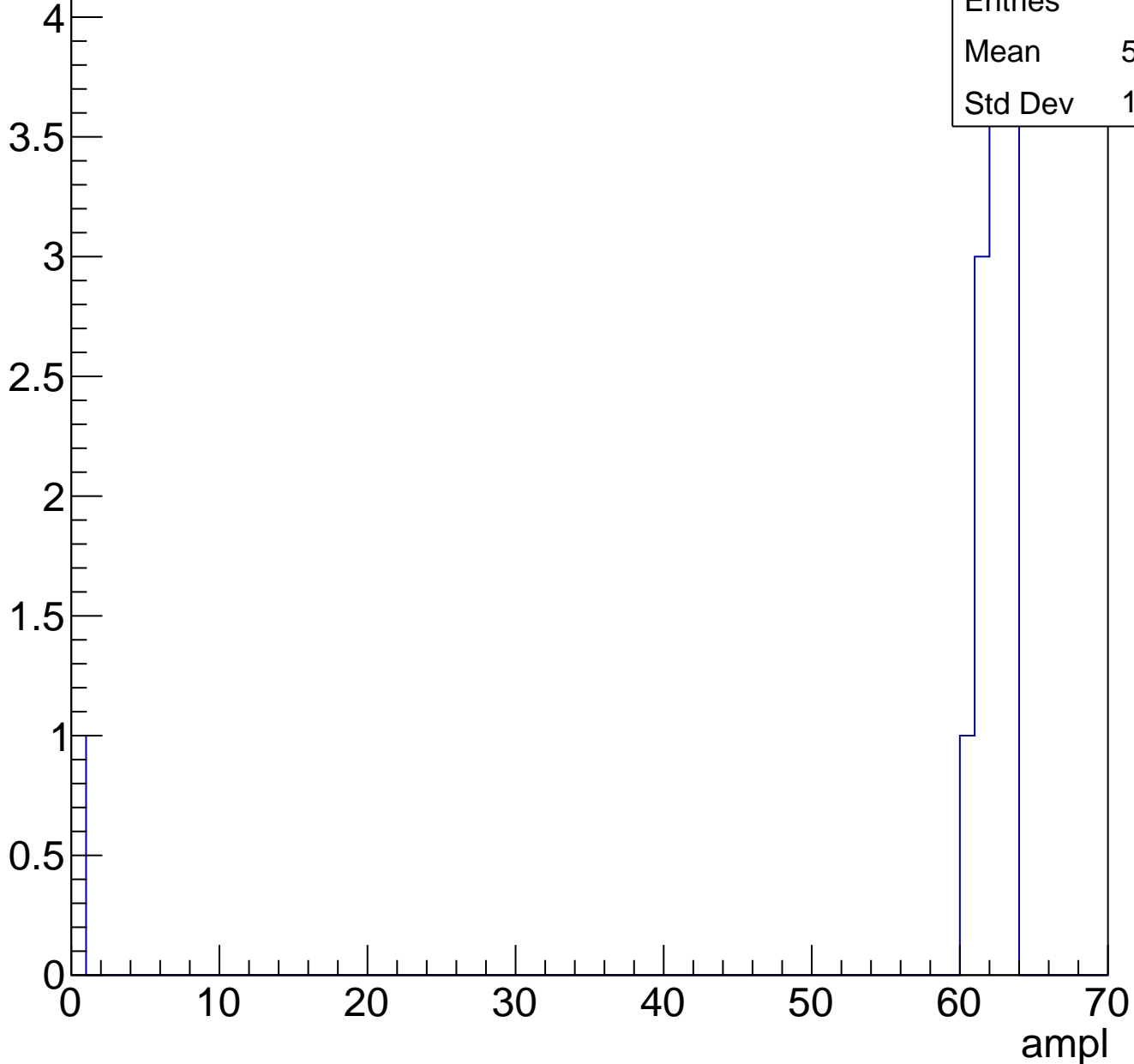
ampl



# B0L000S, U13-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch121, adc0

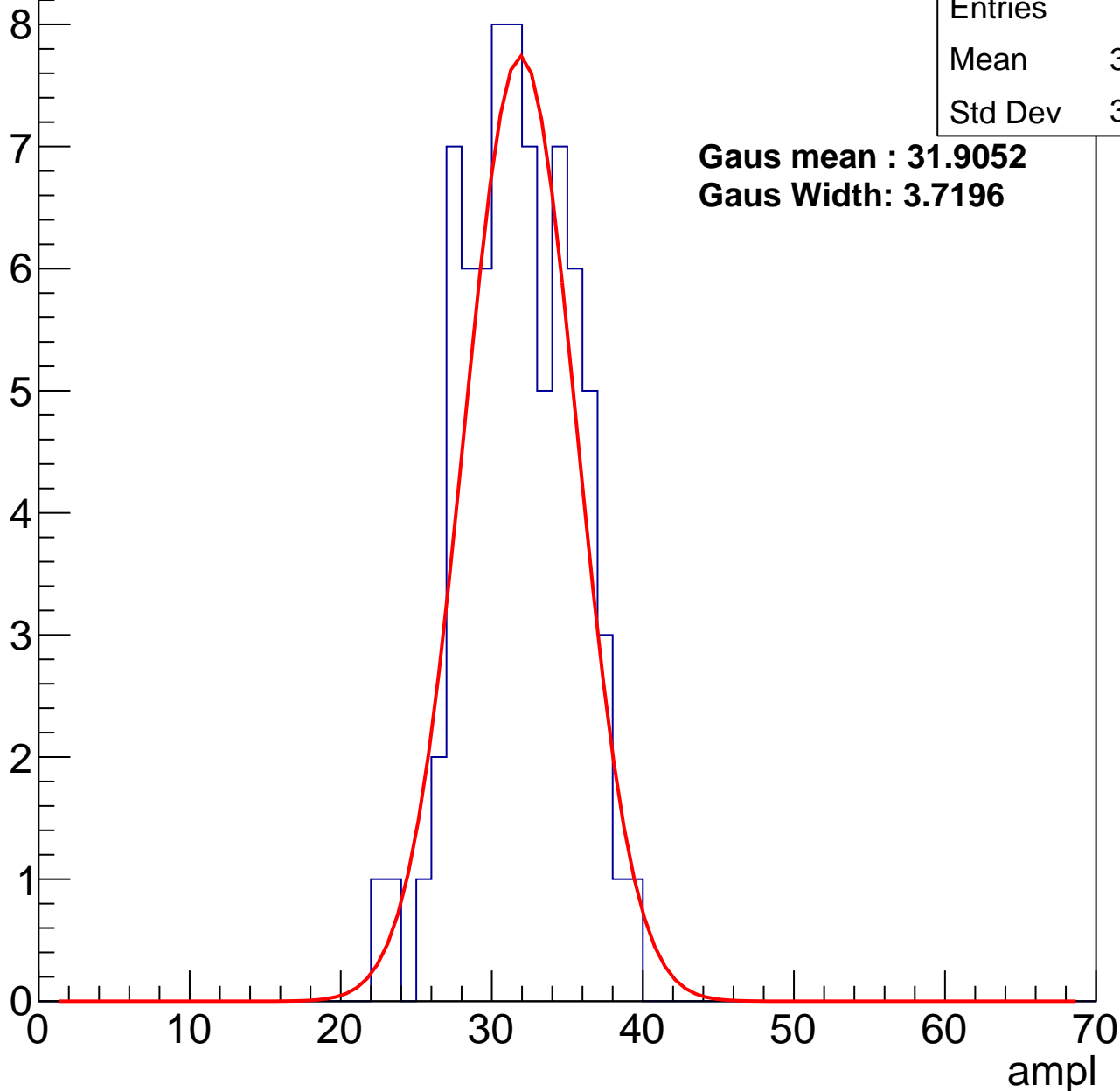
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	31.28
Std Dev	3.569

**Gaus mean : 31.9052**

**Gaus Width: 3.7196**



# B0L000S, U13-ch121, adc1

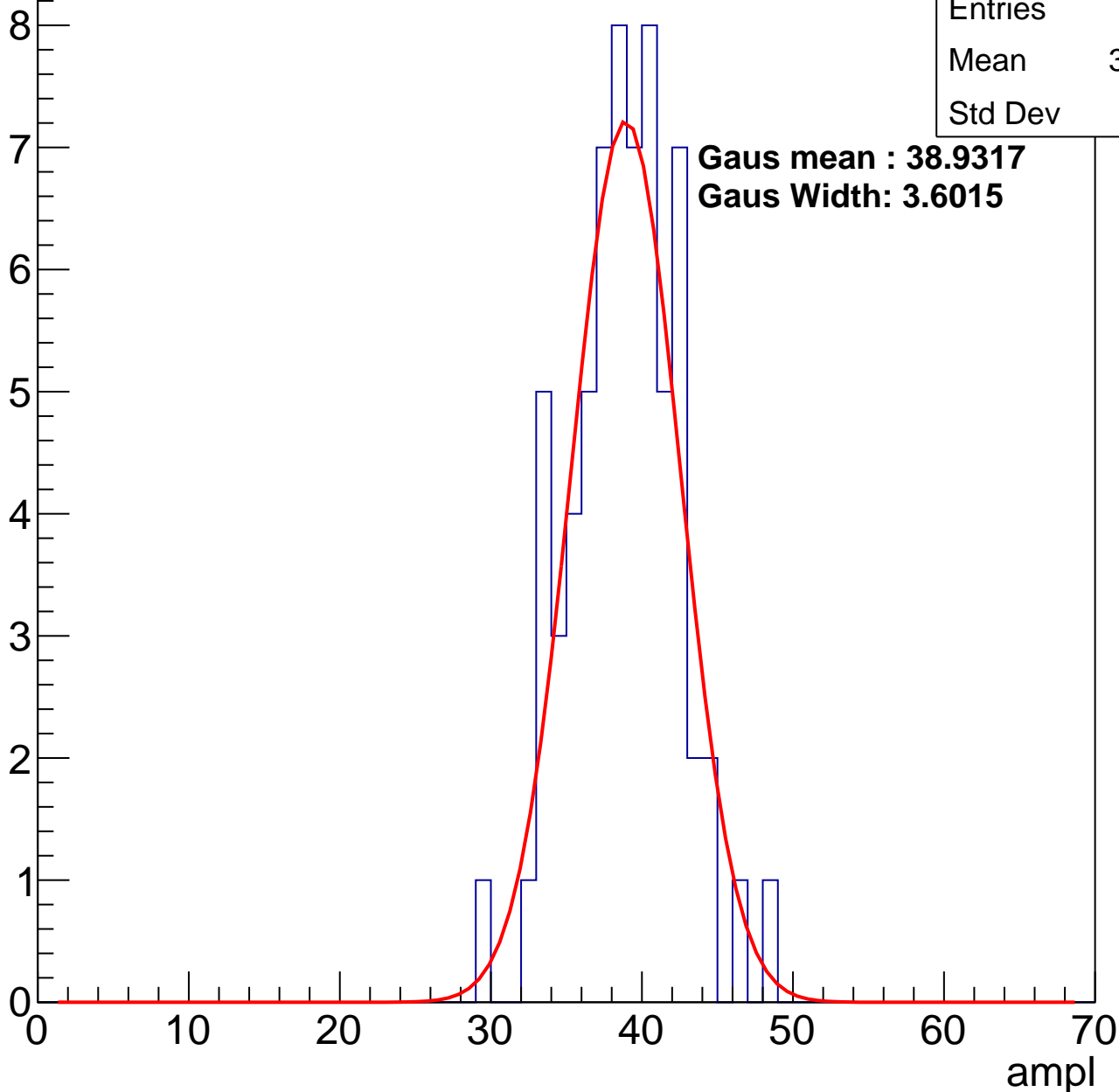
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	38.37
Std Dev	3.51

**Gaus mean : 38.9317**

**Gaus Width: 3.6015**



# B0L000S, U13-ch121, adc2

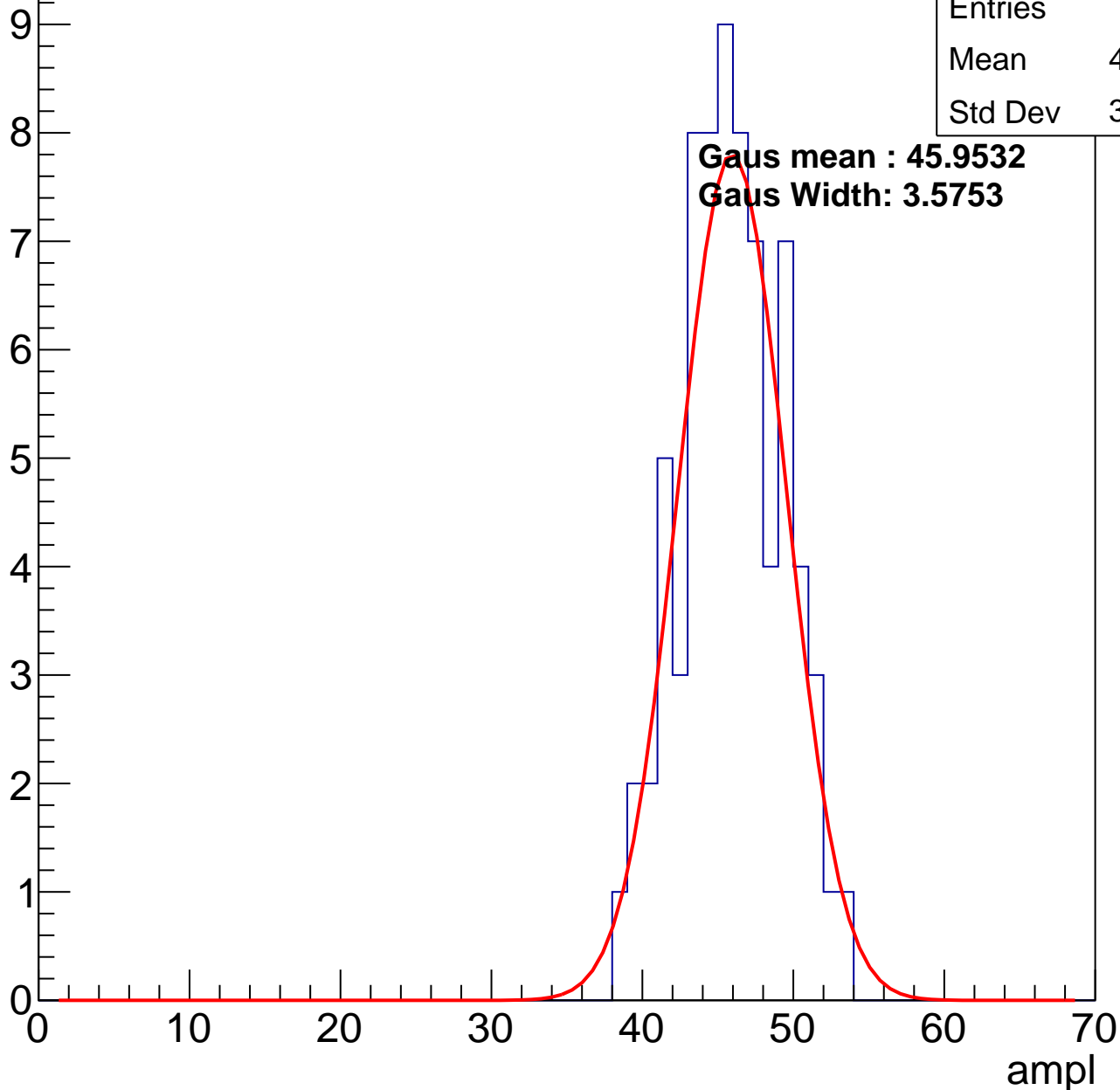
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	45.45
Std Dev	3.327

**Gaus mean : 45.9532**

**Gaus Width: 3.5753**

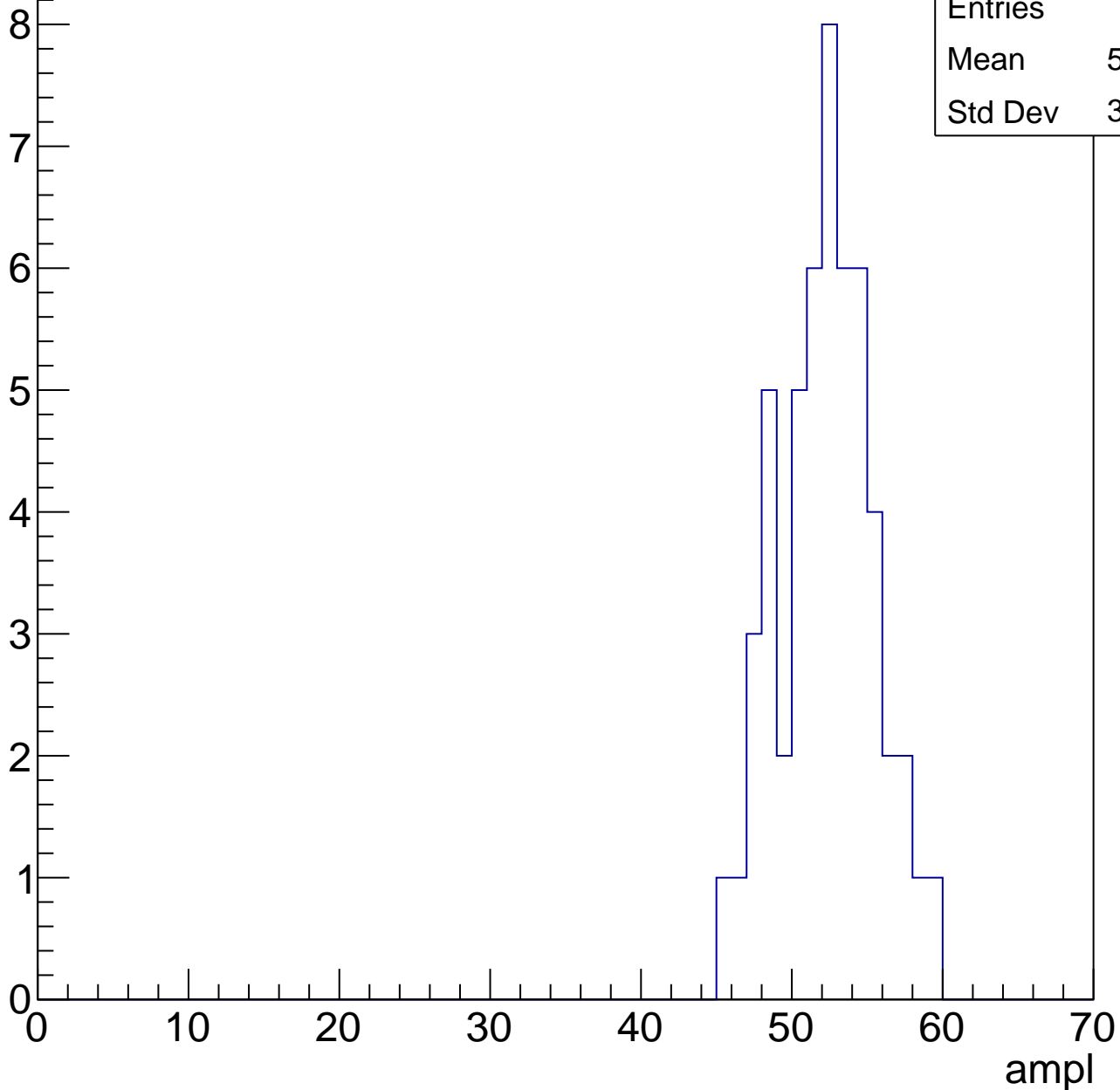


# B0L000S, U13-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	51.83
Std Dev	3.119

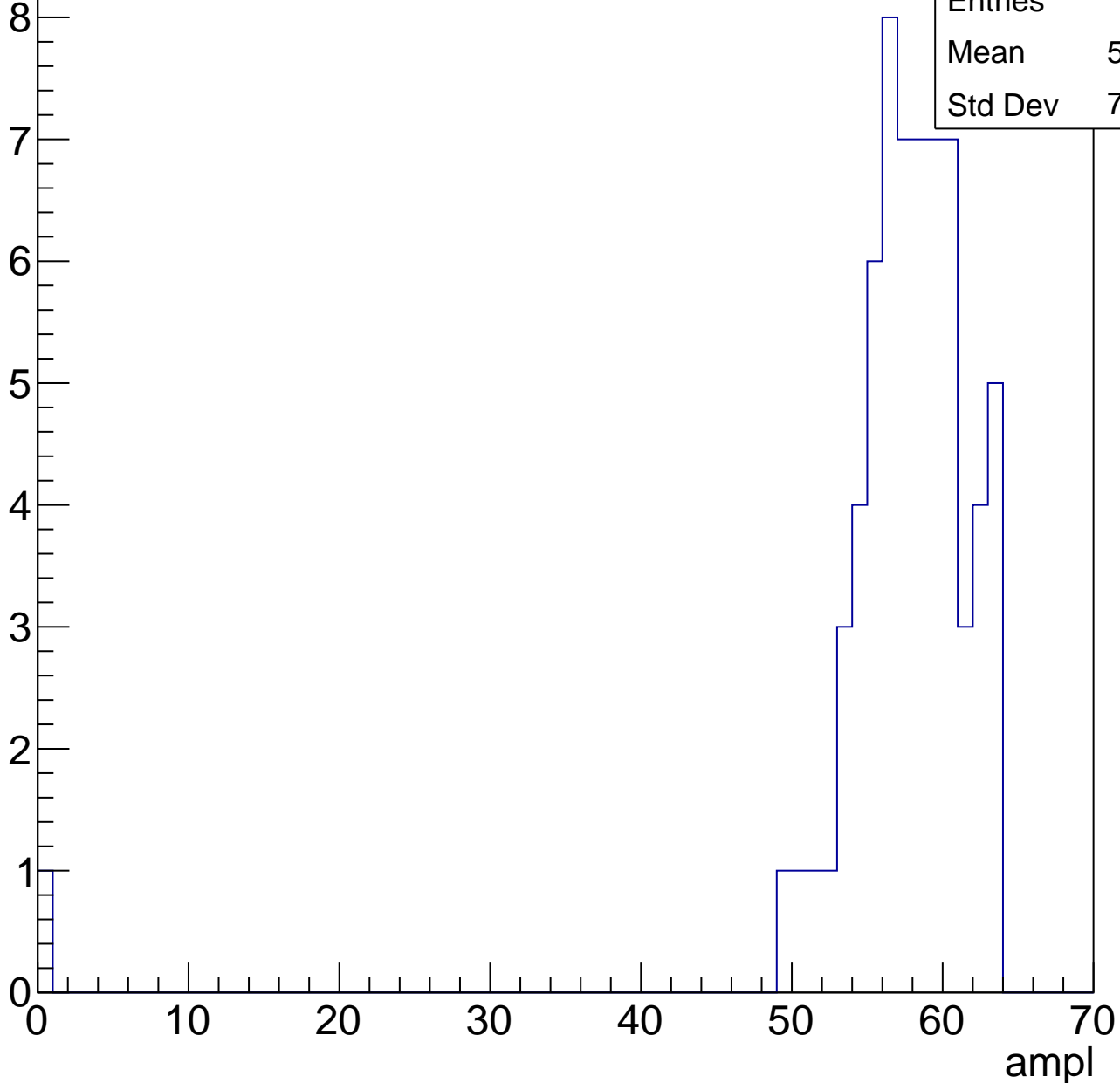


# B0L000S, U13-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.65
Std Dev	7.743

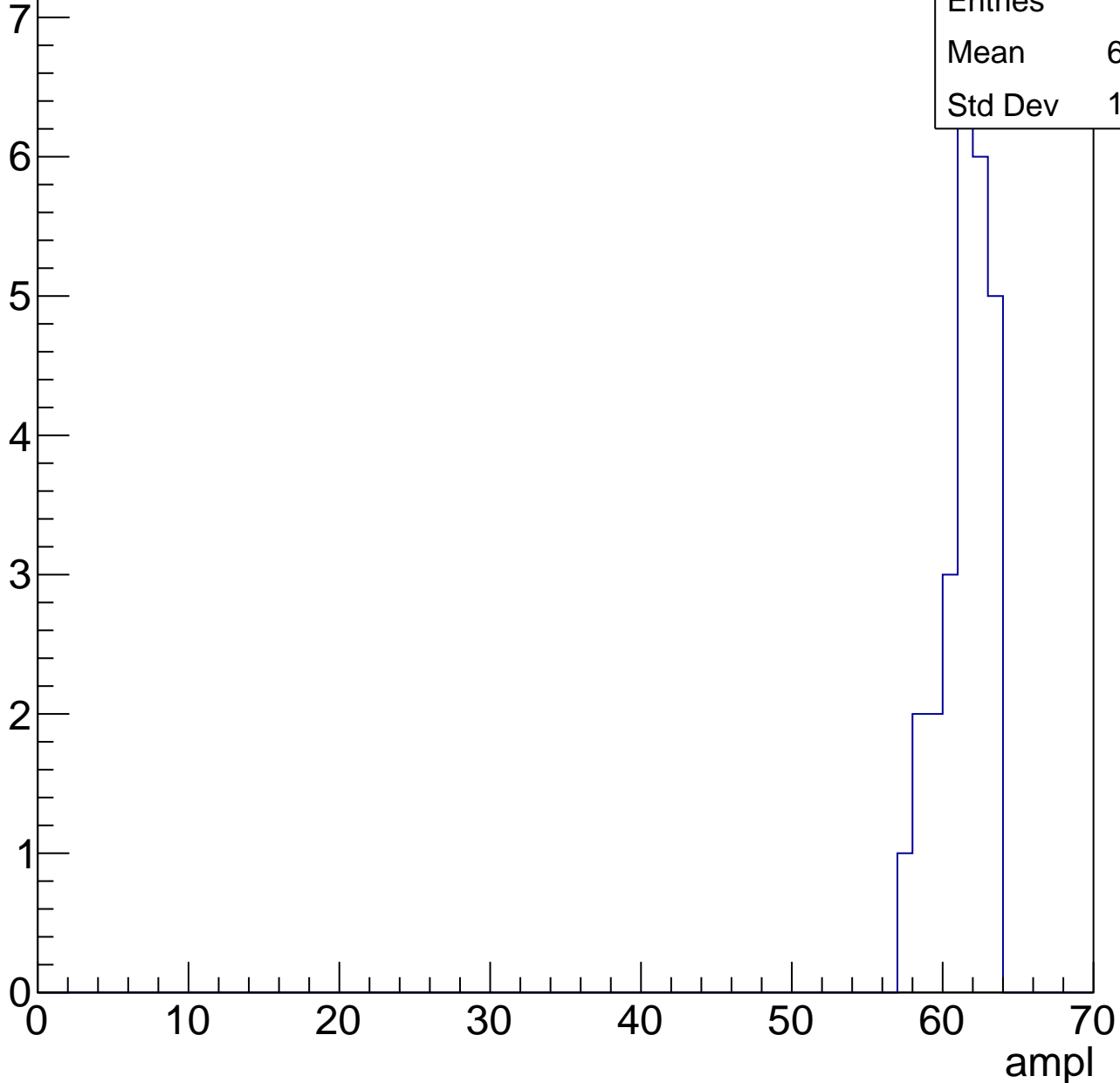


# B0L000S, U13-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	60.96
Std Dev	1.652



# B0L000S, U13-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U13-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch122, adc0

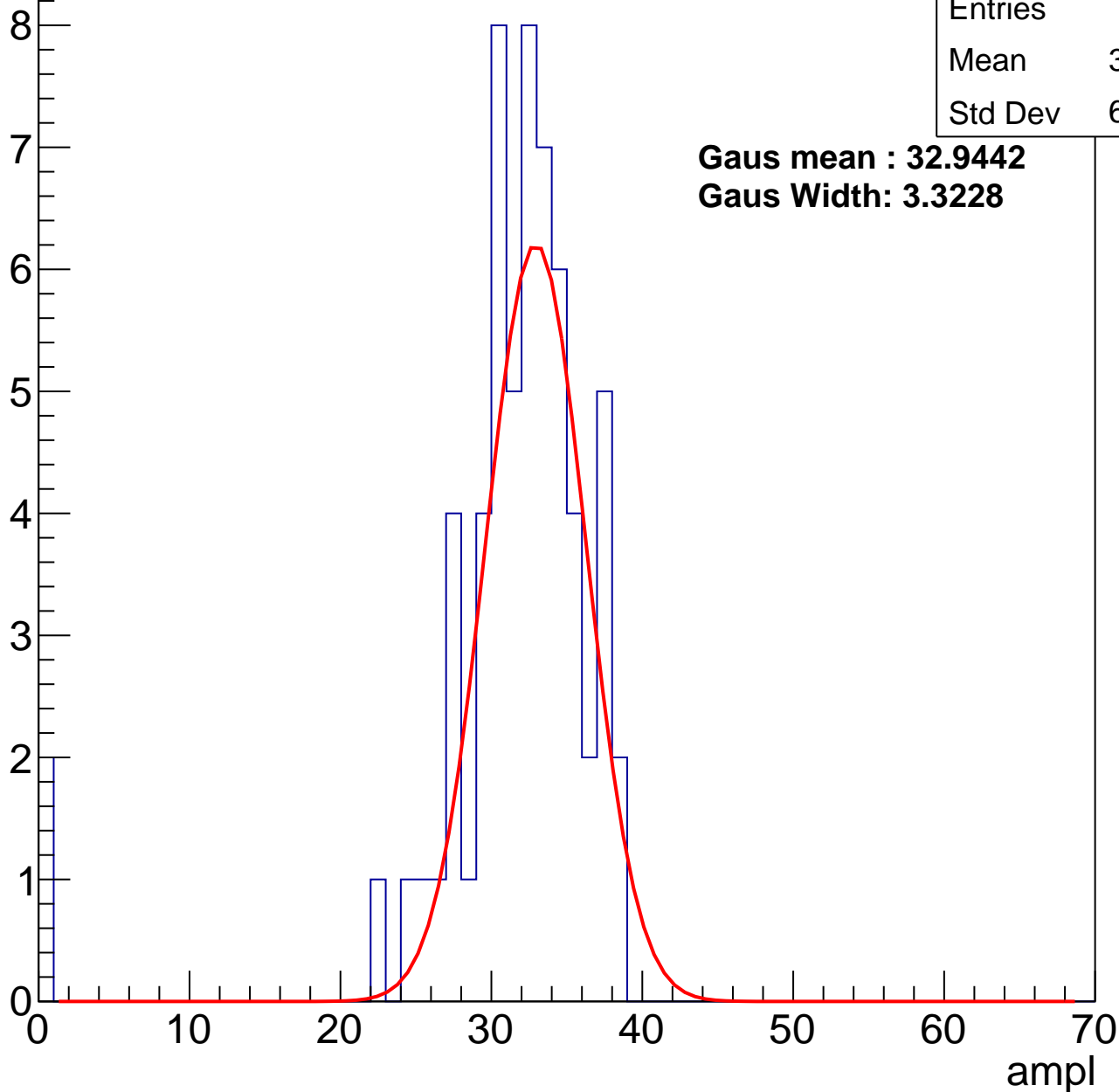
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.77
Std Dev	6.585

**Gaus mean : 32.9442**

**Gaus Width: 3.3228**



# B0L000S, U13-ch122, adc1

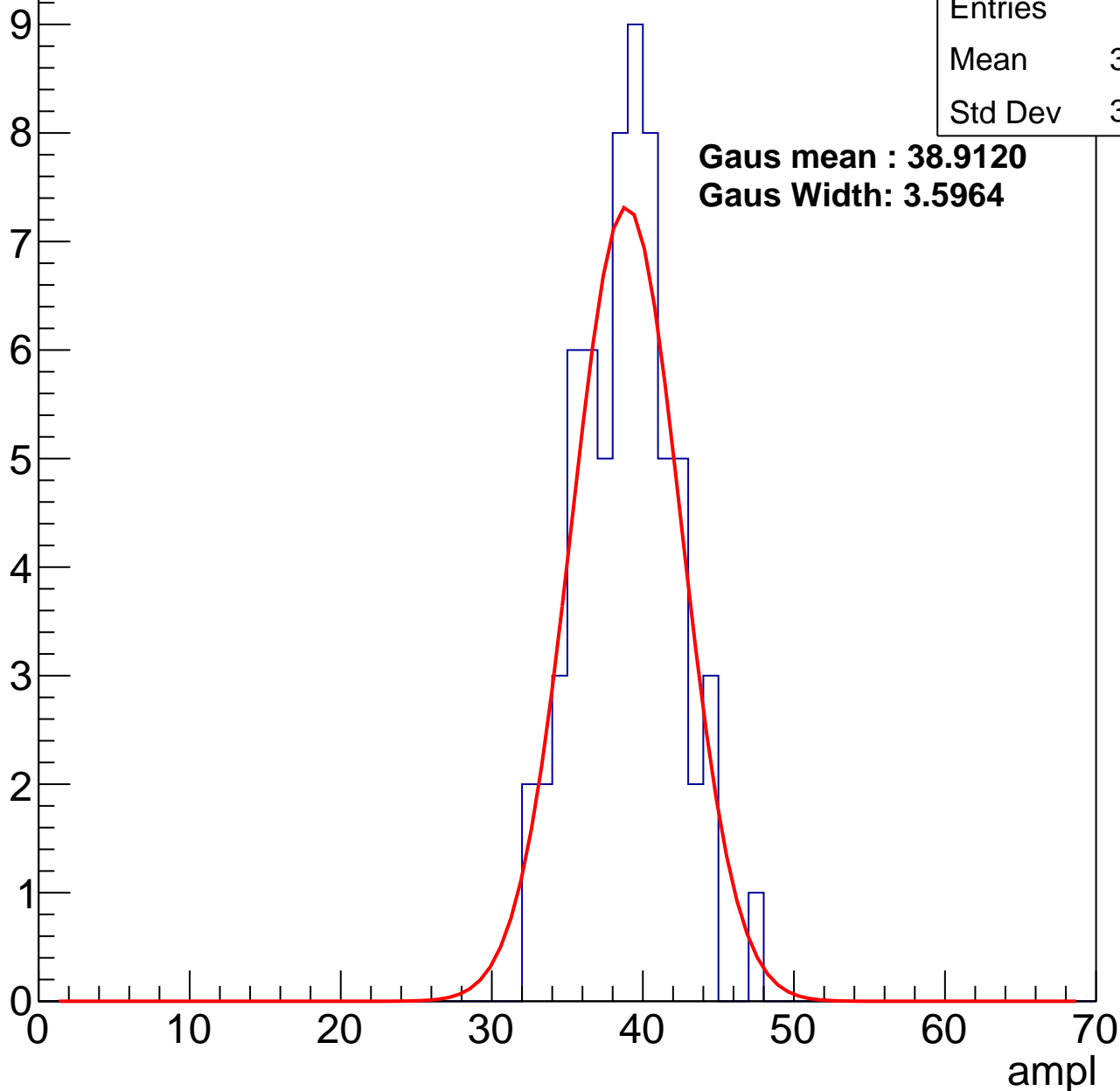
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	38.43
Std Dev	3.152

**Gaus mean : 38.9120**

**Gaus Width: 3.5964**



# B0L000S, U13-ch122, adc2

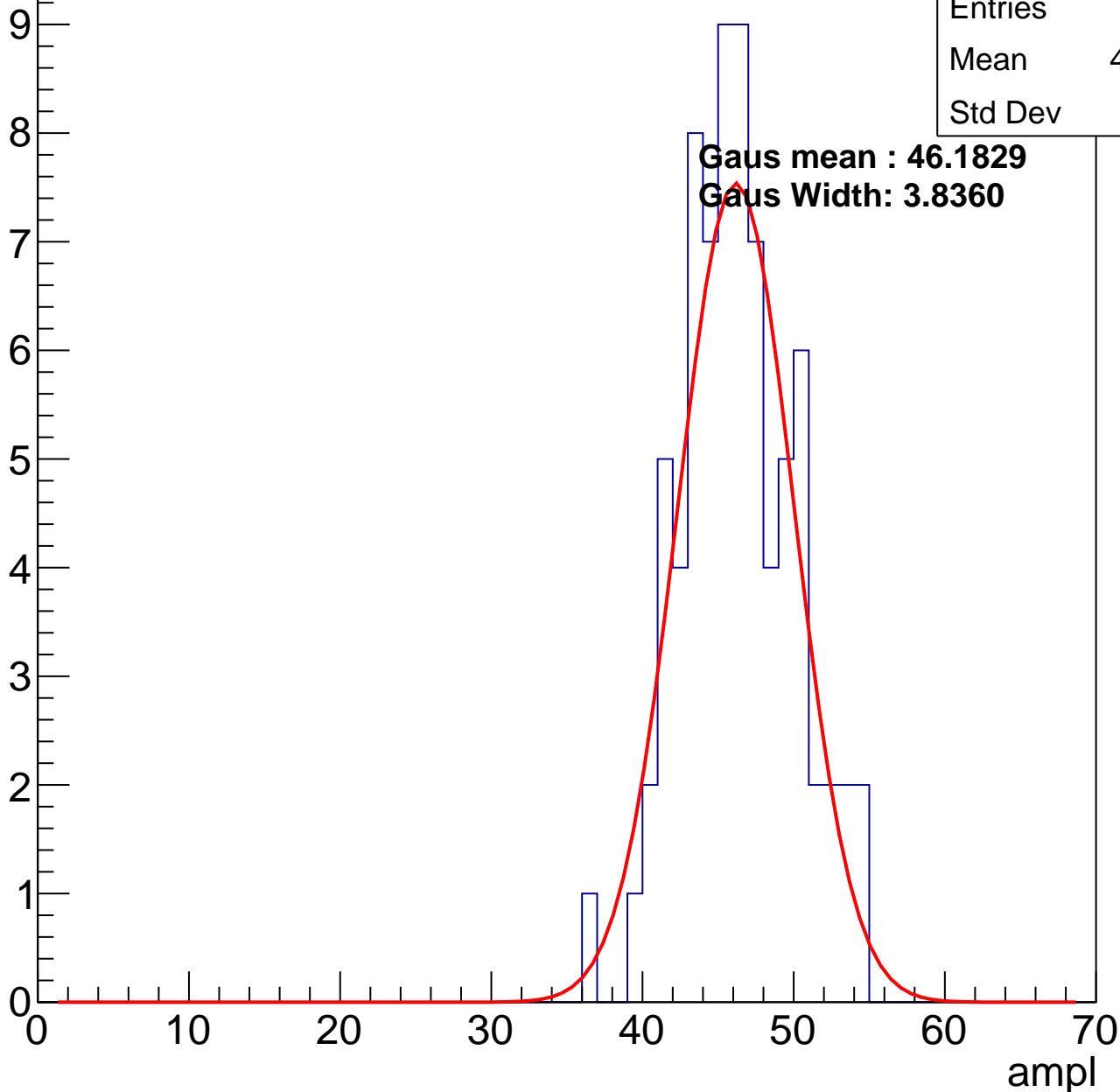
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	45.86
Std Dev	3.68

**Gaus mean : 46.1829**

**Gaus Width: 3.8360**

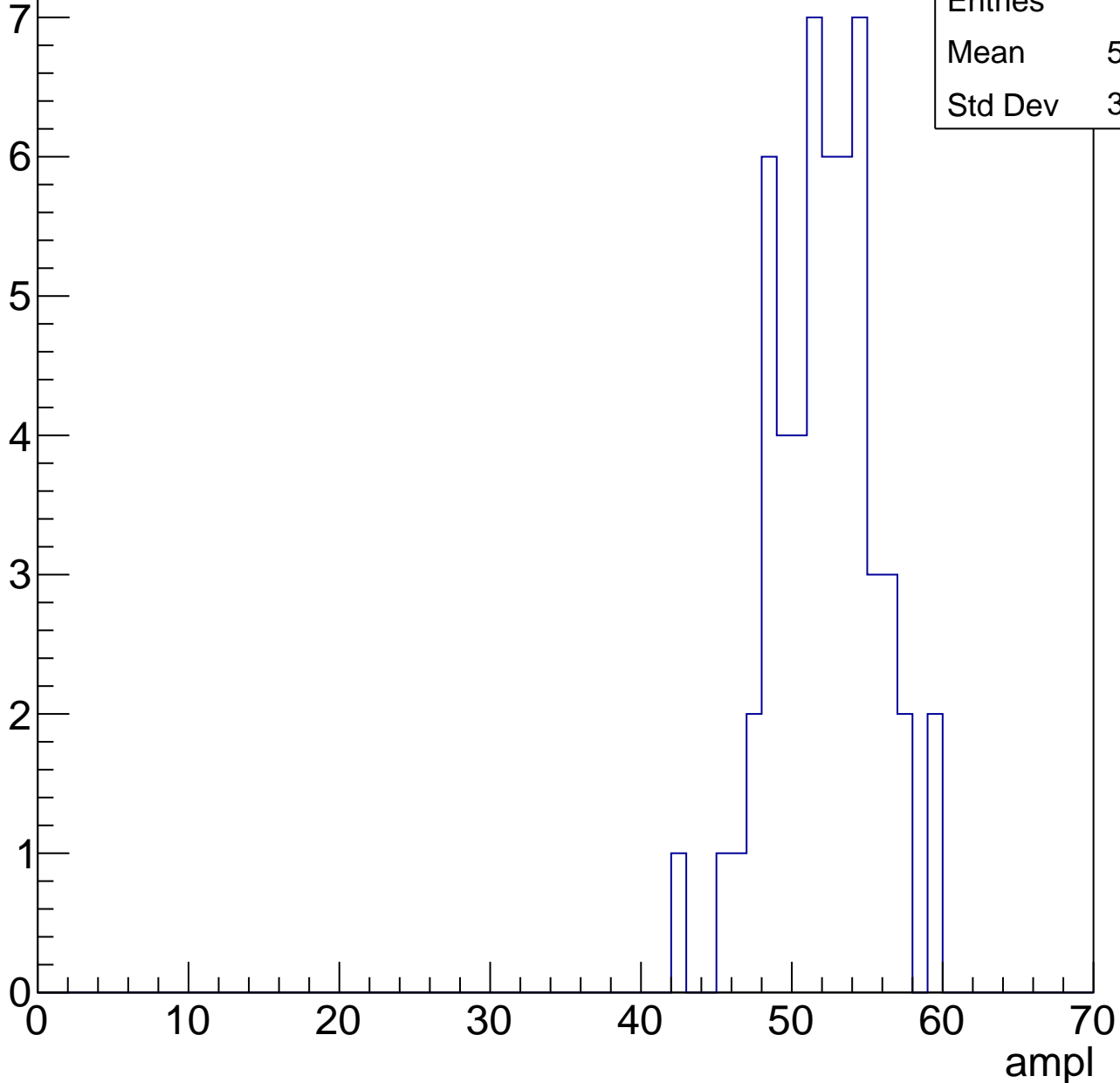


# B0L000S, U13-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	51.65
Std Dev	3.412

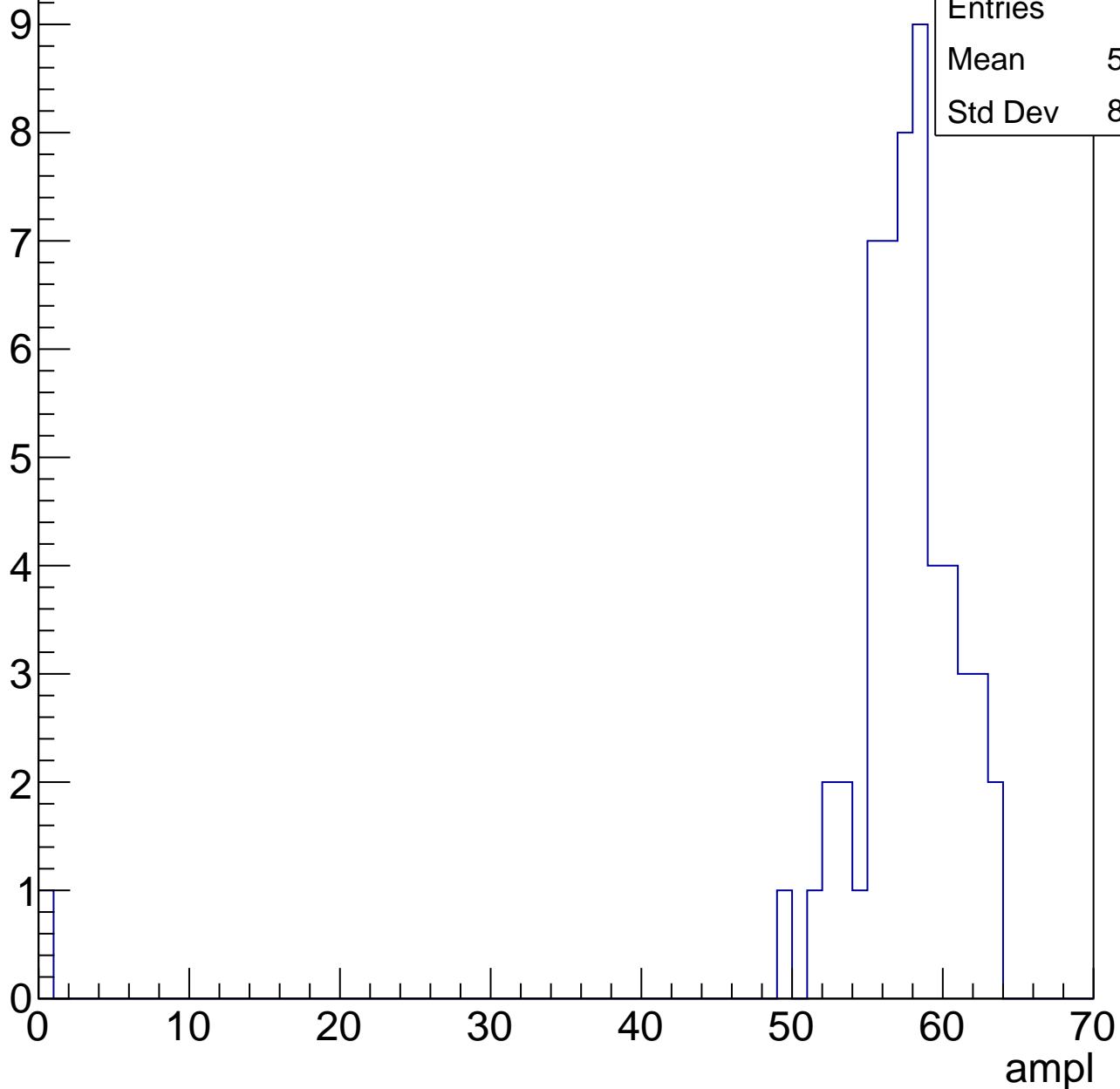


# B0L000S, U13-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.18
Std Dev	8.196



# B0L000S, U13-ch122, adc5

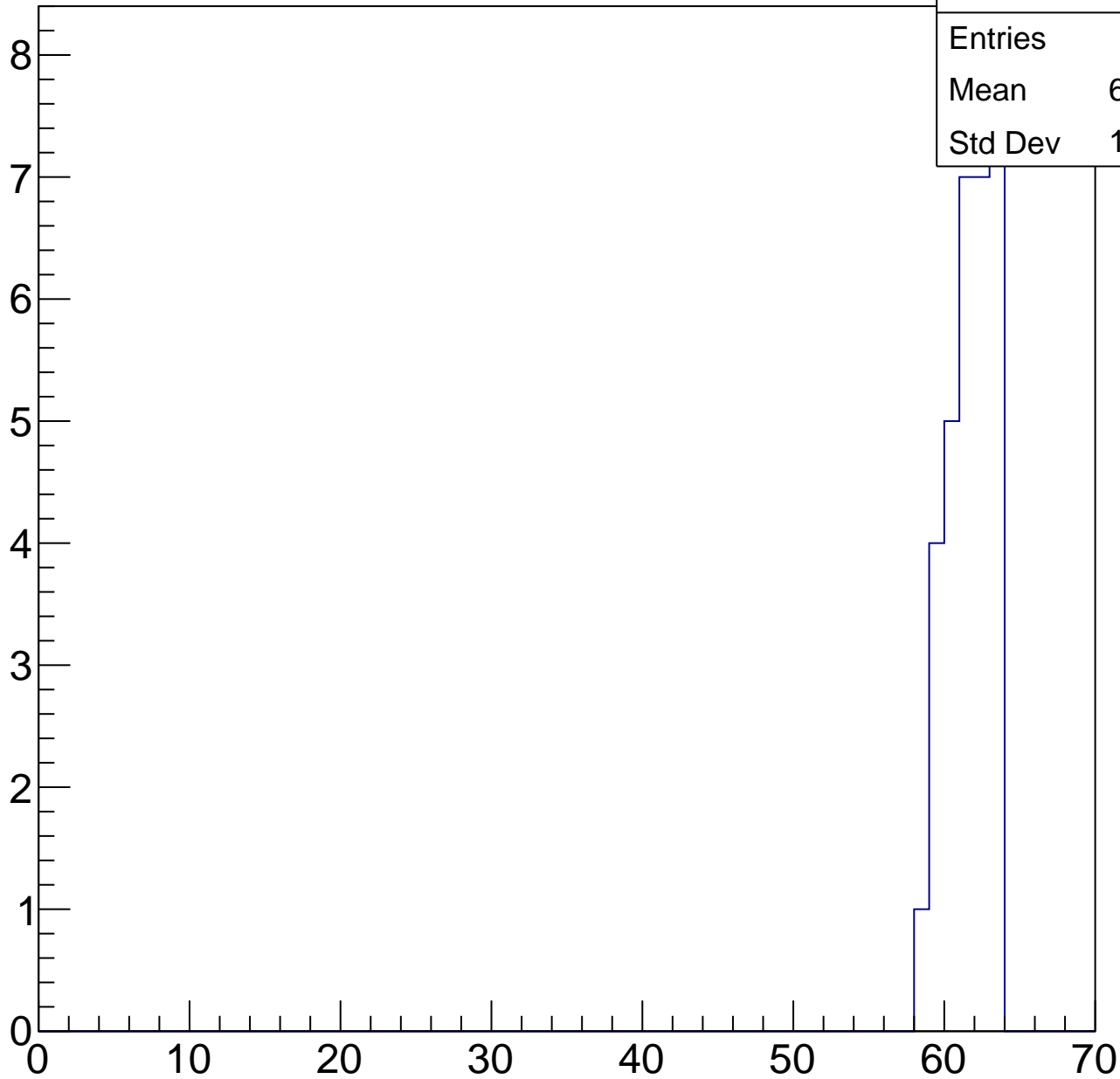
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	32
Mean	61.22
Std Dev	1.452

ampl



# B0L000S, U13-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch123, adc0

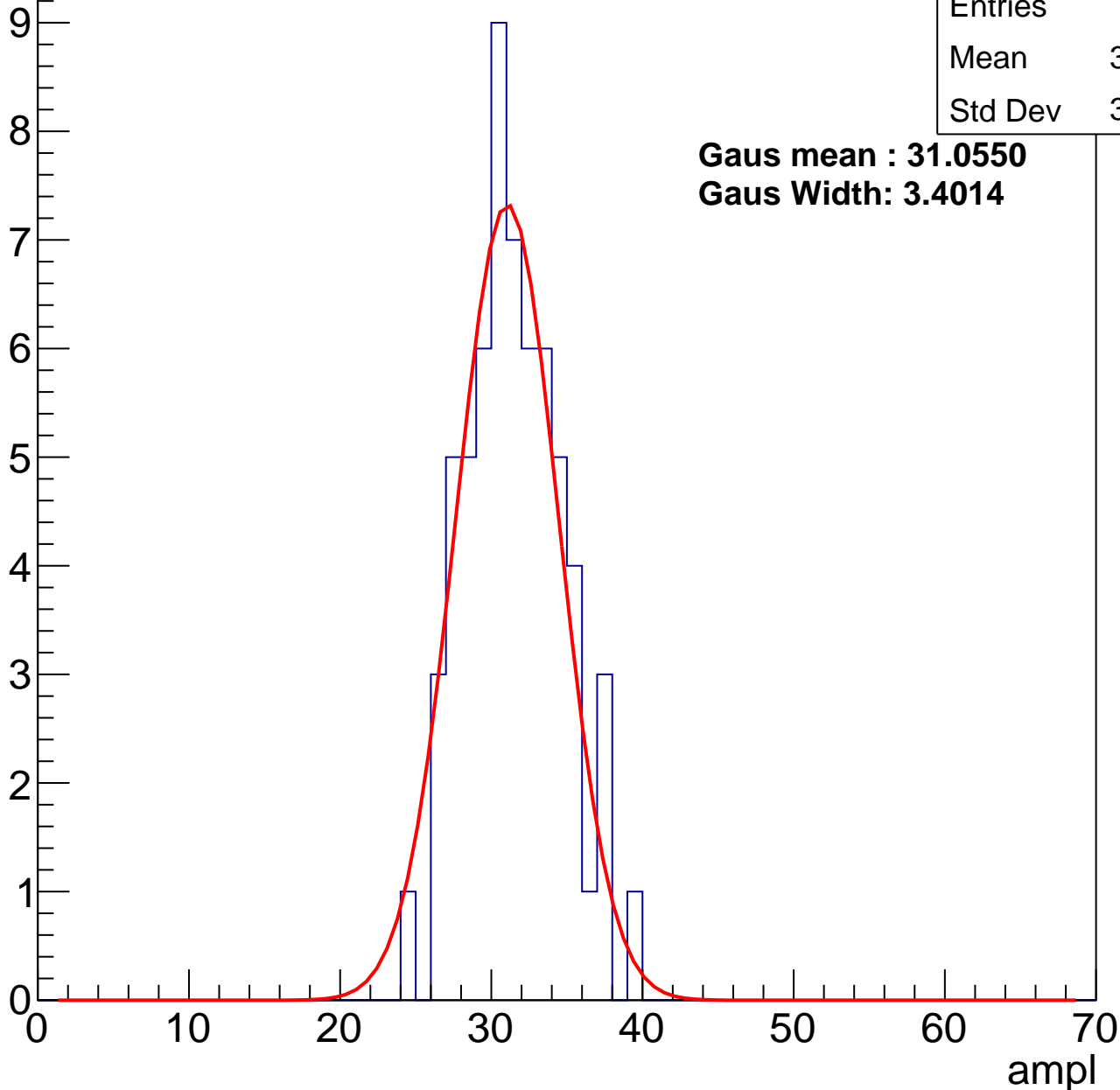
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	31.03
Std Dev	3.157

**Gaus mean : 31.0550**

**Gaus Width: 3.4014**



# B0L000S, U13-ch123, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	38.68
Std Dev	3.706

**Gaus mean : 39.1120**

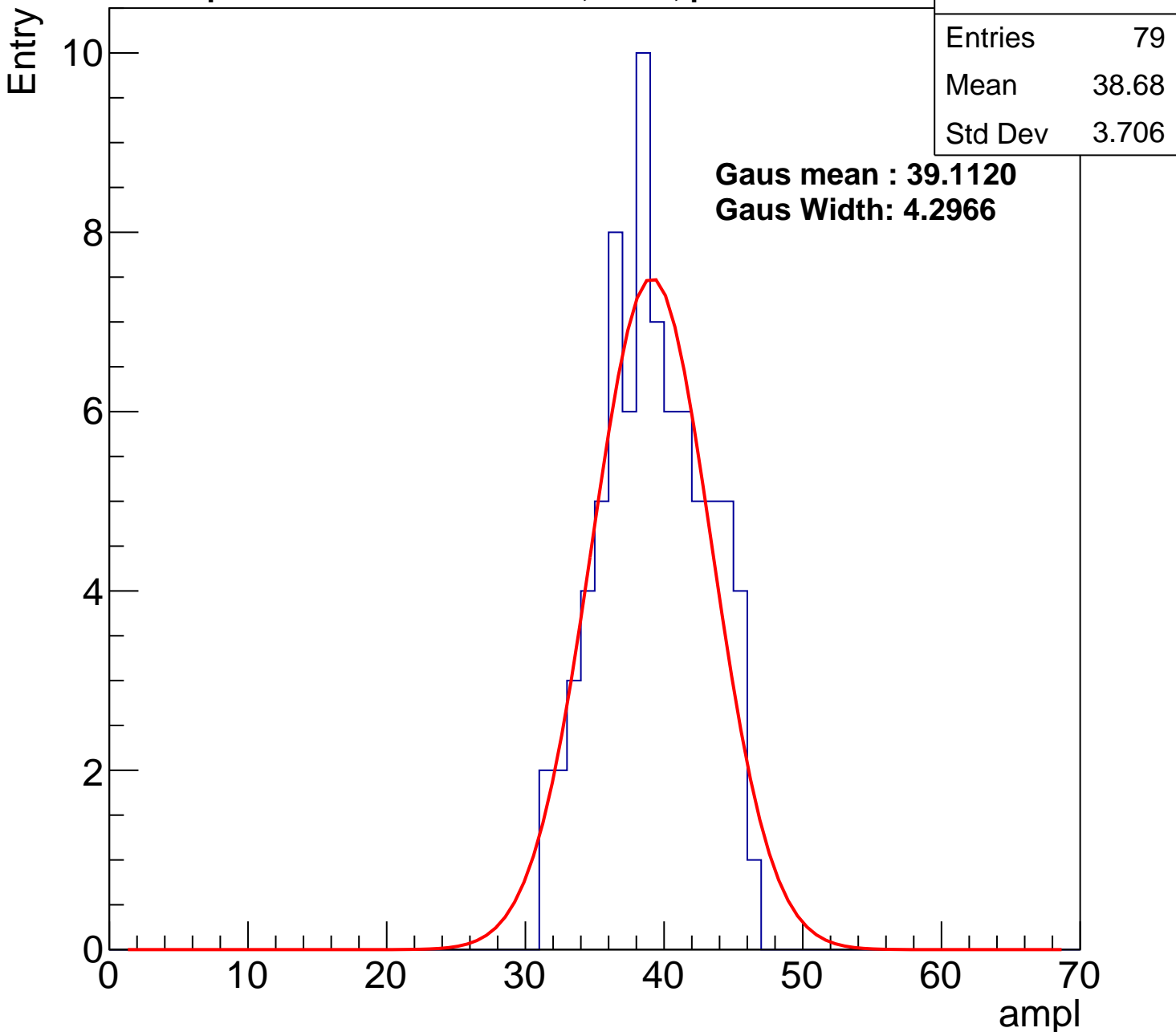
**Gaus Width: 4.2966**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch123, adc2

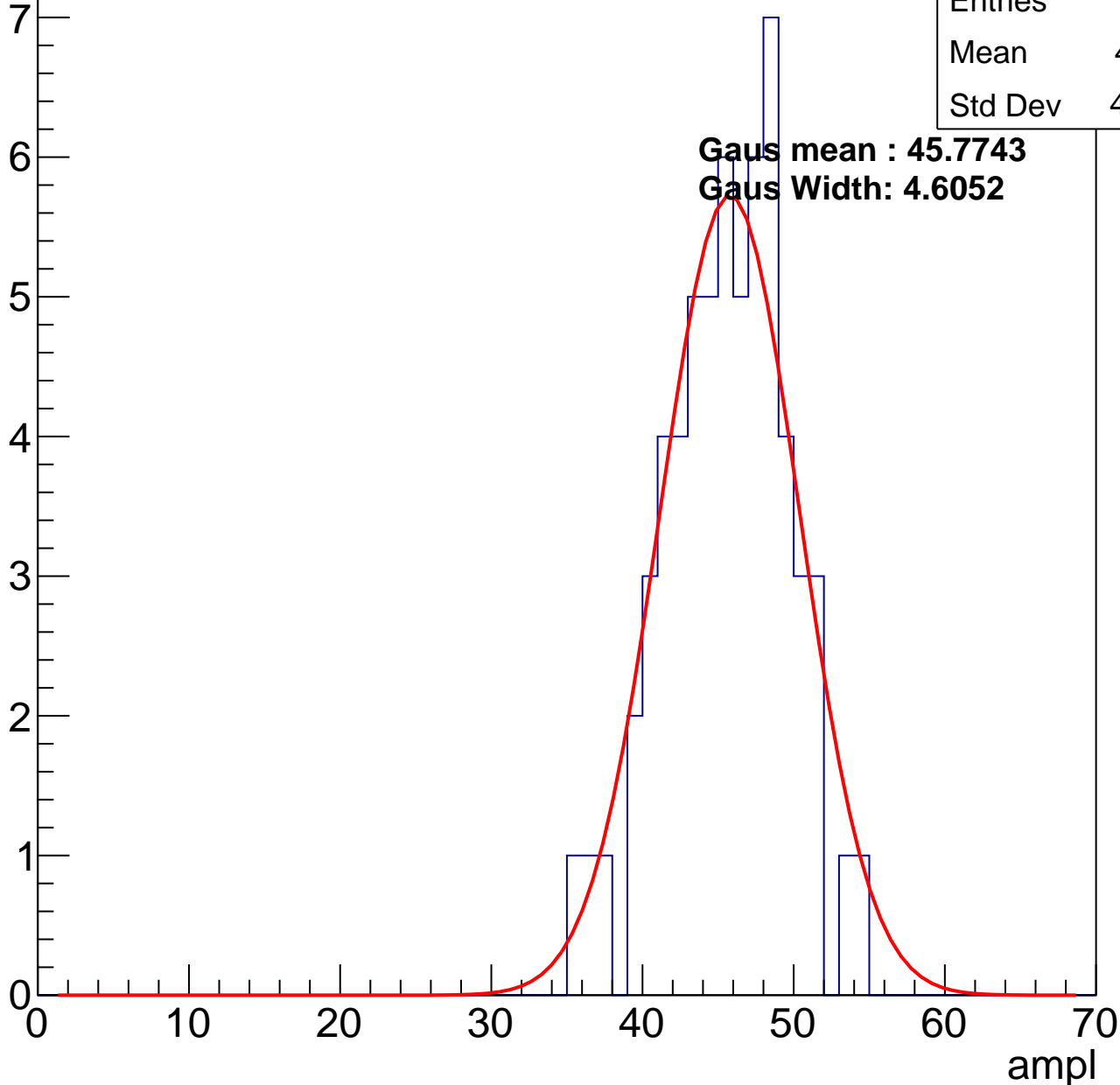
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	45.11
Std Dev	4.017

**Gaus mean : 45.7743**

**Gaus Width: 4.6052**

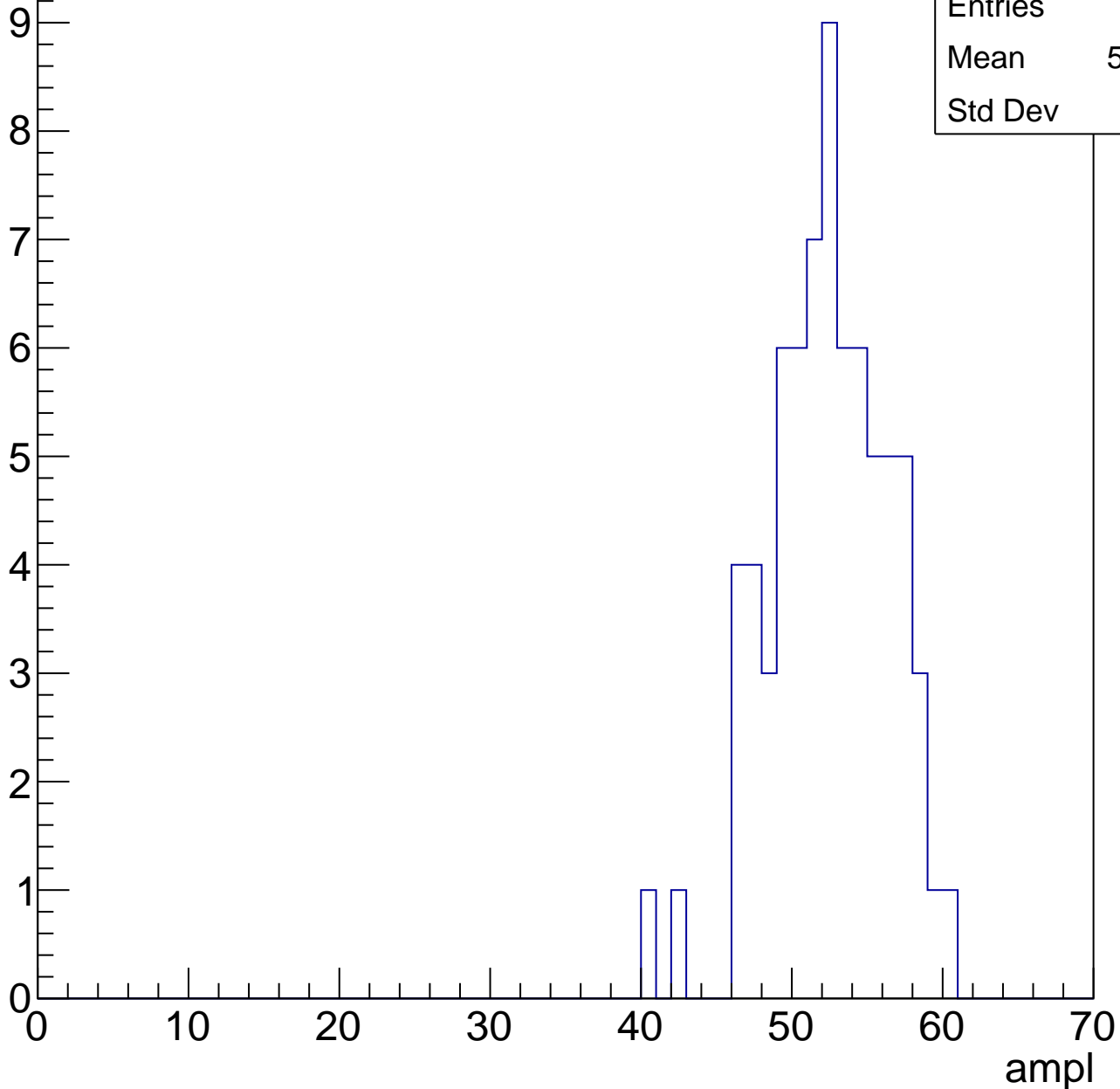


# B0L000S, U13-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

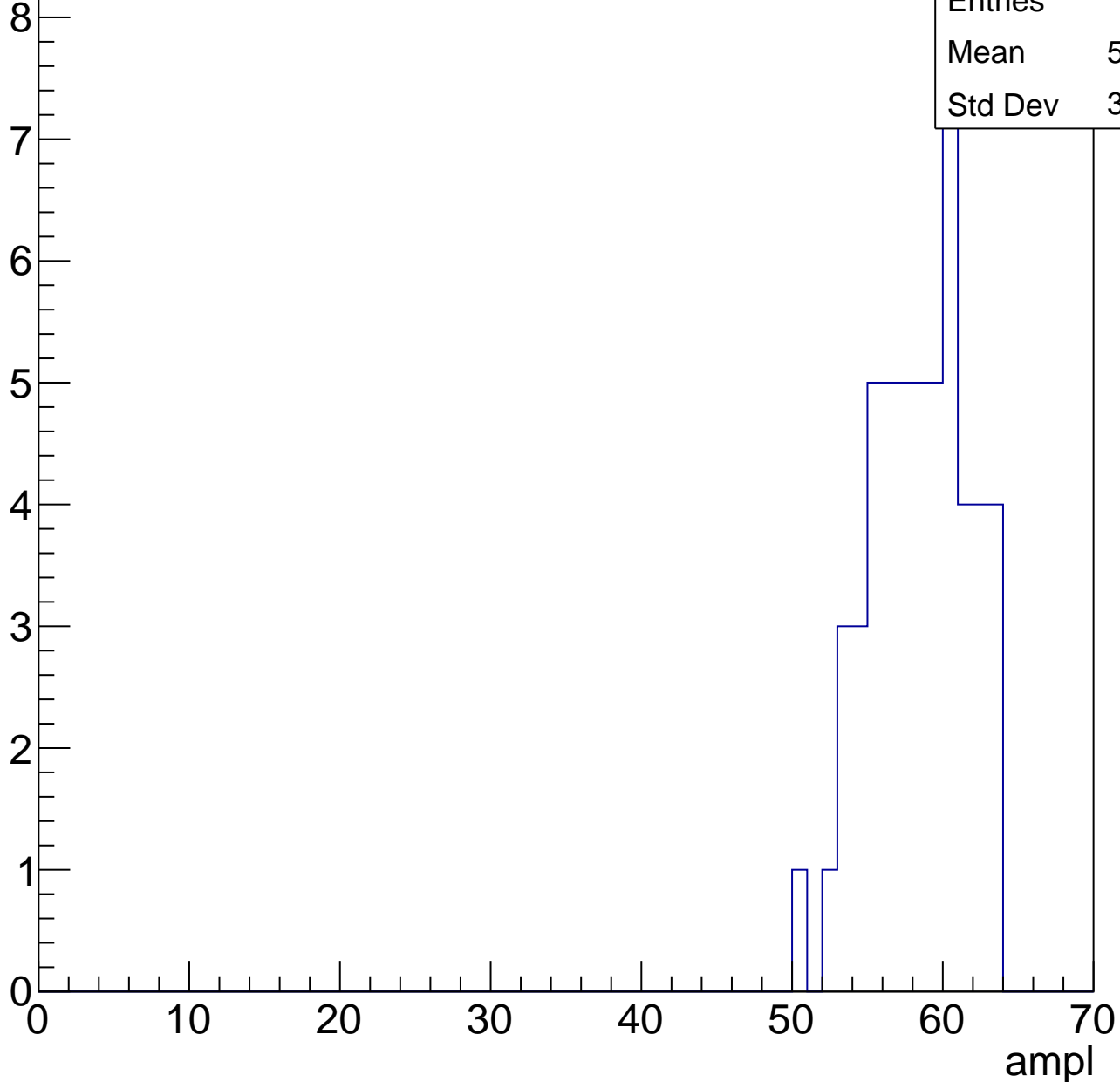
Entries	73
Mean	51.95
Std Dev	3.92



# B0L000S, U13-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

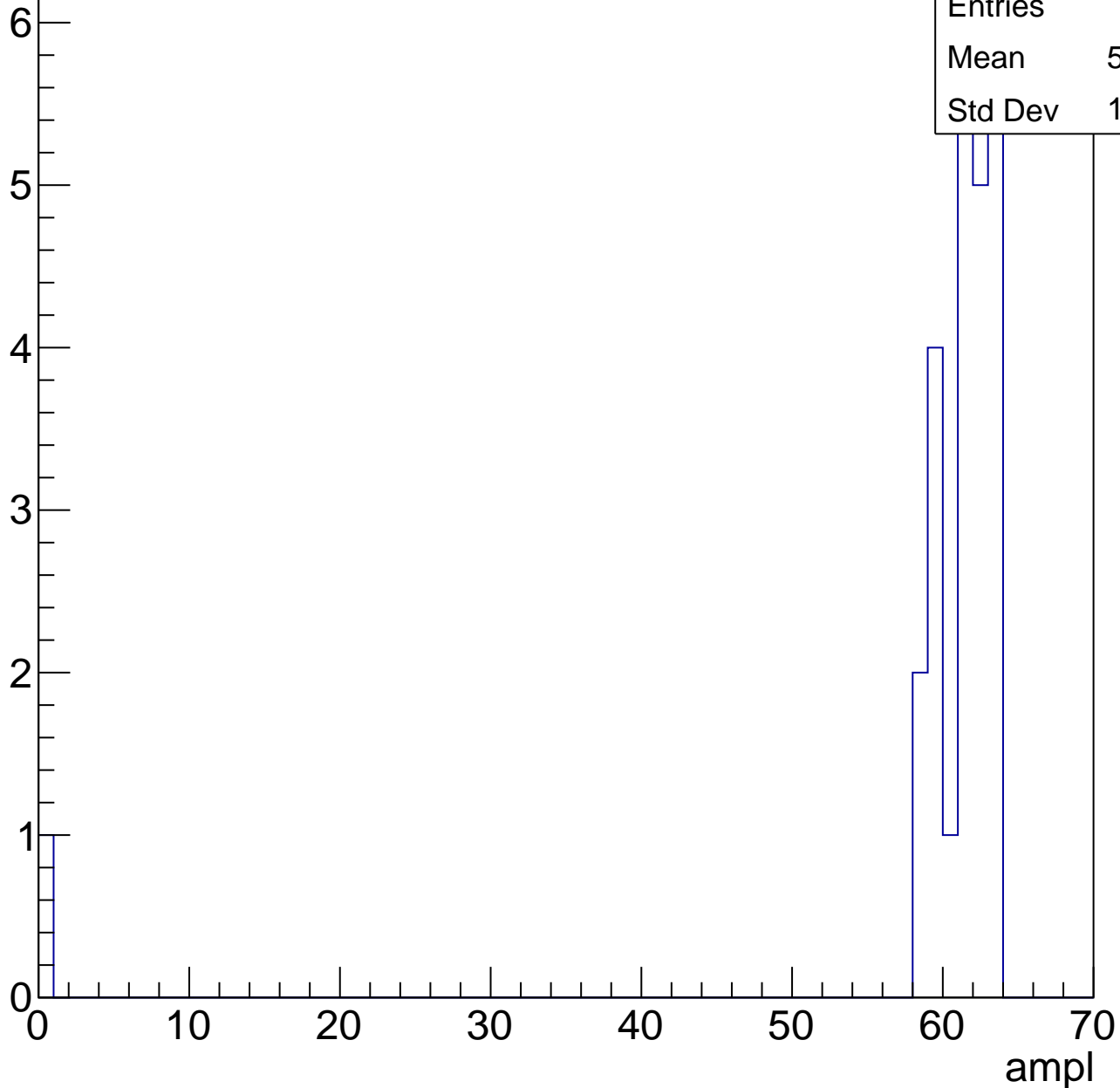
Entry



# B0L000S, U13-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U13-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

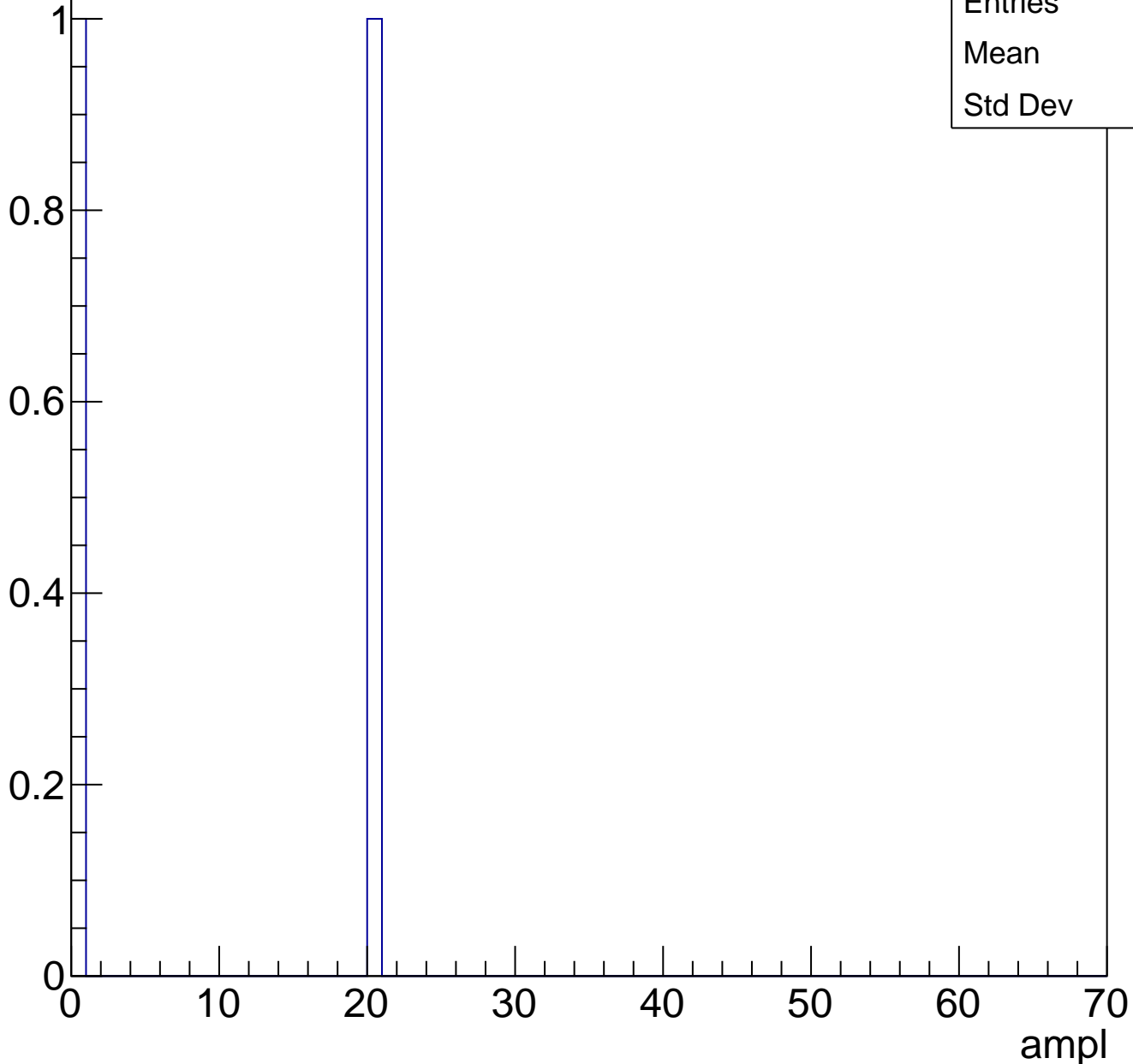




# B0L000S, U13-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U13-ch124, adc0

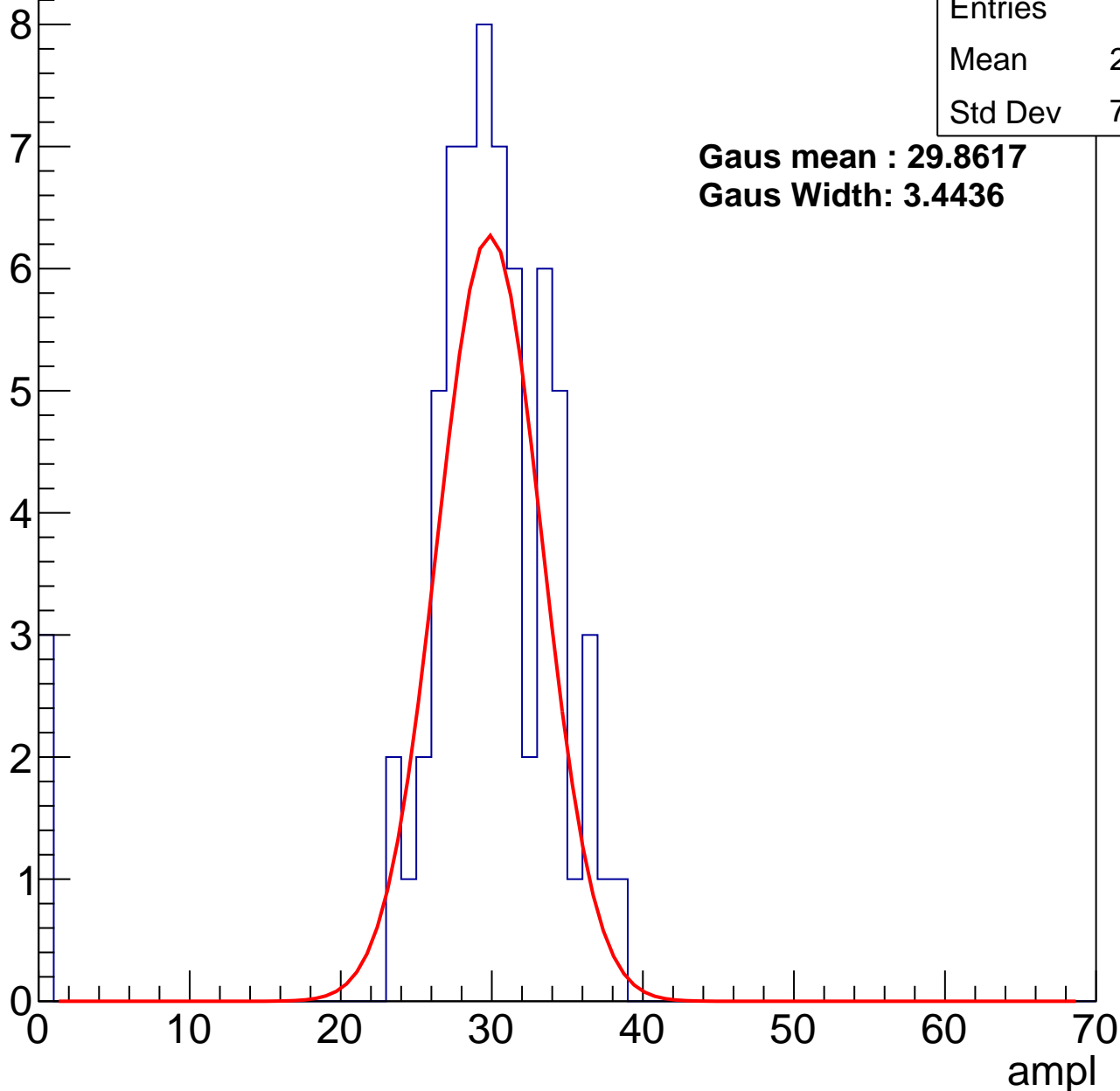
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.55
Std Dev	7.036

**Gaus mean : 29.8617**

**Gaus Width: 3.4436**



# B0L000S, U13-ch124, adc1

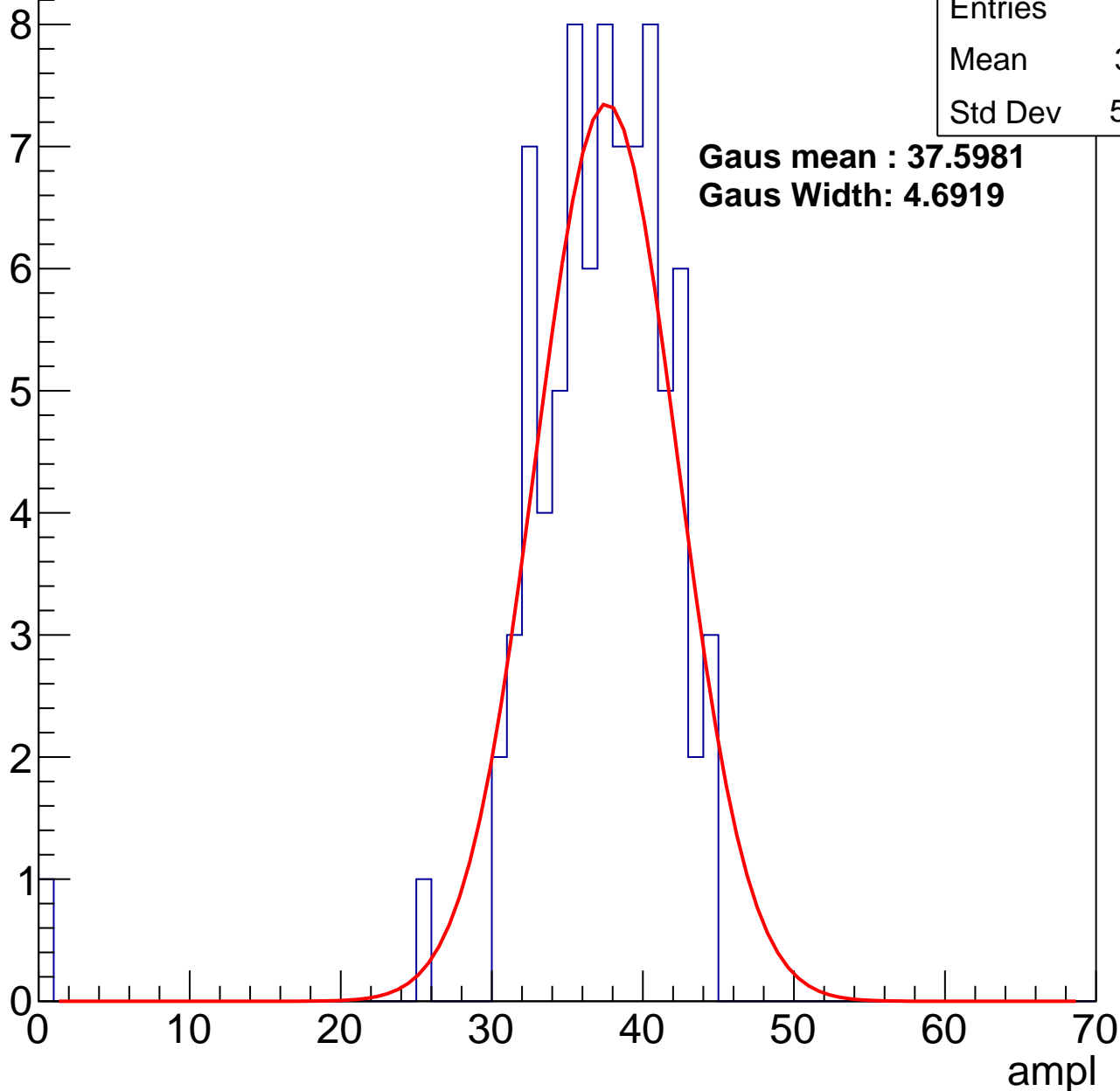
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	36.51
Std Dev	5.572

**Gaus mean : 37.5981**

**Gaus Width: 4.6919**



# B0L000S, U13-ch124, adc2

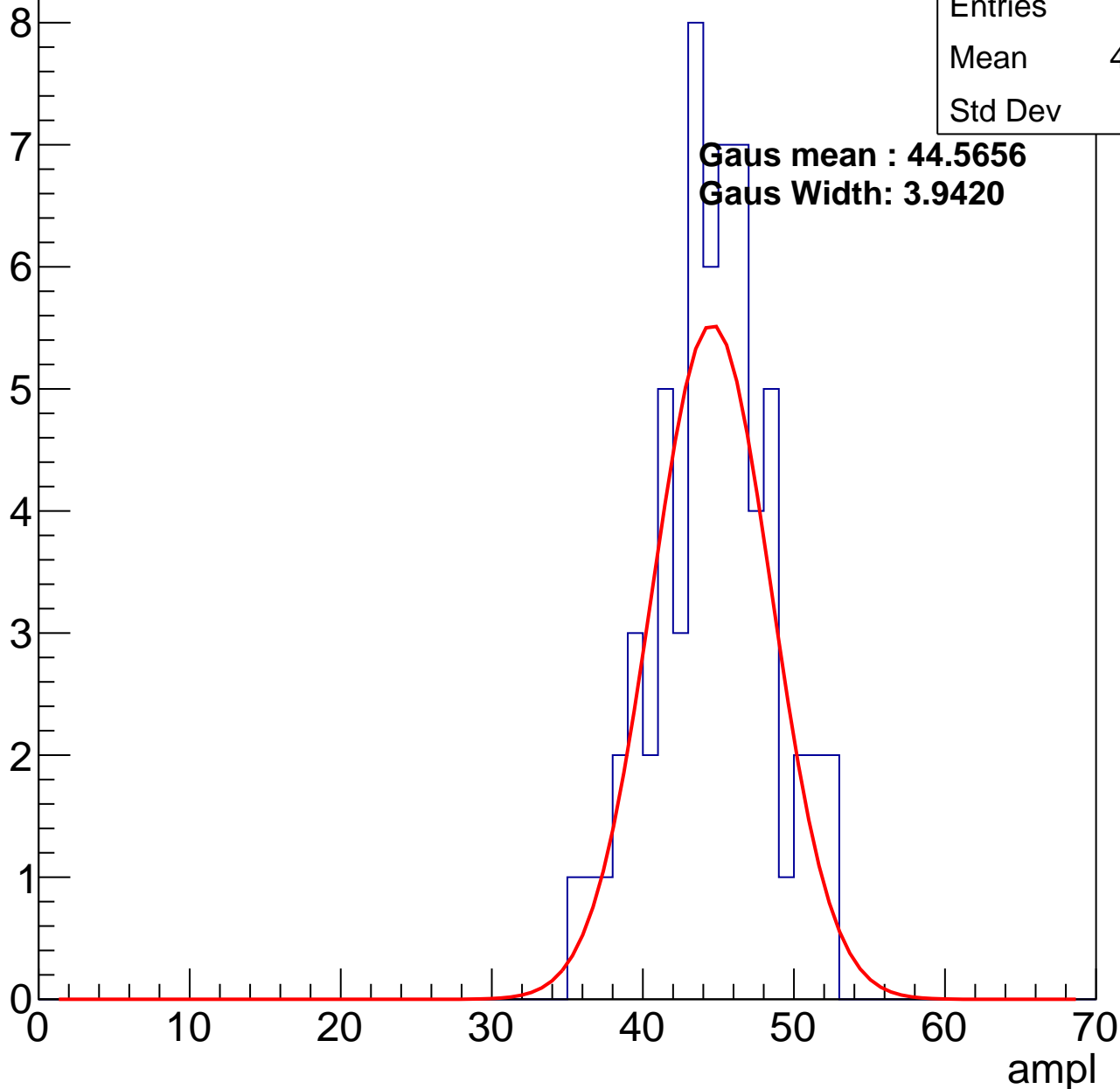
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	44.19
Std Dev	3.83

**Gaus mean : 44.5656**

**Gaus Width: 3.9420**

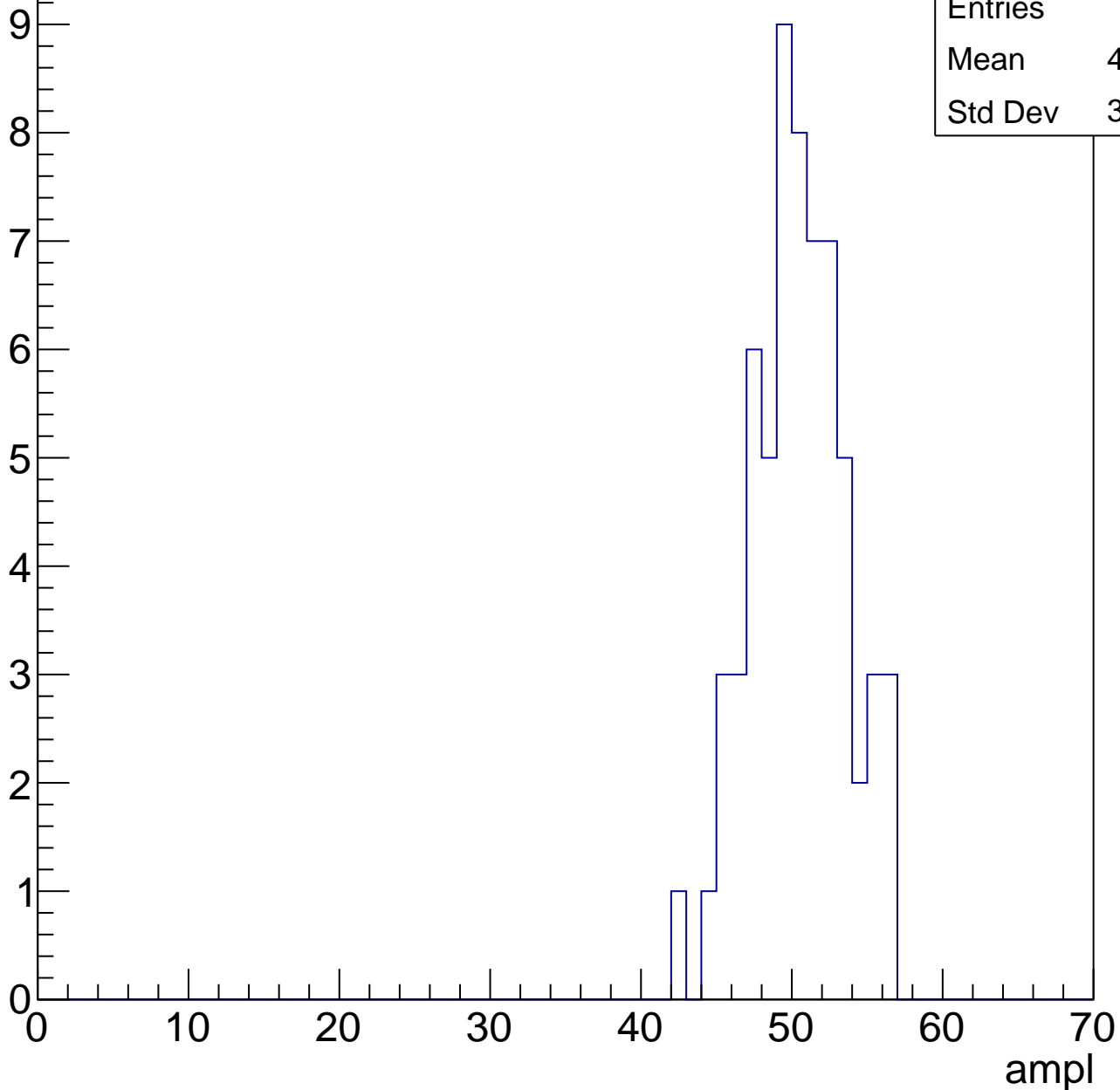


# B0L000S, U13-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.98
Std Dev	3.089

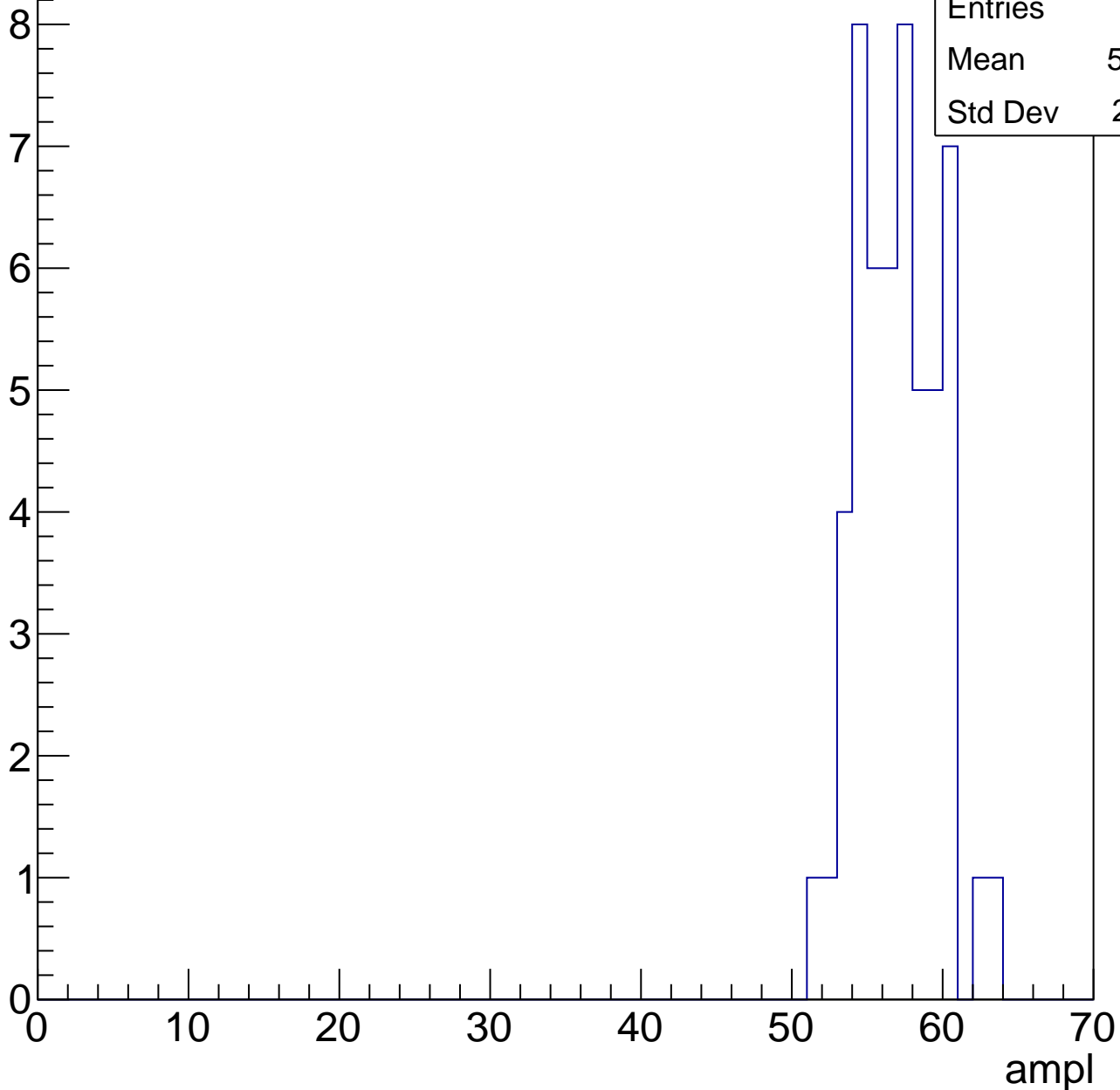


# B0L000S, U13-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.58
Std Dev	2.631

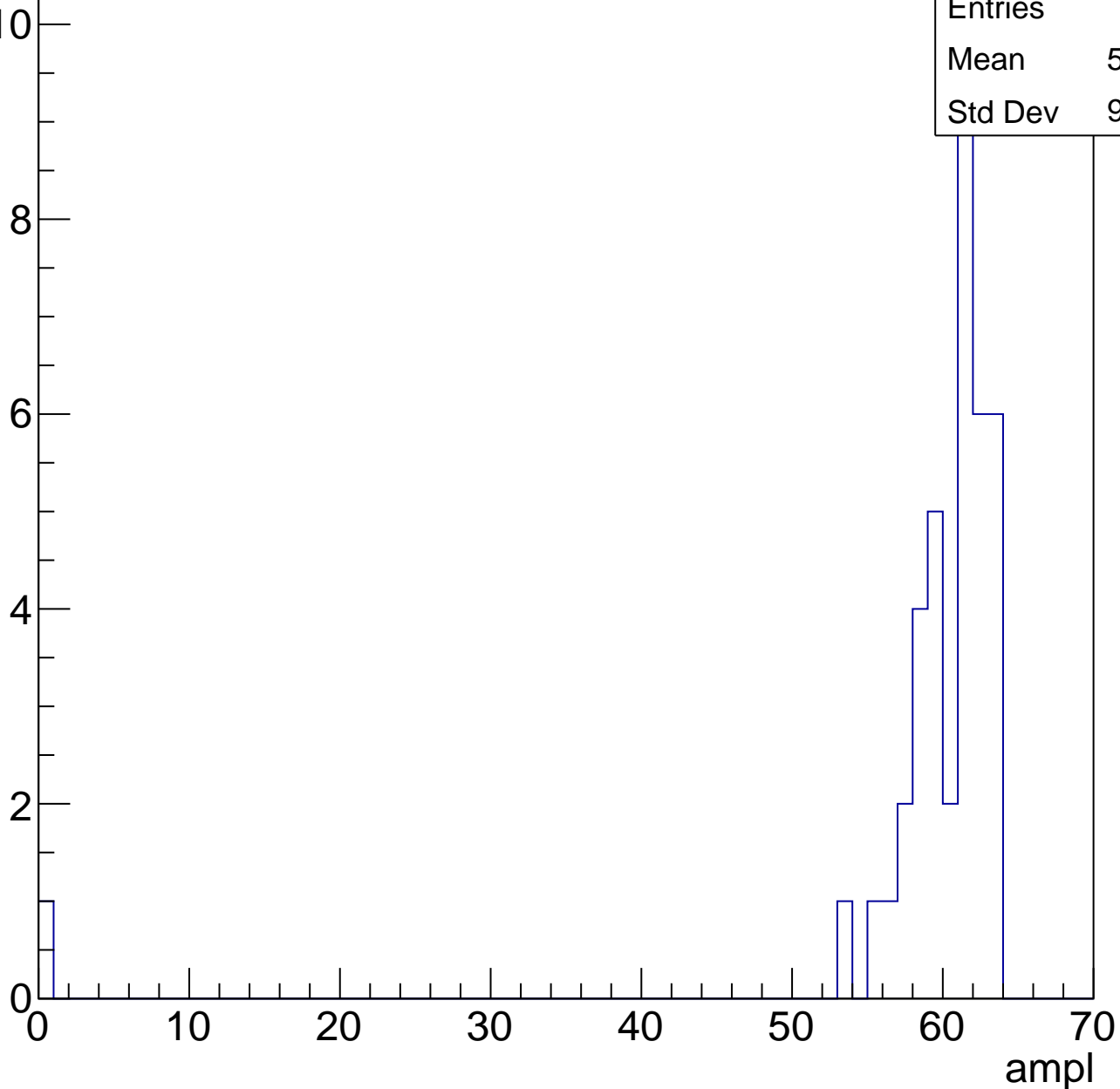


# B0L000S, U13-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

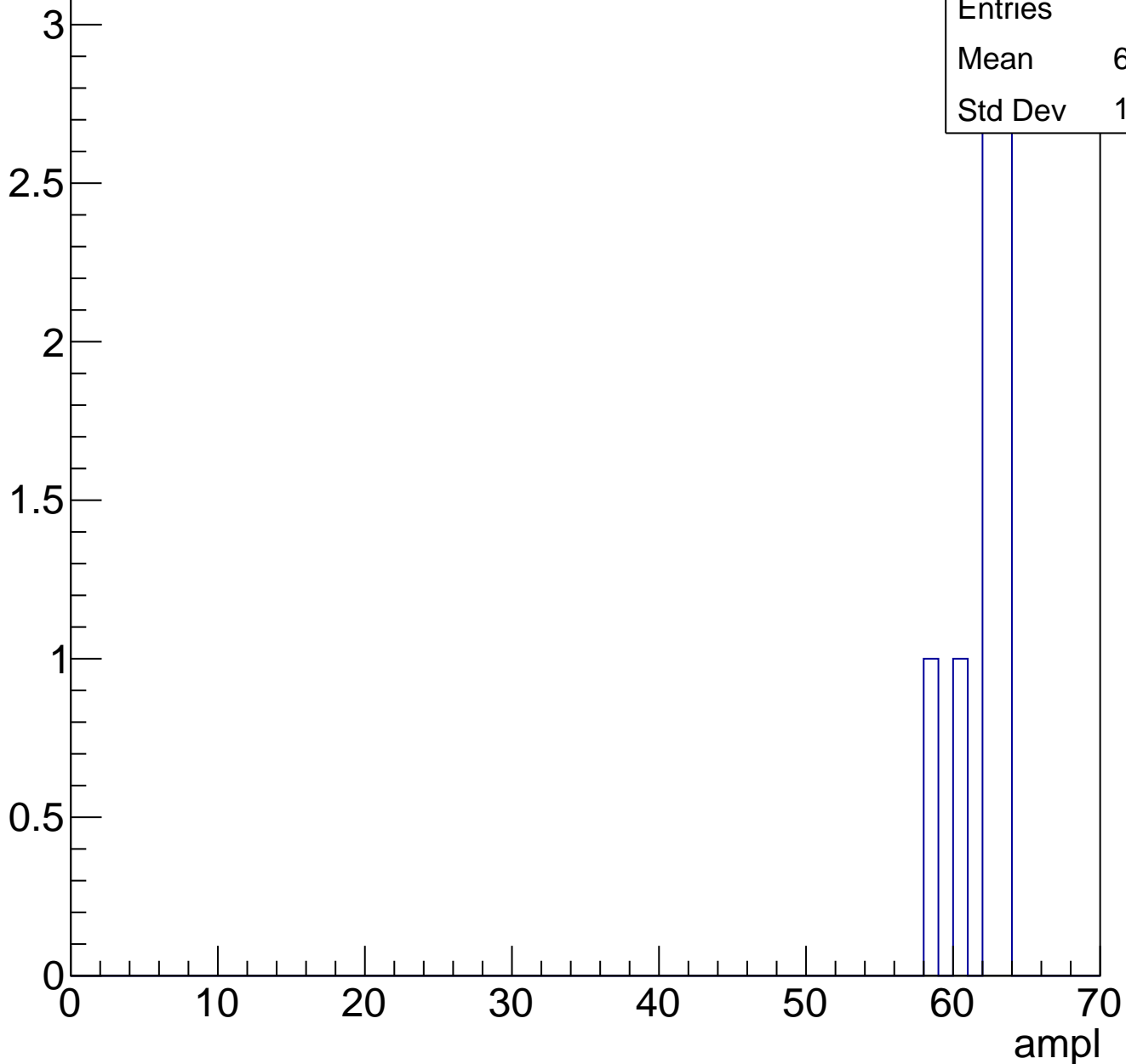
Entries	39
Mean	58.59
Std Dev	9.792



# B0L000S, U13-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U13-ch125, adc0

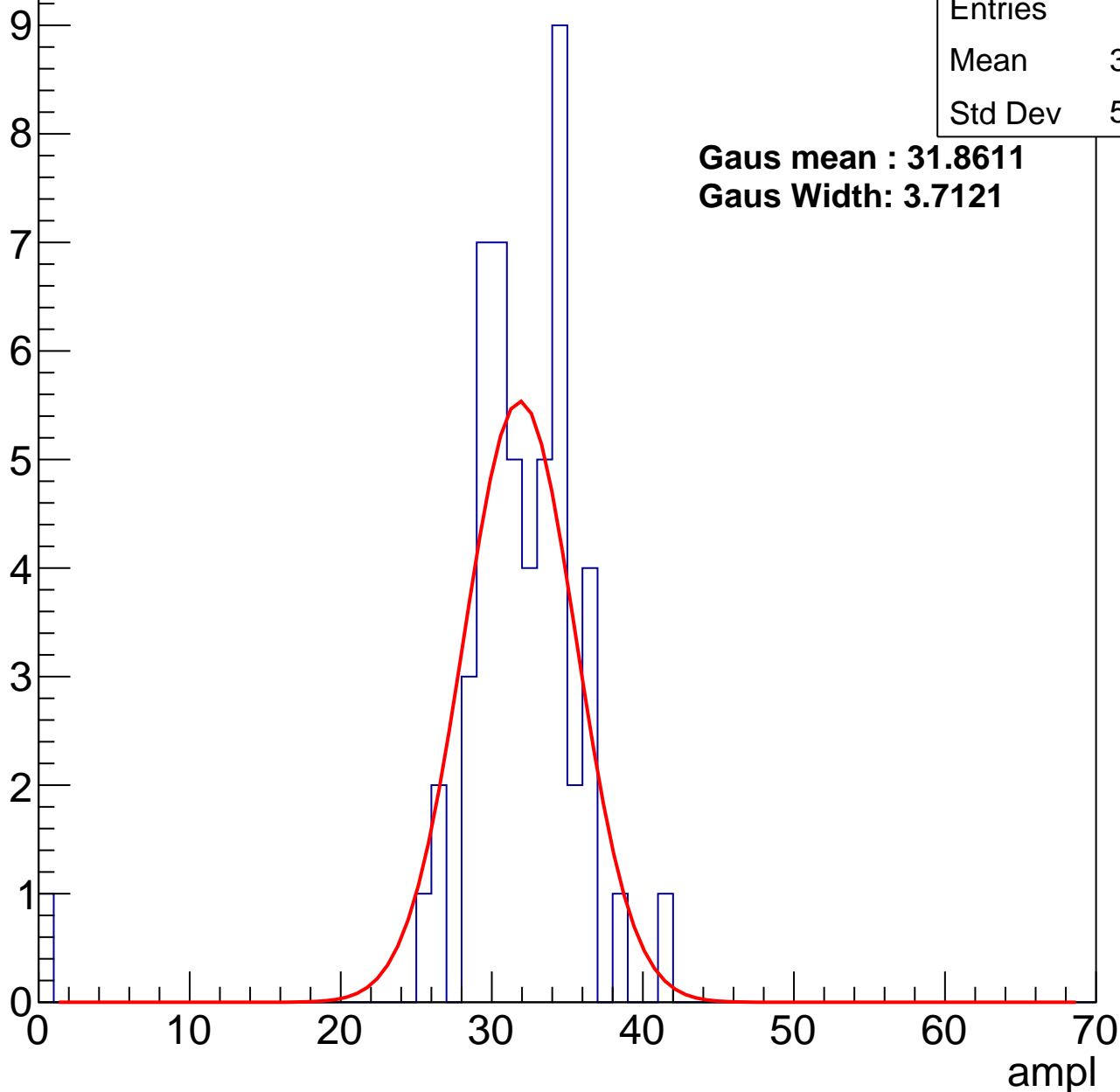
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	31.17
Std Dev	5.359

**Gaus mean : 31.8611**

**Gaus Width: 3.7121**



# B0L000S, U13-ch125, adc1

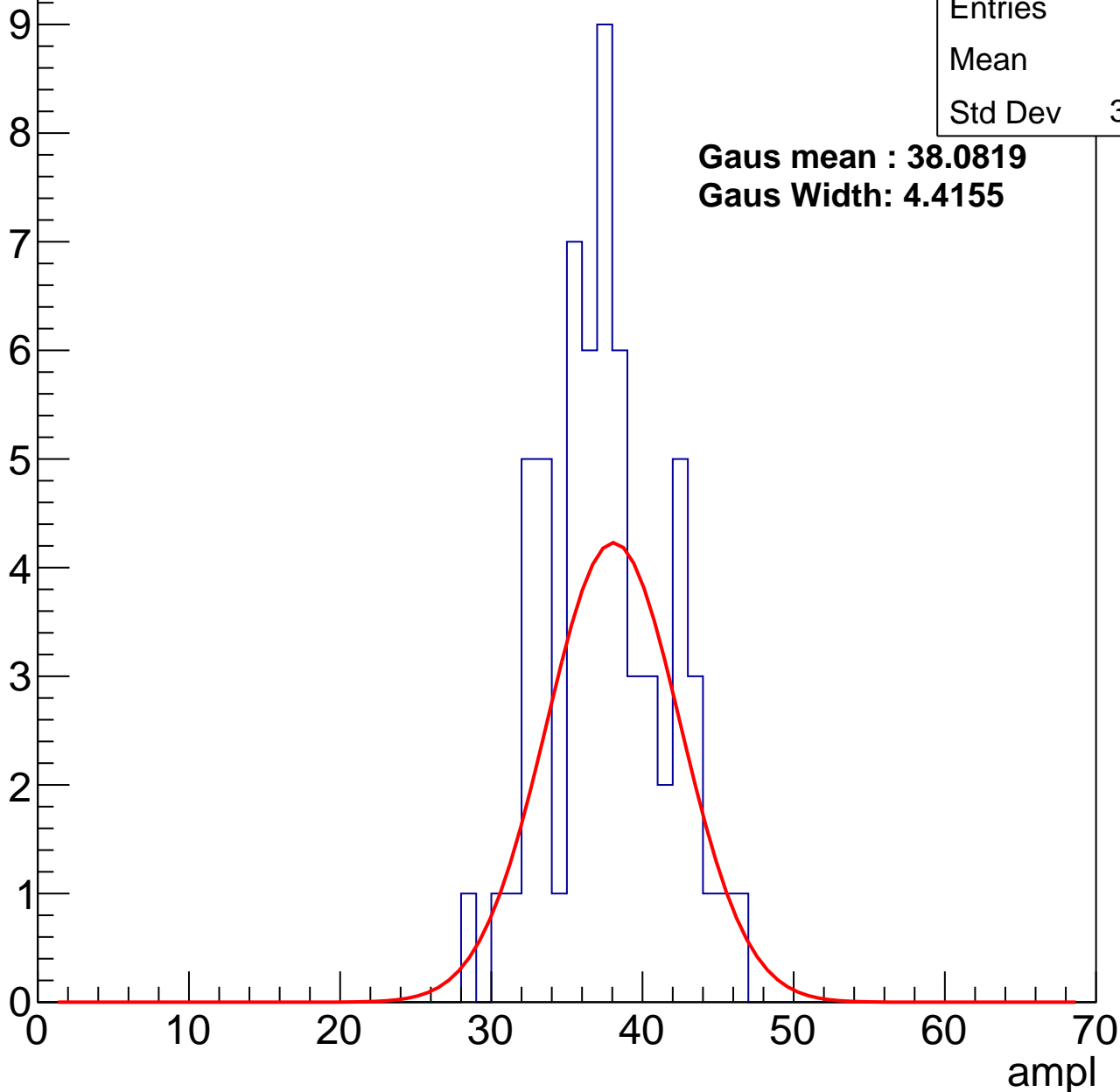
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.1
Std Dev	3.878

**Gaus mean : 38.0819**

**Gaus Width: 4.4155**

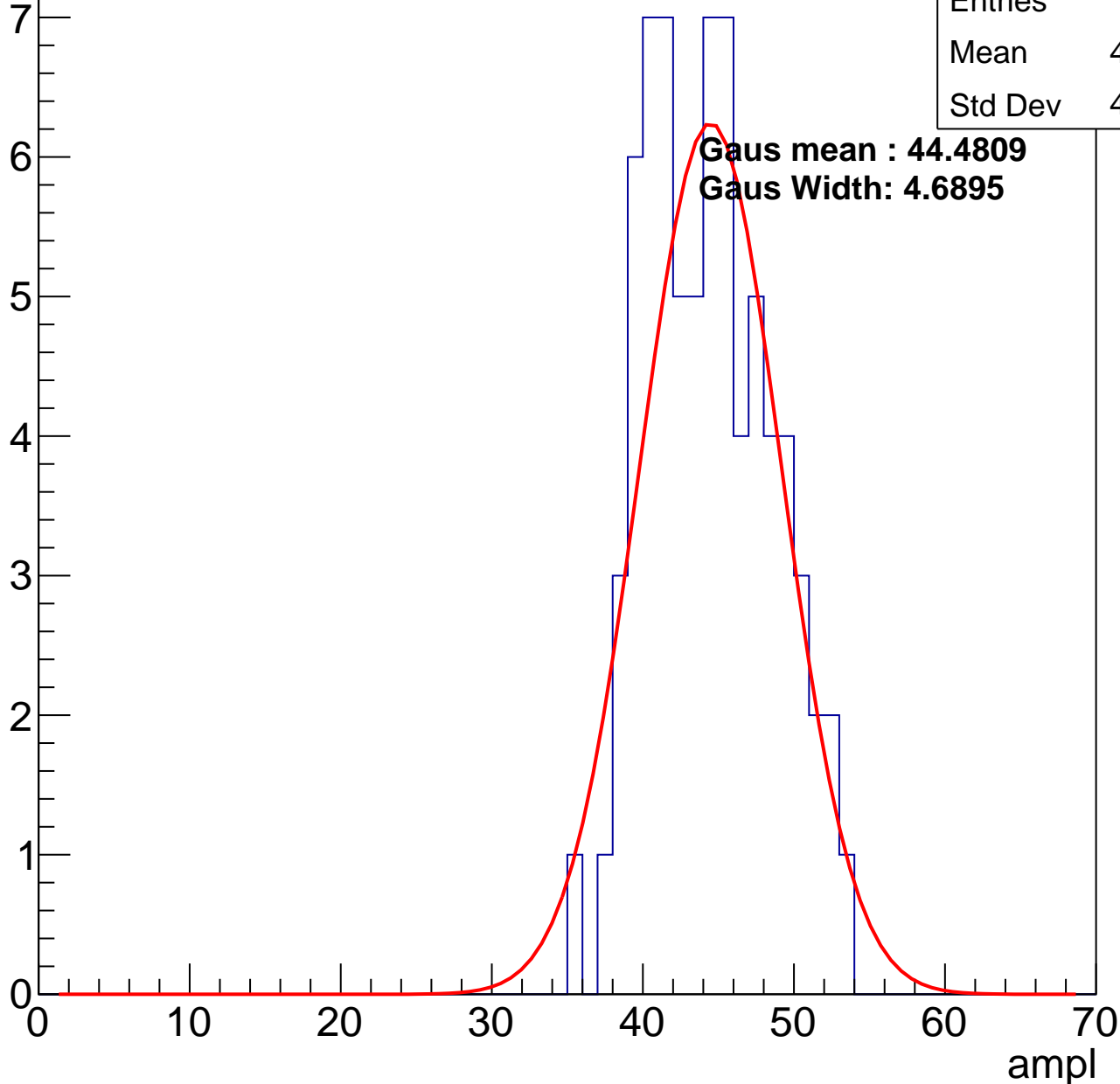


# B0L000S, U13-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.93
Std Dev	4.085

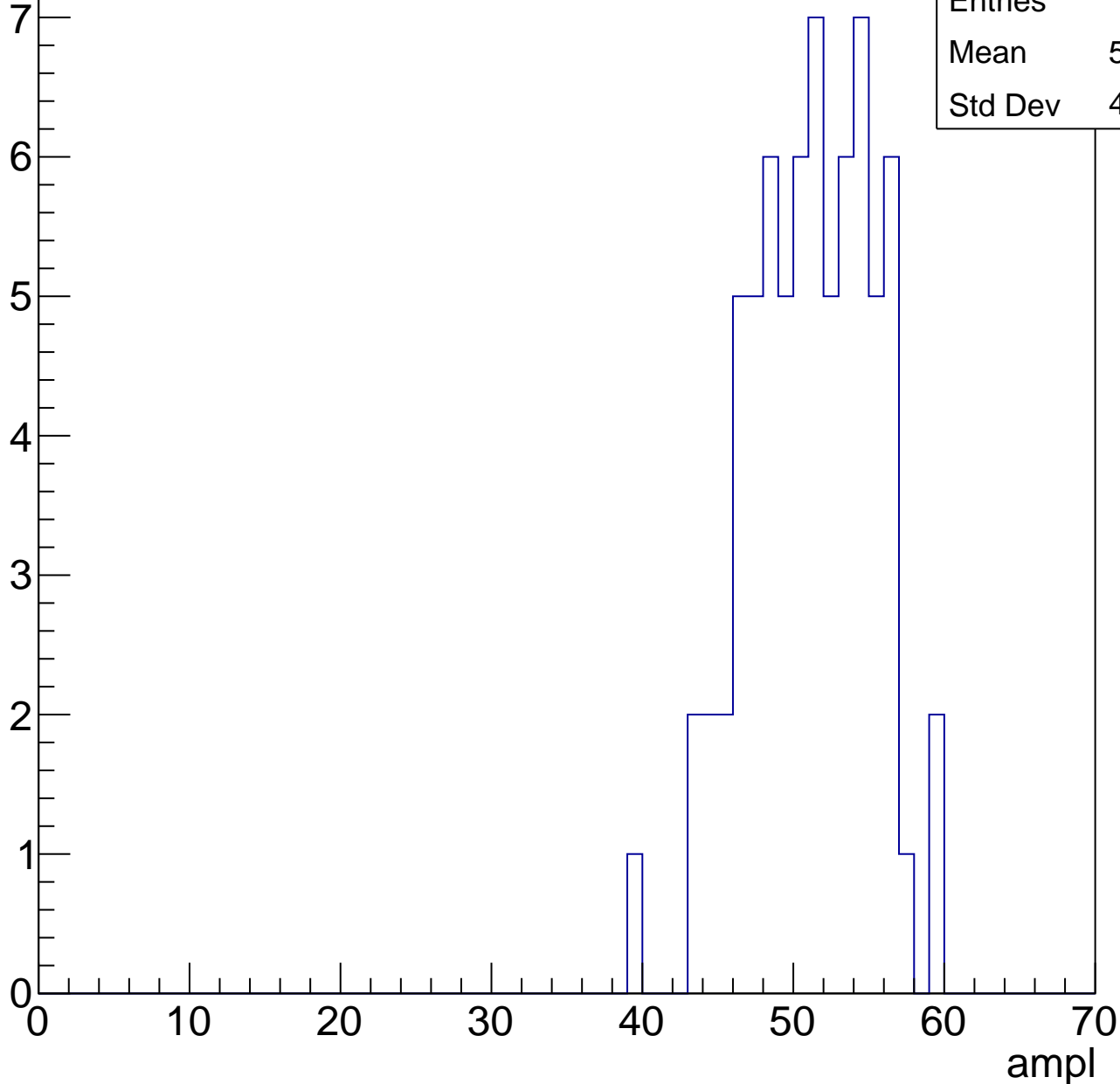


# B0L000S, U13-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	50.68
Std Dev	4.064



# B0L000S, U13-ch125, adc4

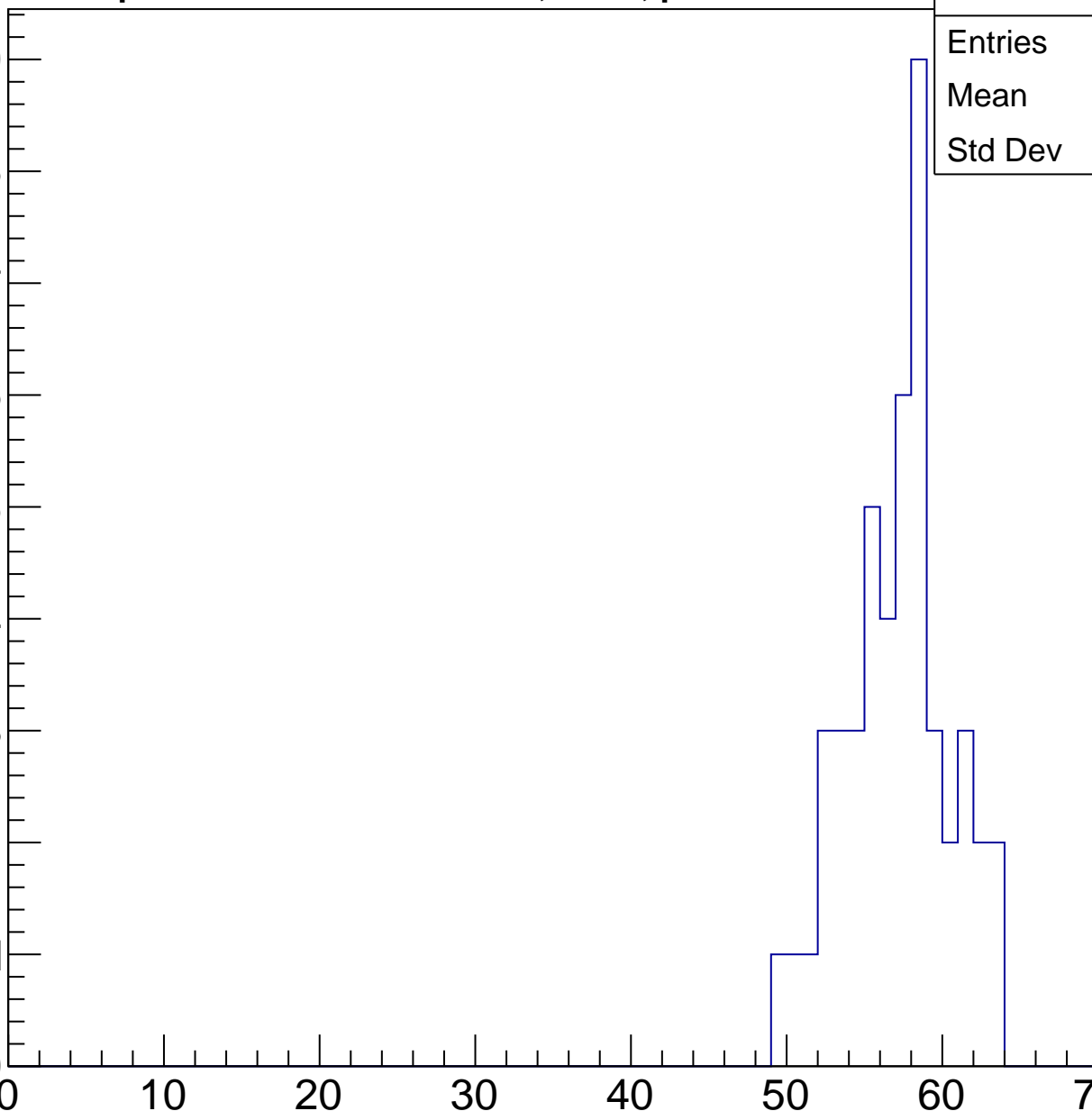
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	56.67
Std Dev	3.312

ampl

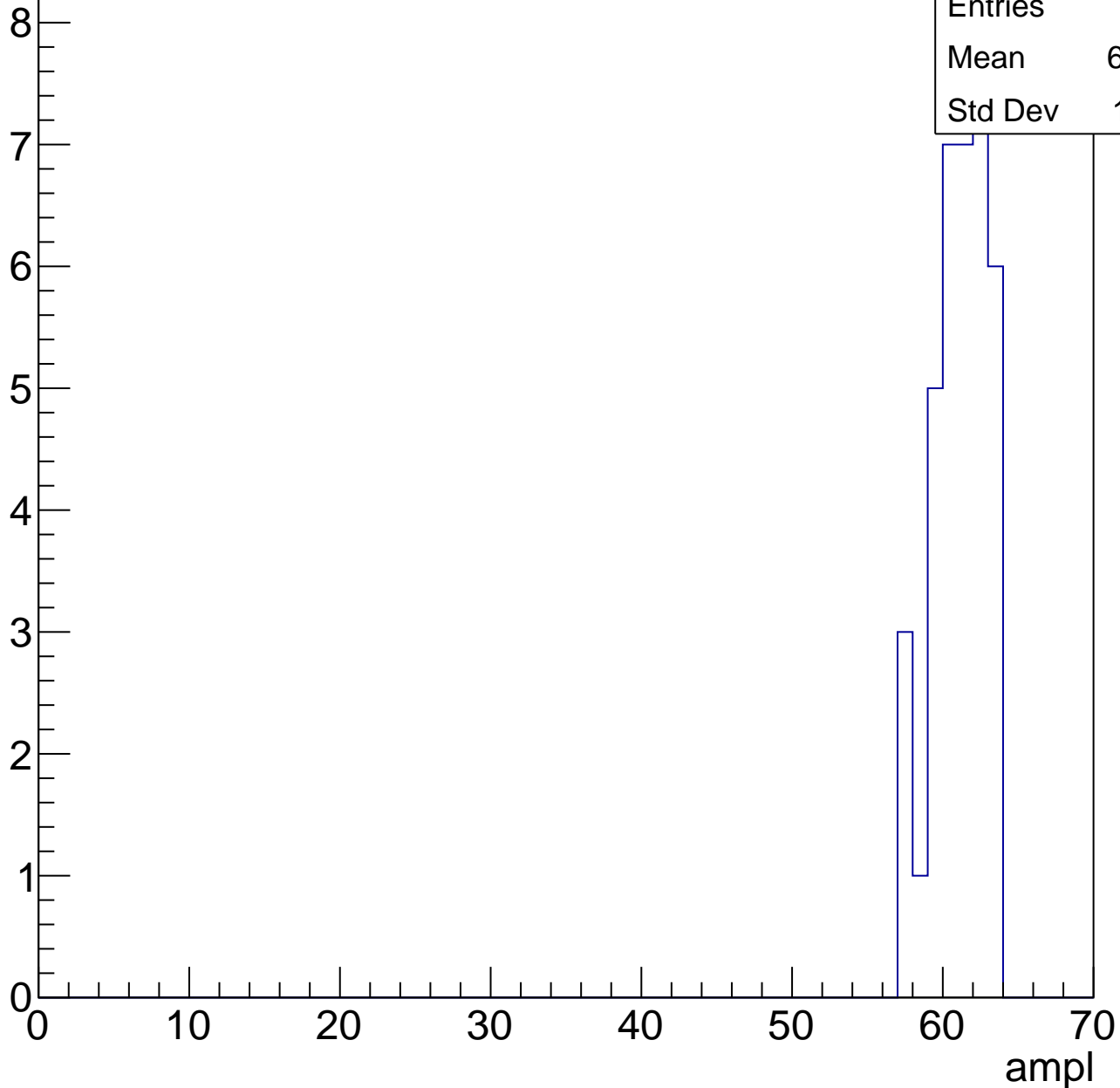


# B0L000S, U13-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

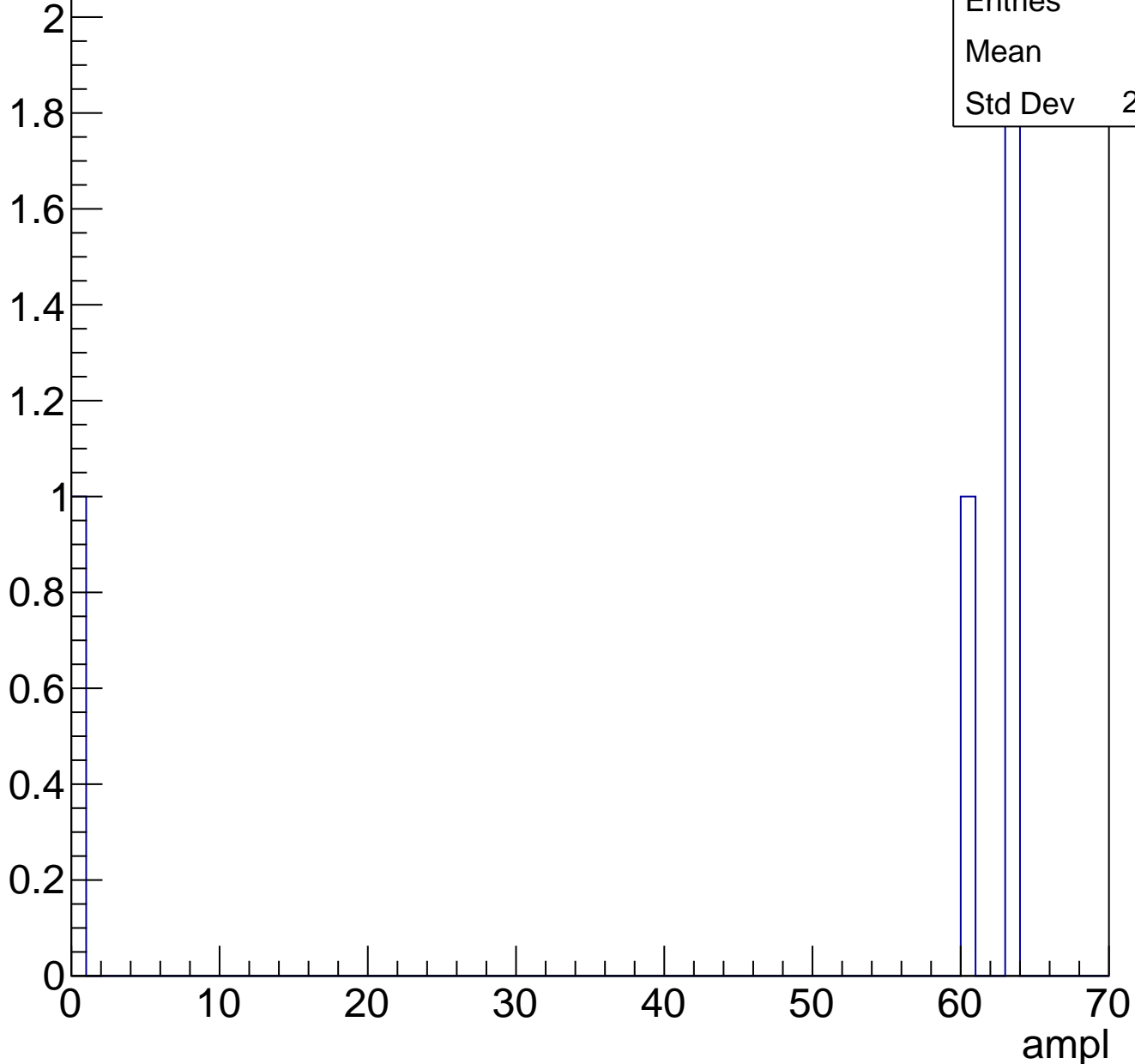
Entries	37
Mean	60.68
Std Dev	1.741



# B0L000S, U13-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch126, adc0

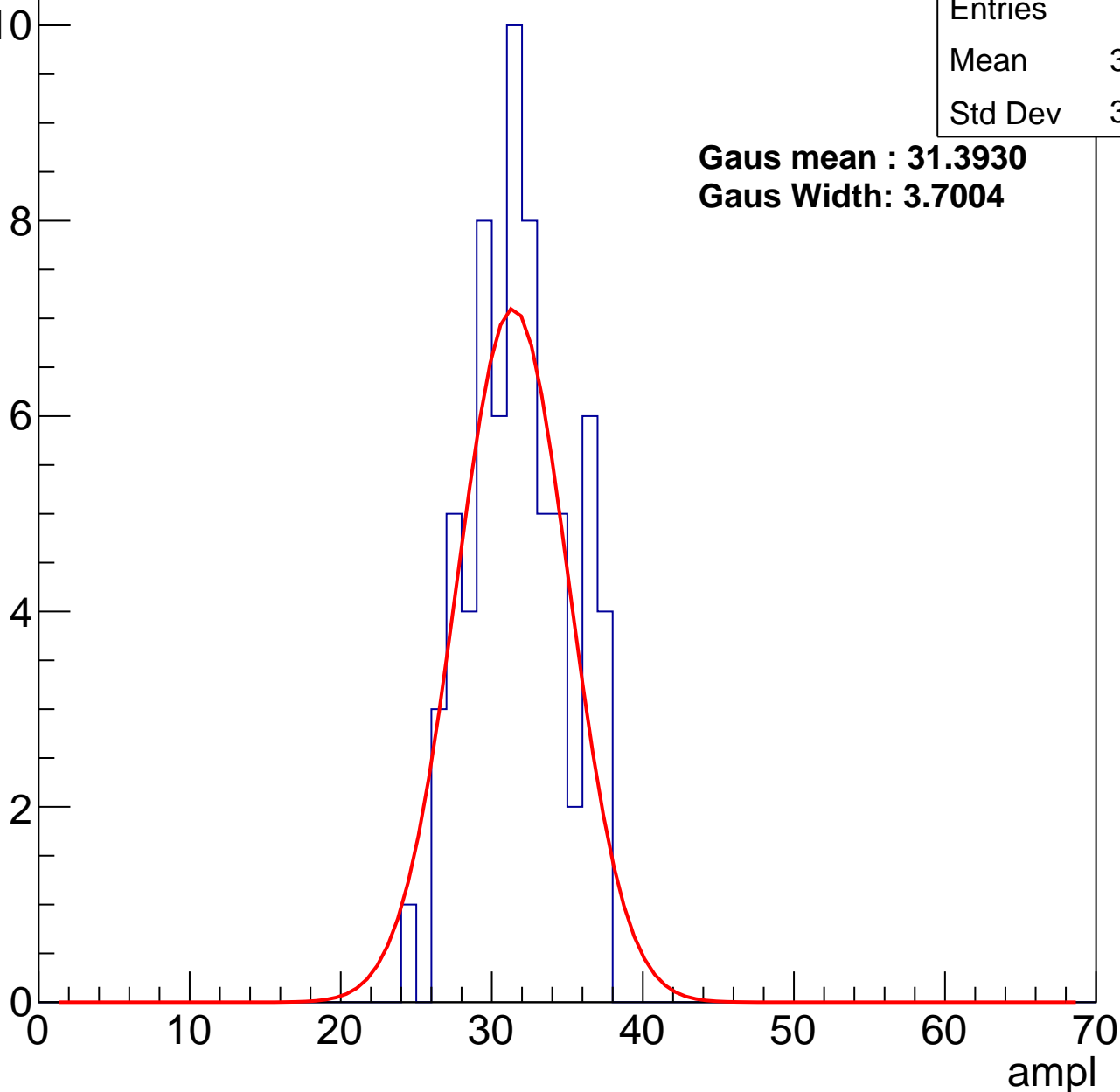
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	31.28
Std Dev	3.166

**Gaus mean : 31.3930**

**Gaus Width: 3.7004**



# B0L000S, U13-ch126, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	37.45
Std Dev	3.414

**Gaus mean : 37.6327**

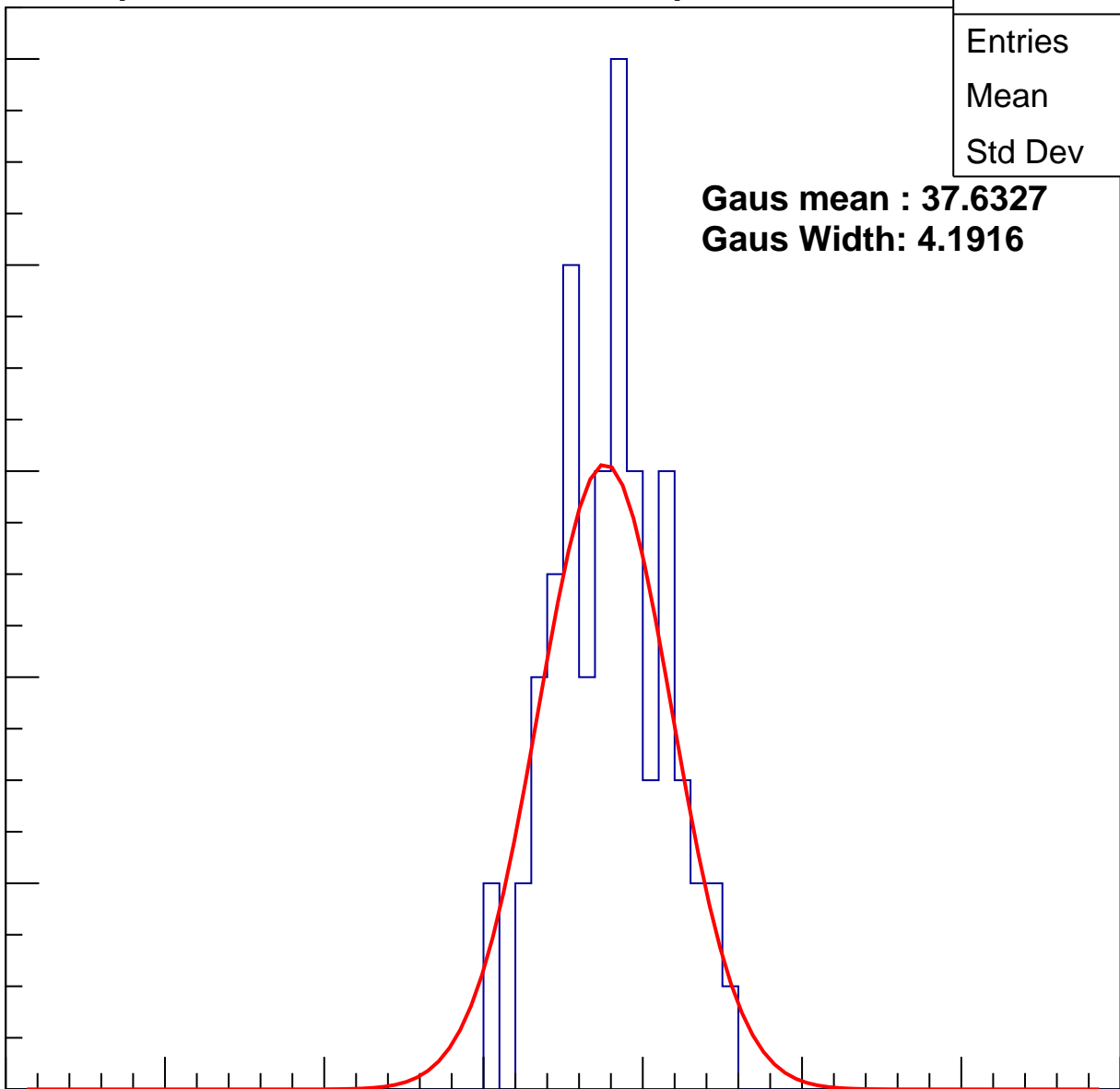
**Gaus Width: 4.1916**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U13-ch126, adc2

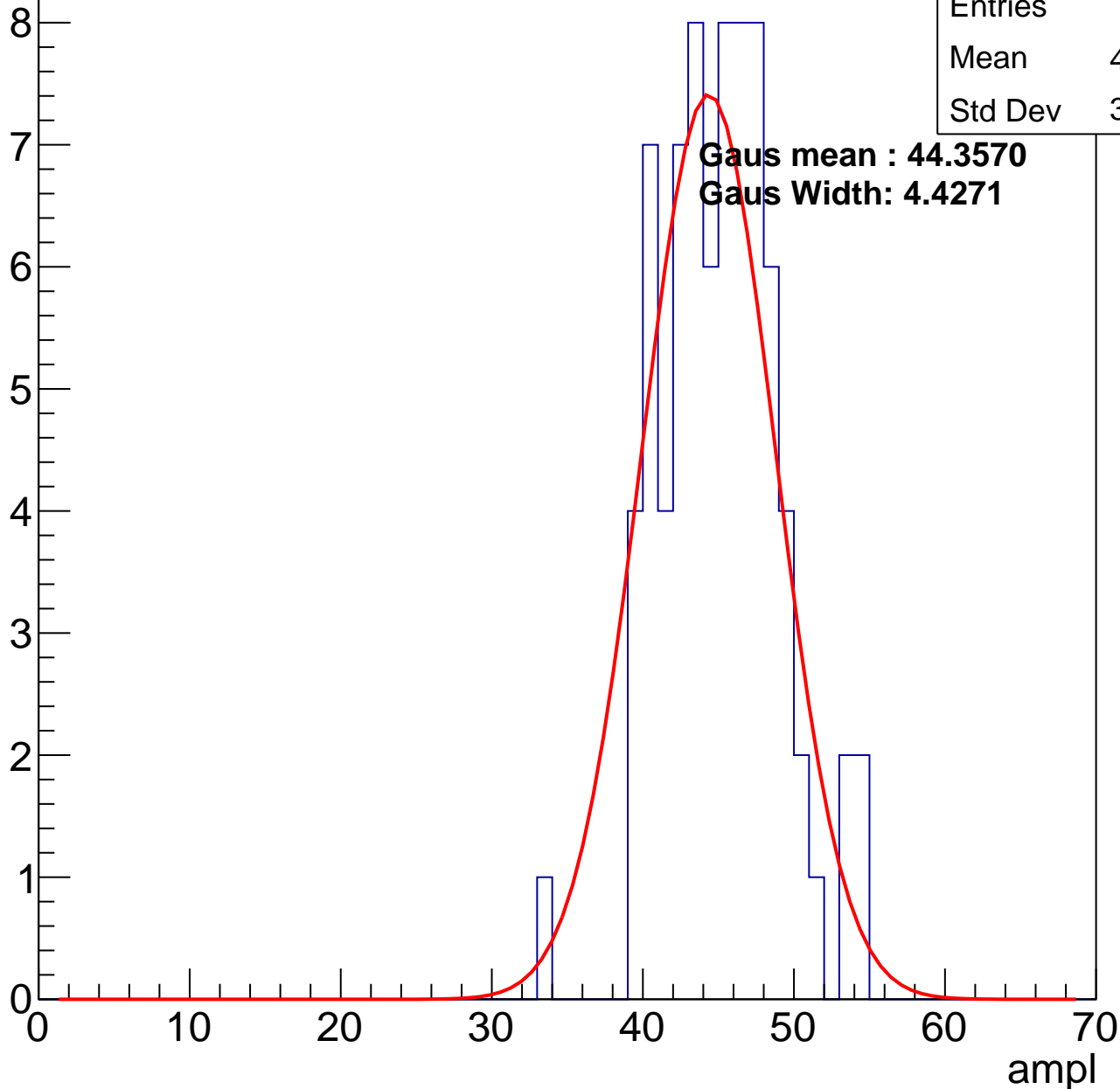
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	44.72
Std Dev	3.849

**Gaus mean : 44.3570**

**Gaus Width: 4.4271**

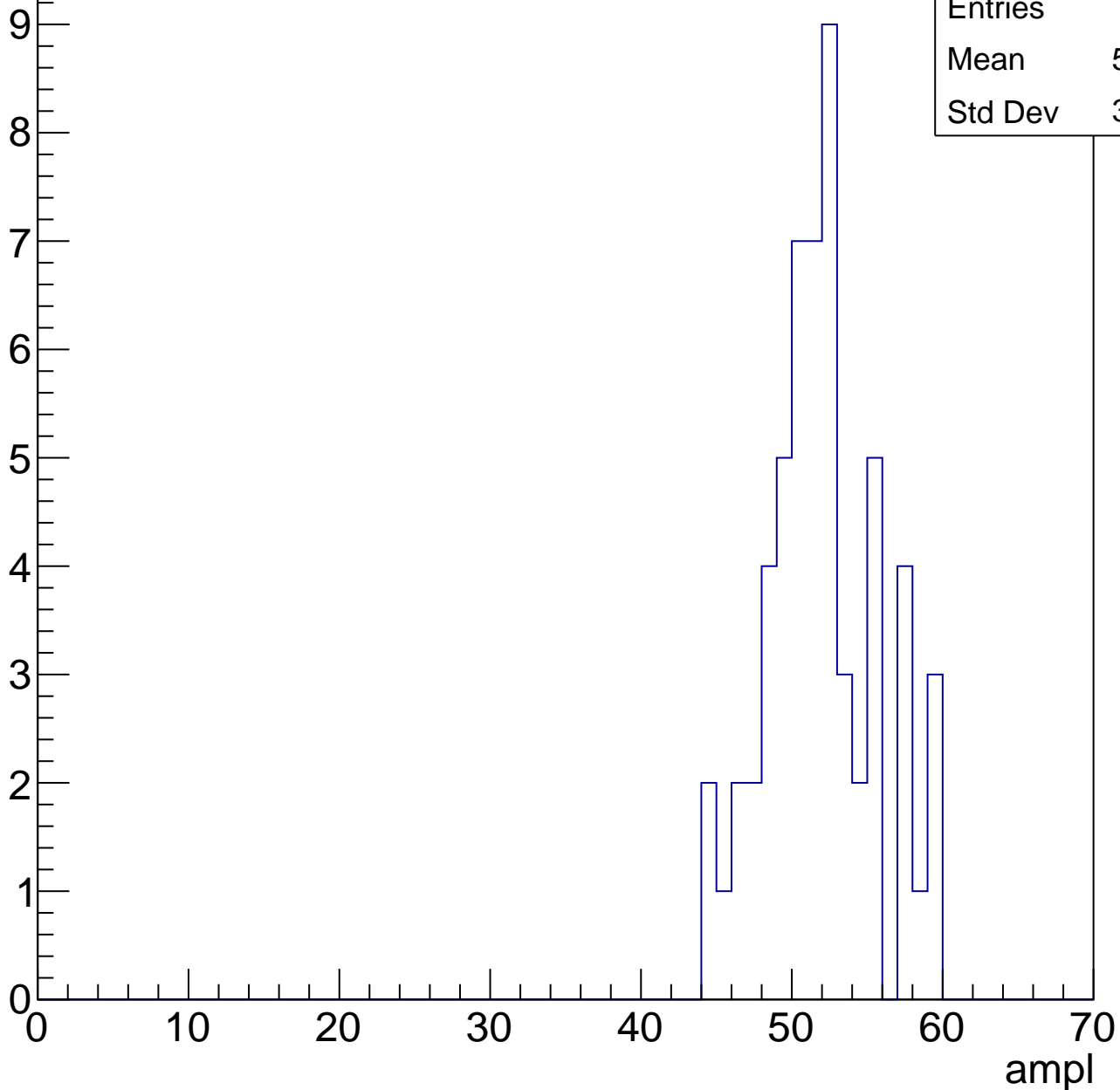


# B0L000S, U13-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	51.51
Std Dev	3.671



# B0L000S, U13-ch126, adc4

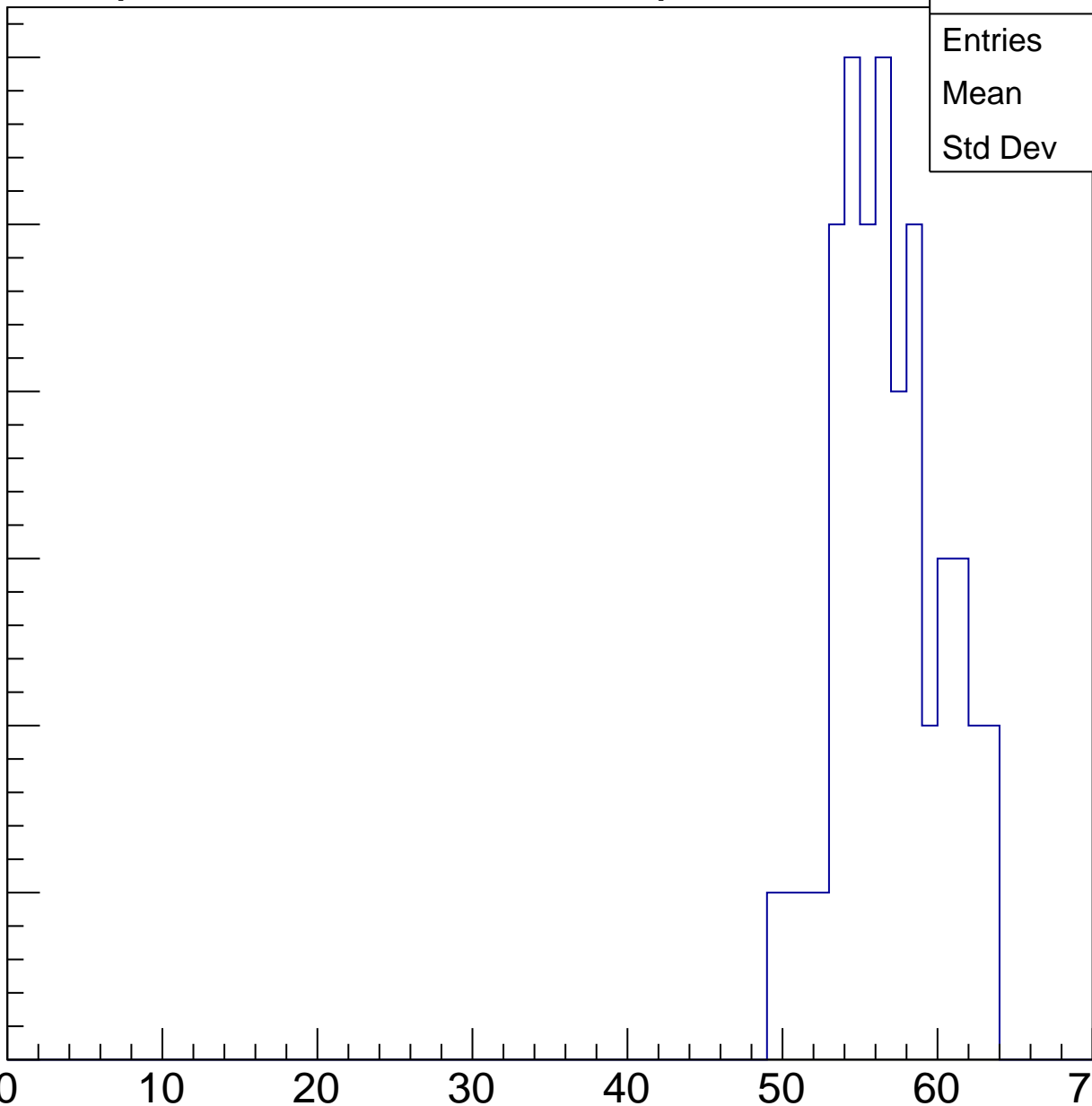
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	56.4
Std Dev	3.35

ampl

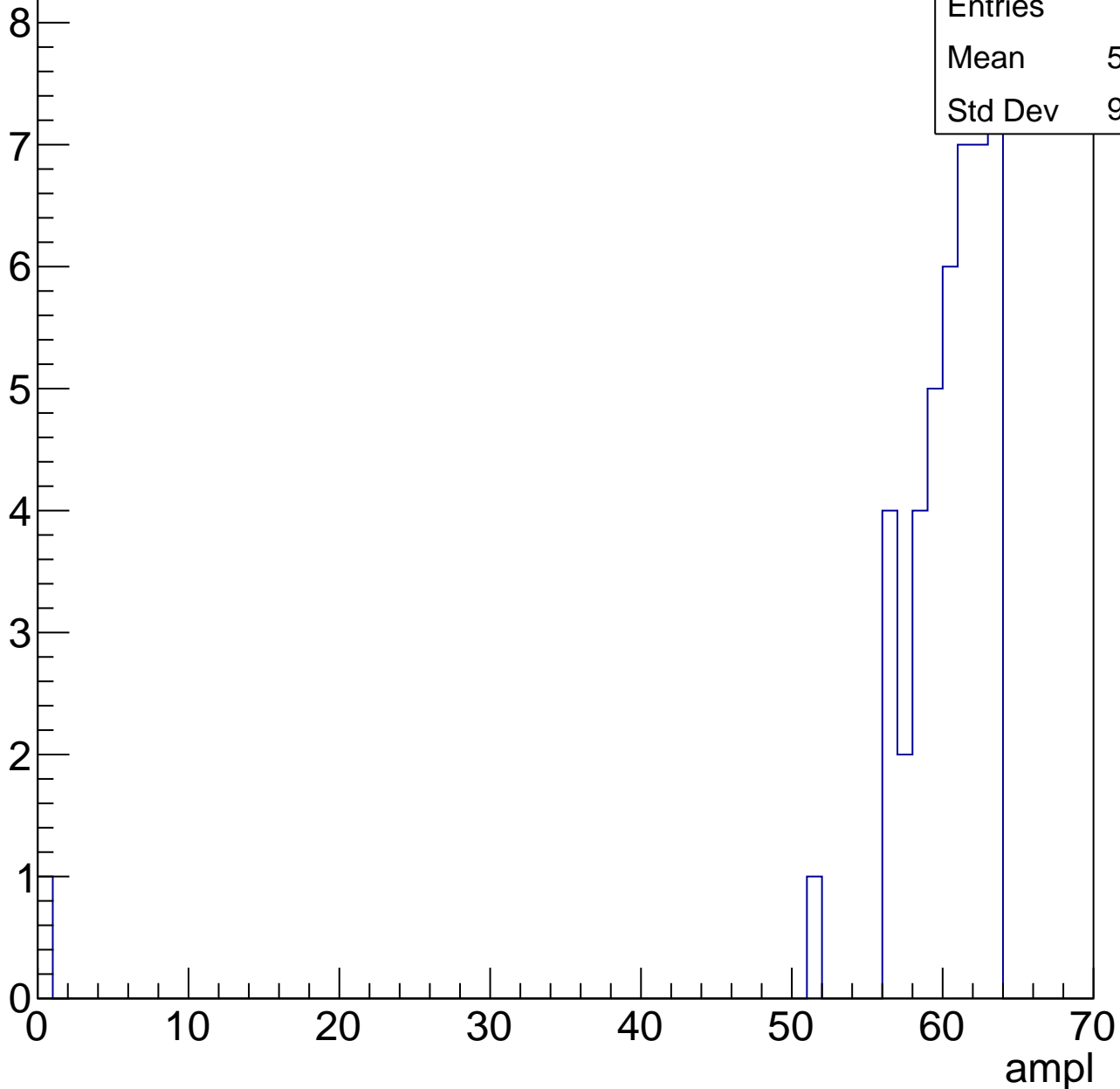


# B0L000S, U13-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.69
Std Dev	9.206



# B0L000S, U13-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch127, adc0

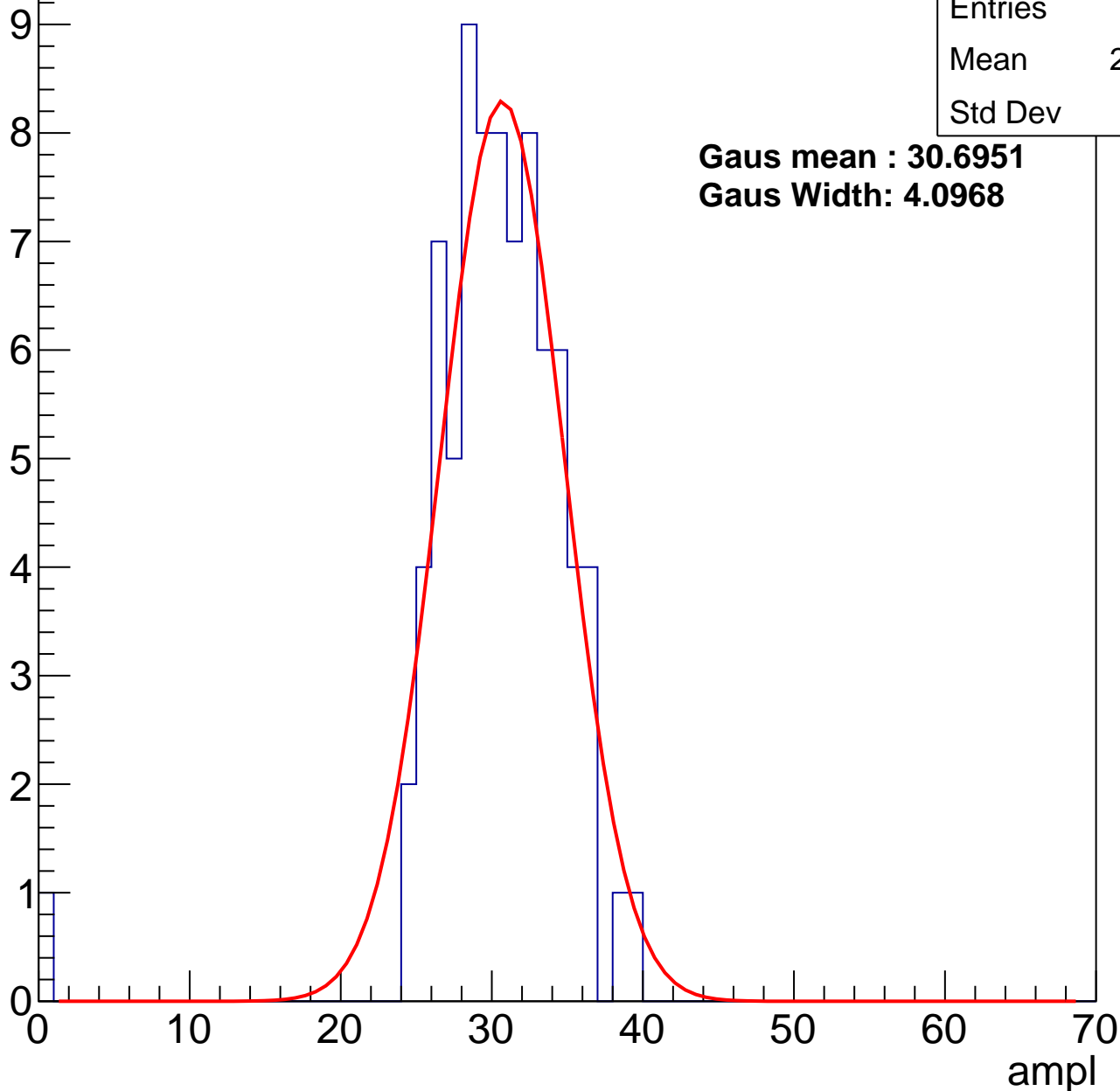
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	29.94
Std Dev	4.78

**Gaus mean : 30.6951**

**Gaus Width: 4.0968**



# B0L000S, U13-ch127, adc1

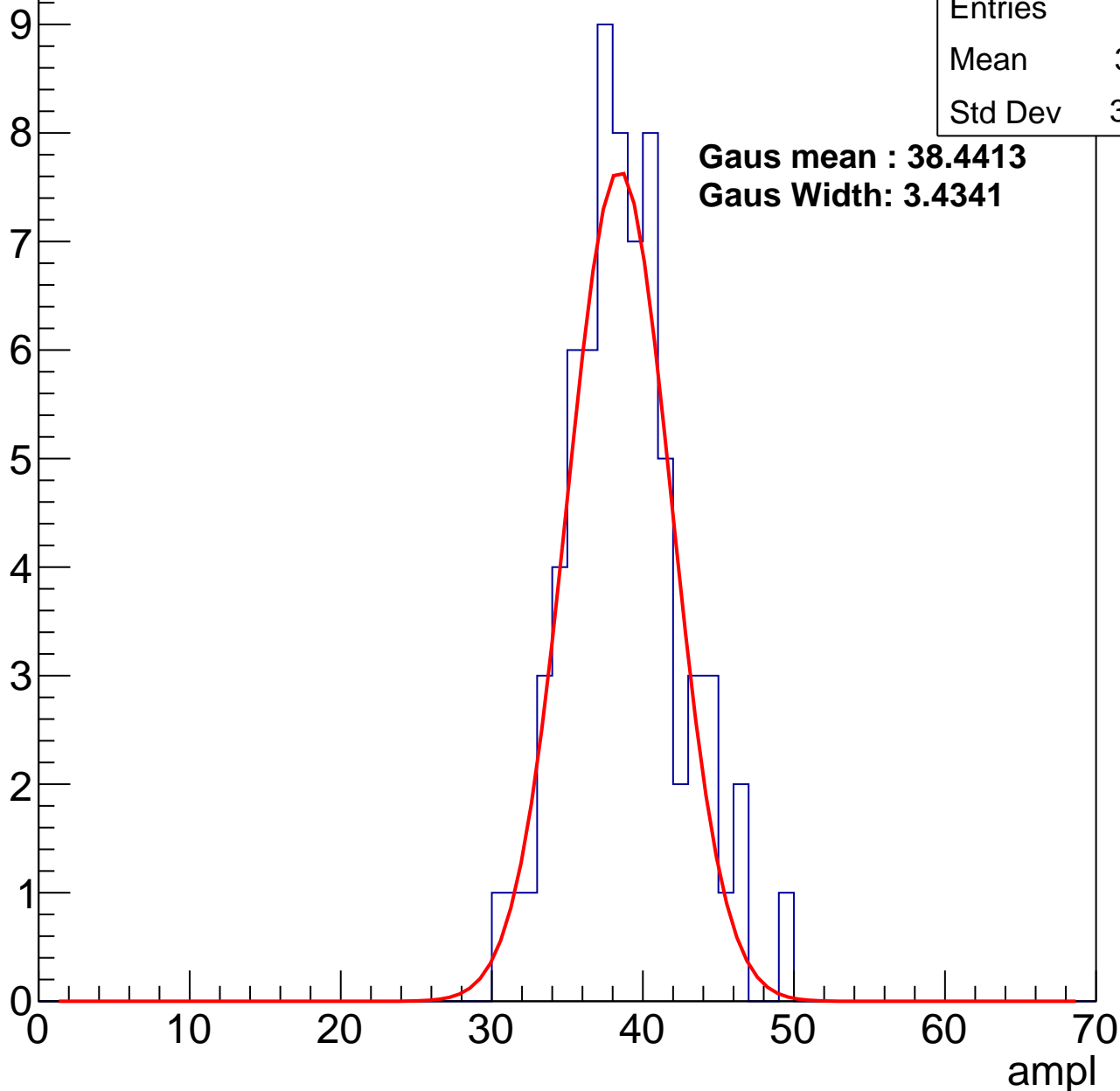
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	38.31
Std Dev	3.687

**Gaus mean : 38.4413**

**Gaus Width: 3.4341**



# B0L000S, U13-ch127, adc2

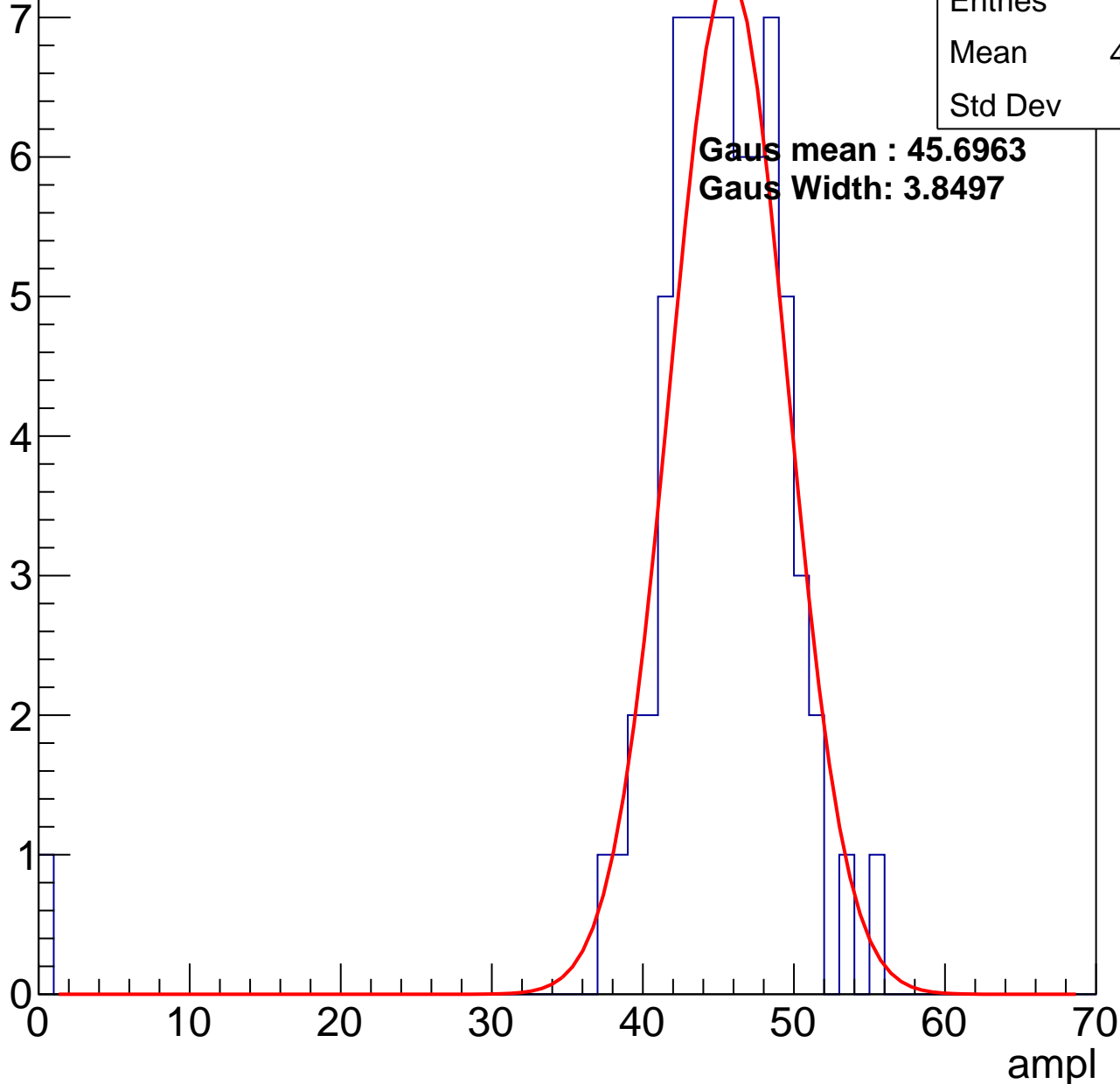
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	44.44
Std Dev	6.39

**Gaus mean : 45.6963**

**Gaus Width: 3.8497**

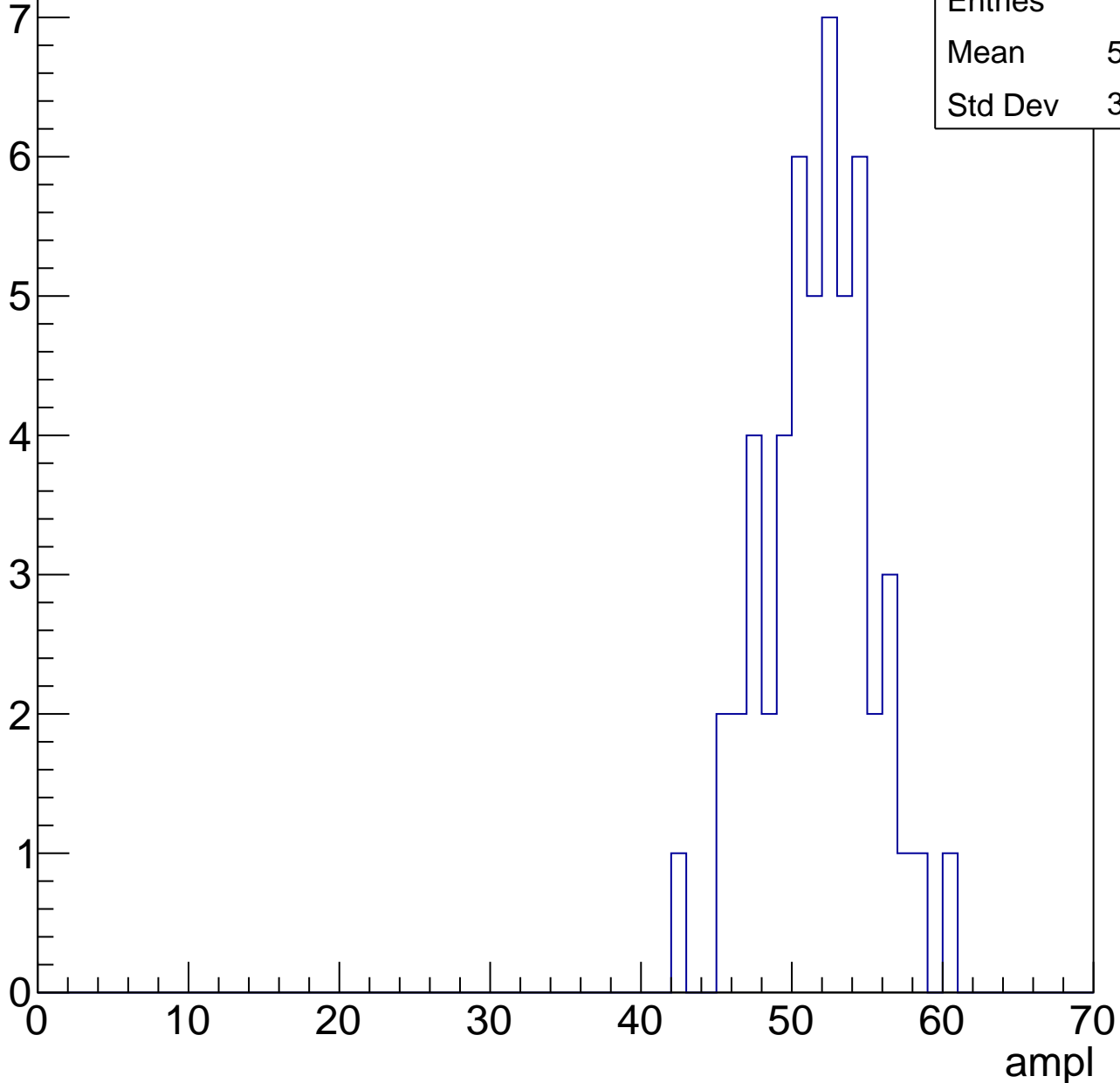


# B0L000S, U13-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	51.25
Std Dev	3.557

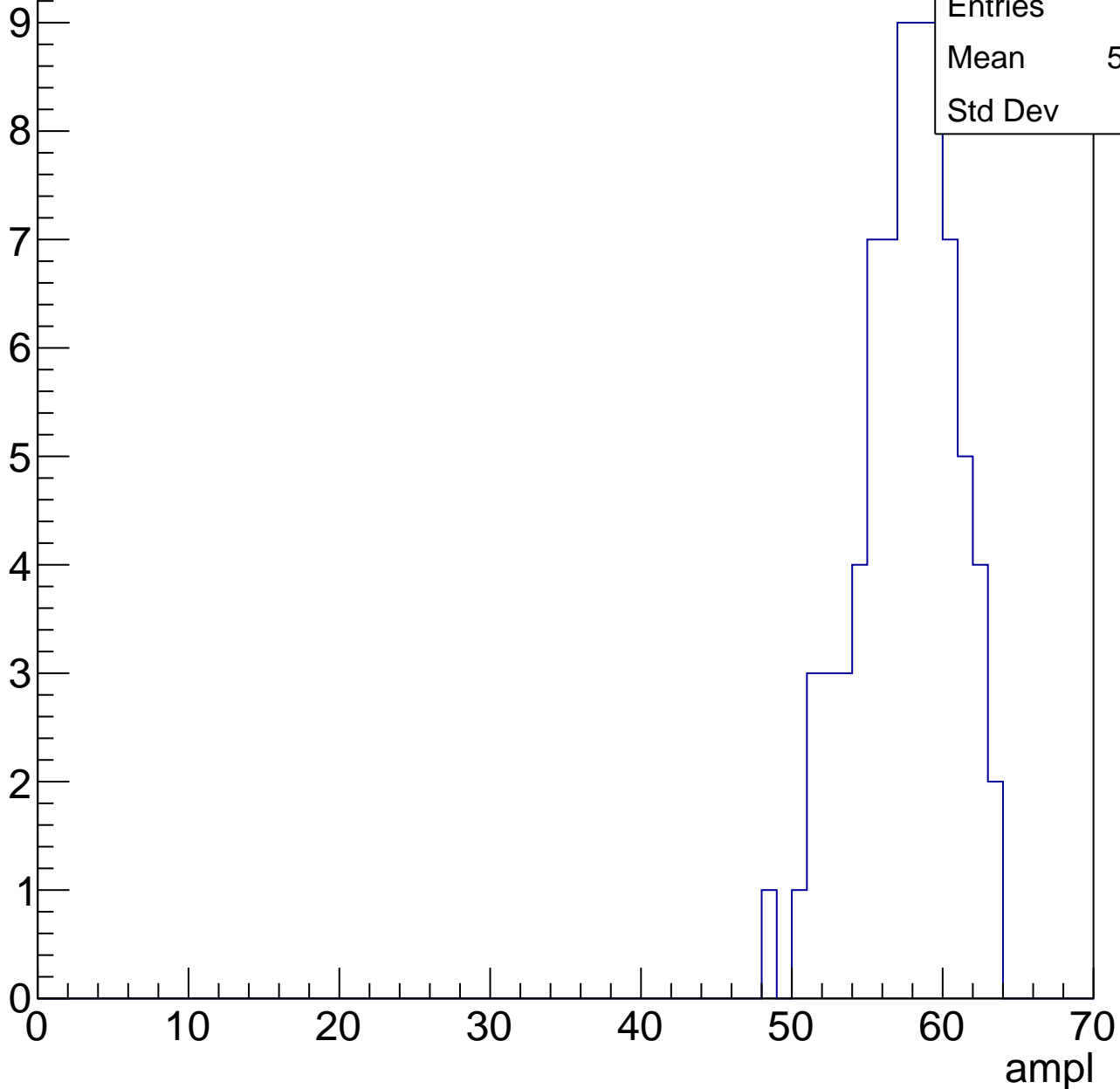


# B0L000S, U13-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	57.08
Std Dev	3.27

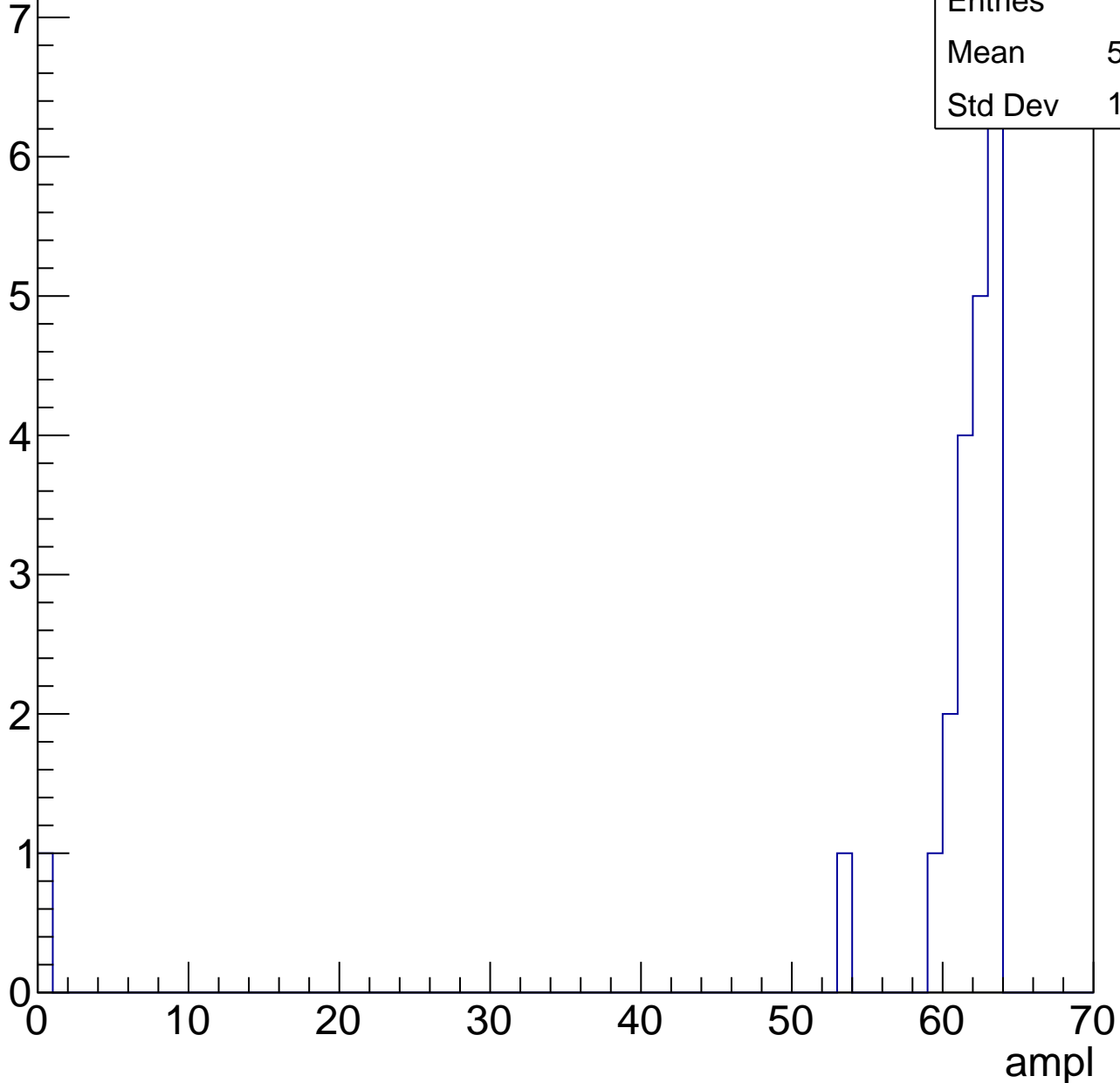


# B0L000S, U13-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	21
Mean	58.43
Std Dev	13.25



# B0L000S, U13-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U13-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U13-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0