



# B1L103S, U22-ch0, adc0

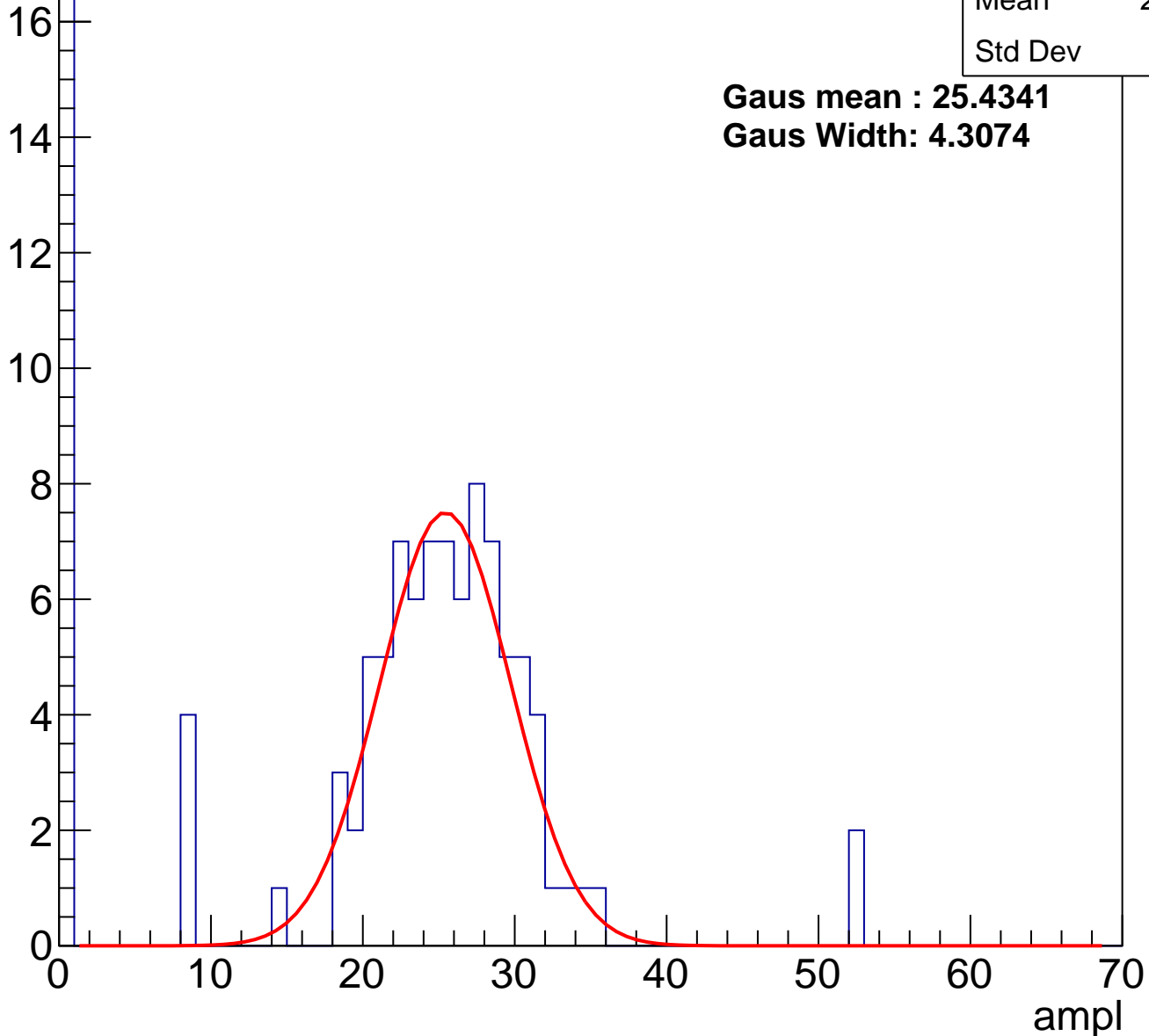
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	105
Mean	20.98
Std Dev	11.1

**Gaus mean : 25.4341**

**Gaus Width: 4.3074**

Entry



# B1L103S, U22-ch0, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	28.04
Std Dev	11.71

**Gaus mean : 33.4893**

**Gaus Width: 3.7656**

Entry

10

8

6

4

2

0

0

10

20

30

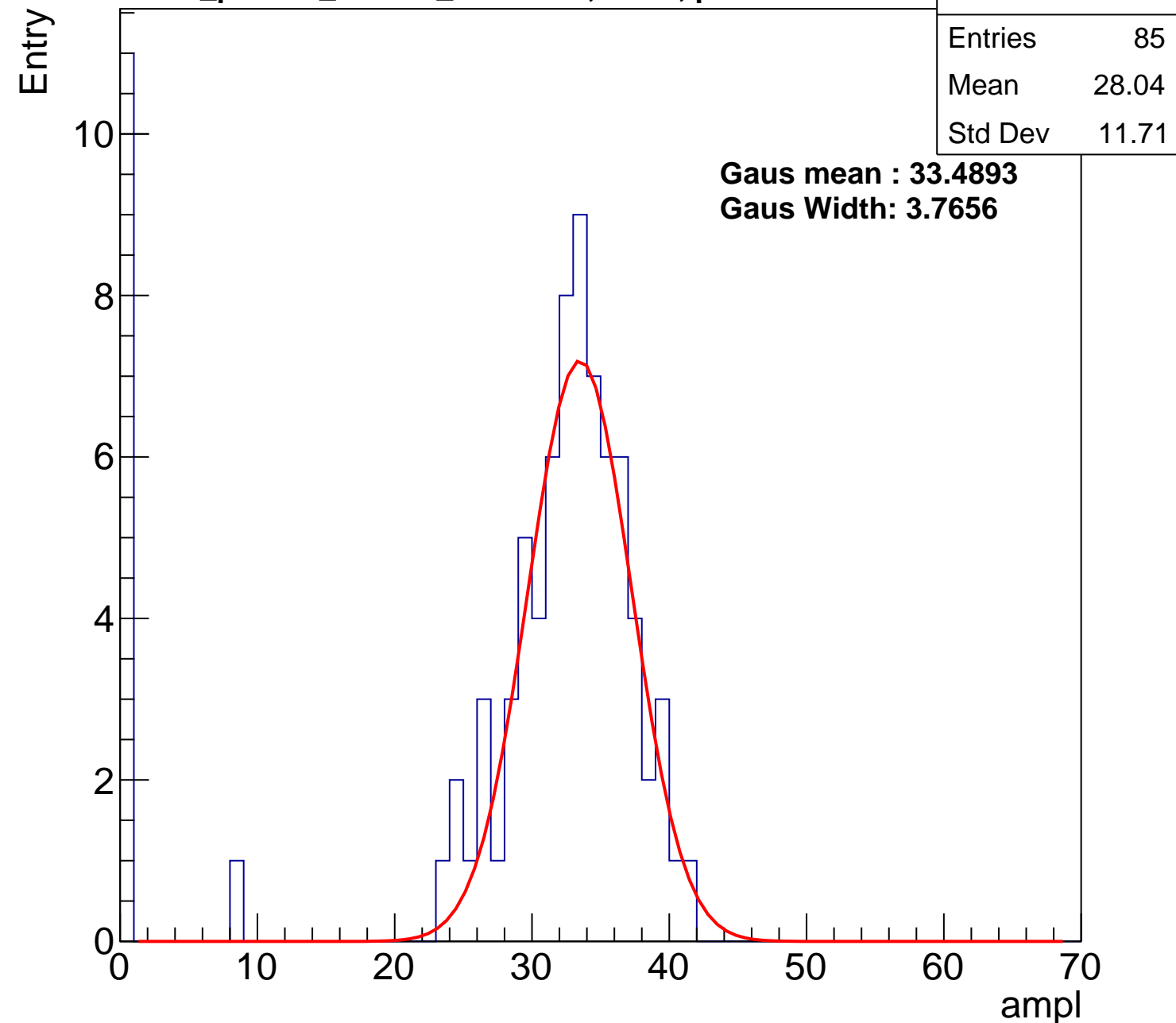
40

50

60

70

ampl



# B1L103S, U22-ch0, adc2

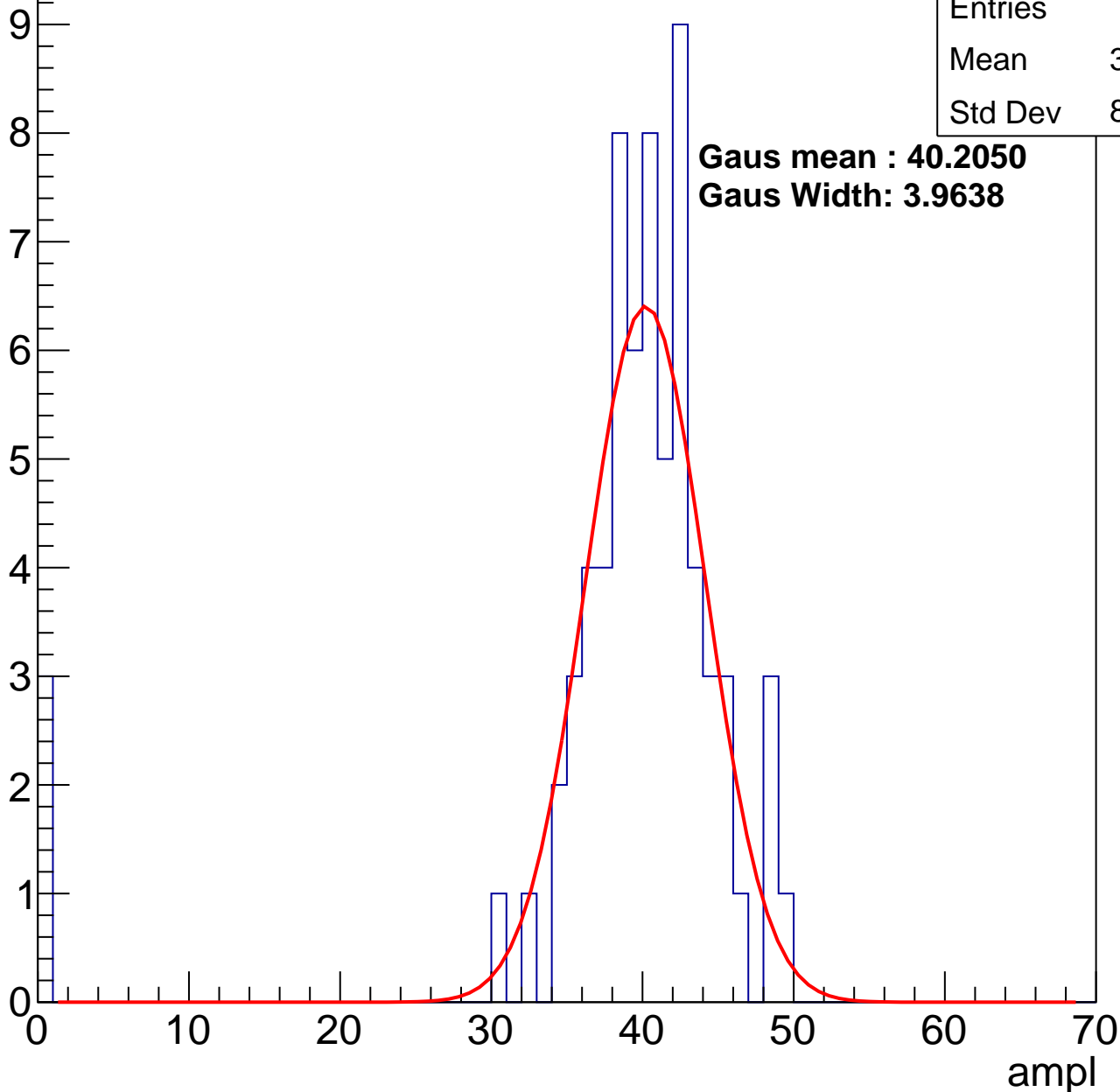
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	38.35
Std Dev	8.984

**Gaus mean : 40.2050**

**Gaus Width: 3.9638**

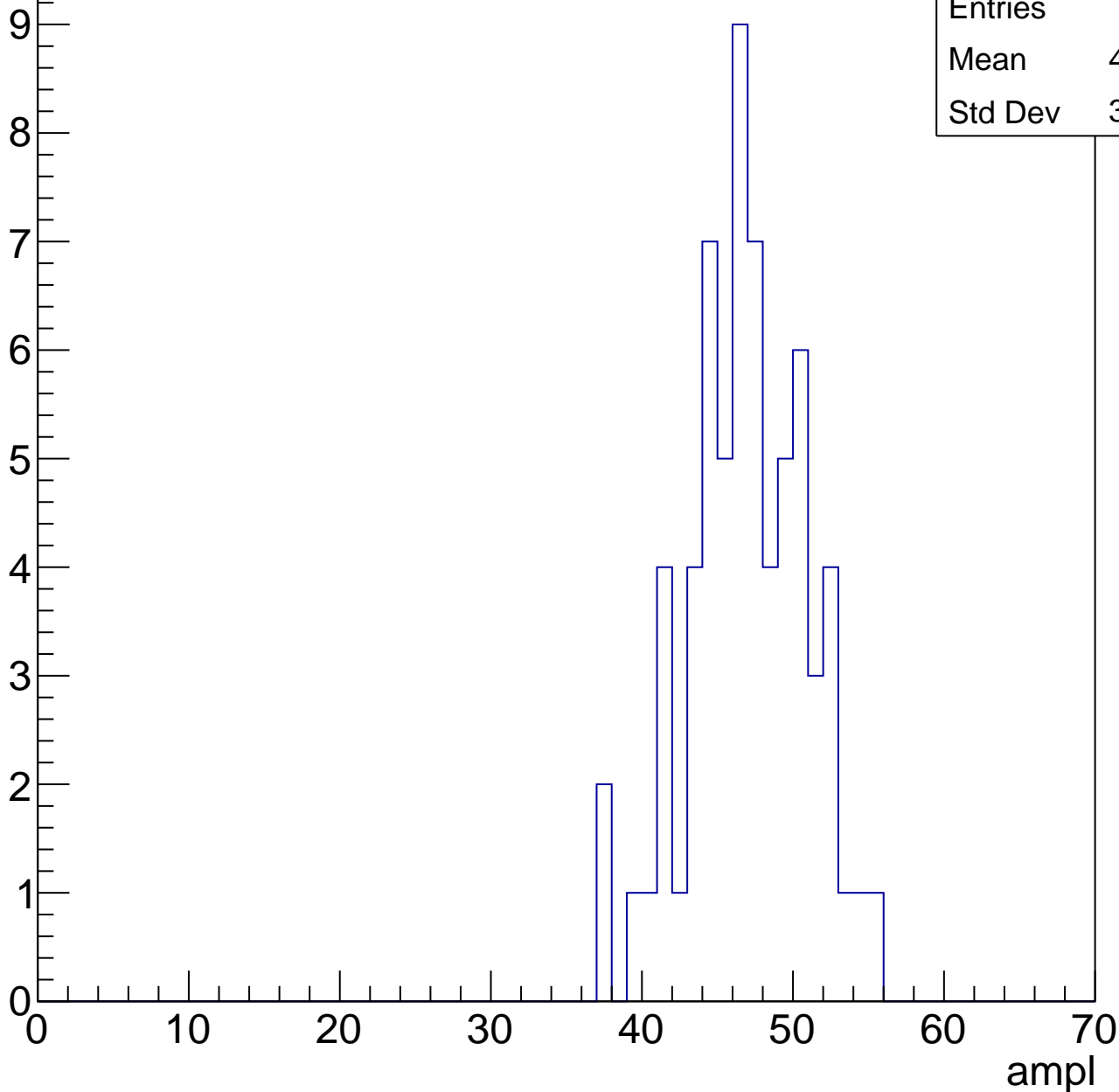


# B1L103S, U22-ch0, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	46.47
Std Dev	3.882

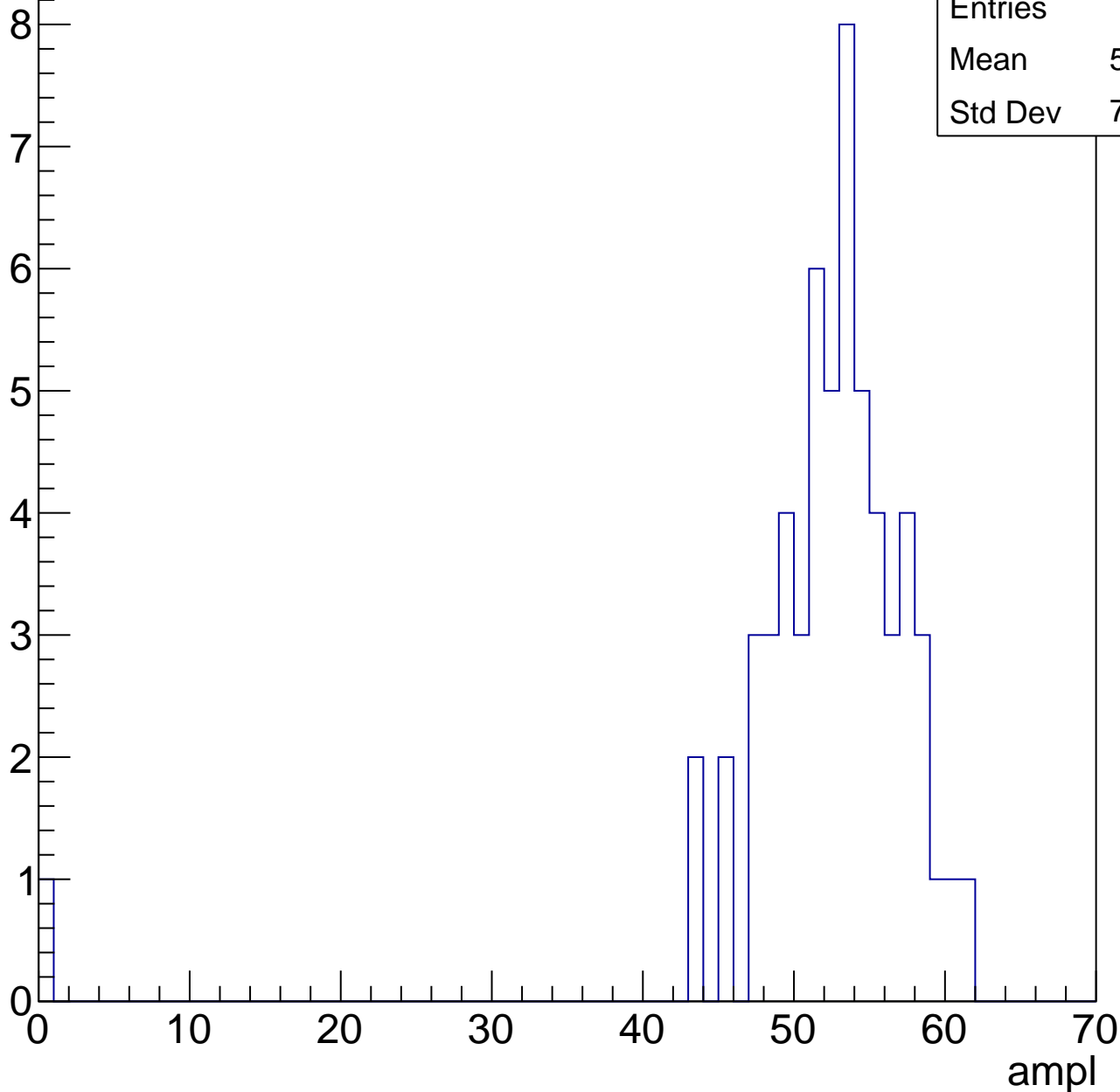


# B1L103S, U22-ch0, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.47
Std Dev	7.853

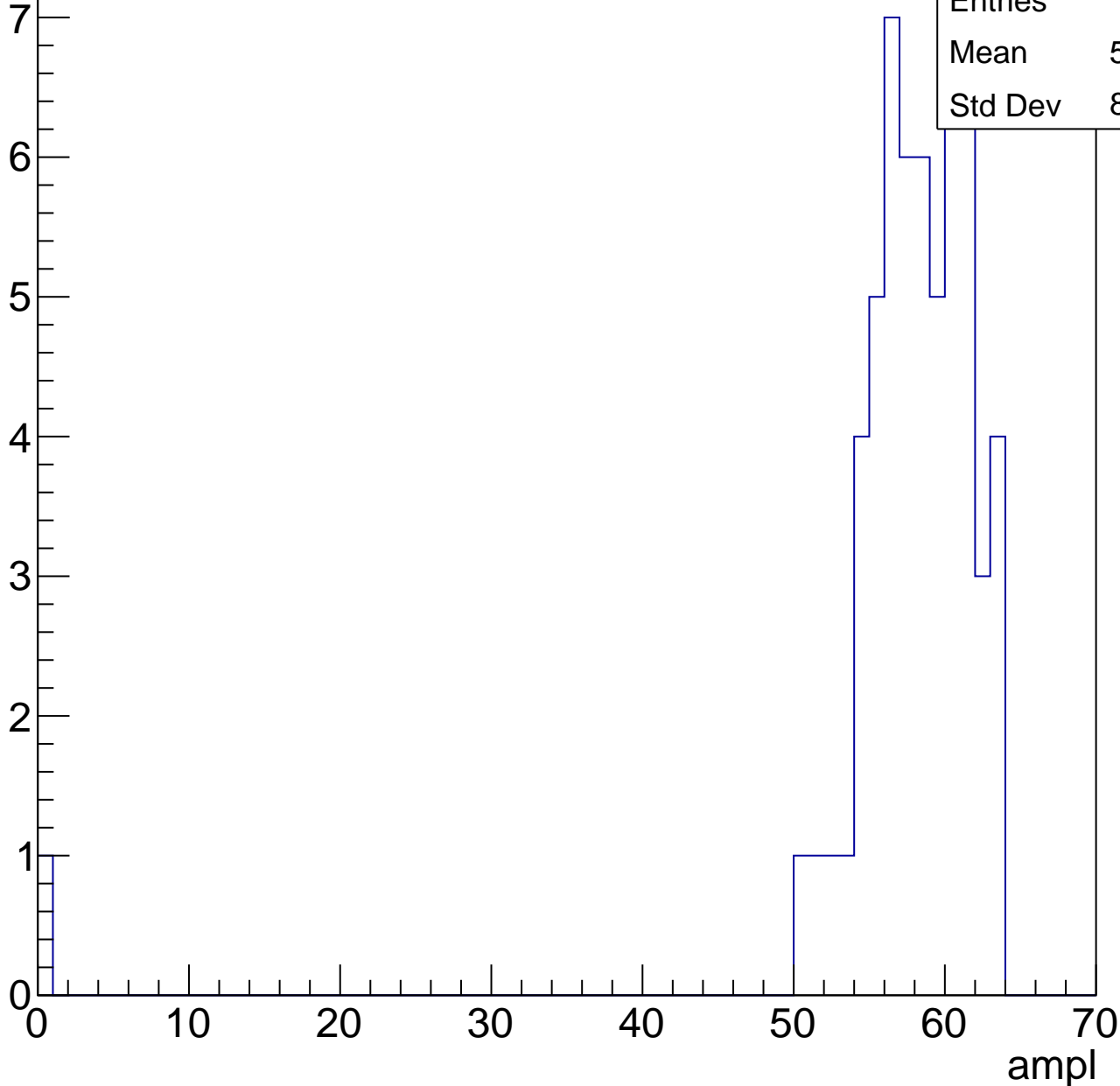


# B1L103S, U22-ch0, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	56.93
Std Dev	8.086

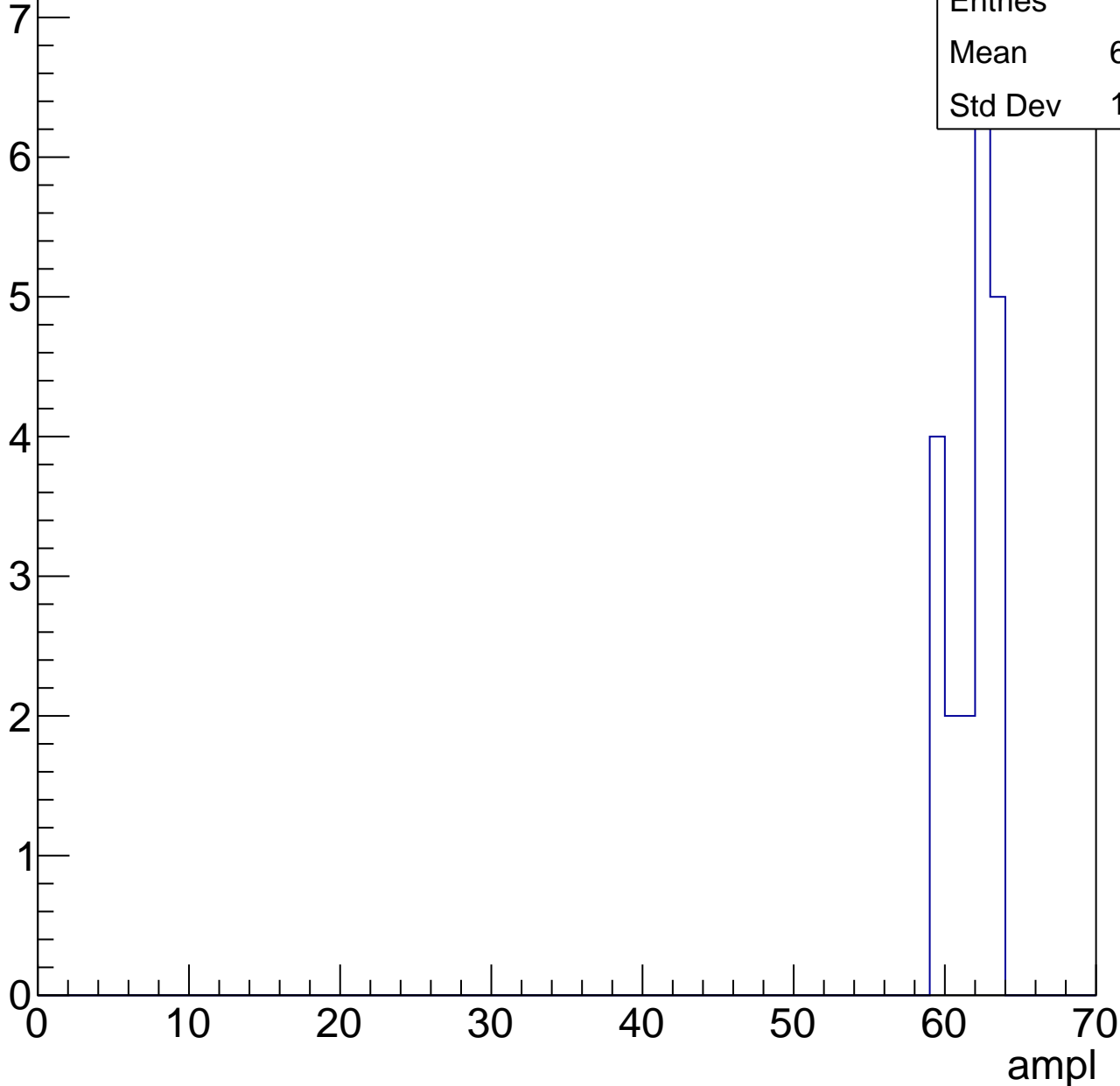


# B1L103S, U22-ch0, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	61.35
Std Dev	1.459





# B1L103S, U22-ch0, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

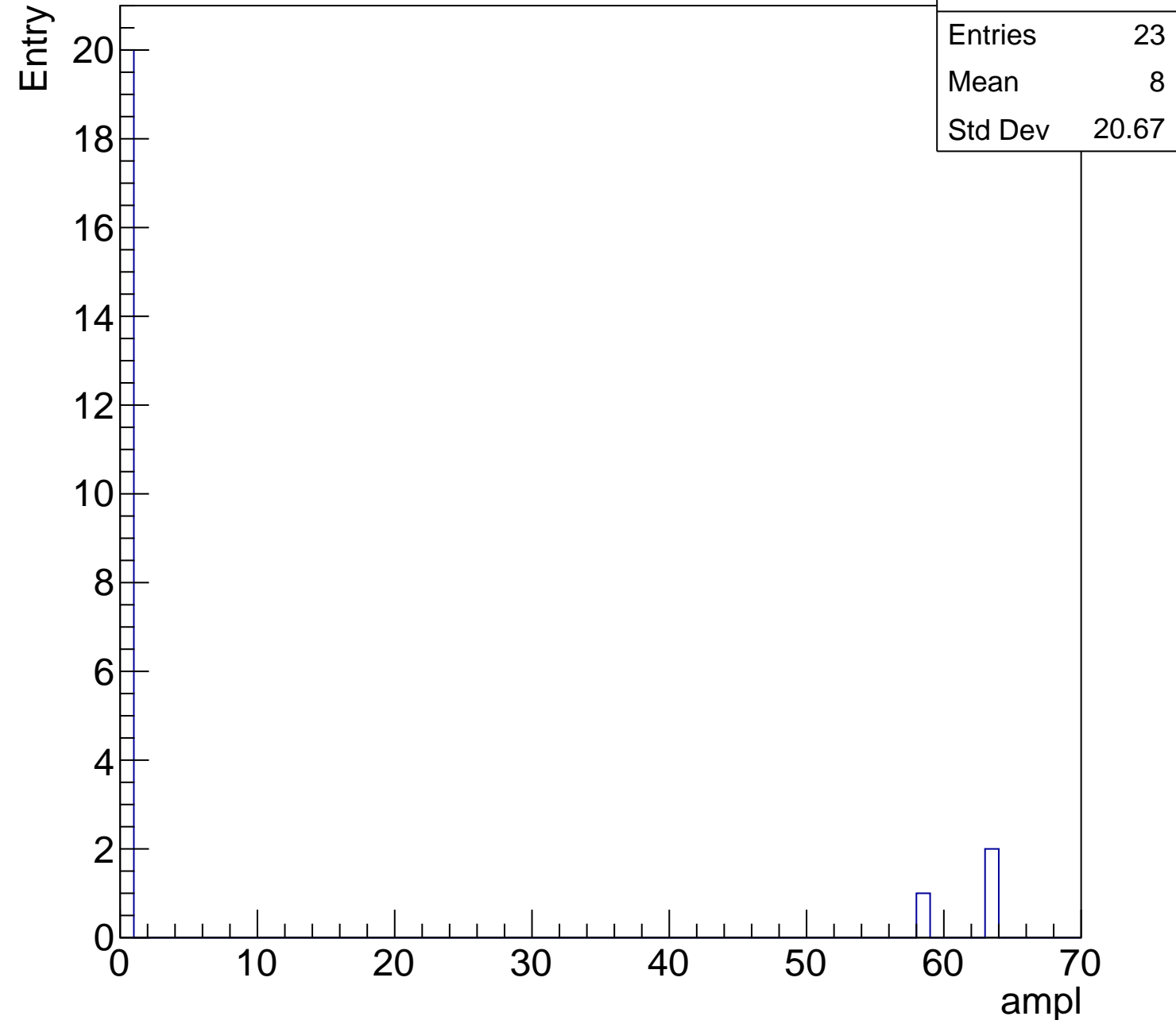
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	23
Mean	8
Std Dev	20.67

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch1, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	71
Mean	23.48
Std Dev	11.46

**Gaus mean : 29.4636**

**Gaus Width: 3.8460**

Entry

12

10

8

6

4

2

0

0

10

20

30

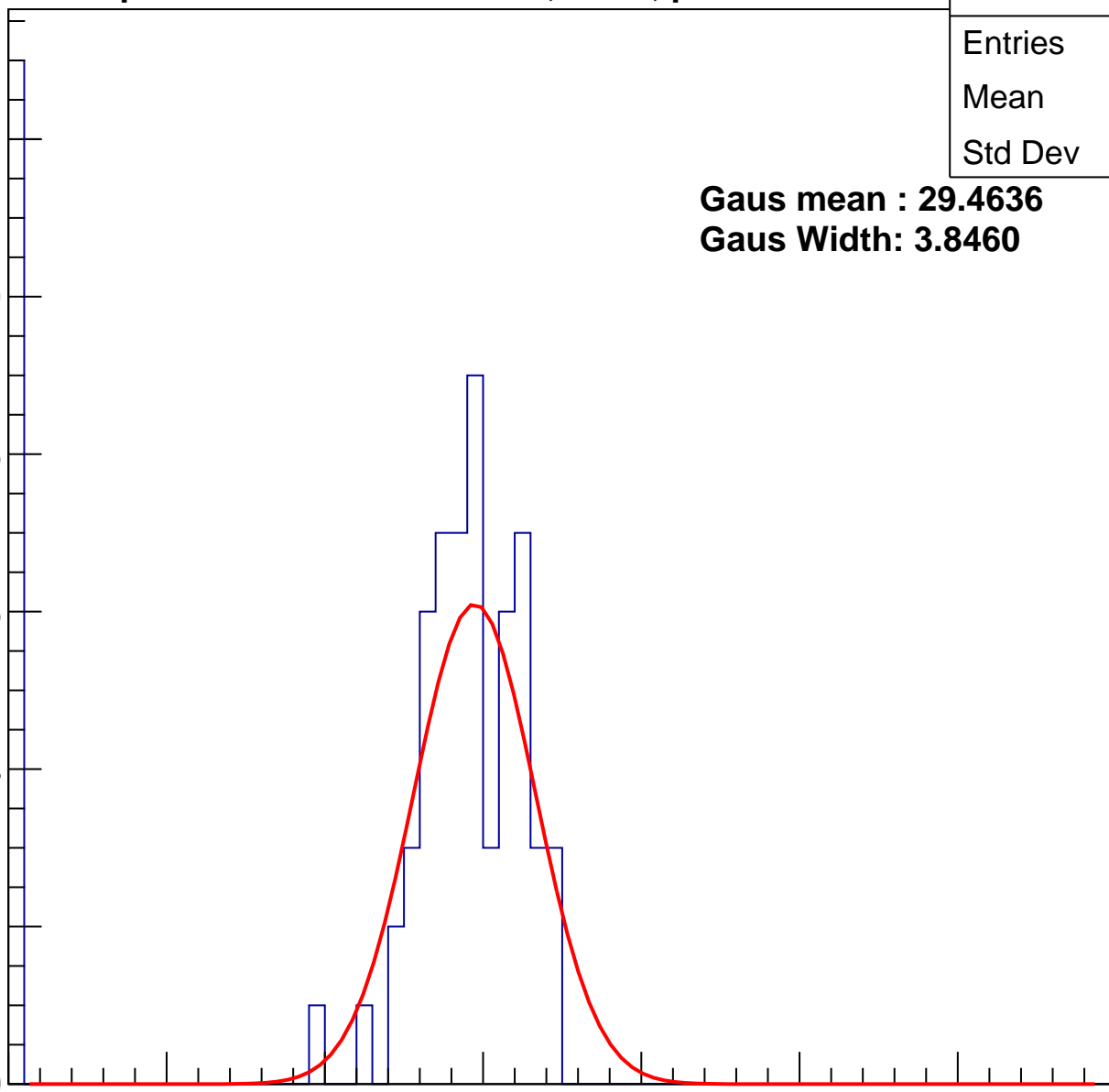
40

50

60

70

ampl



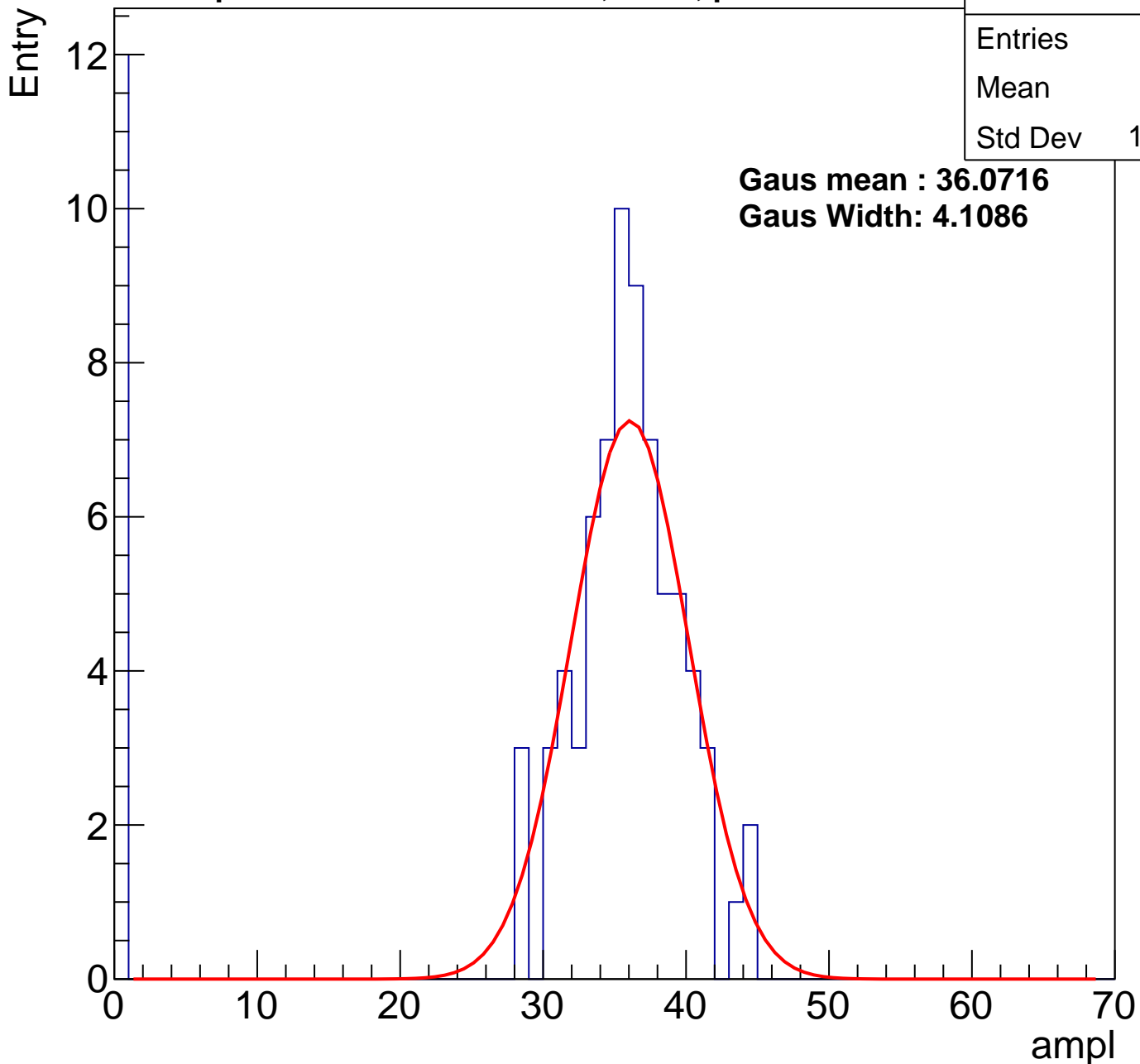
# B1L103S, U22-ch1, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	30.5
Std Dev	12.88

**Gaus mean : 36.0716**

**Gaus Width: 4.1086**



# B1L103S, U22-ch1, adc2

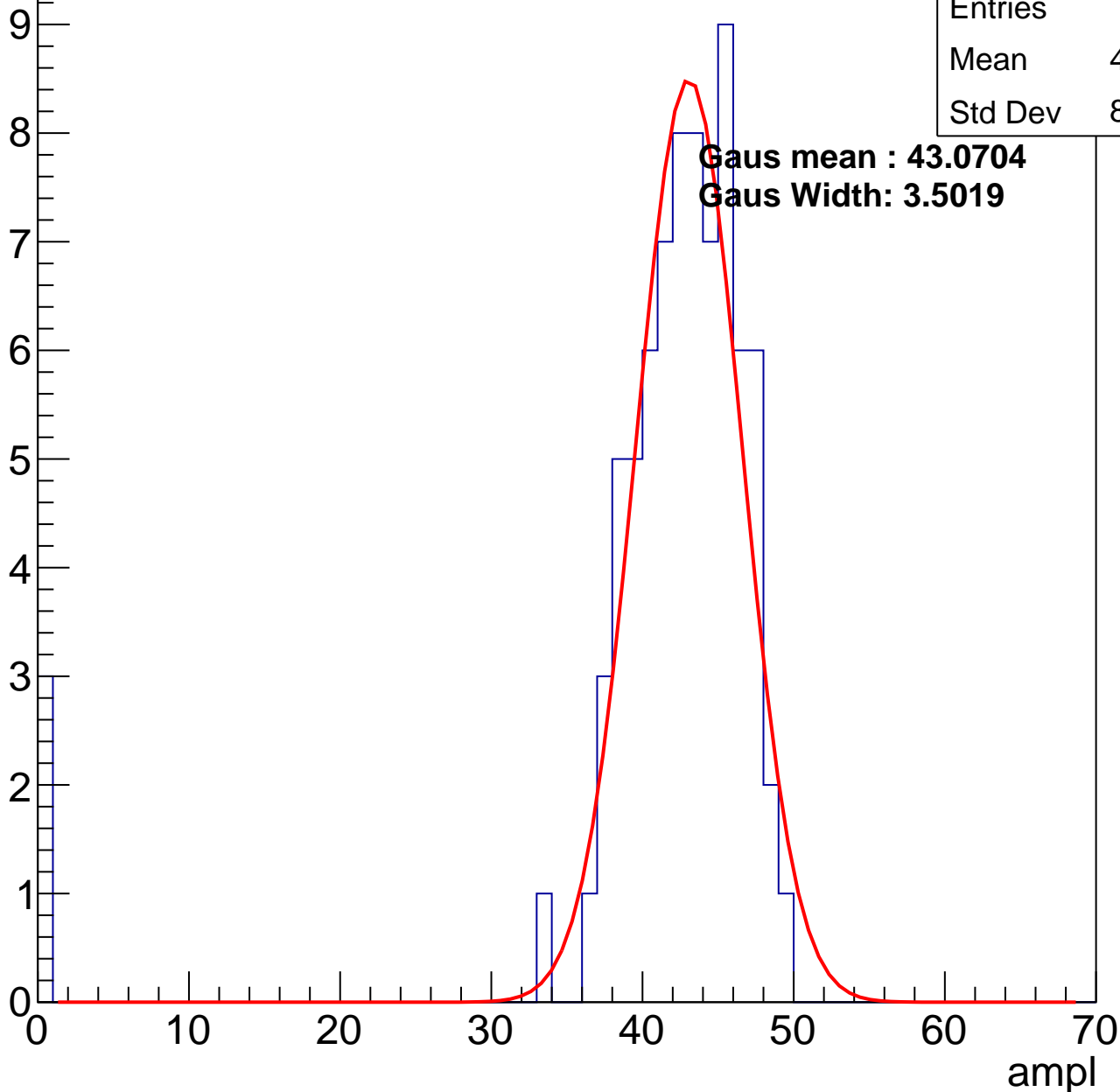
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	40.87
Std Dev	8.789

**Gaus mean : 43.0704**

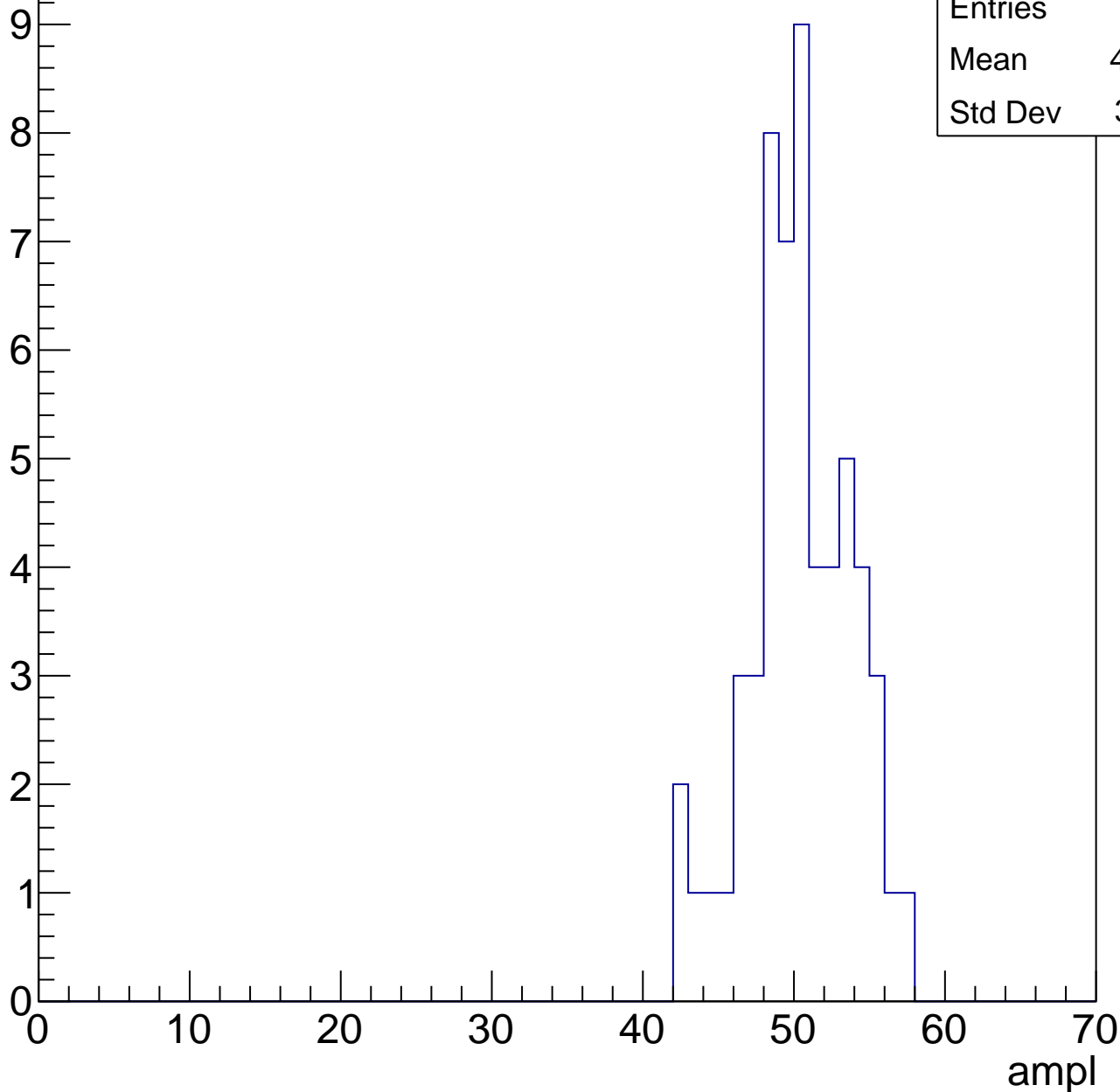
**Gaus Width: 3.5019**



# B1L103S, U22-ch1, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

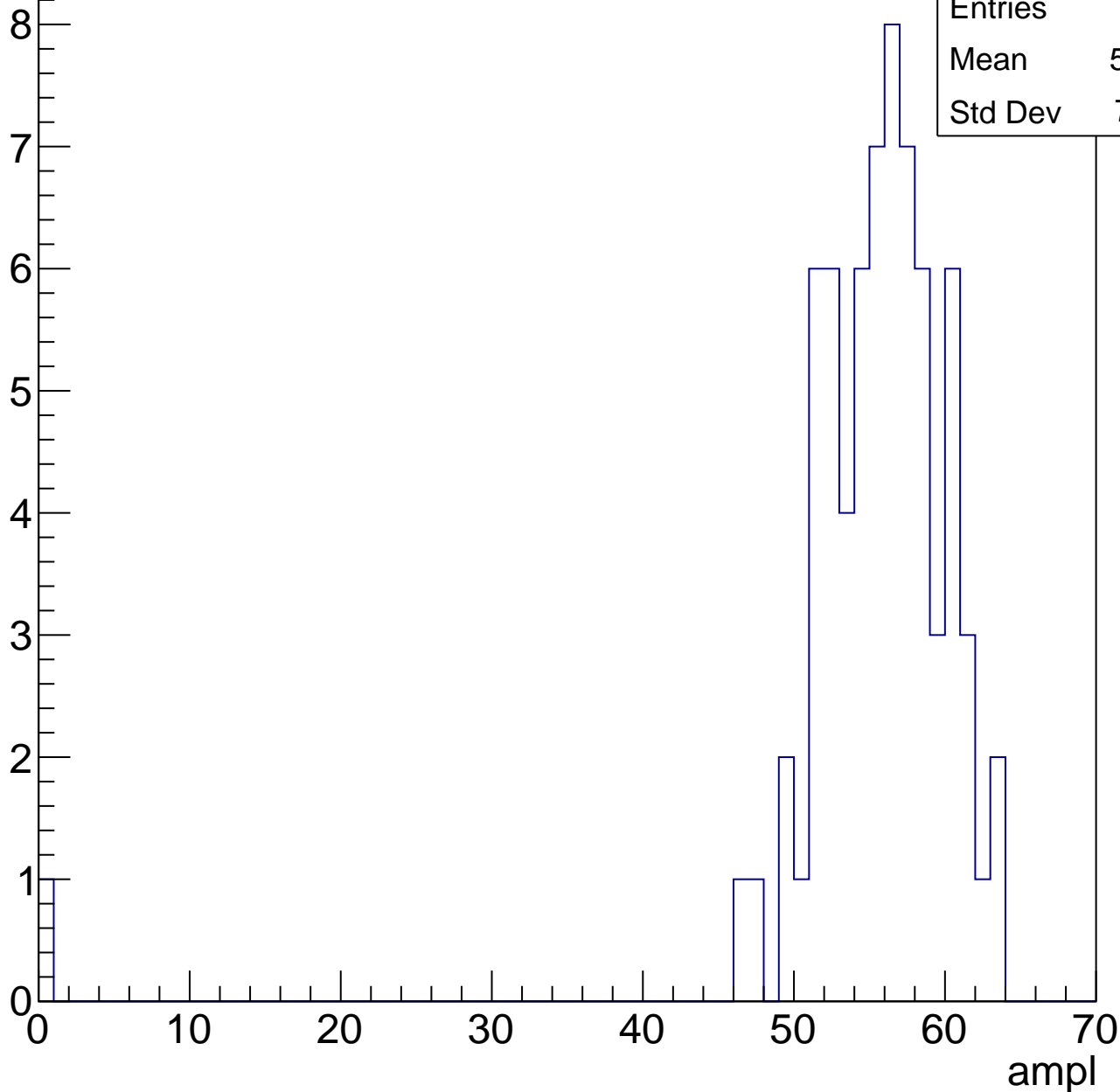


# B1L103S, U22-ch1, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

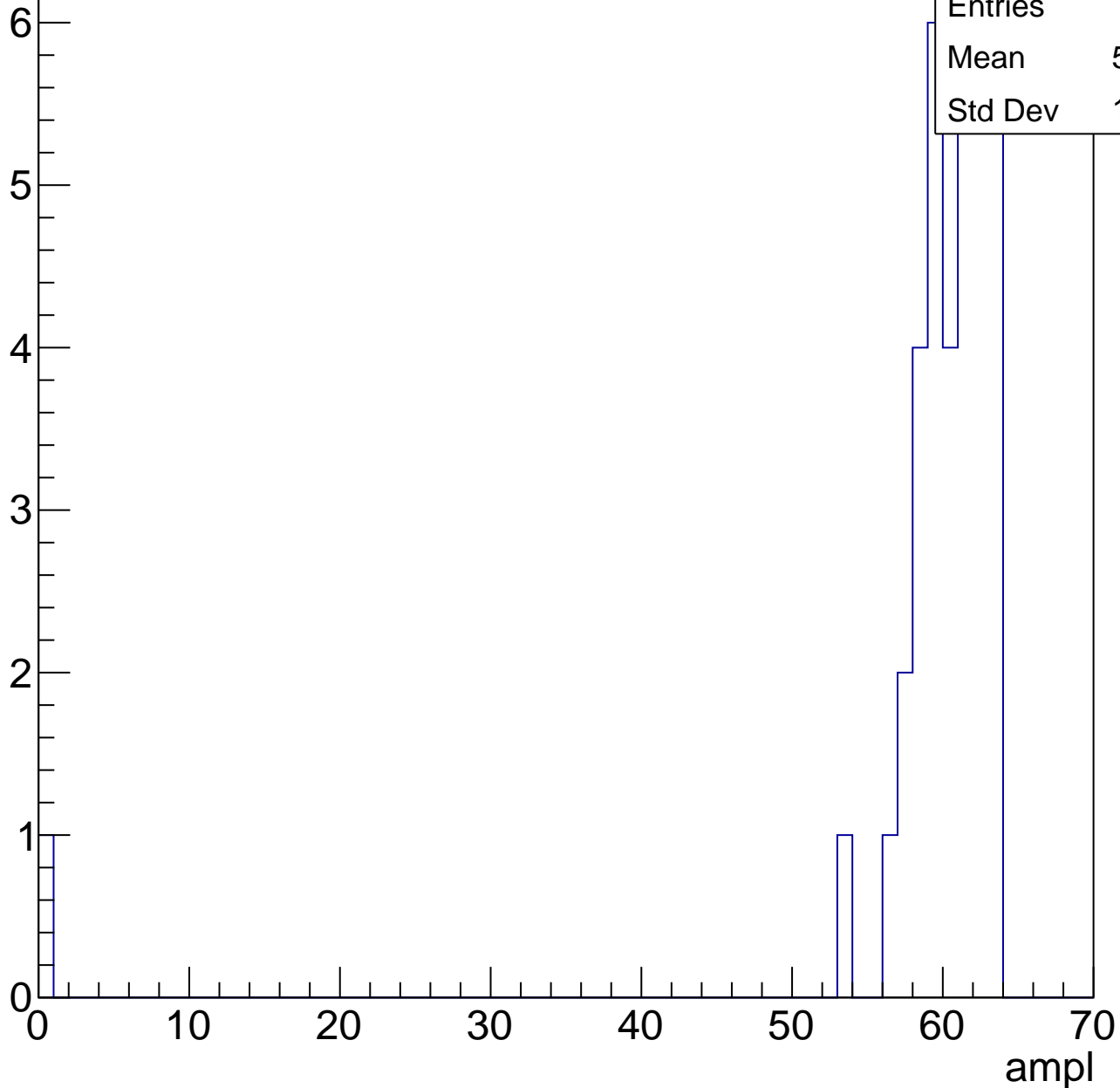
Entries	71
Mean	54.69
Std Dev	7.511



# B1L103S, U22-ch1, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

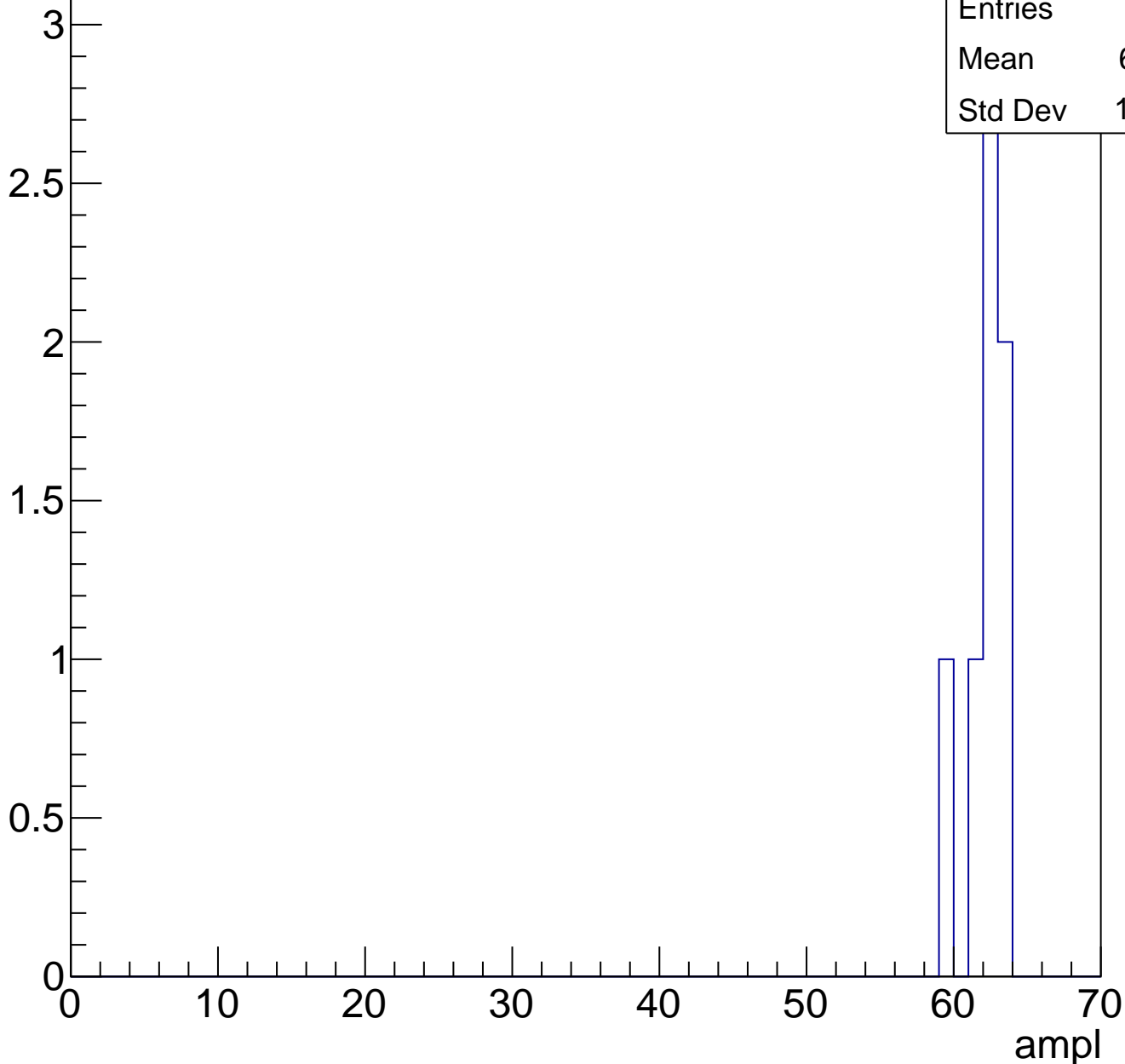
Entry



# B1L103S, U22-ch1, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch1, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

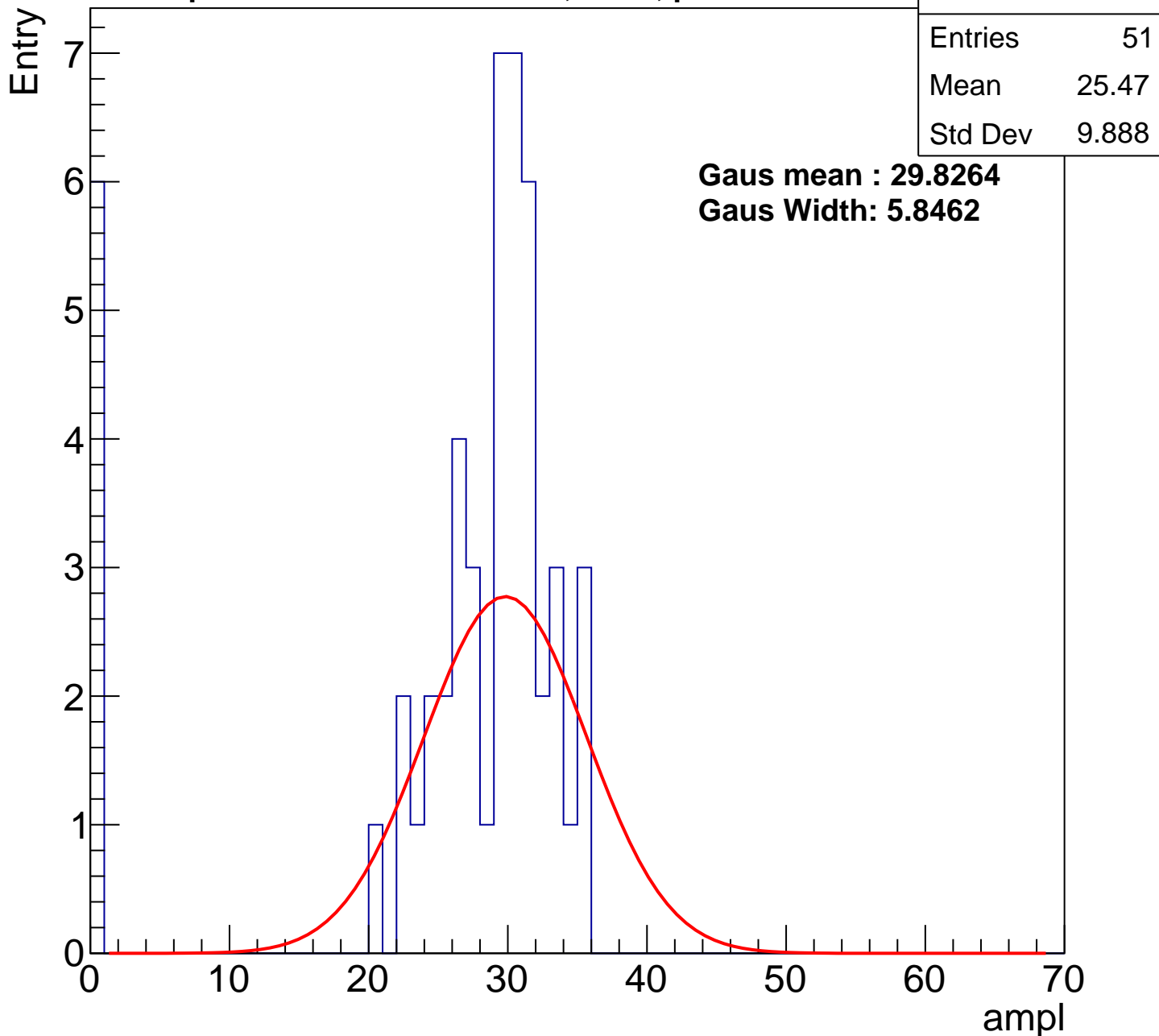
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch2, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2



# B1L103S, U22-ch2, adc1

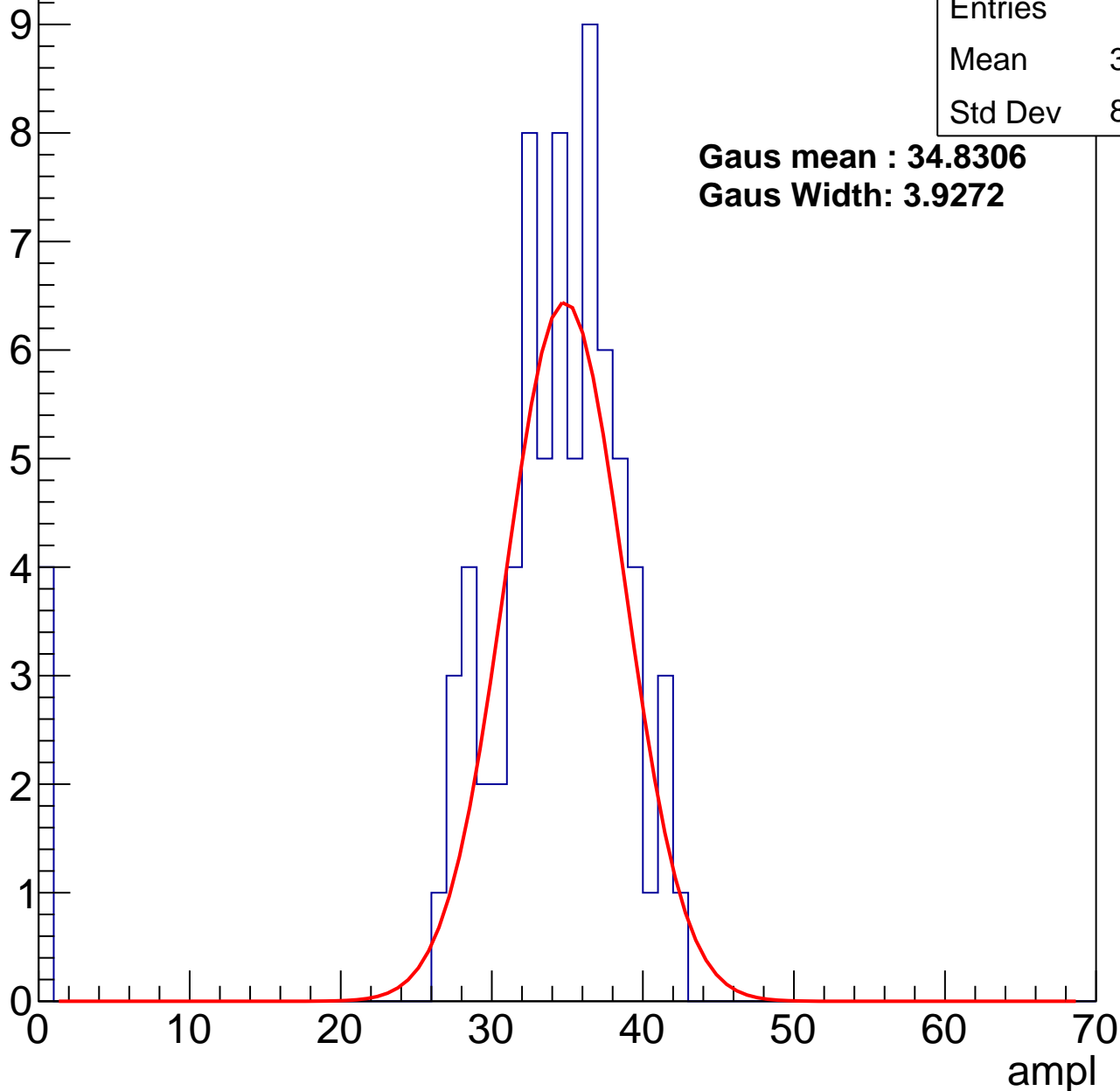
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	32.35
Std Dev	8.525

**Gaus mean : 34.8306**

**Gaus Width: 3.9272**



# B1L103S, U22-ch2, adc2

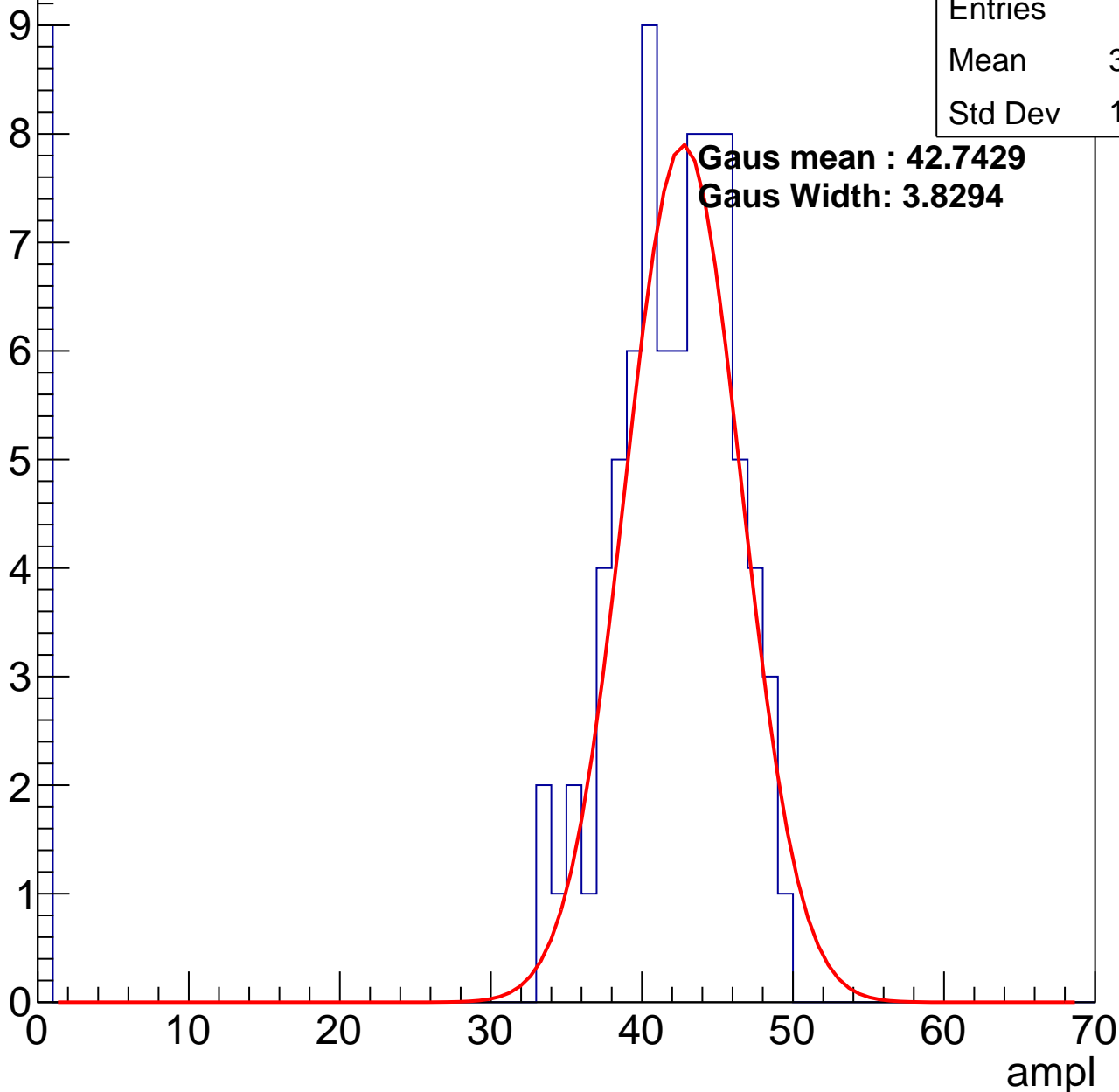
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	88
Mean	37.53
Std Dev	13.15

**Gaus mean : 42.7429**

**Gaus Width: 3.8294**

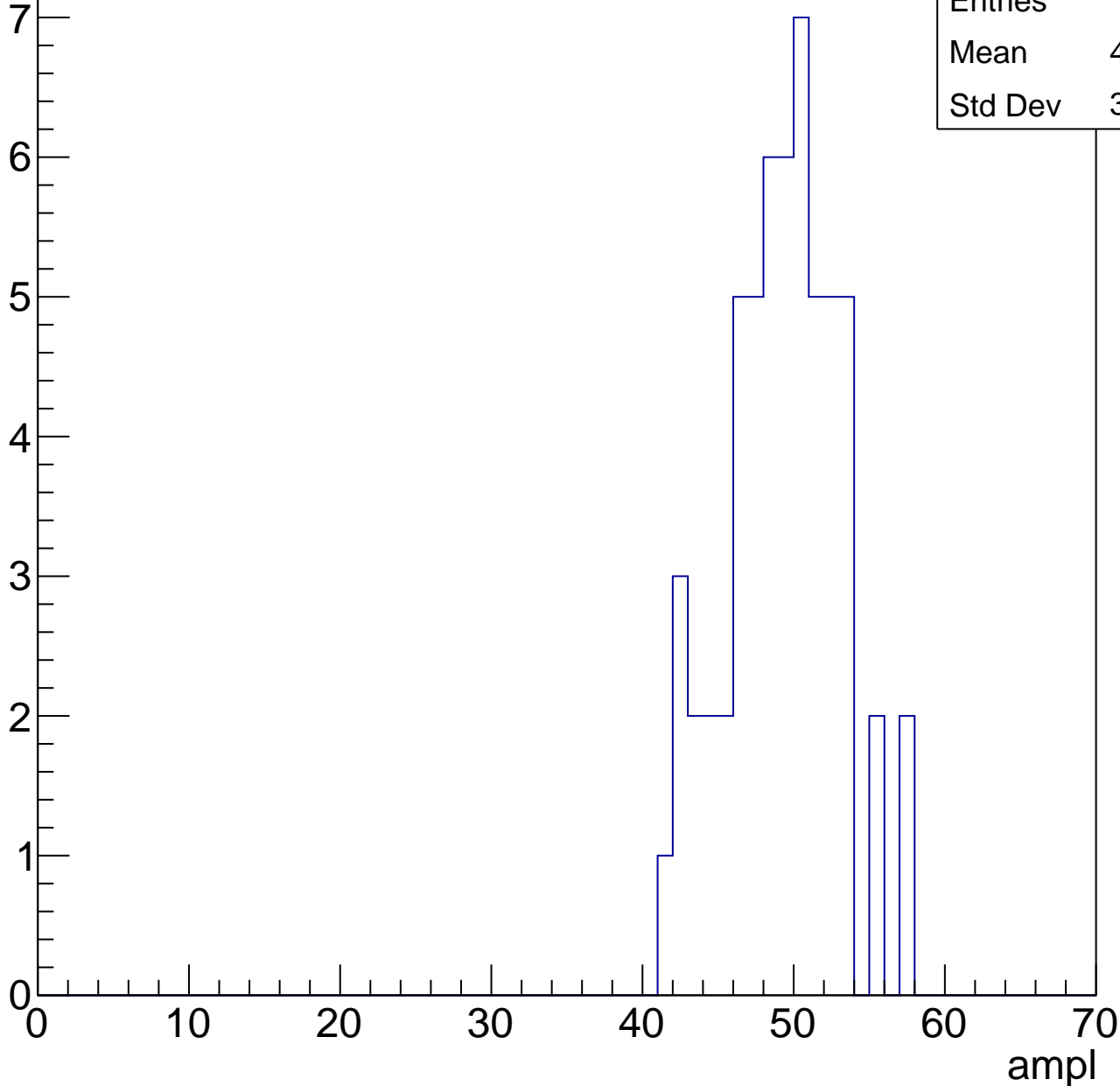


# B1L103S, U22-ch2, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	48.83
Std Dev	3.682

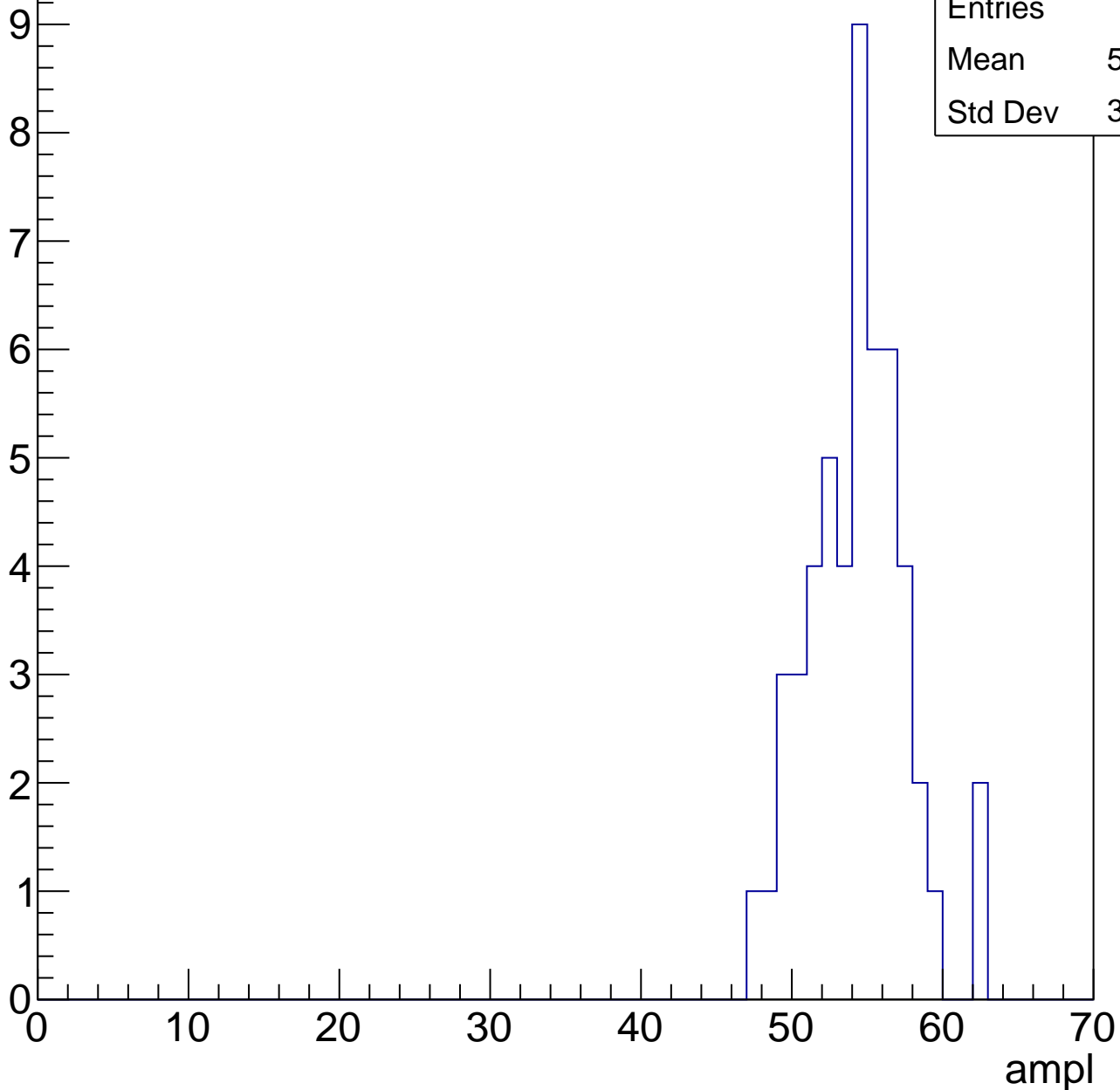


# B1L103S, U22-ch2, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	51
Mean	53.86
Std Dev	3.187

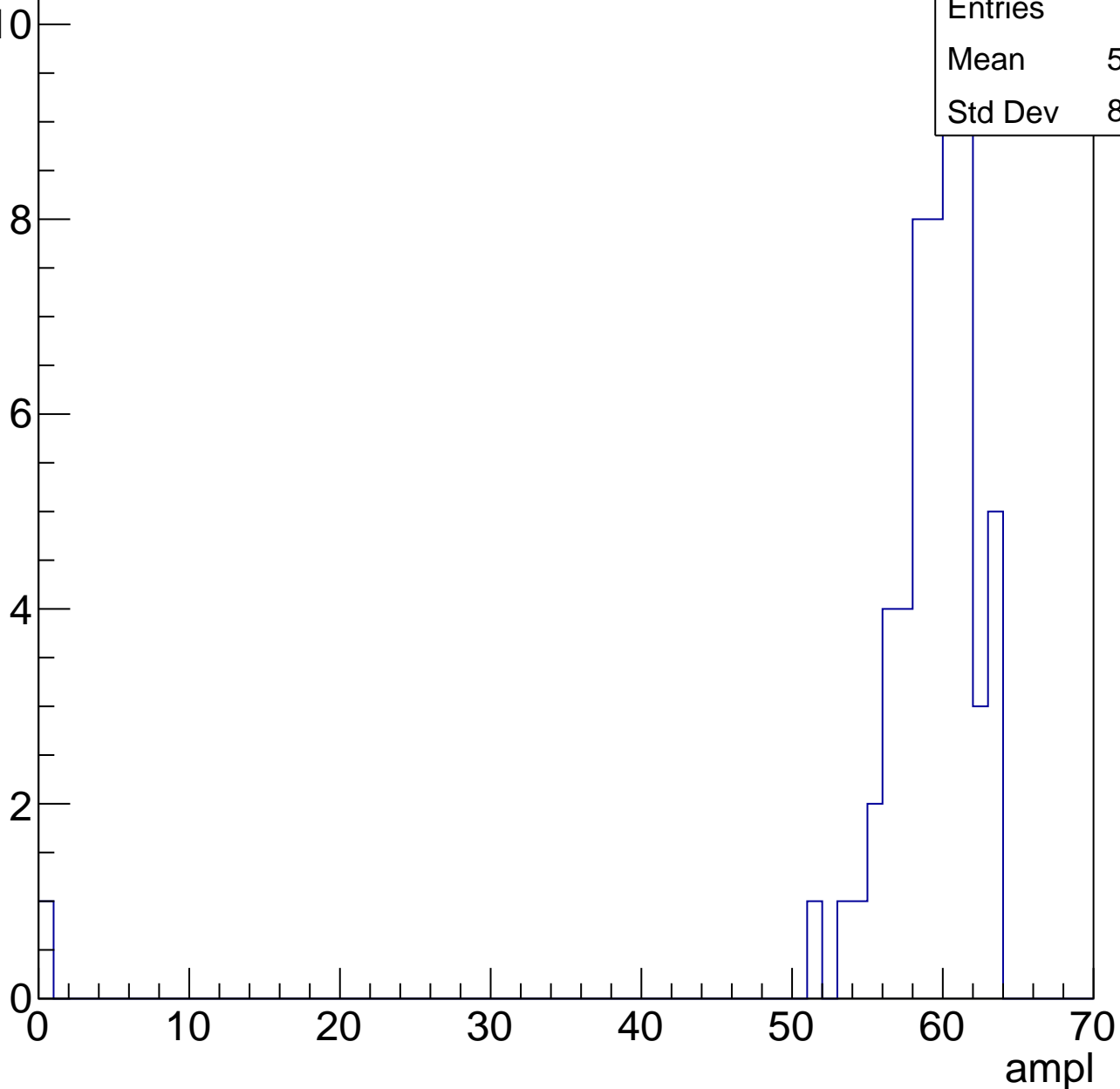


# B1L103S, U22-ch2, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.02
Std Dev	8.164

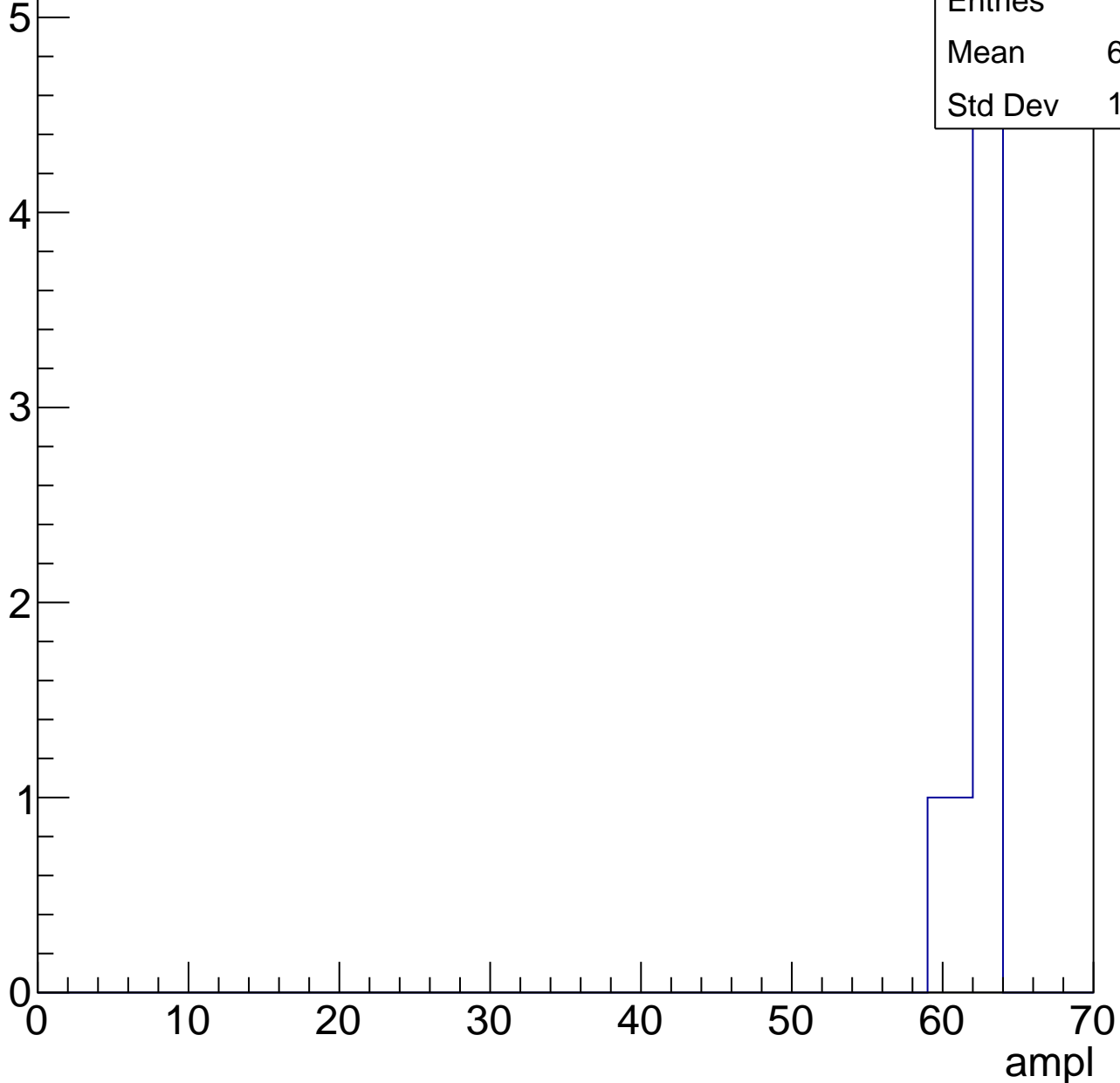


# B1L103S, U22-ch2, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.92
Std Dev	1.206





# B1L103S, U22-ch2, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch3, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	114
Mean	25.47
Std Dev	10.61

**Gaus mean : 29.8471**

**Gaus Width: 4.3140**

Entry

14

12

10

8

6

4

2

0

0

10

20

30

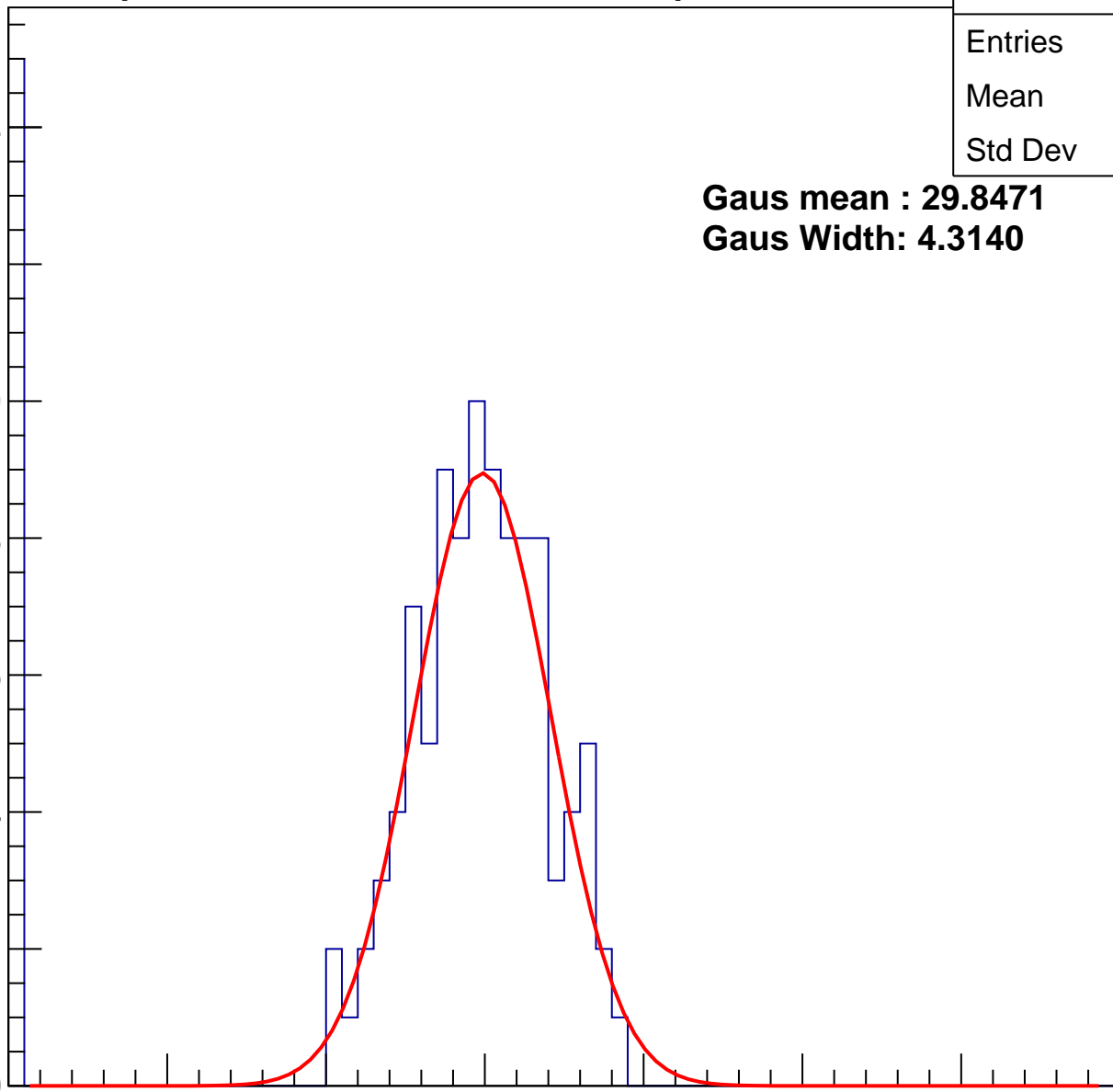
40

50

60

70

ampl



# B1L103S, U22-ch3, adc1

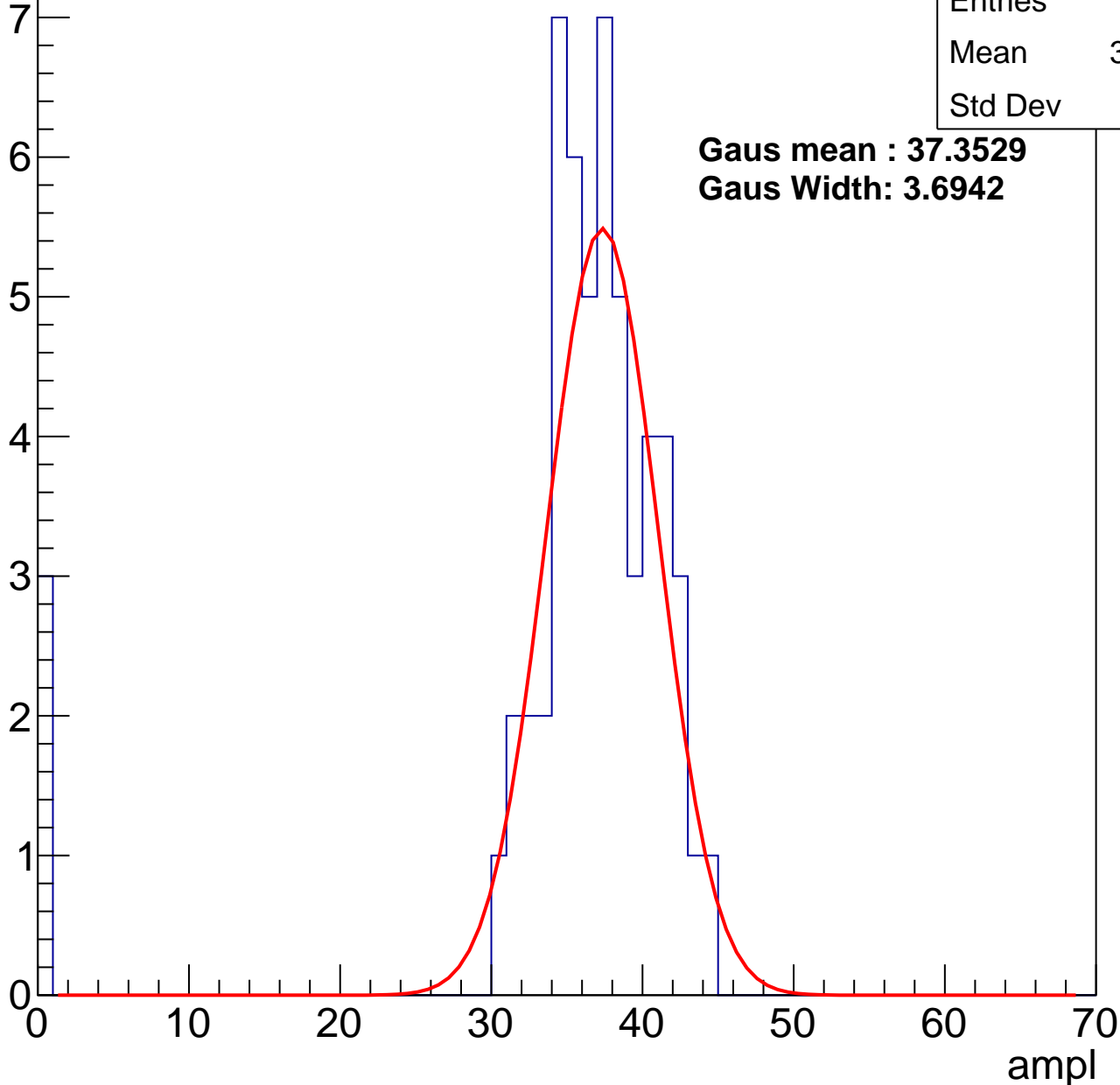
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	34.88
Std Dev	8.89

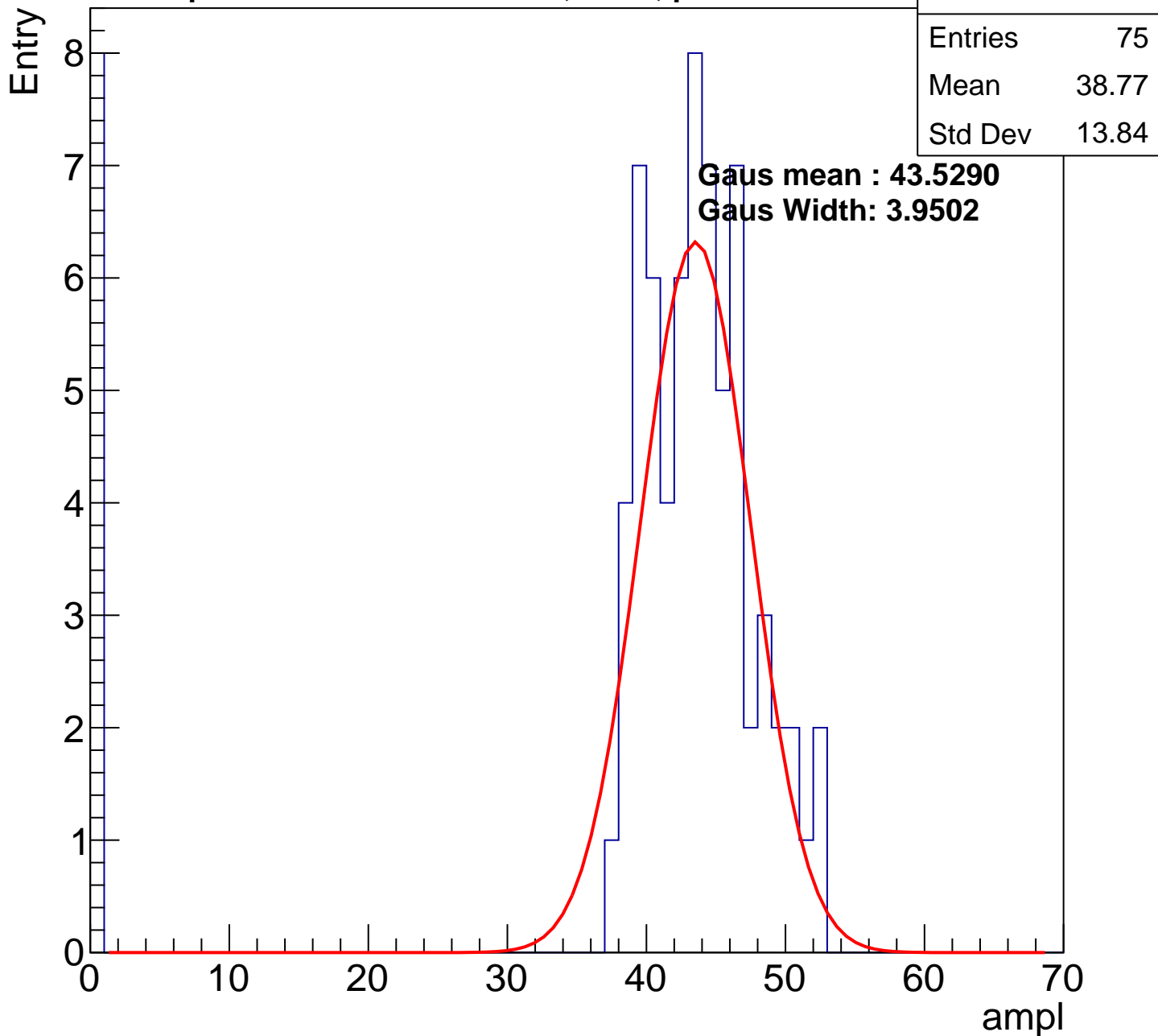
**Gaus mean : 37.3529**

**Gaus Width: 3.6942**



# B1L103S, U22-ch3, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

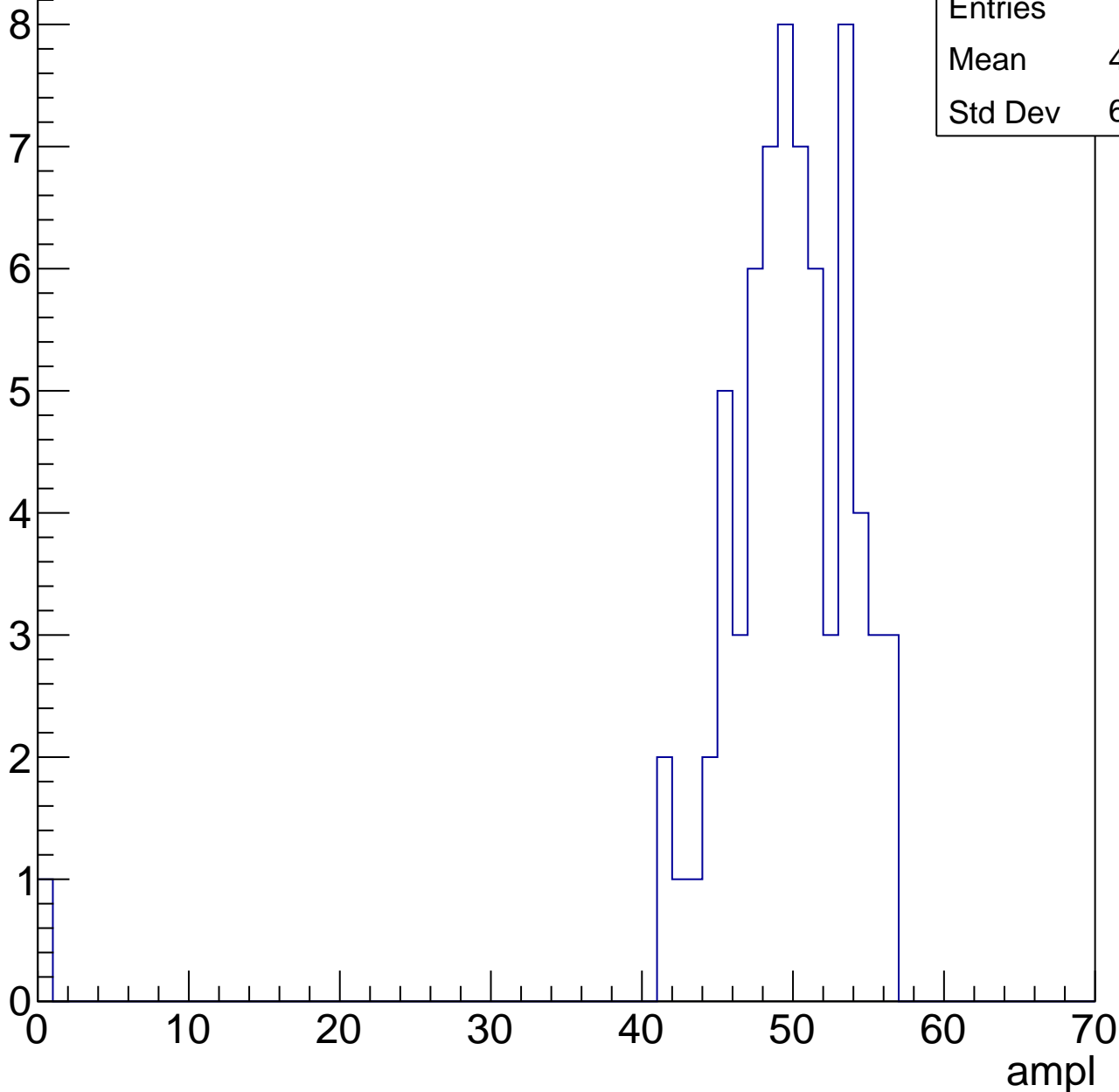


# B1L103S, U22-ch3, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.76
Std Dev	6.906

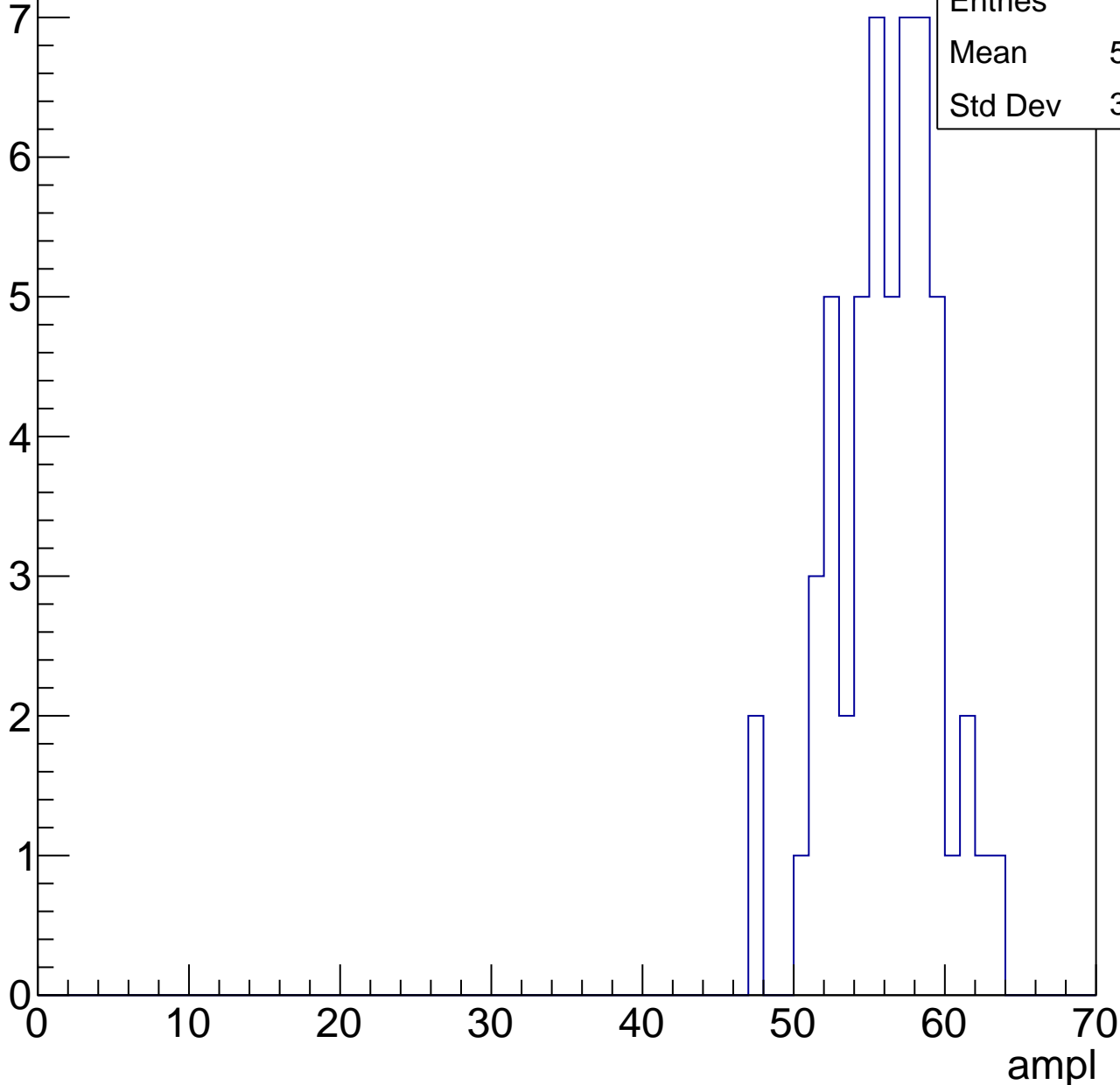


# B1L103S, U22-ch3, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	55.65
Std Dev	3.395

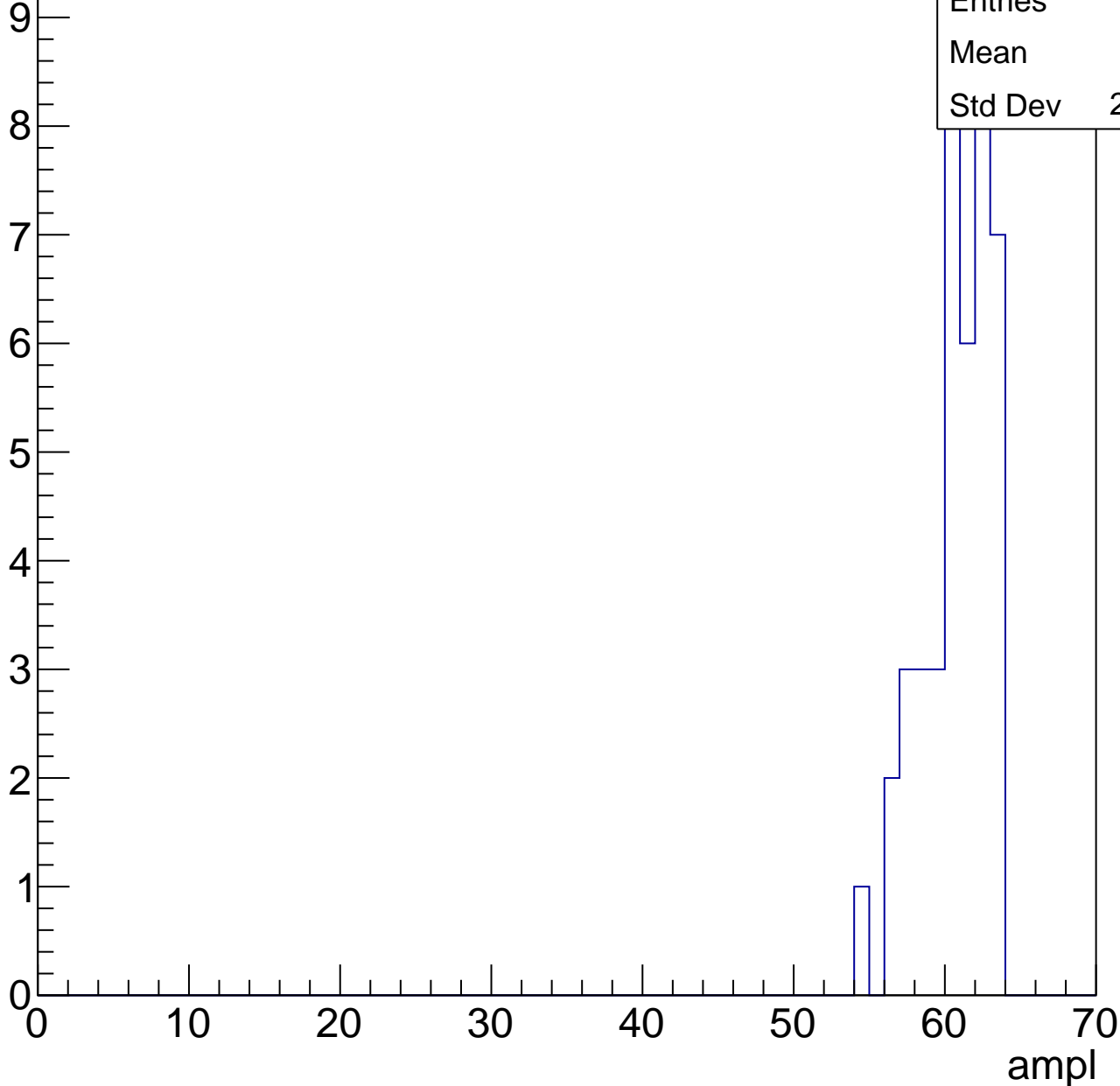


# B1L103S, U22-ch3, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

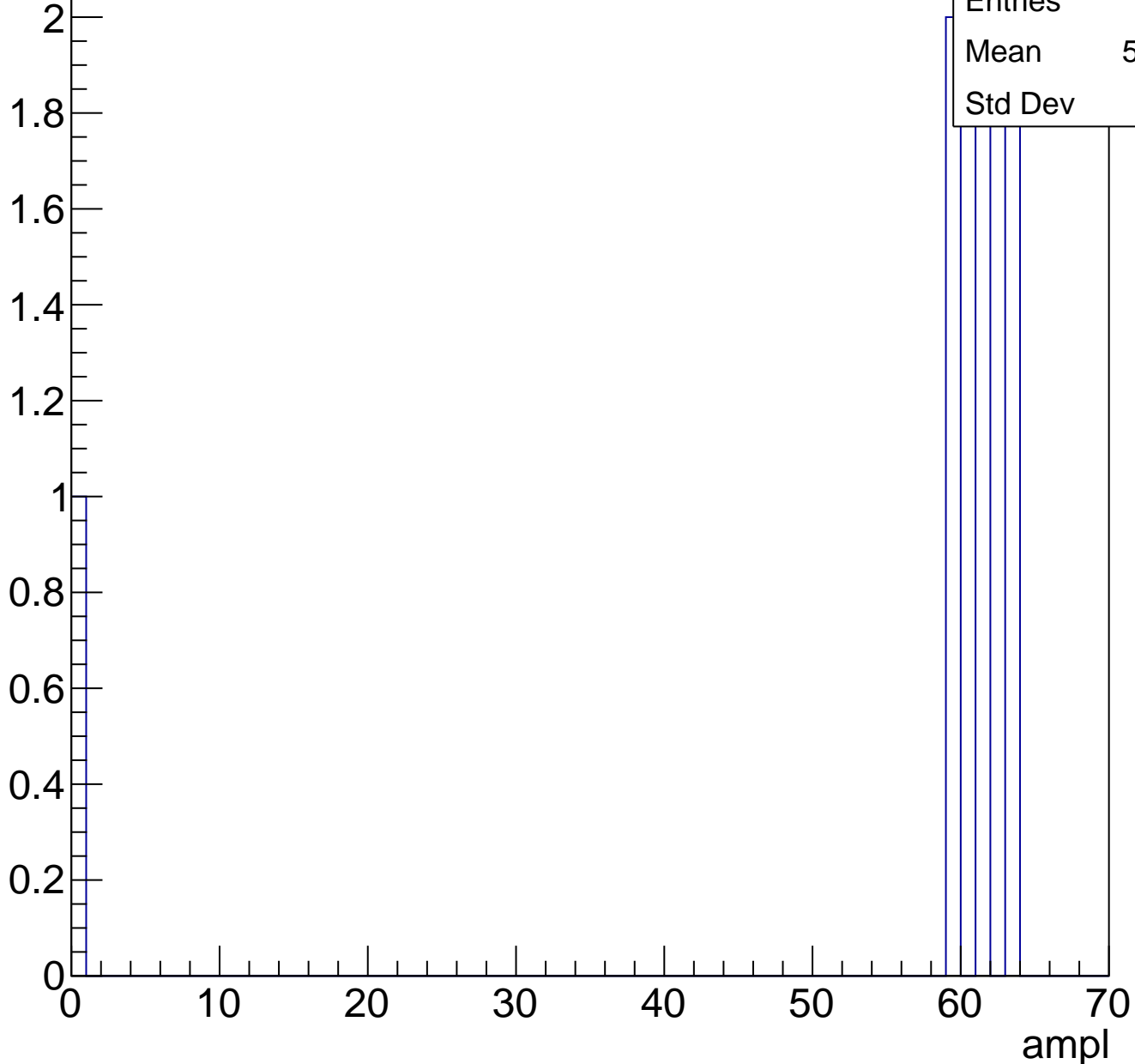
Entries	43
Mean	60.3
Std Dev	2.216



# B1L103S, U22-ch3, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch3, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch4, adc0

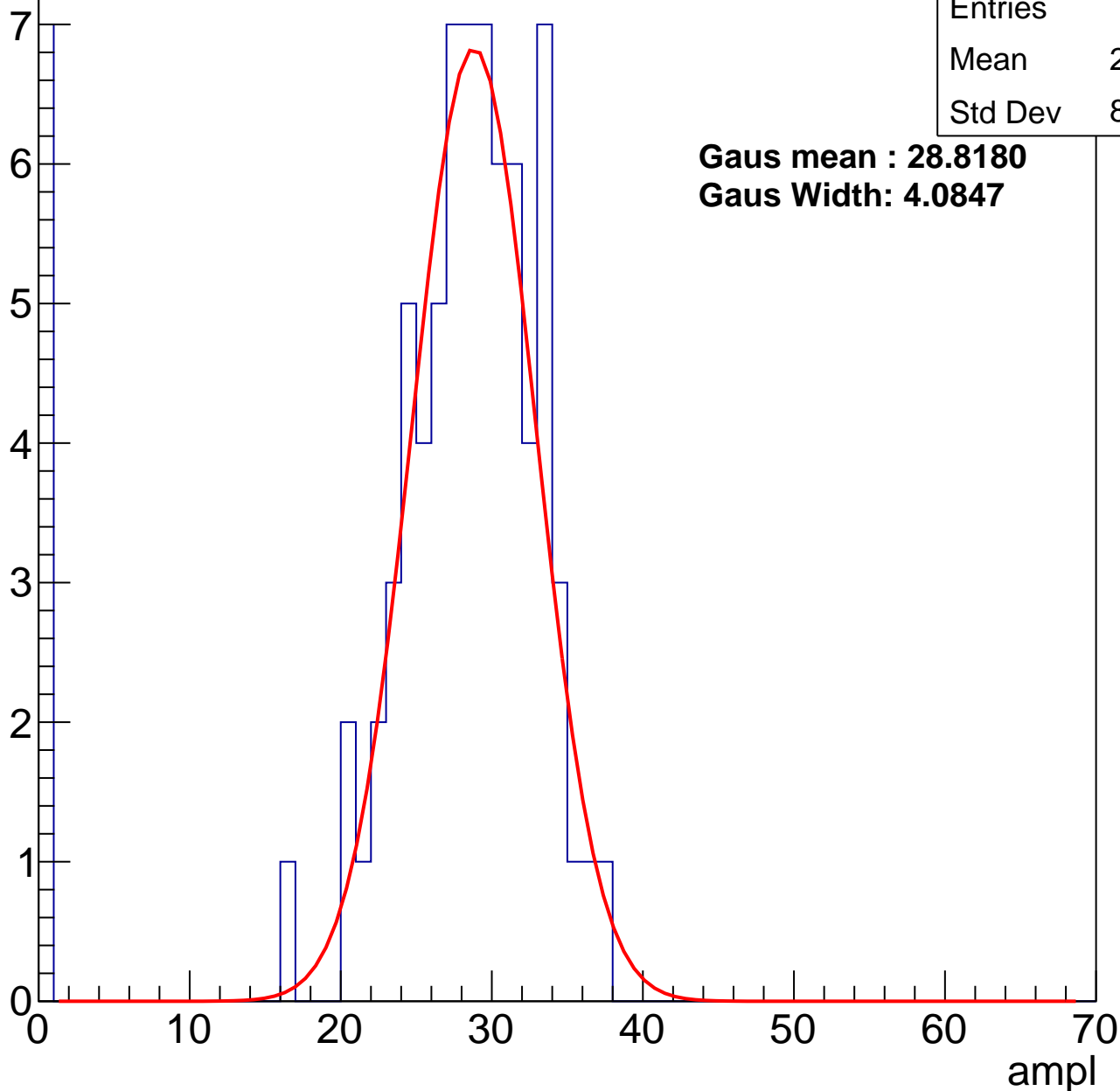
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	80
Mean	25.79
Std Dev	8.898

**Gaus mean : 28.8180**

**Gaus Width: 4.0847**



# B1L103S, U22-ch4, adc1

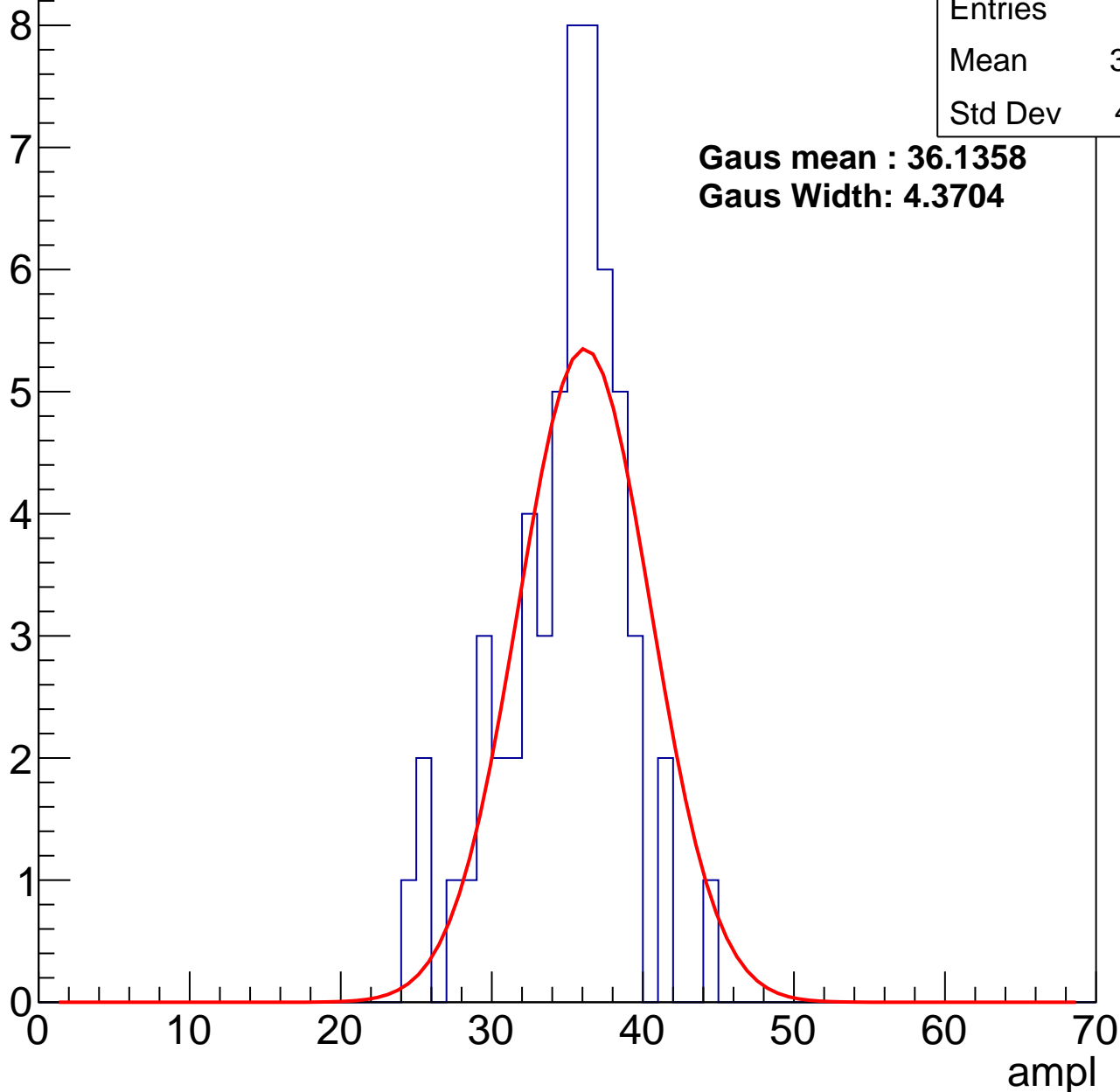
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	34.35
Std Dev	4.041

**Gaus mean : 36.1358**

**Gaus Width: 4.3704**



# B1L103S, U22-ch4, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

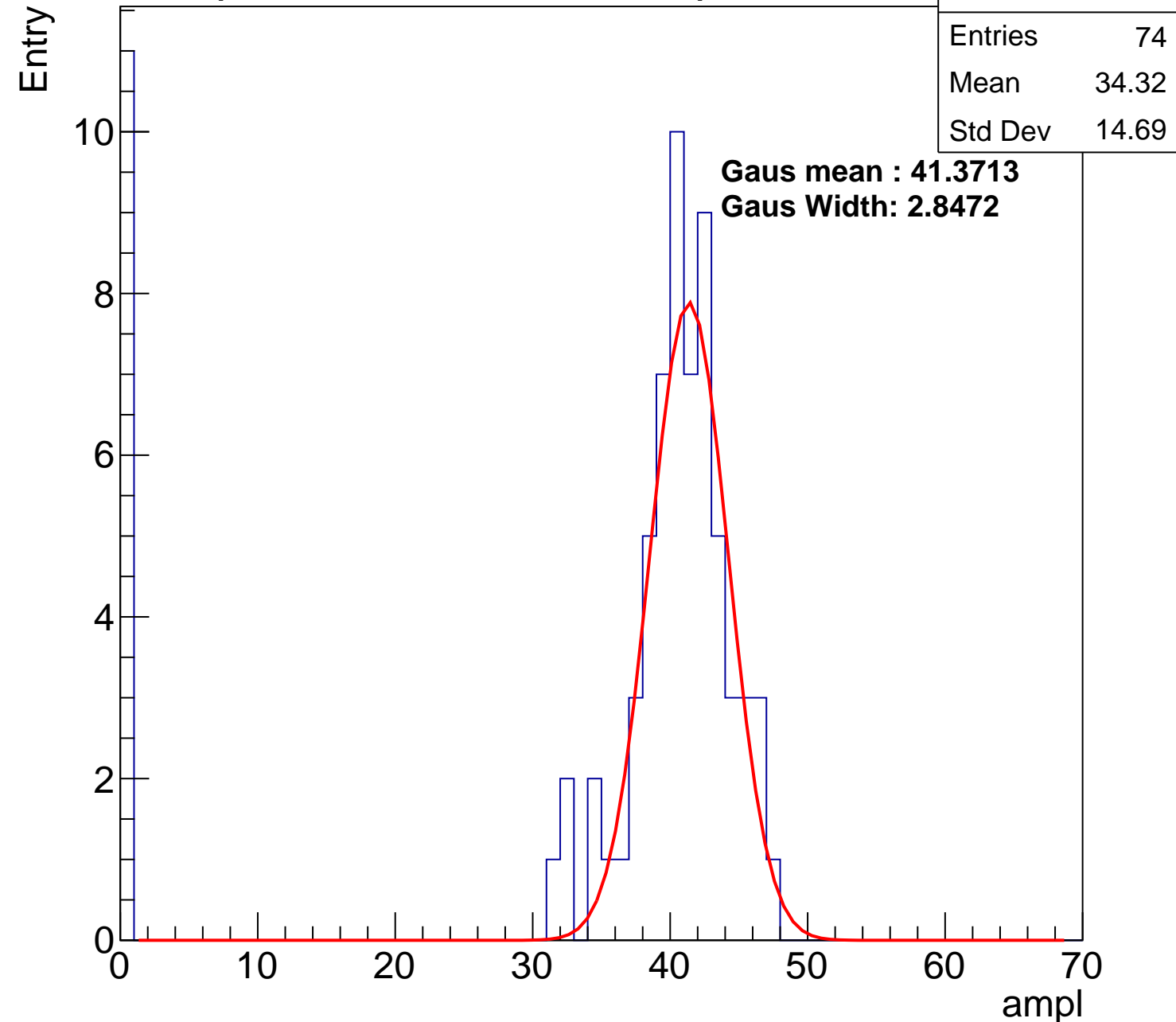
70

ampl

Entries	74
Mean	34.32
Std Dev	14.69

**Gaus mean : 41.3713**

**Gaus Width: 2.8472**



# B1L103S, U22-ch4, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

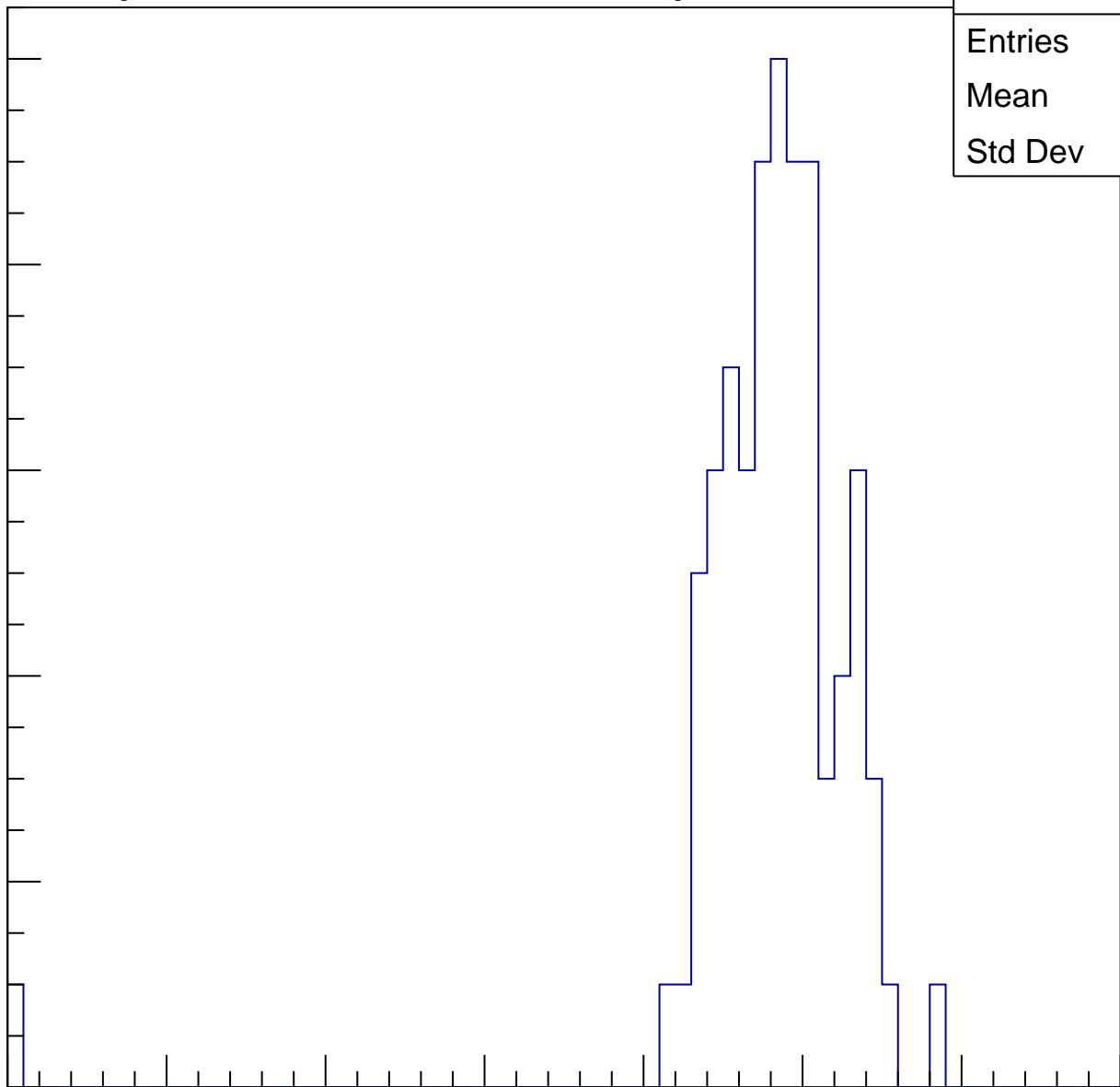
Entries	82
Mean	47.57
Std Dev	6.28

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

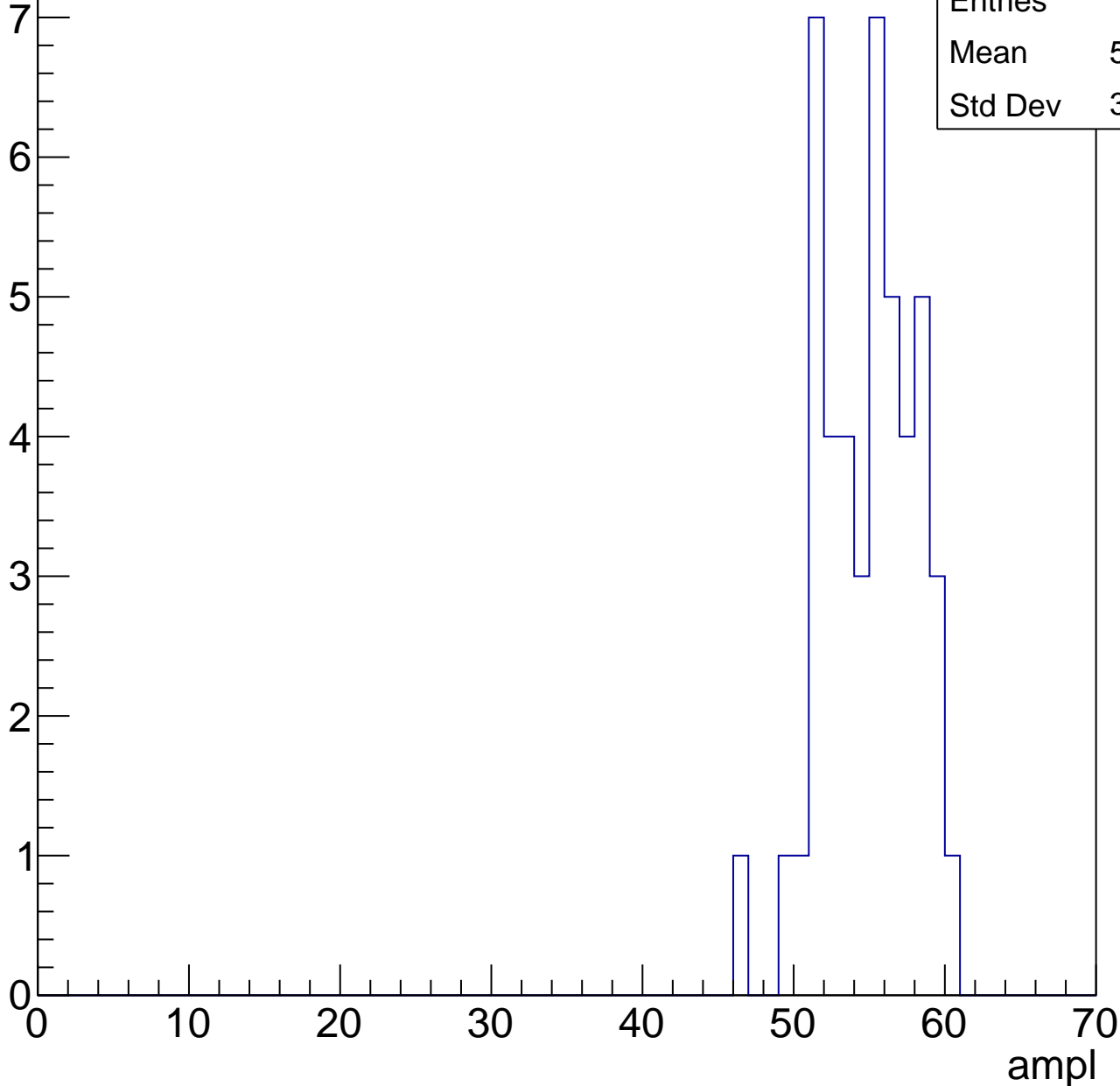


# B1L103S, U22-ch4, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.43
Std Dev	3.069

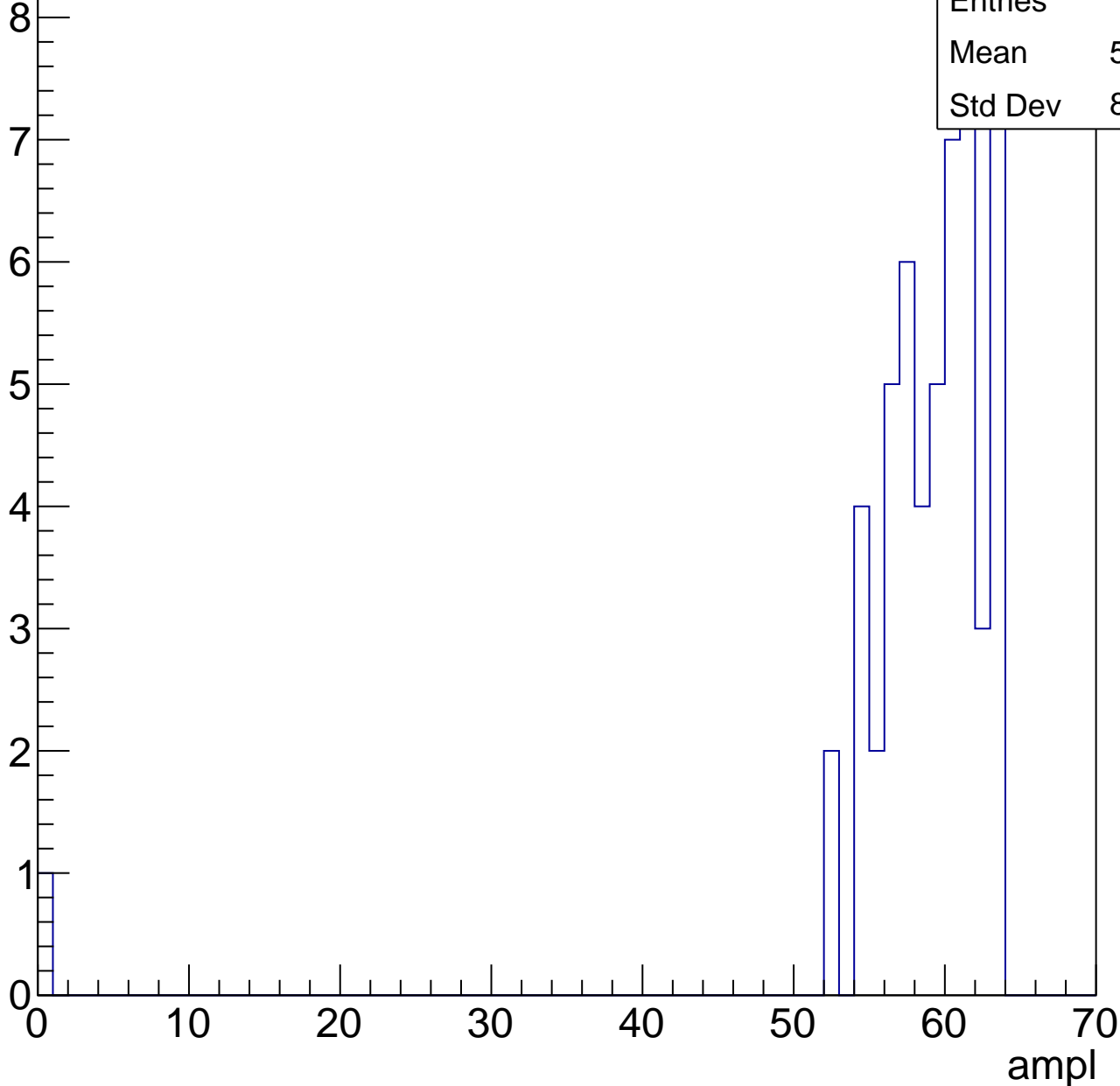


# B1L103S, U22-ch4, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

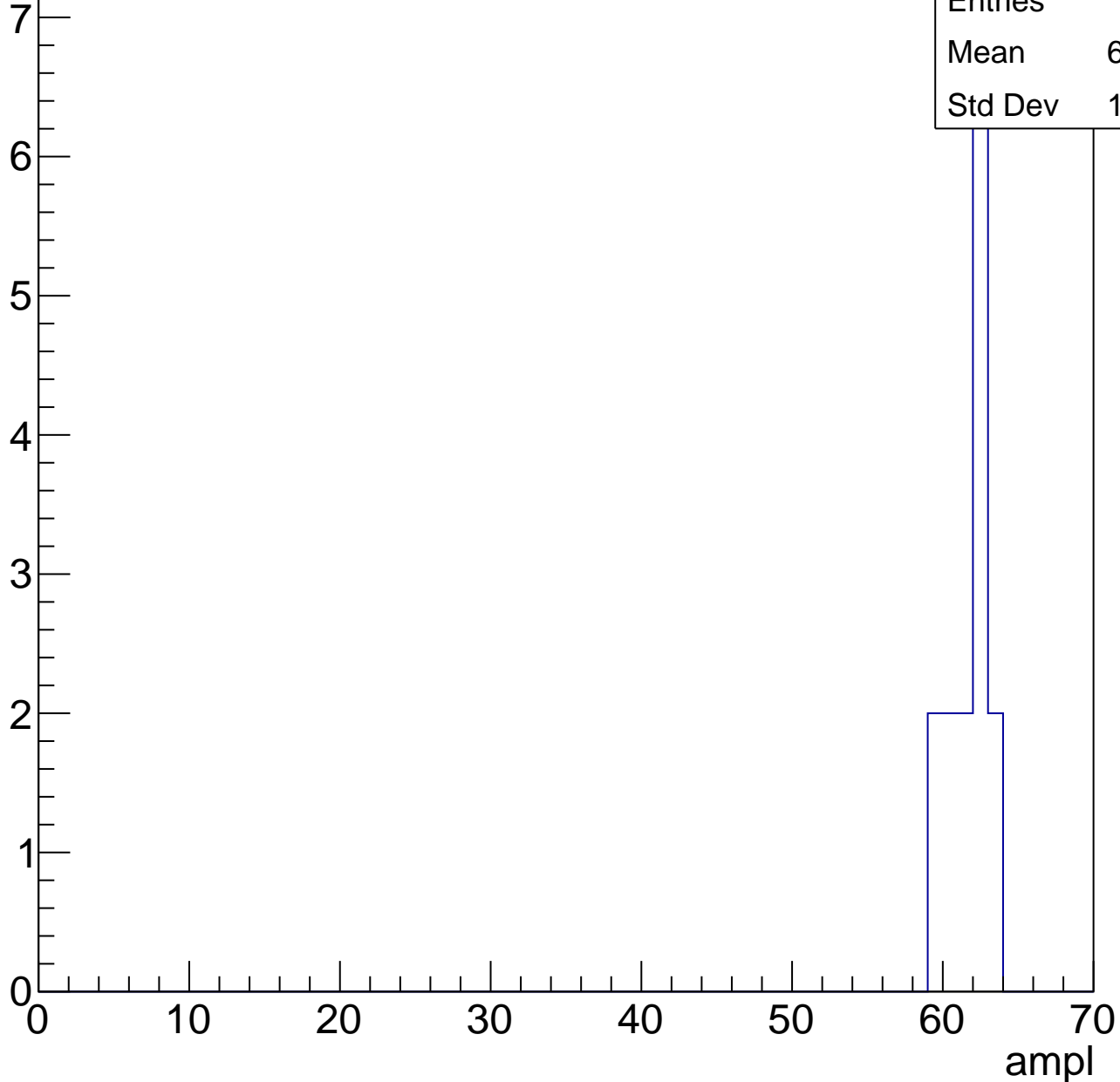
Entries	55
Mean	57.76
Std Dev	8.414



# B1L103S, U22-ch4, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch4, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch5, adc0

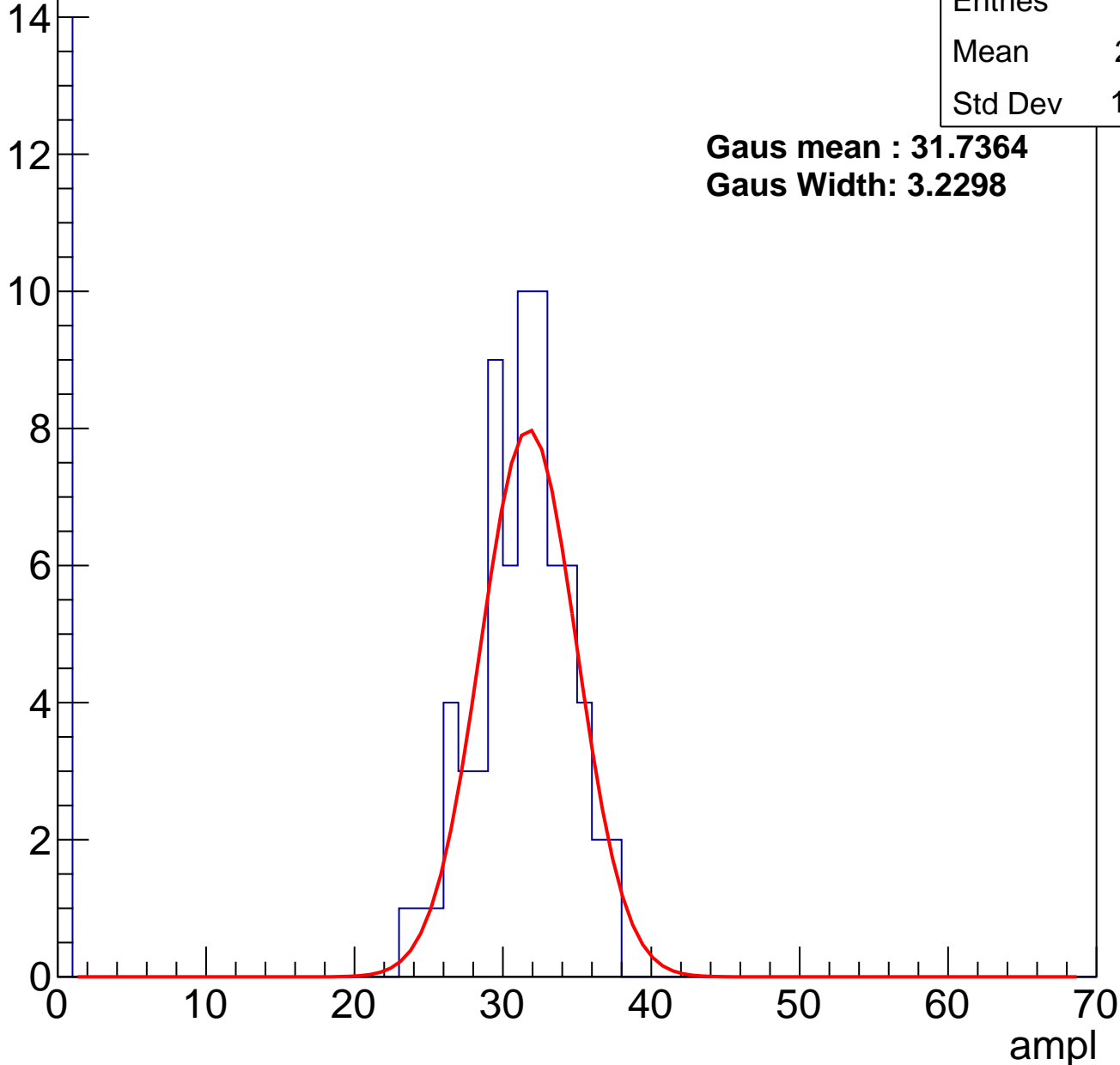
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	25.61
Std Dev	11.95

**Gaus mean : 31.7364**

**Gaus Width: 3.2298**

Entry



# B1L103S, U22-ch5, adc1

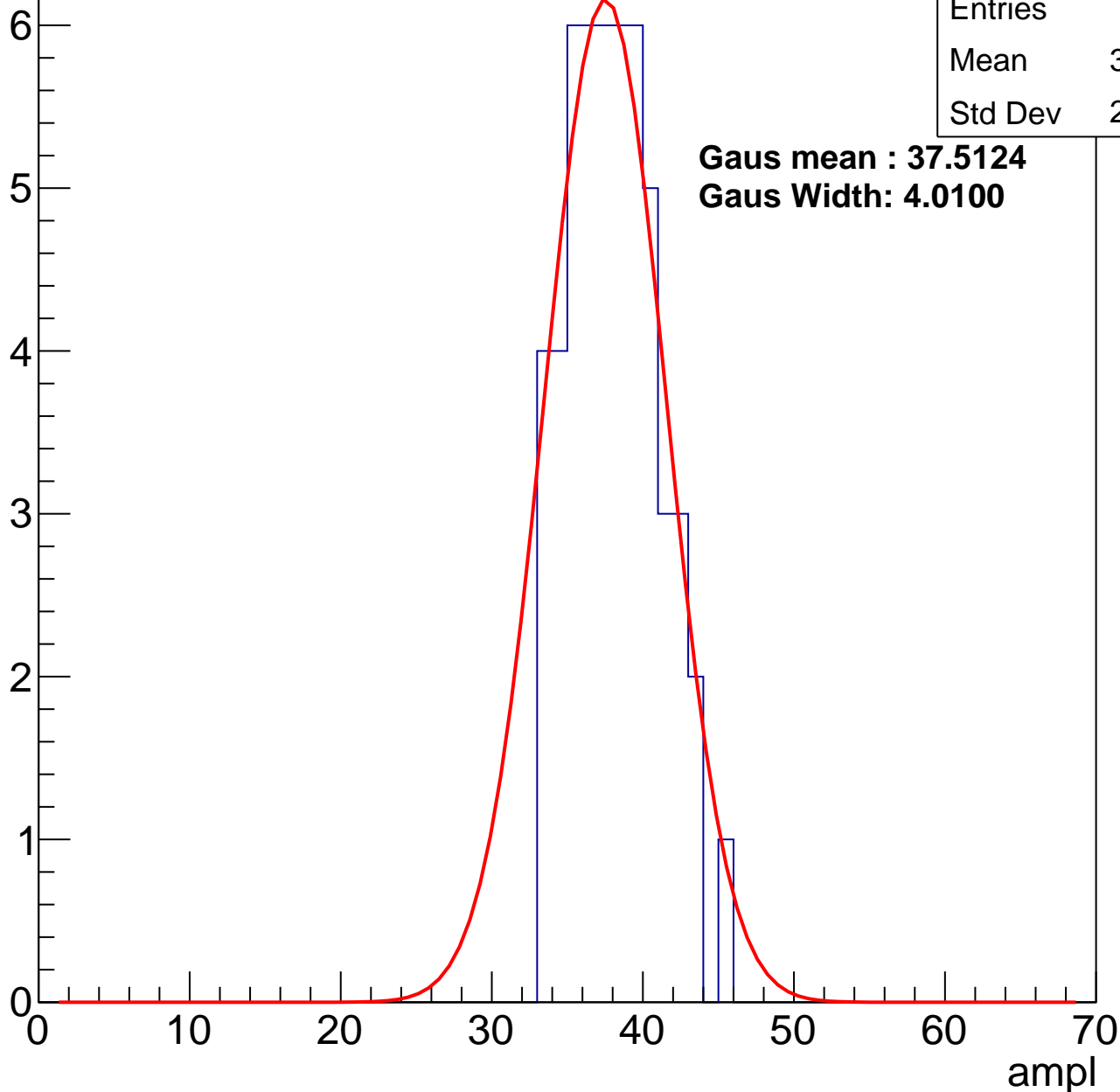
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	37.65
Std Dev	2.915

**Gaus mean : 37.5124**

**Gaus Width: 4.0100**



# B1L103S, U22-ch5, adc2

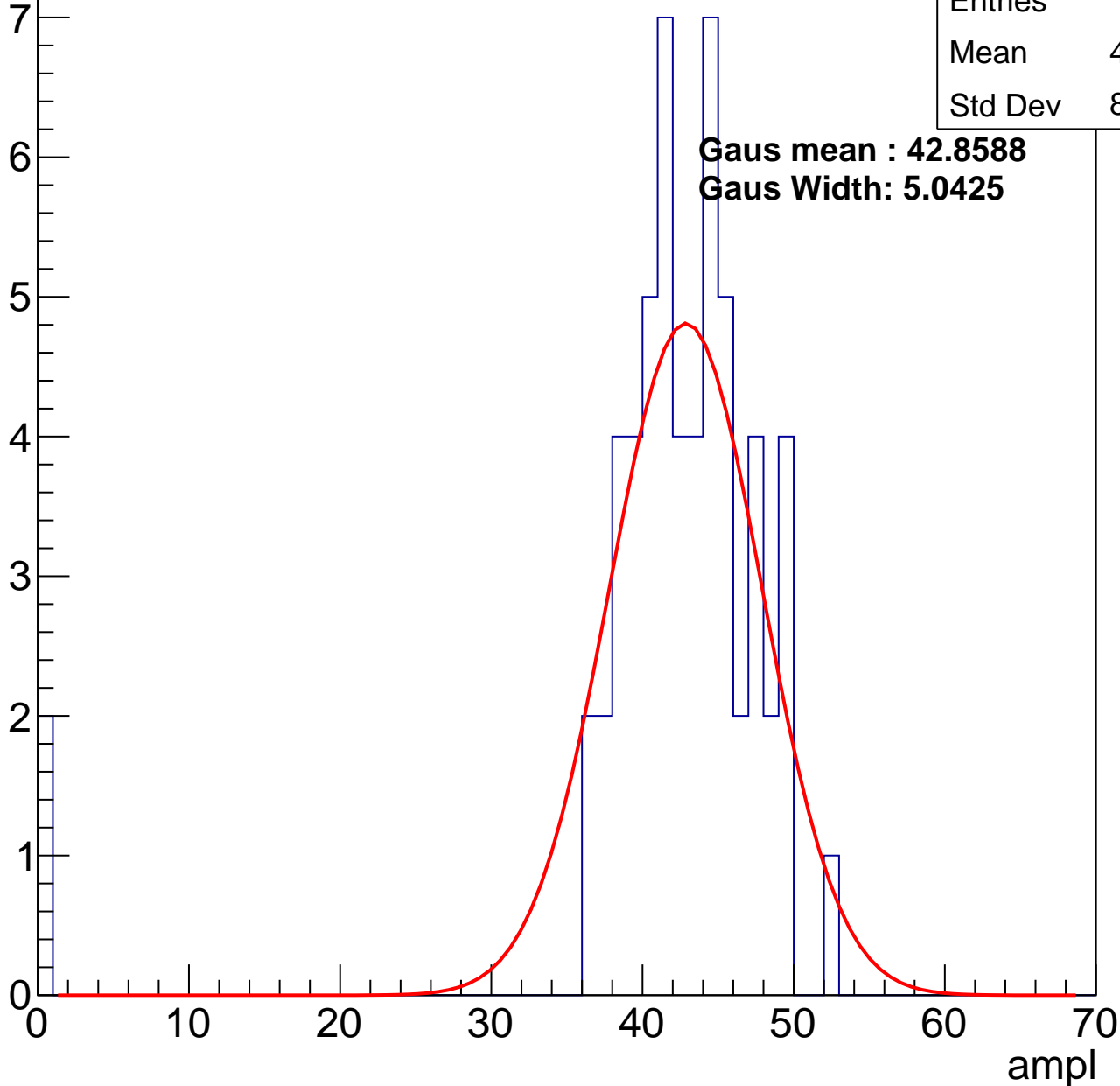
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	41.32
Std Dev	8.564

**Gaus mean : 42.8588**

**Gaus Width: 5.0425**

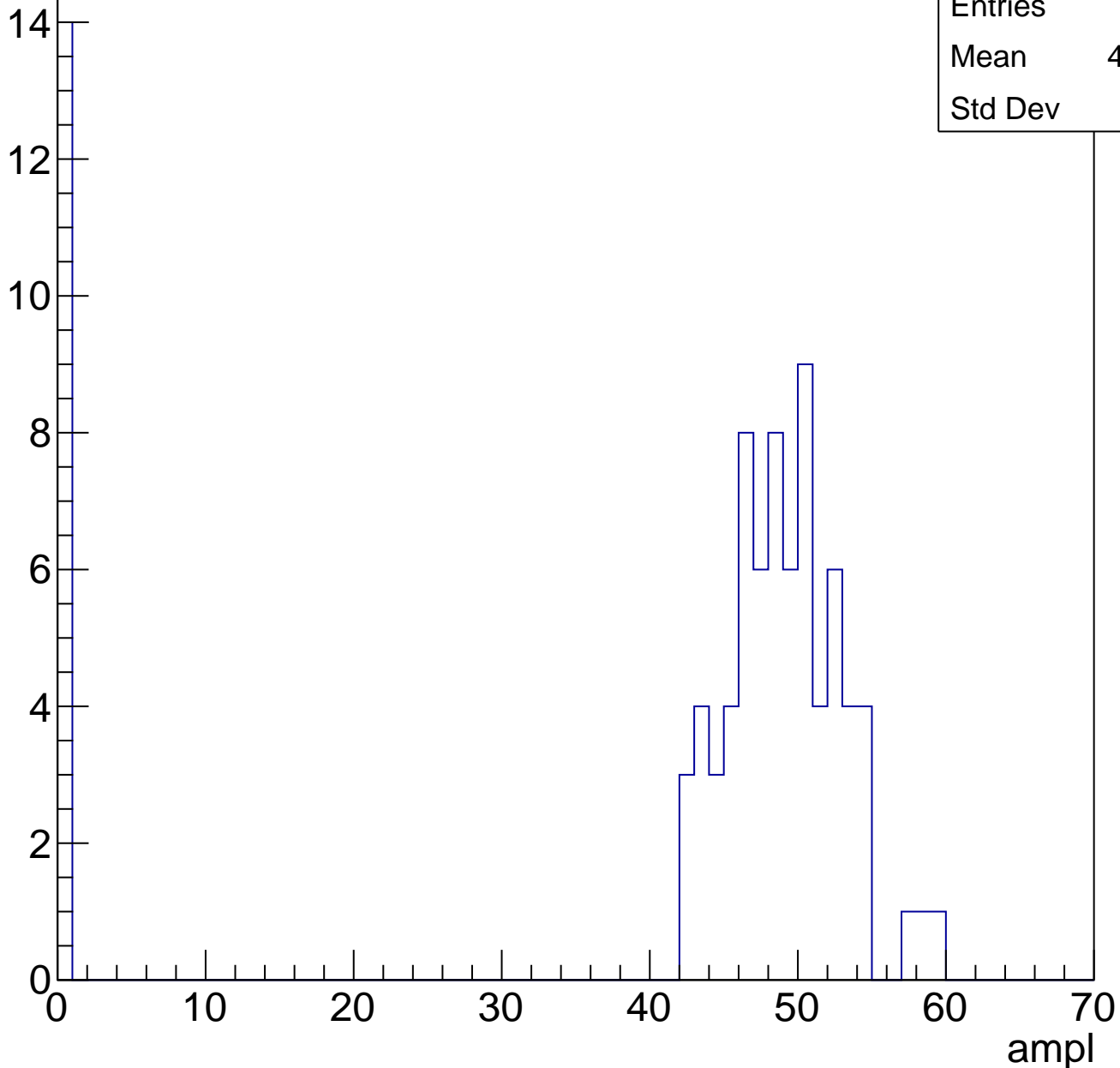


# B1L103S, U22-ch5, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	40.77
Std Dev	18.3

Entry

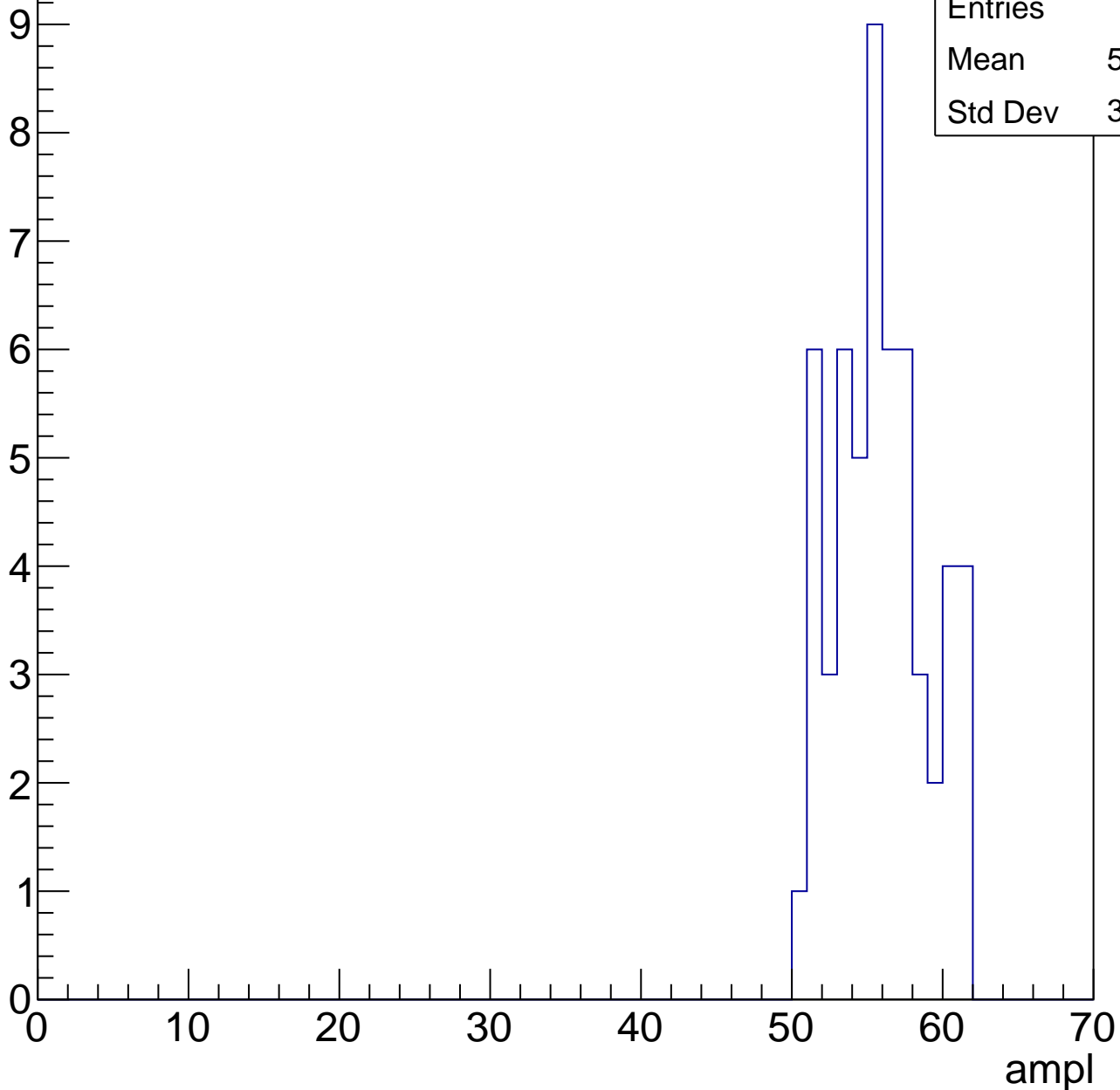


# B1L103S, U22-ch5, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.44
Std Dev	3.014

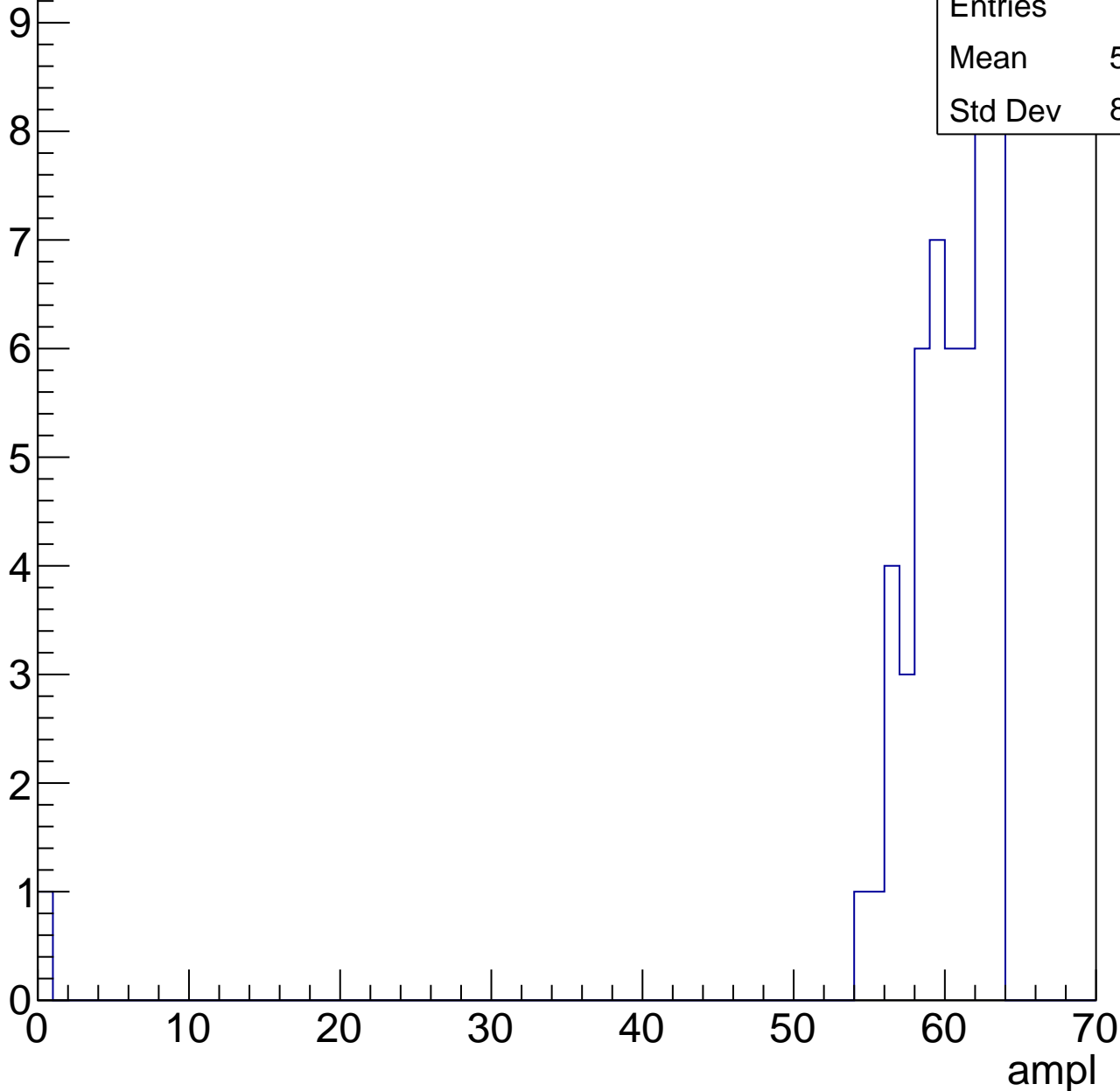


# B1L103S, U22-ch5, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	58.73
Std Dev	8.569



# B1L103S, U22-ch5, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

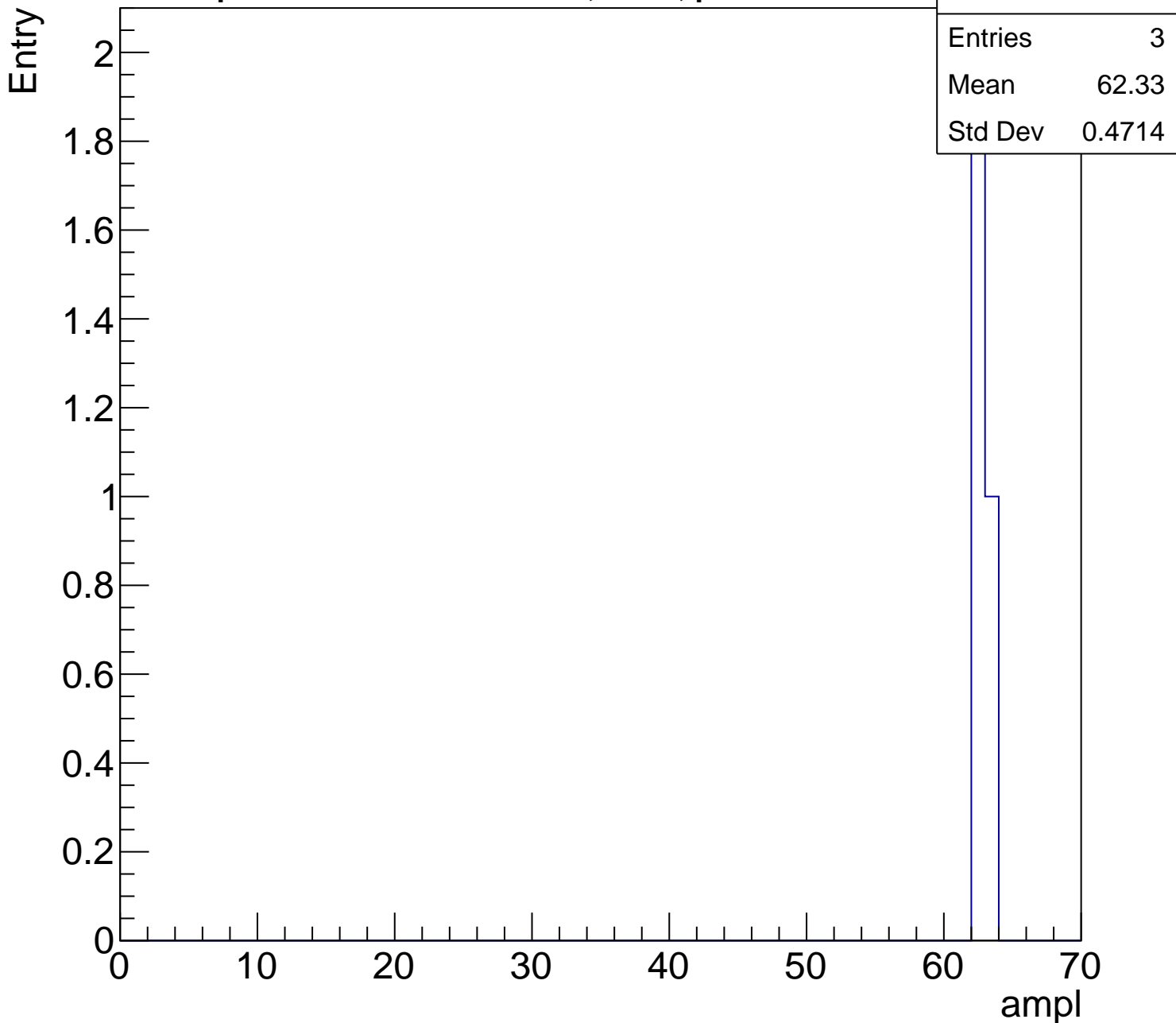
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.33
Std Dev	0.4714

0 10 20 30 40 50 60 70

ampl





# B1L103S, U22-ch5, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch6, adc0

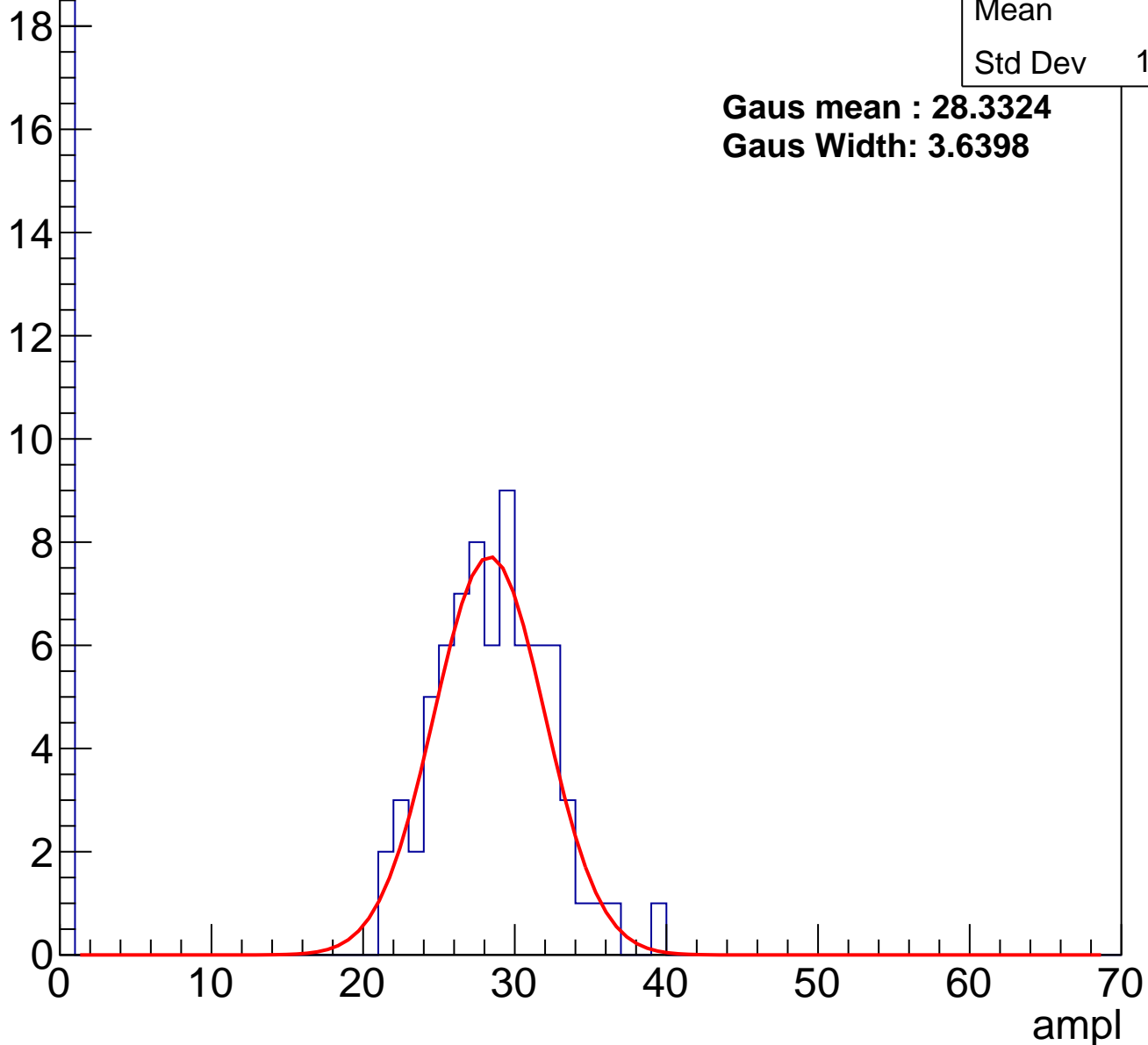
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	92
Mean	22.3
Std Dev	11.83

**Gaus mean : 28.3324**

**Gaus Width: 3.6398**

Entry



# B1L103S, U22-ch6, adc1

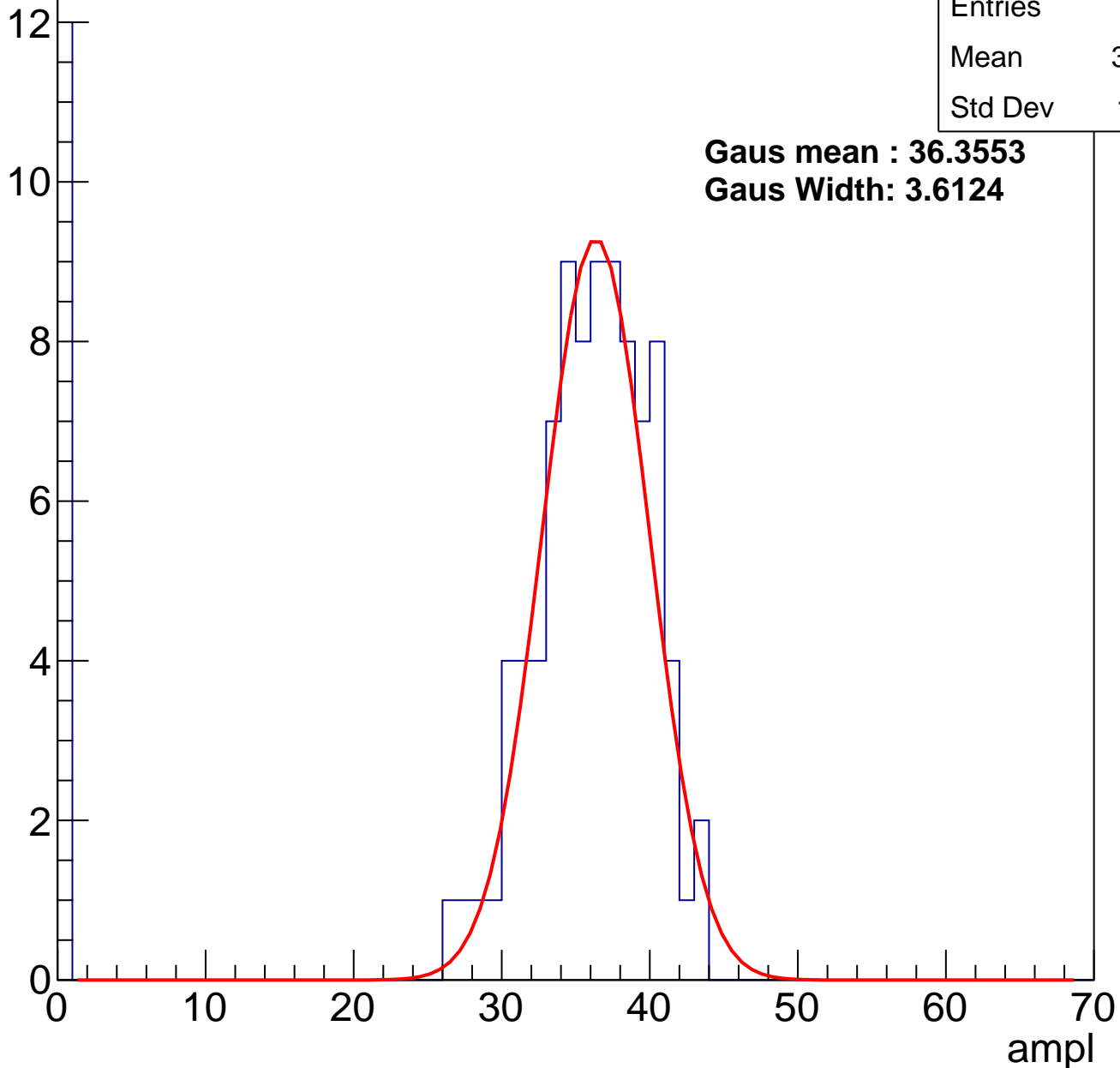
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	100
Mean	31.45
Std Dev	12.11

**Gaus mean : 36.3553**

**Gaus Width: 3.6124**

Entry



# B1L103S, U22-ch6, adc2

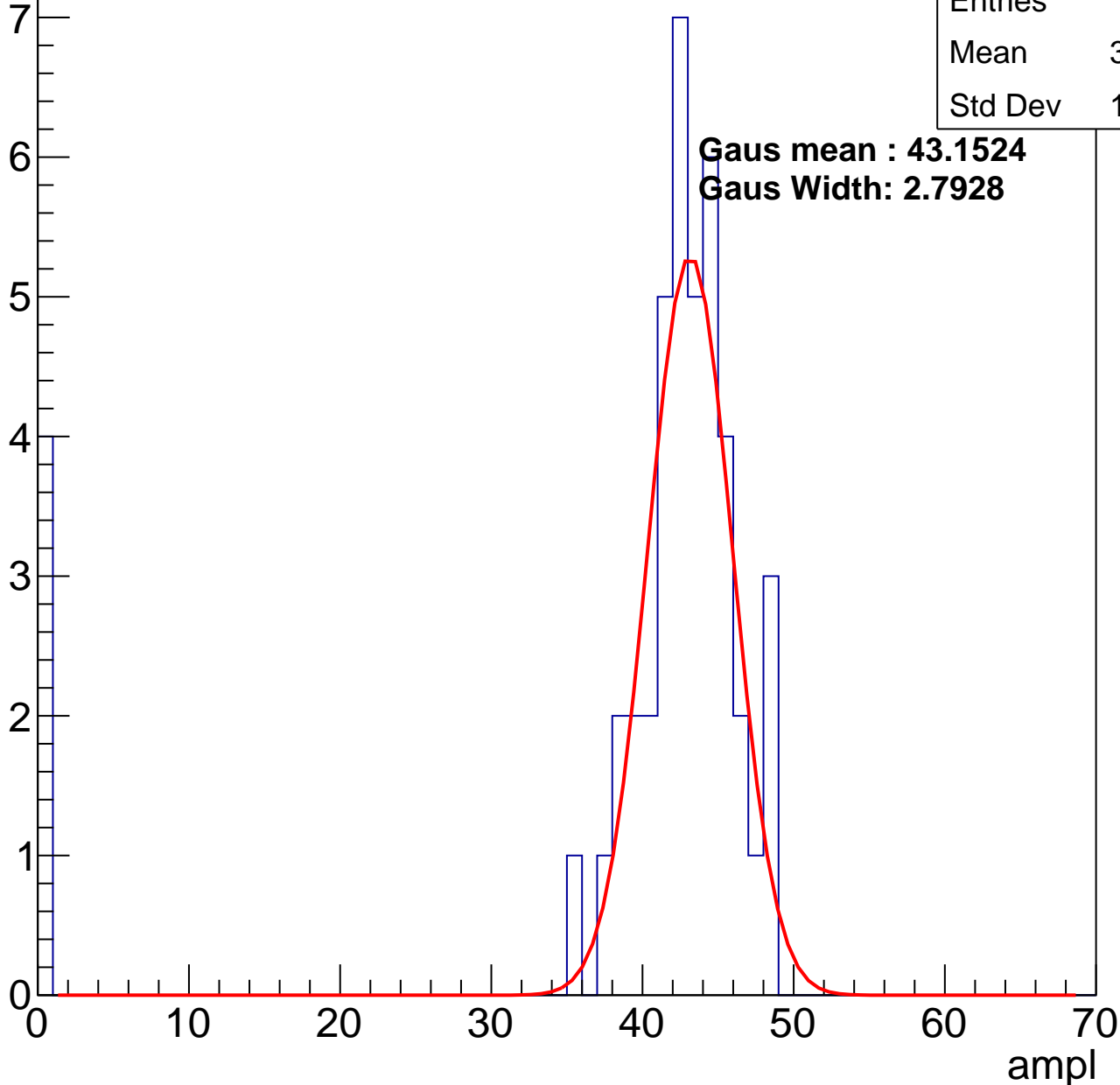
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	45
Mean	38.82
Std Dev	12.44

**Gaus mean : 43.1524**

**Gaus Width: 2.7928**



# B1L103S, U22-ch6, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

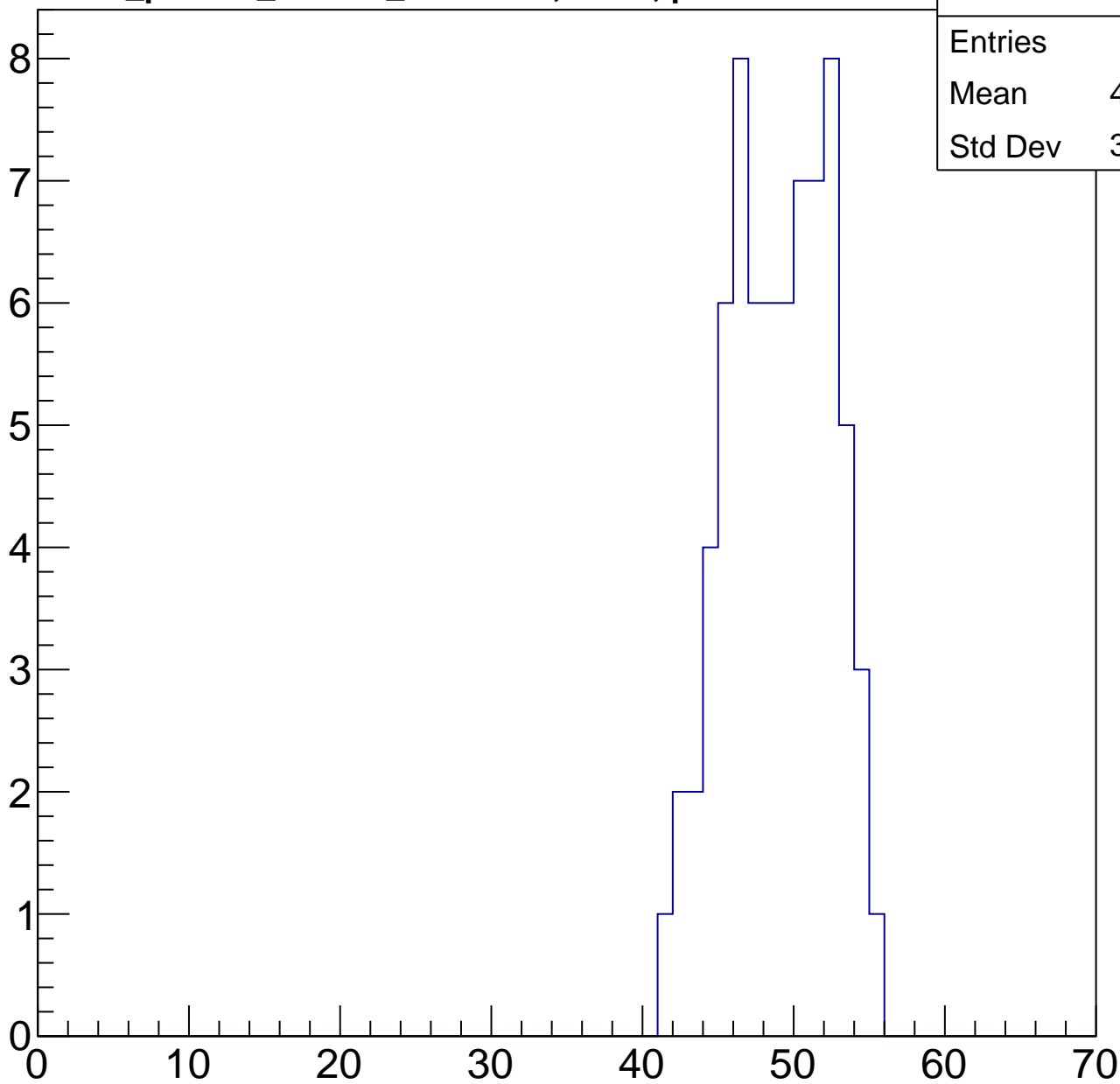
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	72
Mean	48.53
Std Dev	3.362

ampl

0 10 20 30 40 50 60 70

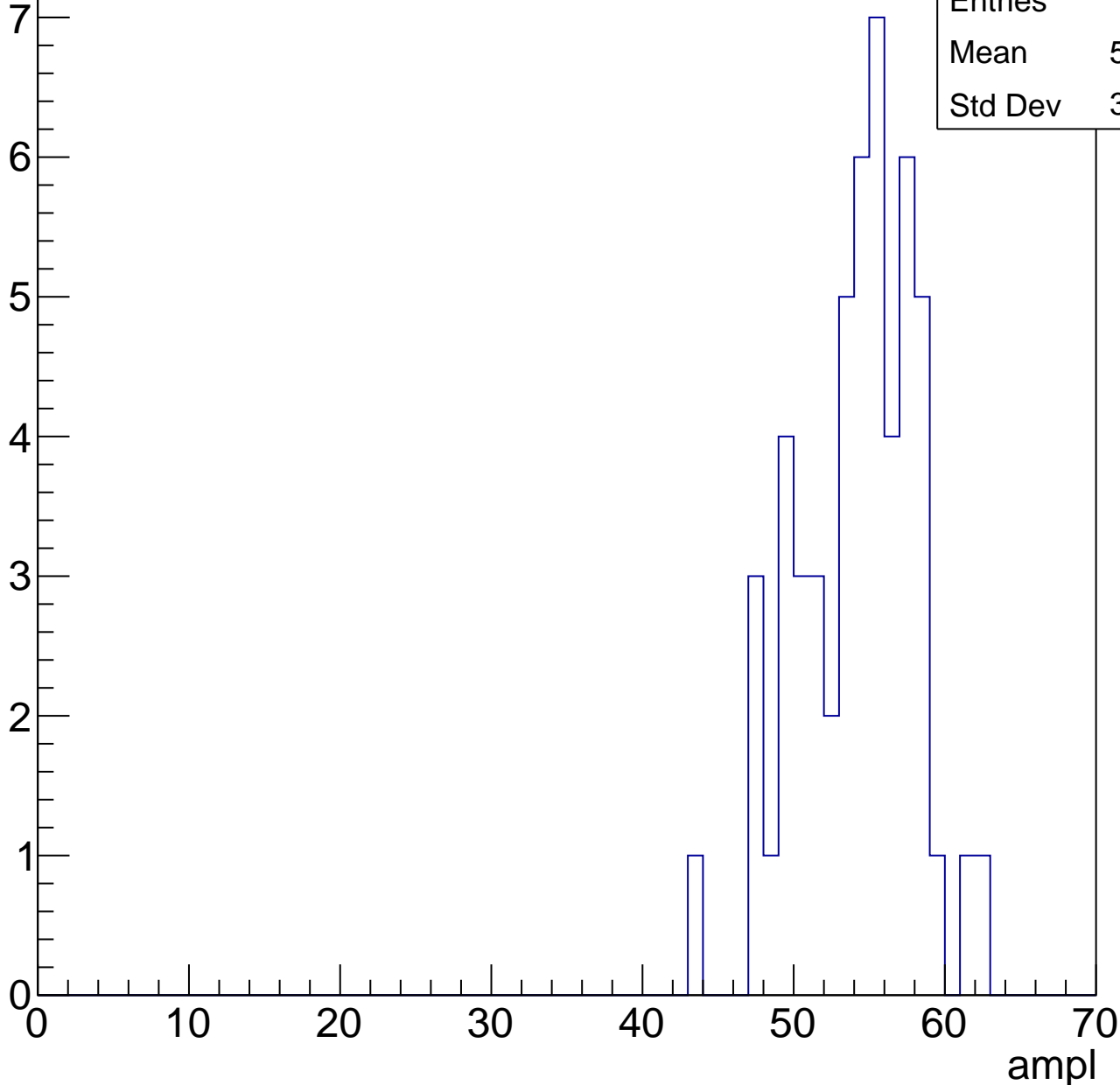


# B1L103S, U22-ch6, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

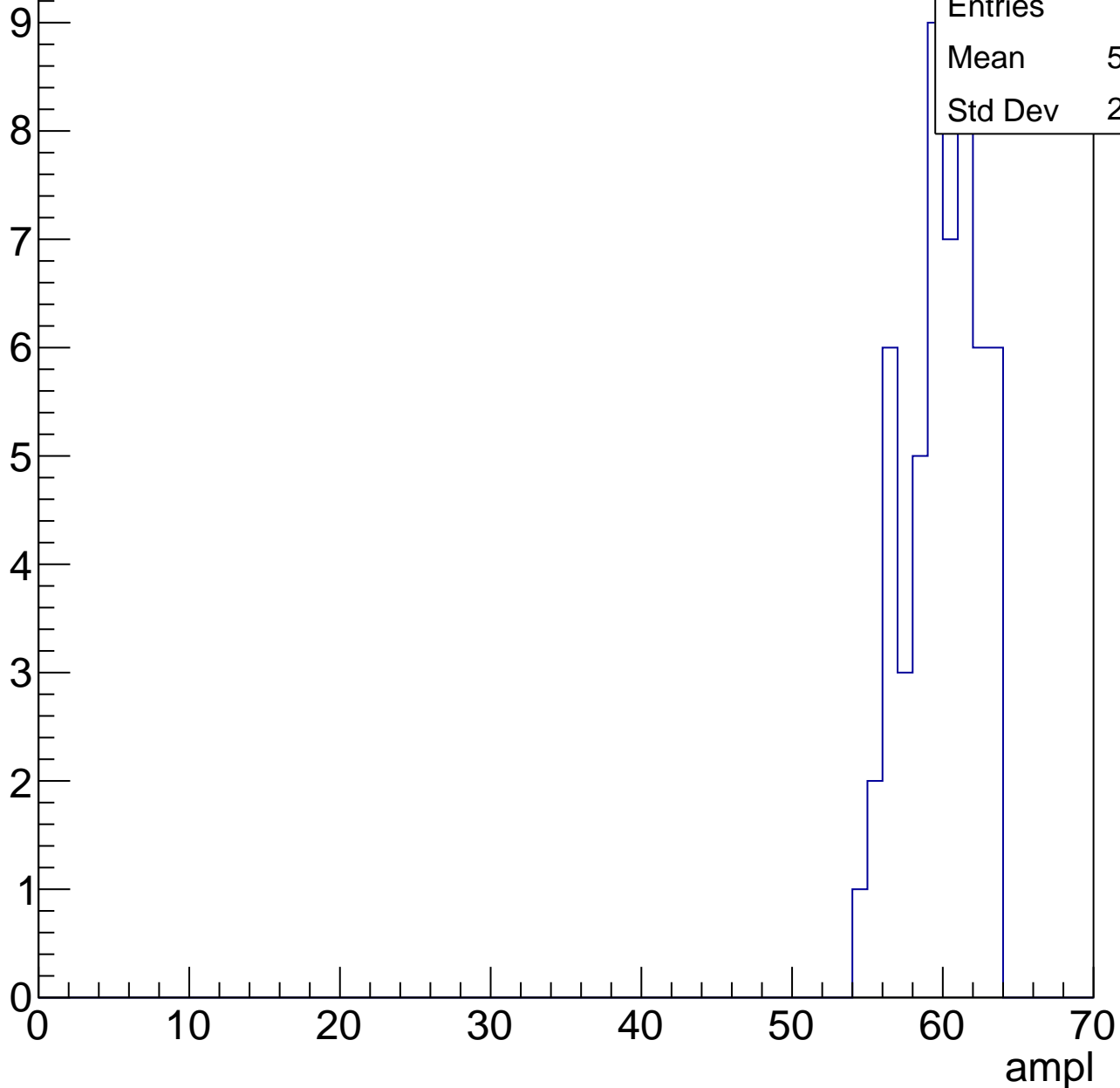
Entries	53
Mean	53.72
Std Dev	3.843



# B1L103S, U22-ch6, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

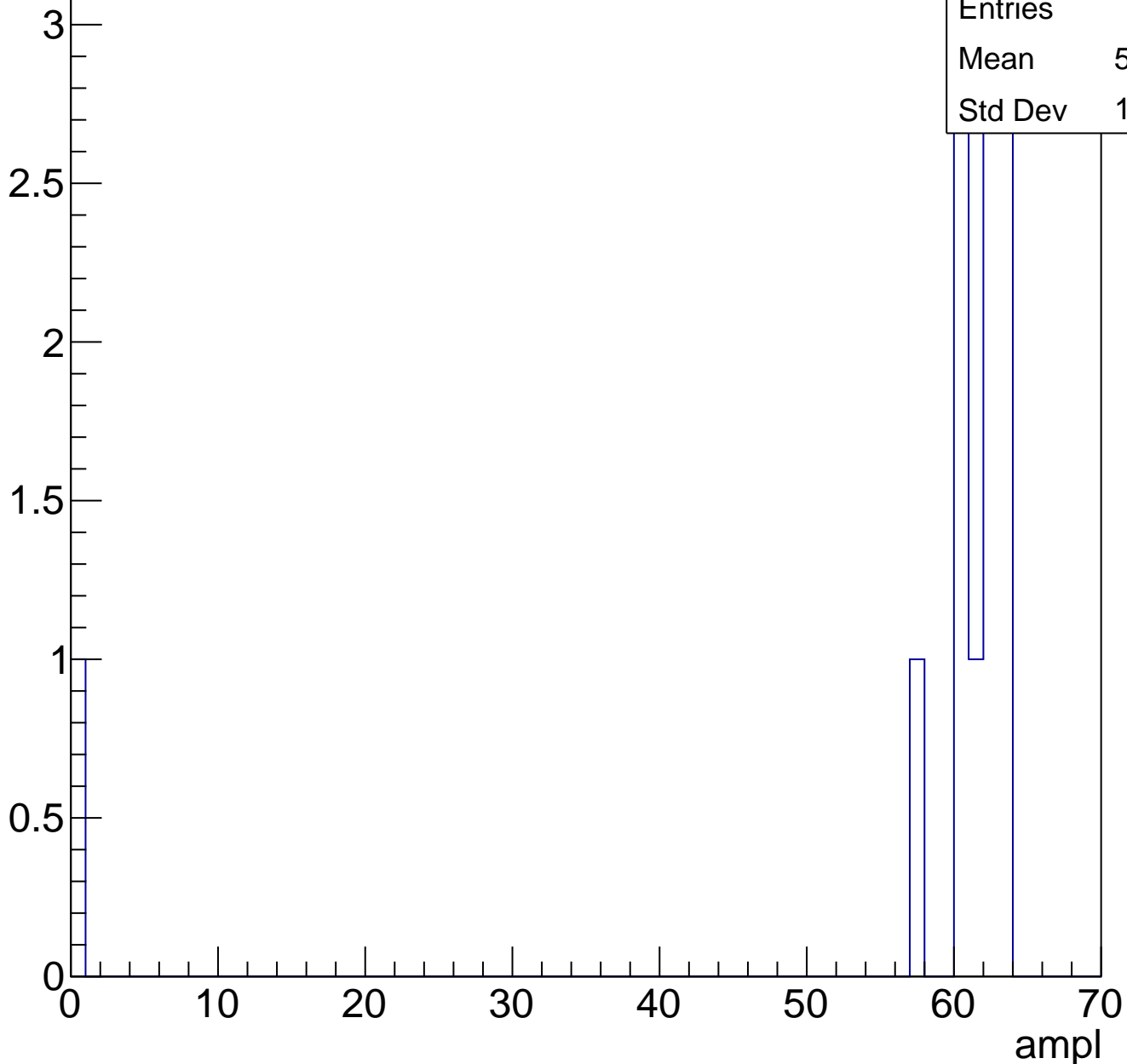
Entry



# B1L103S, U22-ch6, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch6, adc7

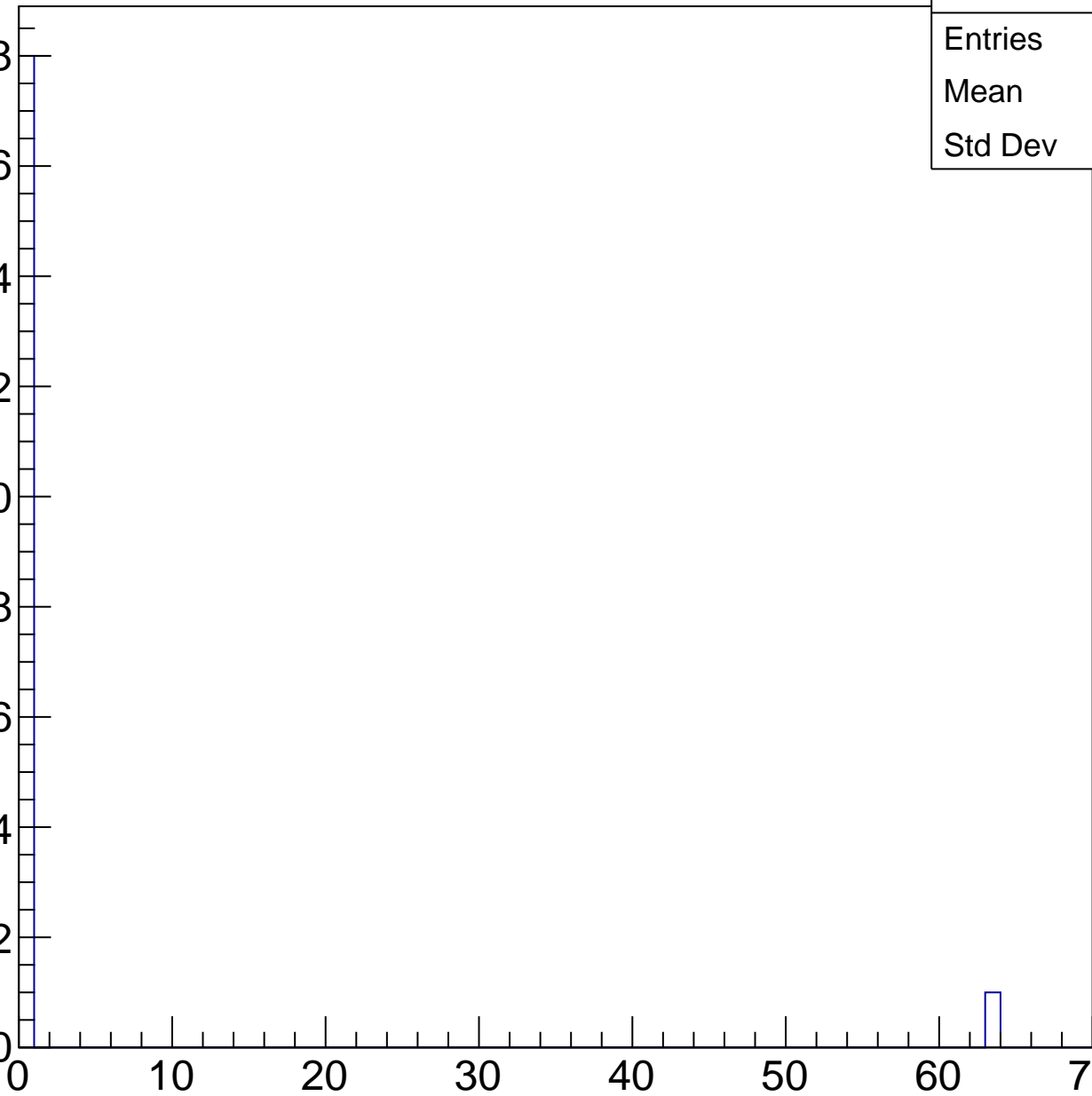
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	3.316
Std Dev	14.07

ampl



# B1L103S, U22-ch7, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	24.48
Std Dev	11.09

**Gaus mean : 29.7480**

**Gaus Width: 3.2923**

Entry

12

10

8

6

4

2

0

0

10

20

30

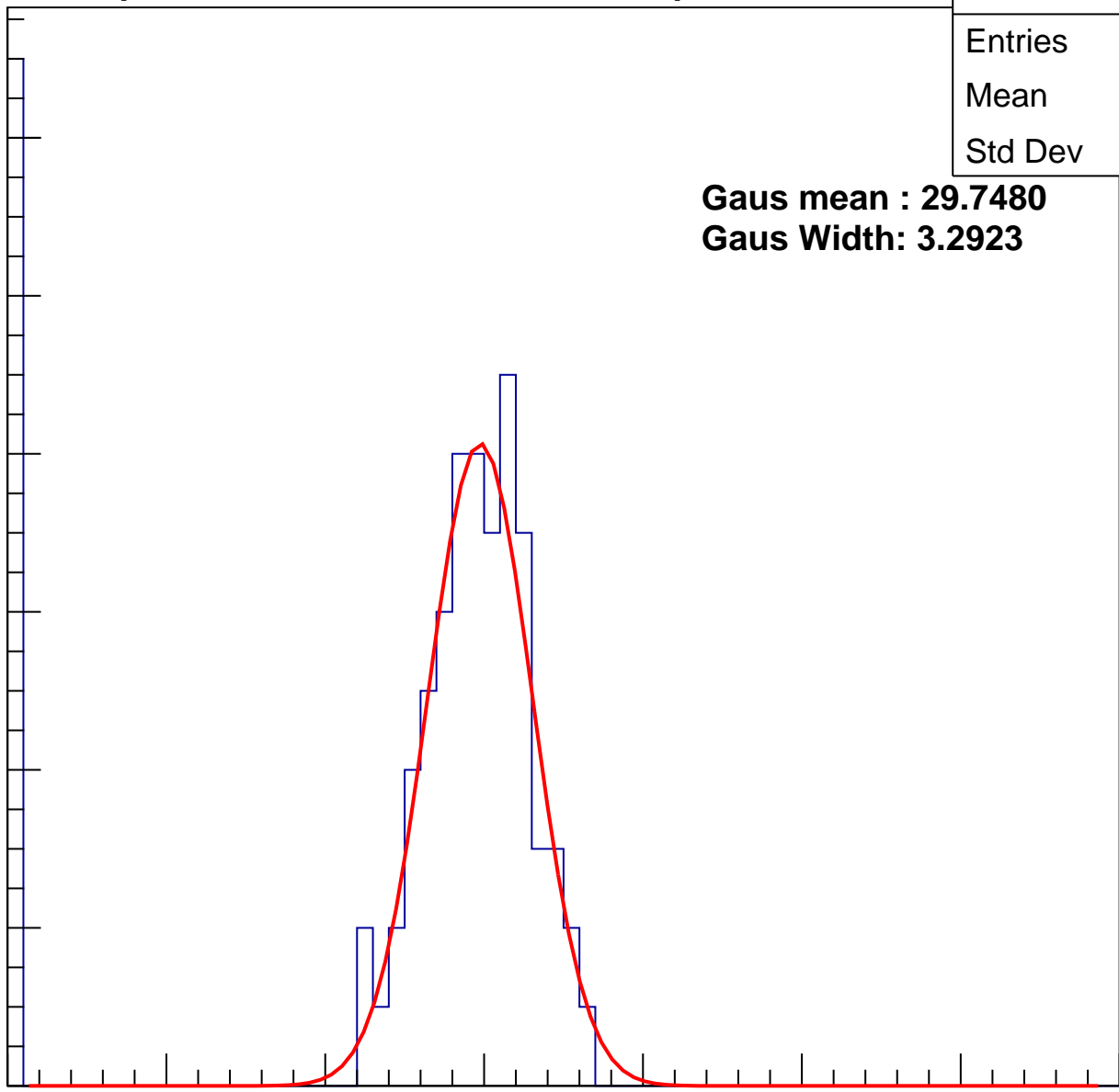
40

50

60

70

ampl



# B1L103S, U22-ch7, adc1

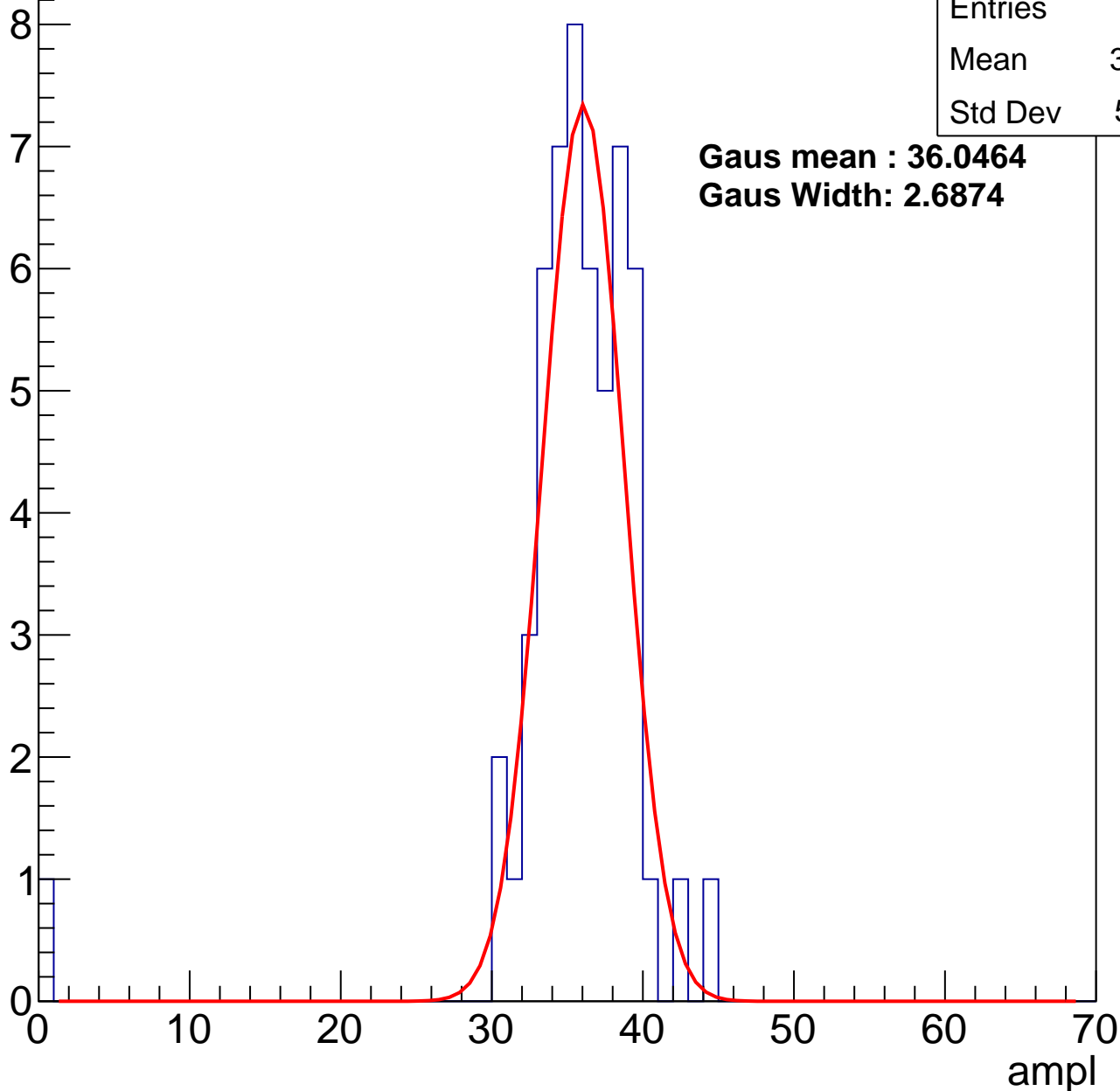
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.09
Std Dev	5.541

**Gaus mean : 36.0464**

**Gaus Width: 2.6874**



# B1L103S, U22-ch7, adc2

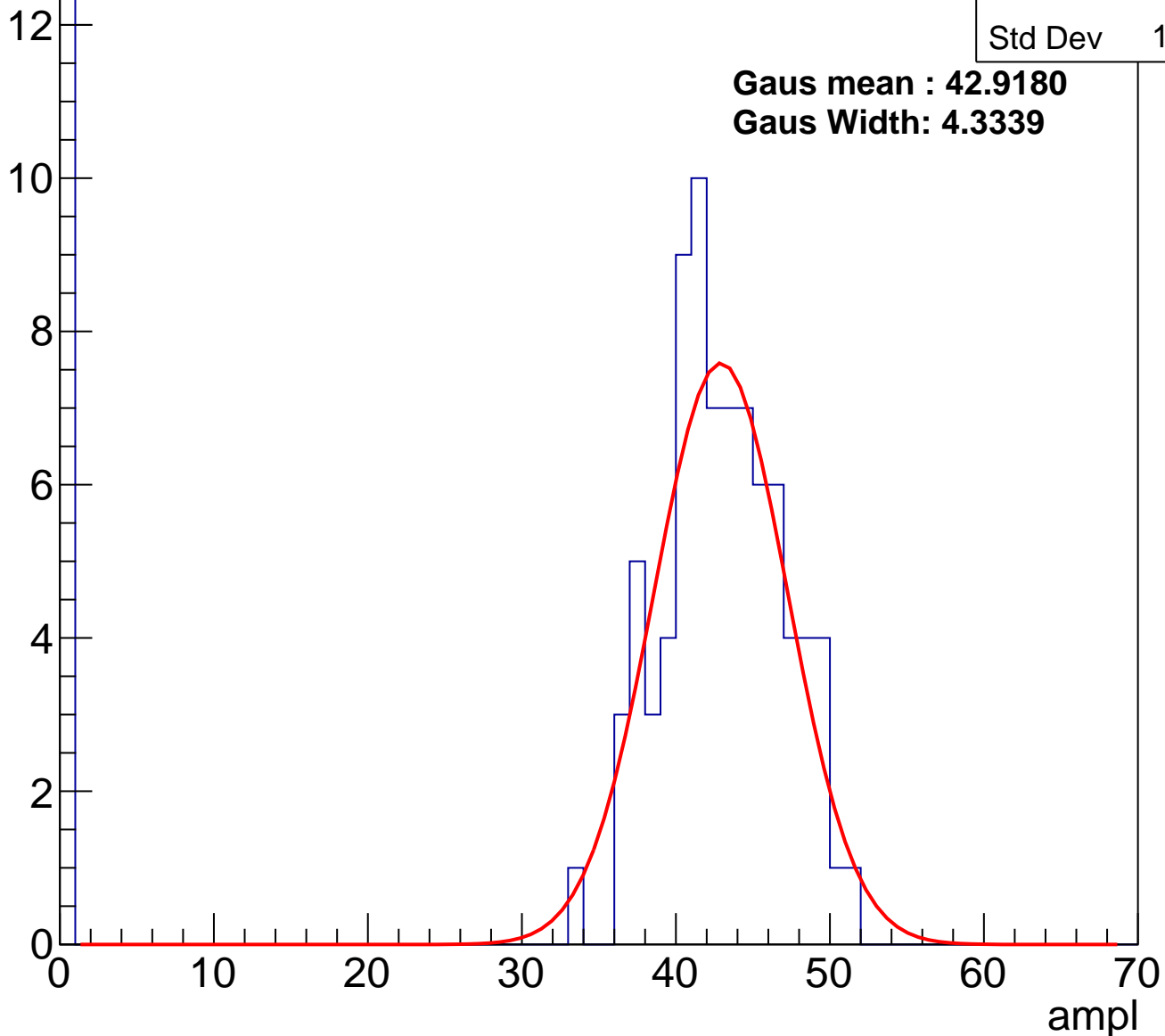
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	95
Mean	36.76
Std Dev	15.06

**Gaus mean : 42.9180**

**Gaus Width: 4.3339**

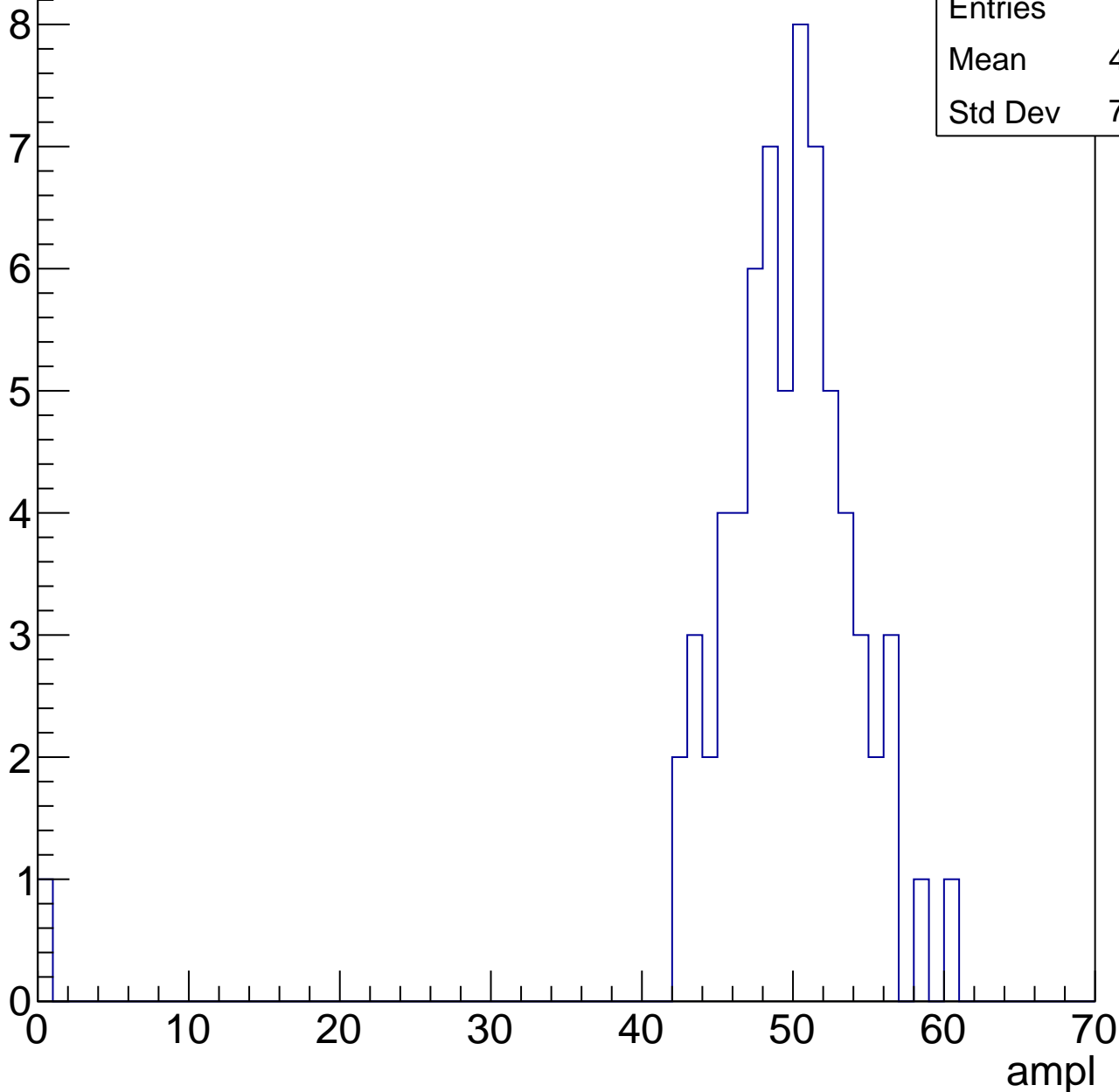


# B1L103S, U22-ch7, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

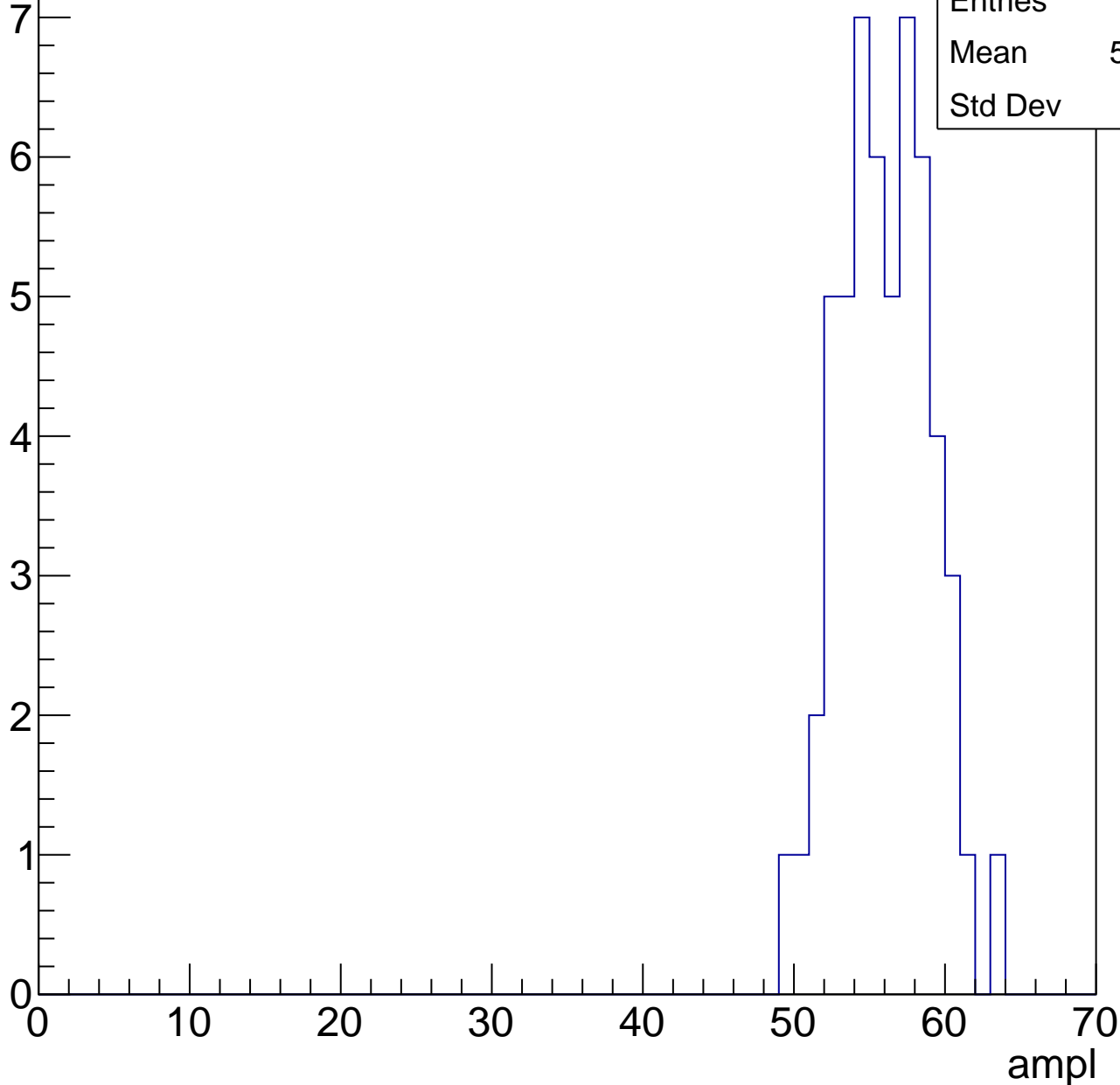
Entries	68
Mean	48.75
Std Dev	7.097



# B1L103S, U22-ch7, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

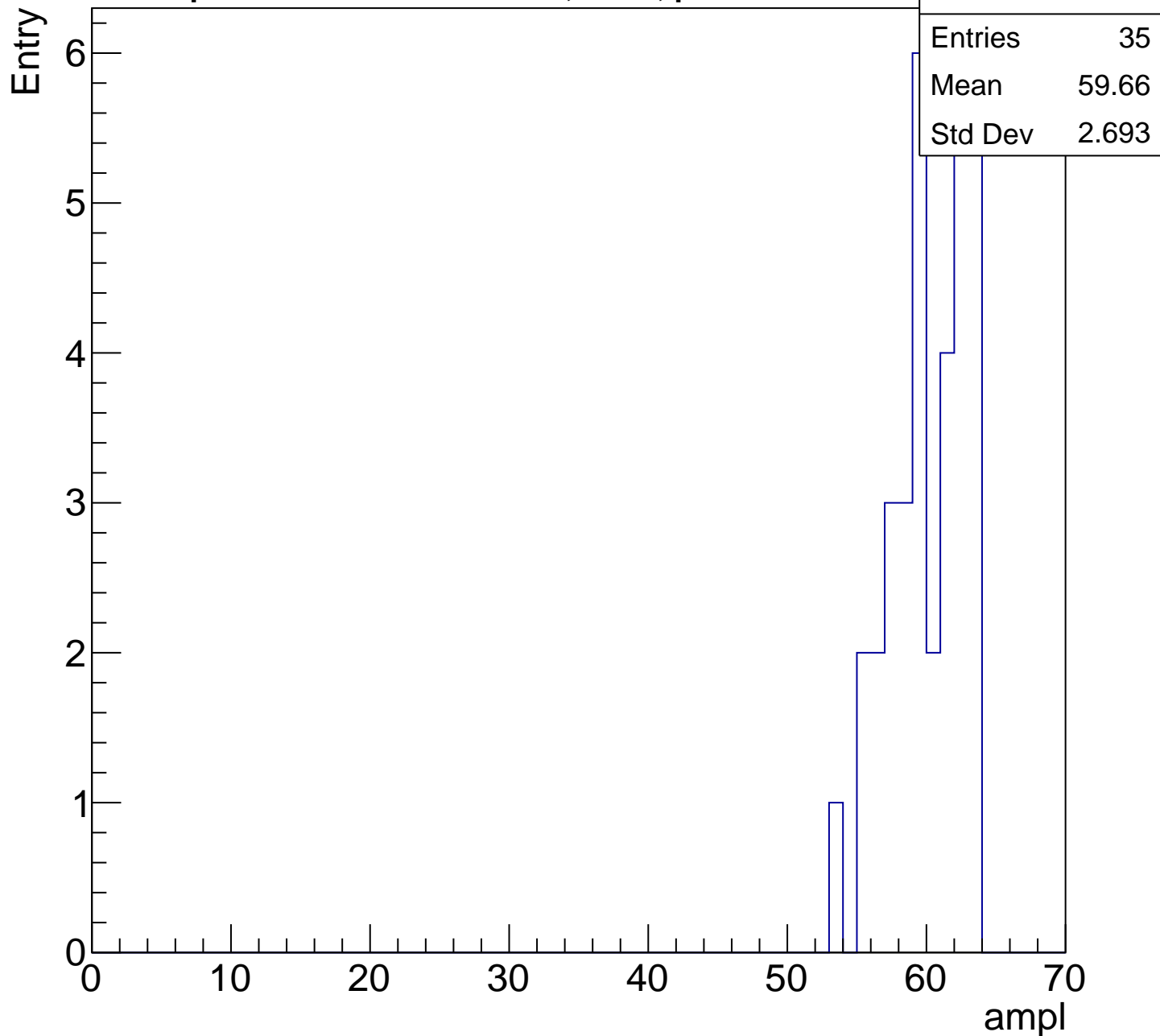
Entry



Entries	54
Mean	55.57
Std Dev	2.96

# B1L103S, U22-ch7, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

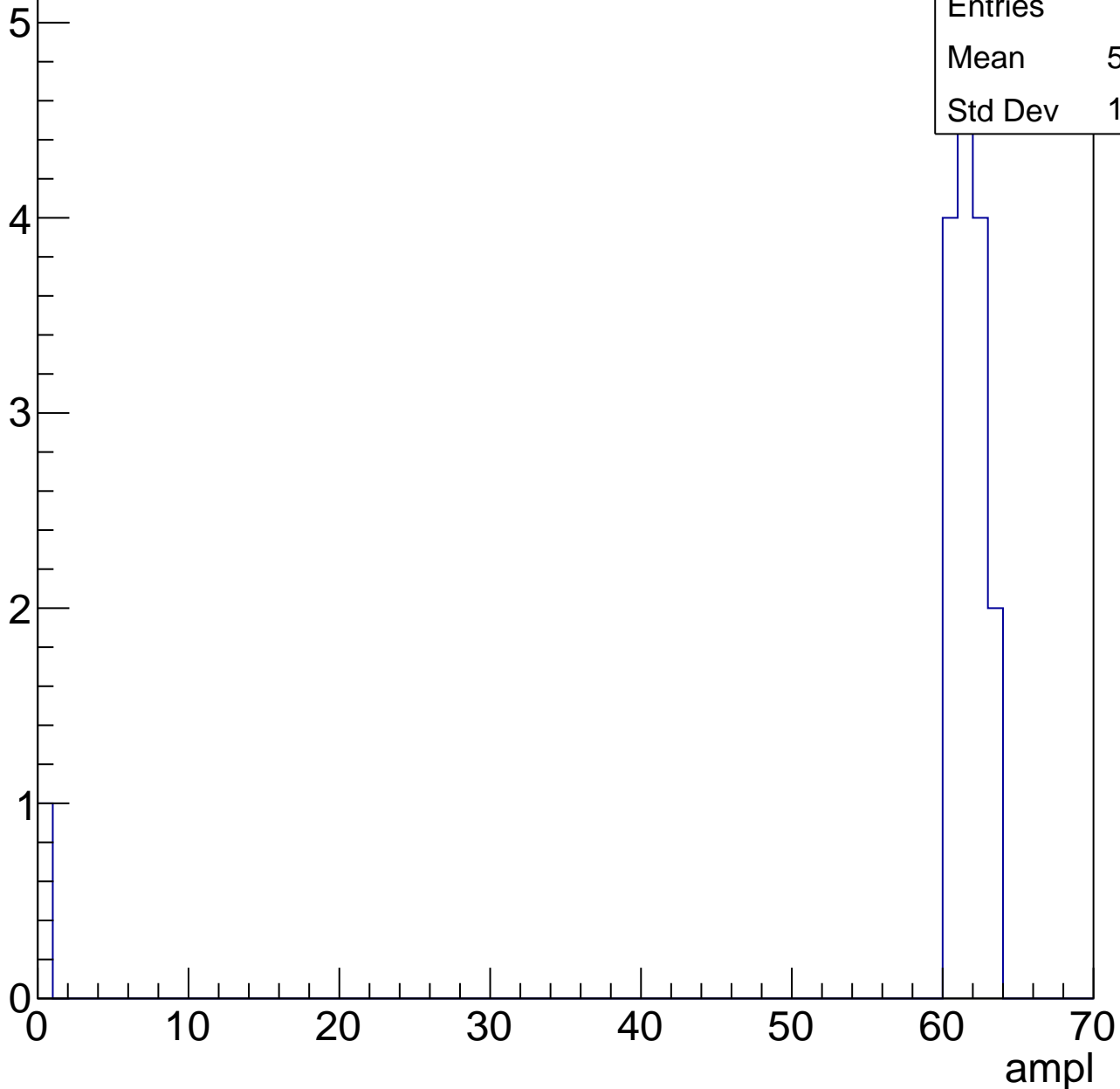


# B1L103S, U22-ch7, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.44
Std Dev	14.86





# B1L103S, U22-ch7, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch8, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	24.21
Std Dev	9.758

**Gaus mean : 28.3150**

**Gaus Width: 3.3428**

Entry

10

8

6

4

2

0

0

10

20

30

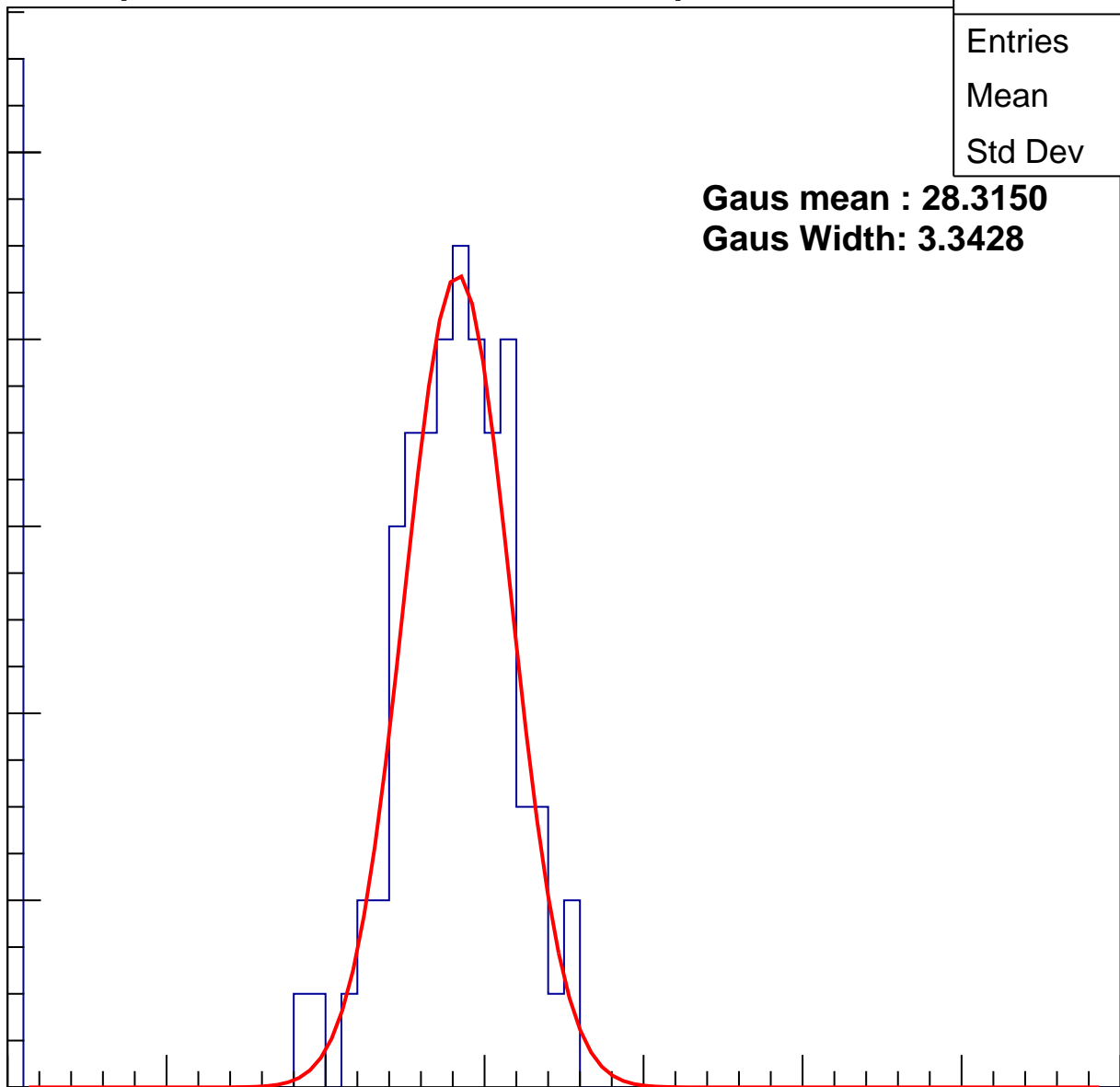
40

50

60

70

ampl



# B1L103S, U22-ch8, adc1

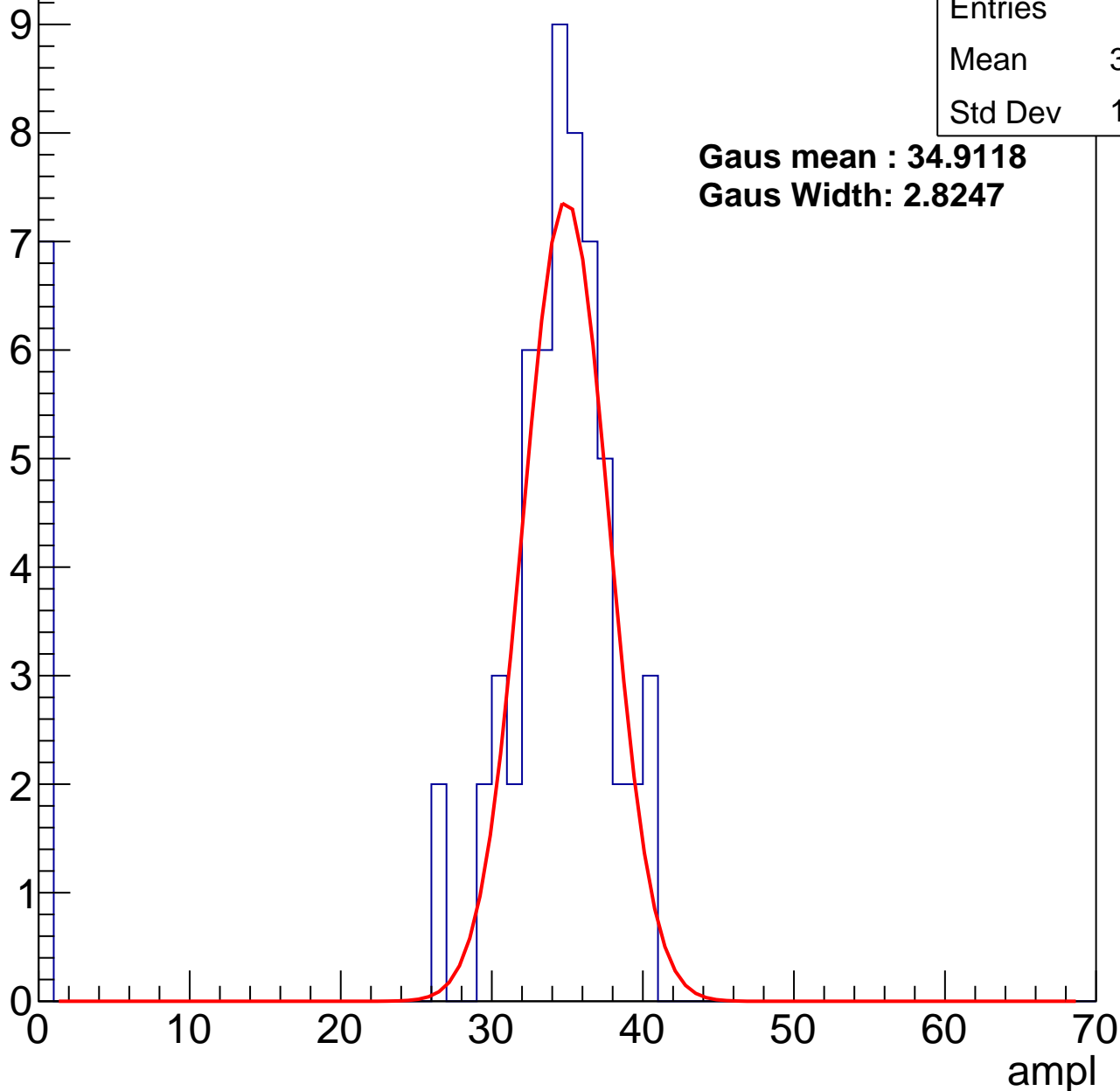
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	30.45
Std Dev	11.06

**Gaus mean : 34.9118**

**Gaus Width: 2.8247**



# B1L103S, U22-ch8, adc2

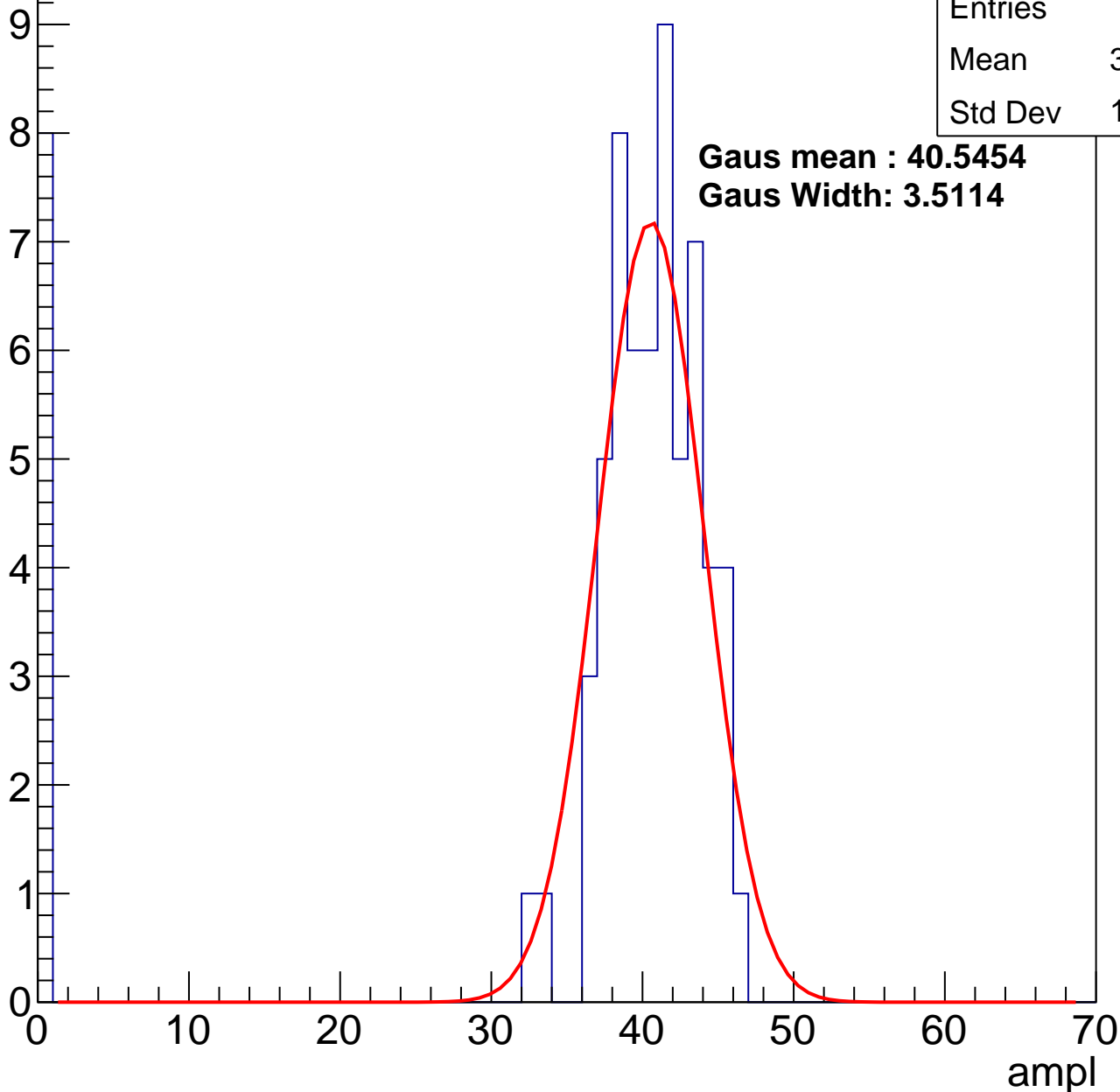
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.56
Std Dev	13.28

**Gaus mean : 40.5454**

**Gaus Width: 3.5114**

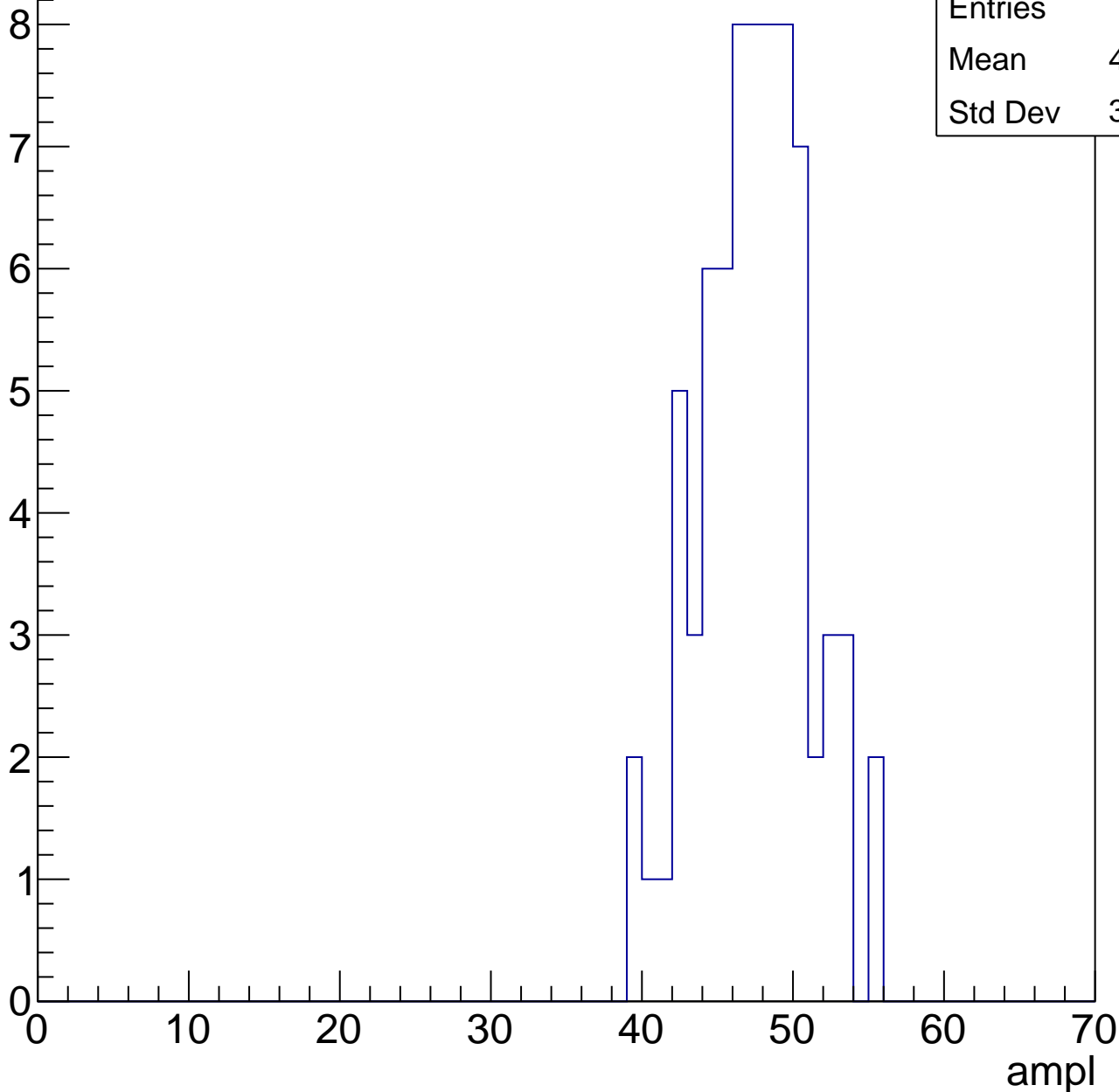


# B1L103S, U22-ch8, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	46.97
Std Dev	3.546

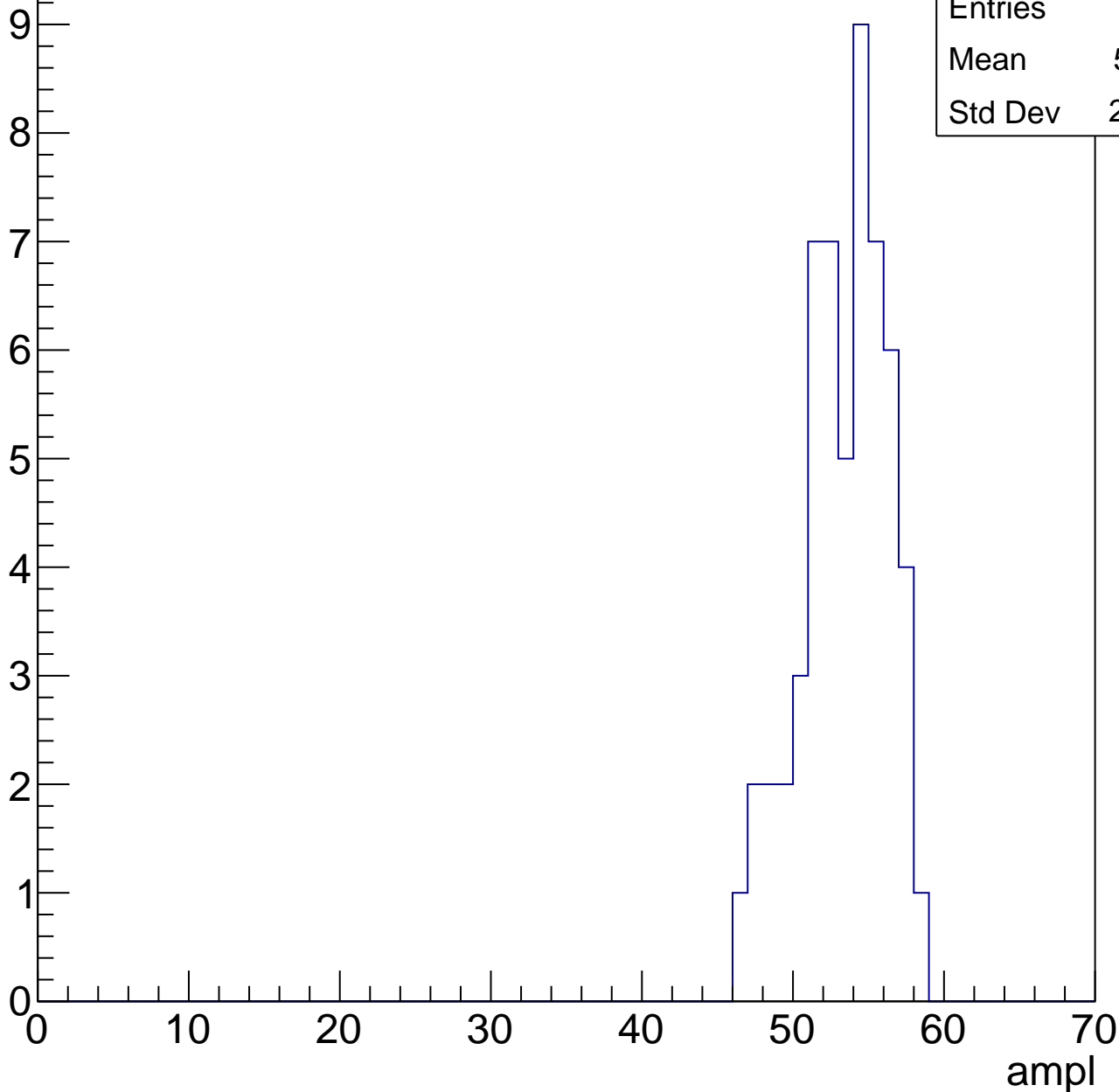


# B1L103S, U22-ch8, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.91
Std Dev	2.818



# B1L103S, U22-ch8, adc5

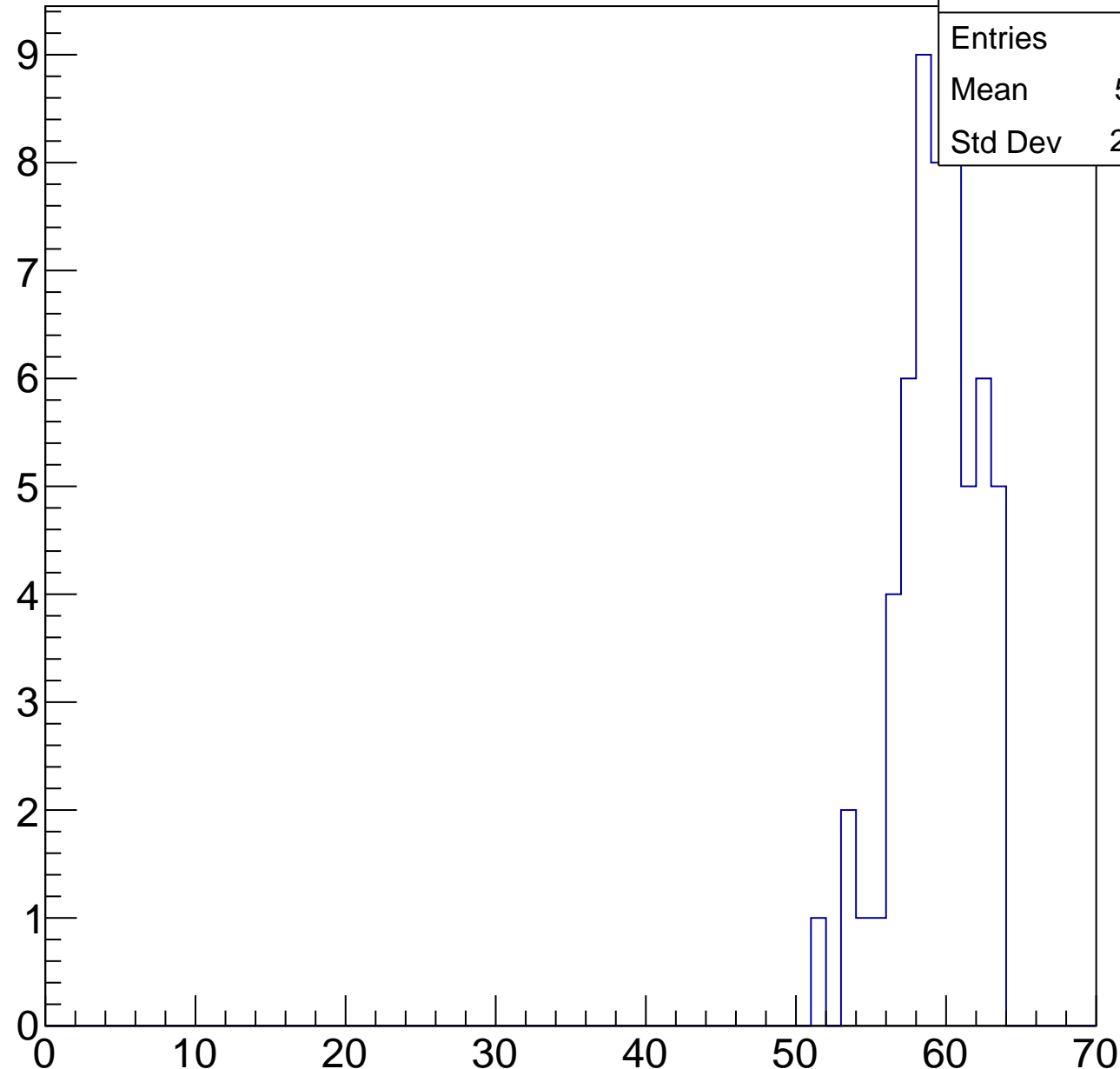
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	57
Mean	58.91
Std Dev	2.677

ampl

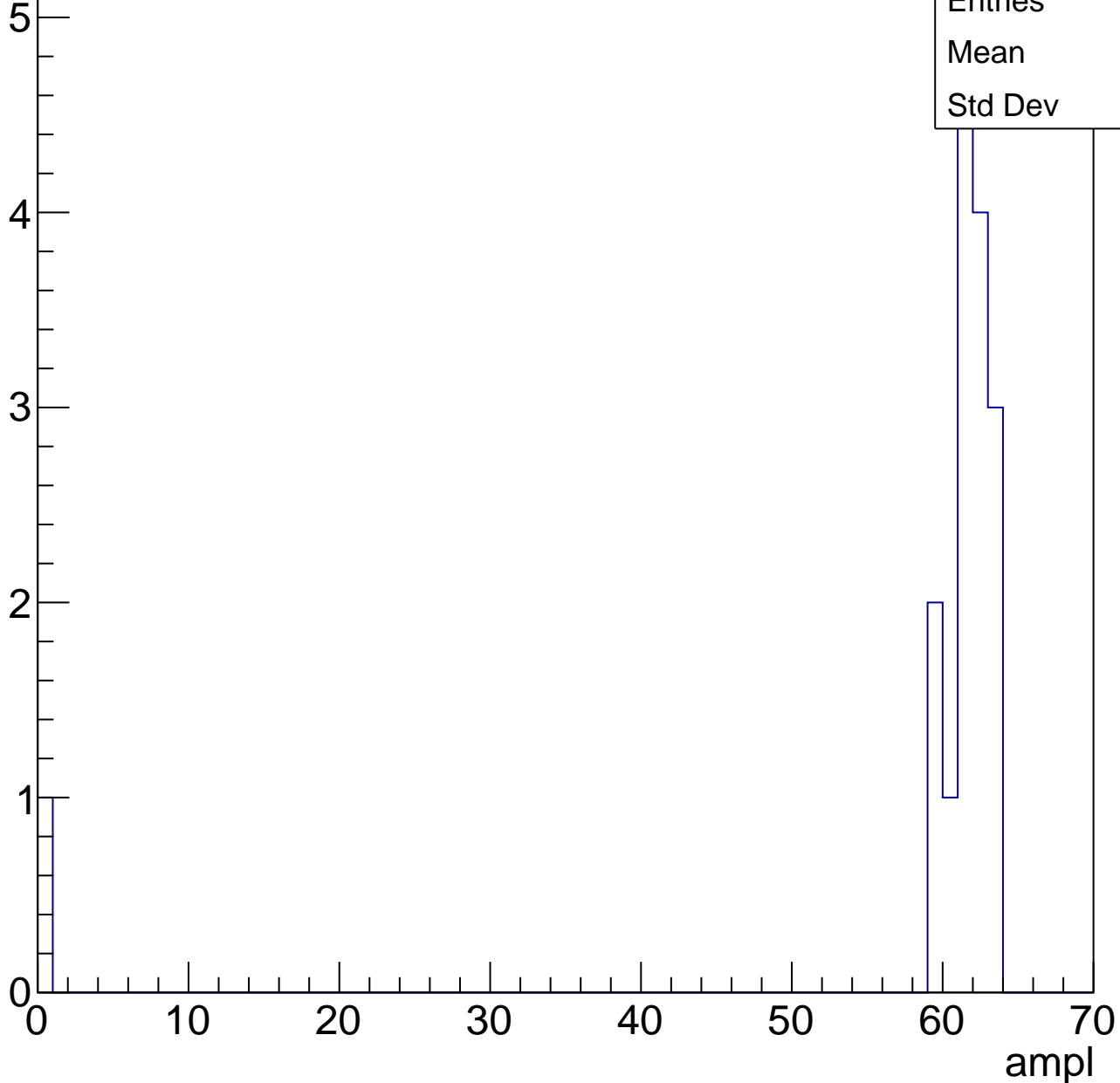


# B1L103S, U22-ch8, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.5
Std Dev	14.9

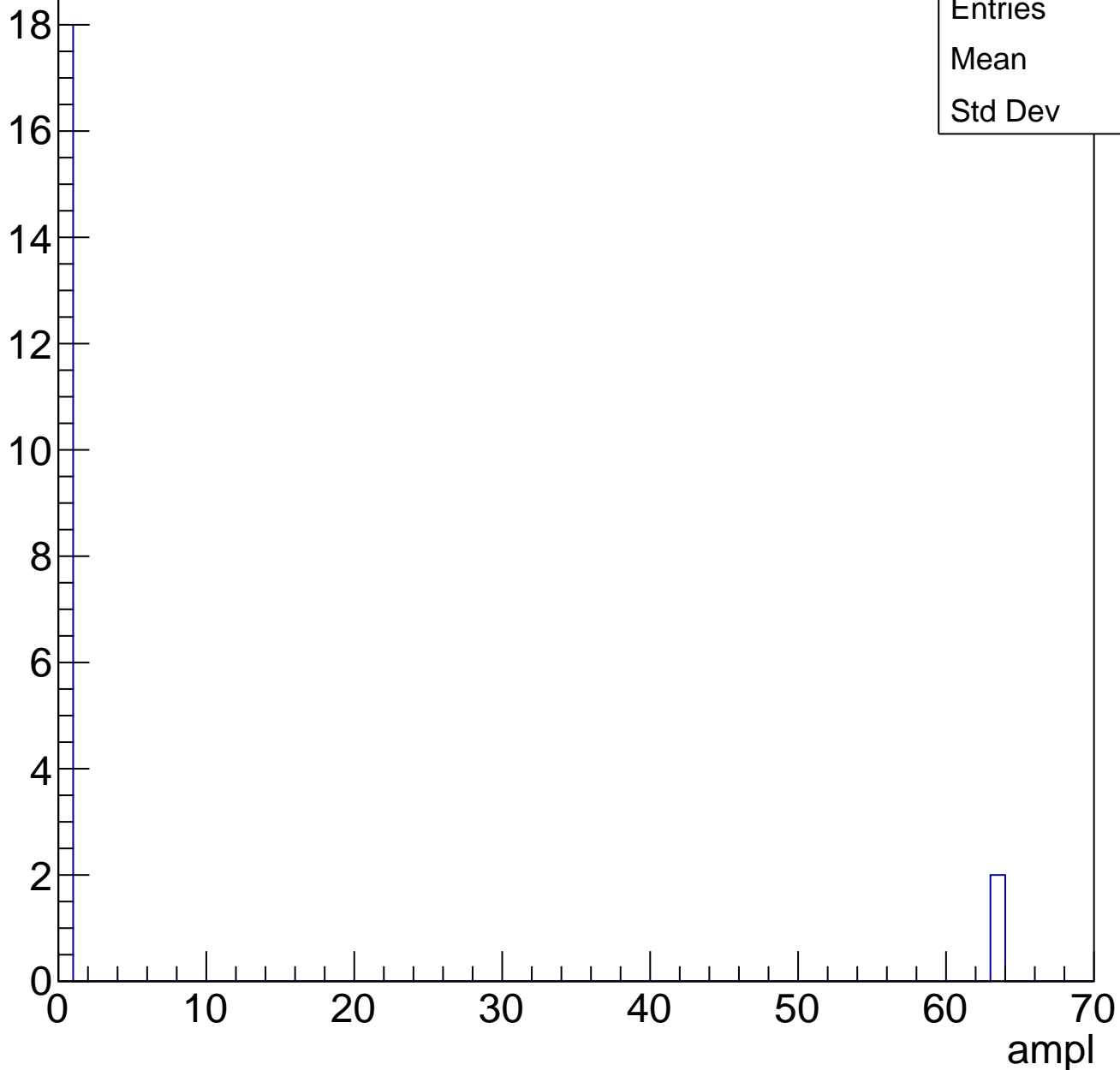




# B1L103S, U22-ch8, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch9, adc0

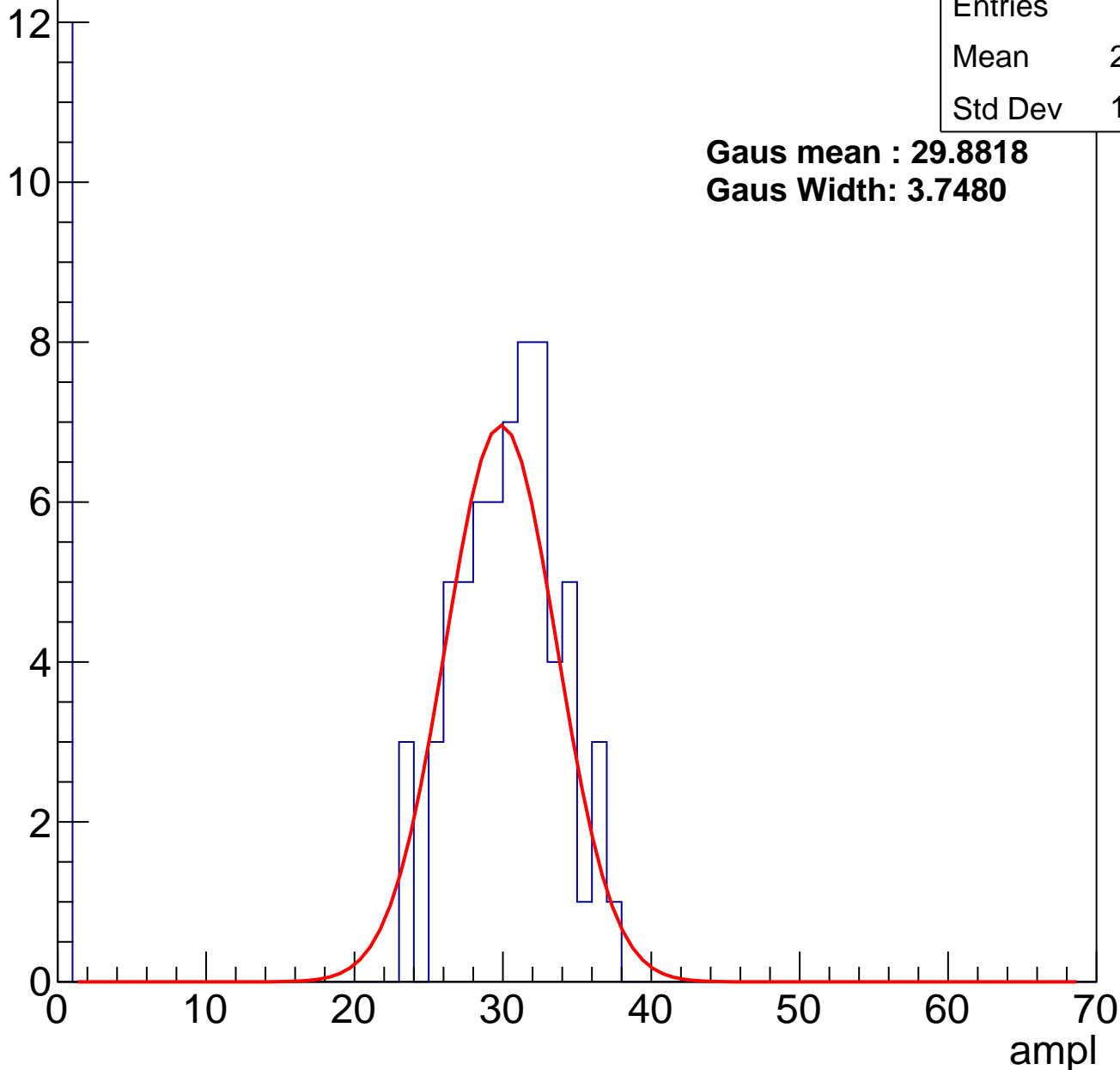
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	25.29
Std Dev	11.28

**Gaus mean : 29.8818**

**Gaus Width: 3.7480**

Entry



# B1L103S, U22-ch9, adc1

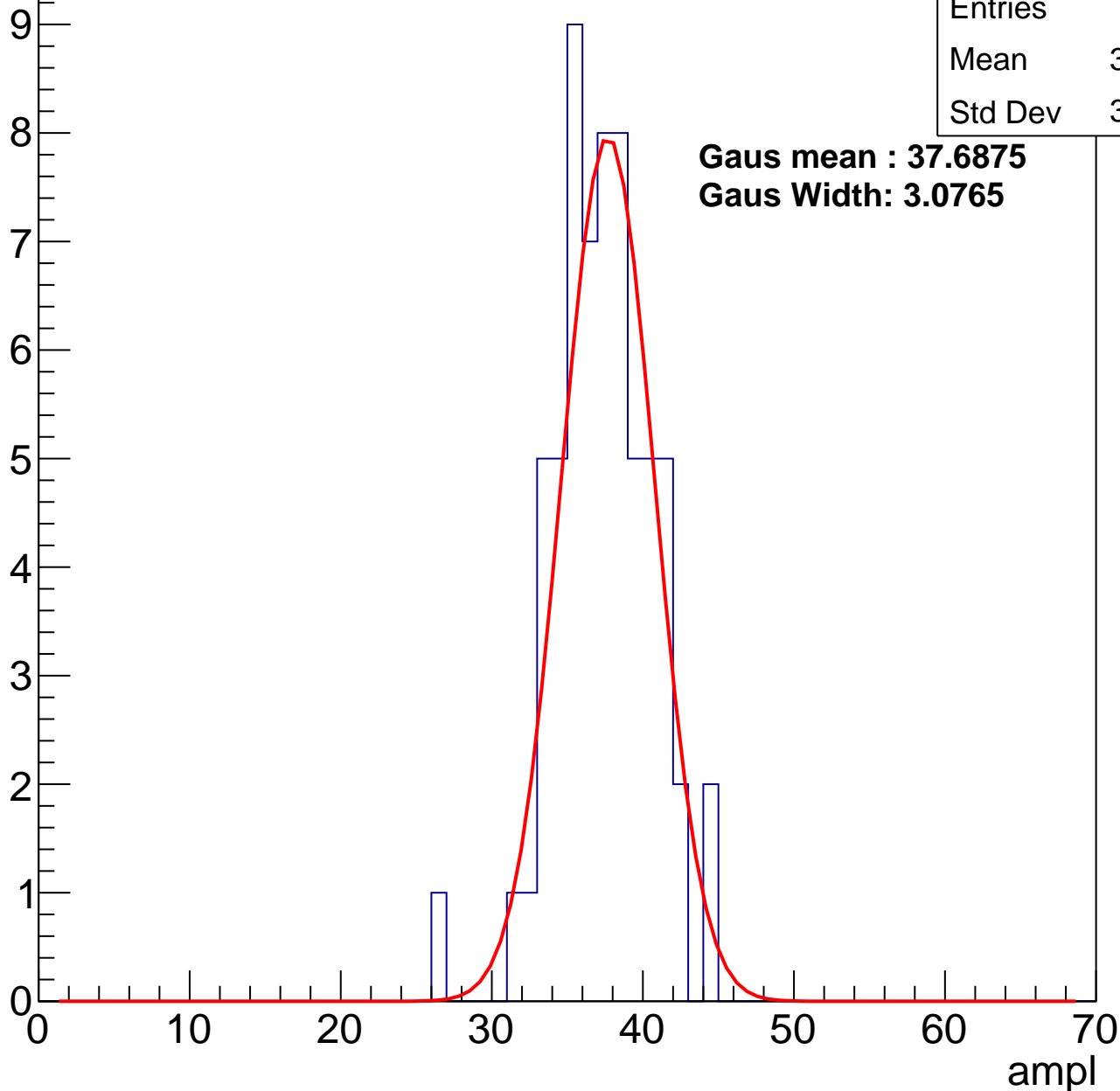
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.92
Std Dev	3.188

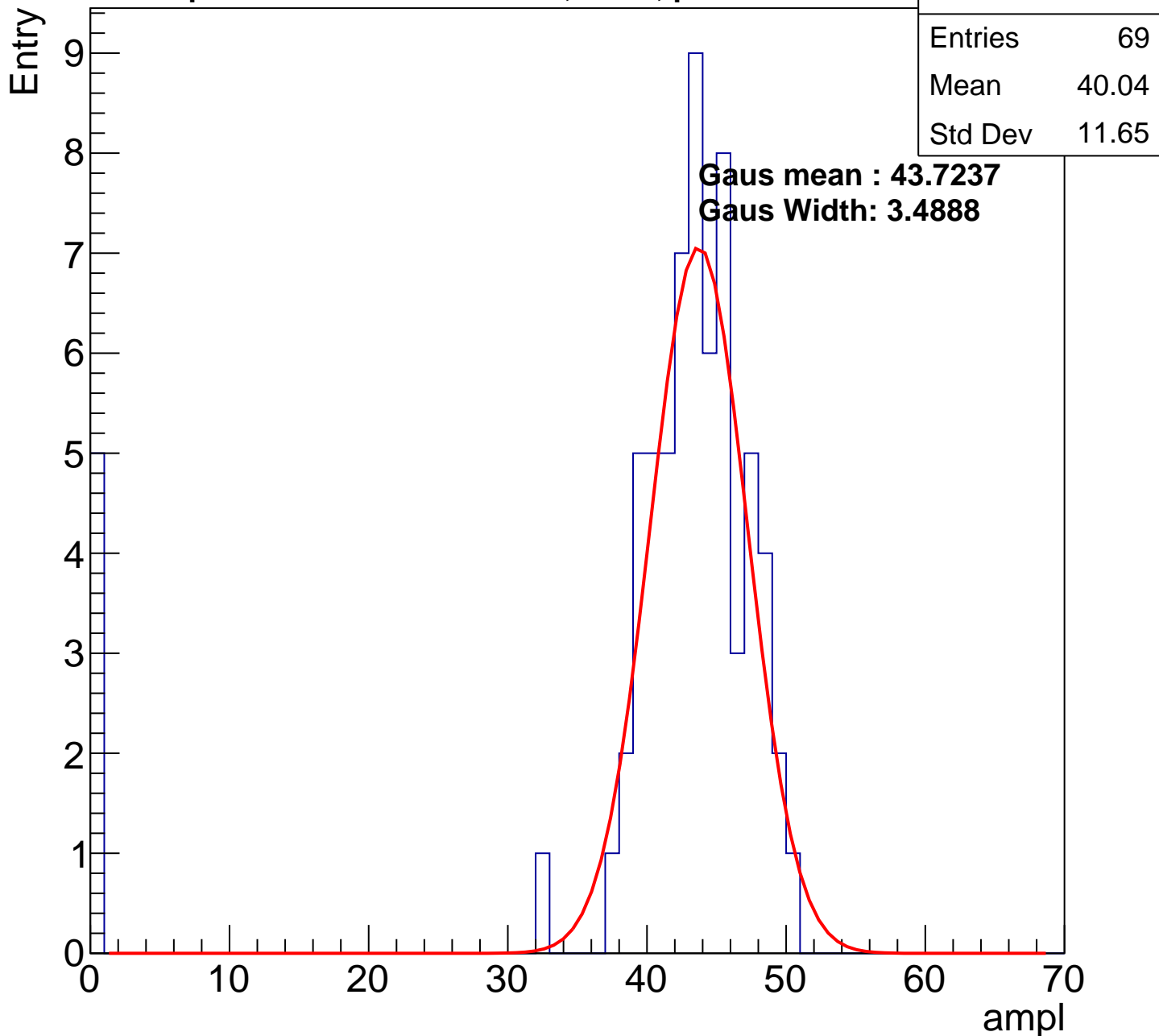
**Gaus mean : 37.6875**

**Gaus Width: 3.0765**



# B1L103S, U22-ch9, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

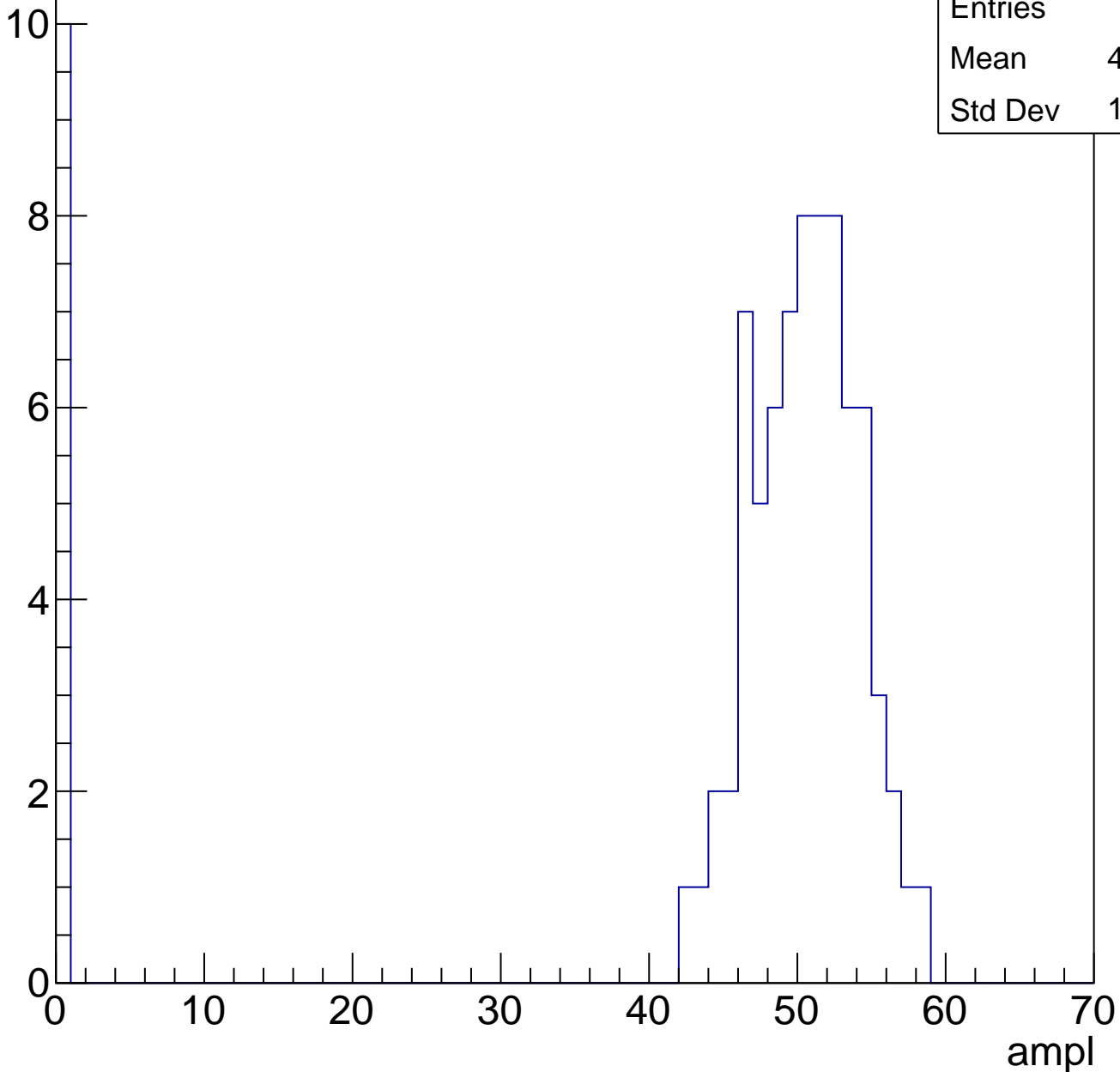


# B1L103S, U22-ch9, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	44.15
Std Dev	16.55

Entry

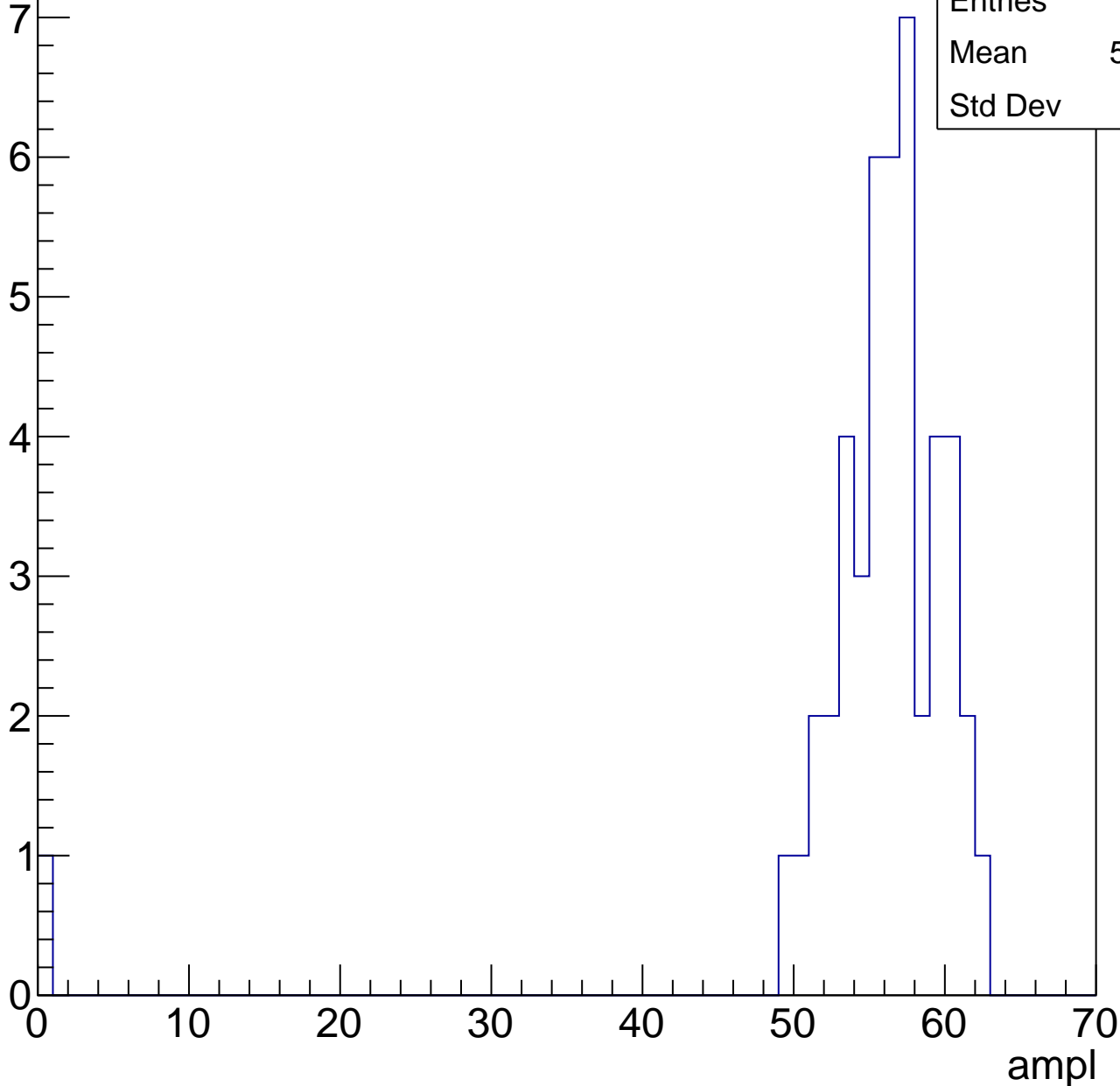


# B1L103S, U22-ch9, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	54.78
Std Dev	8.71

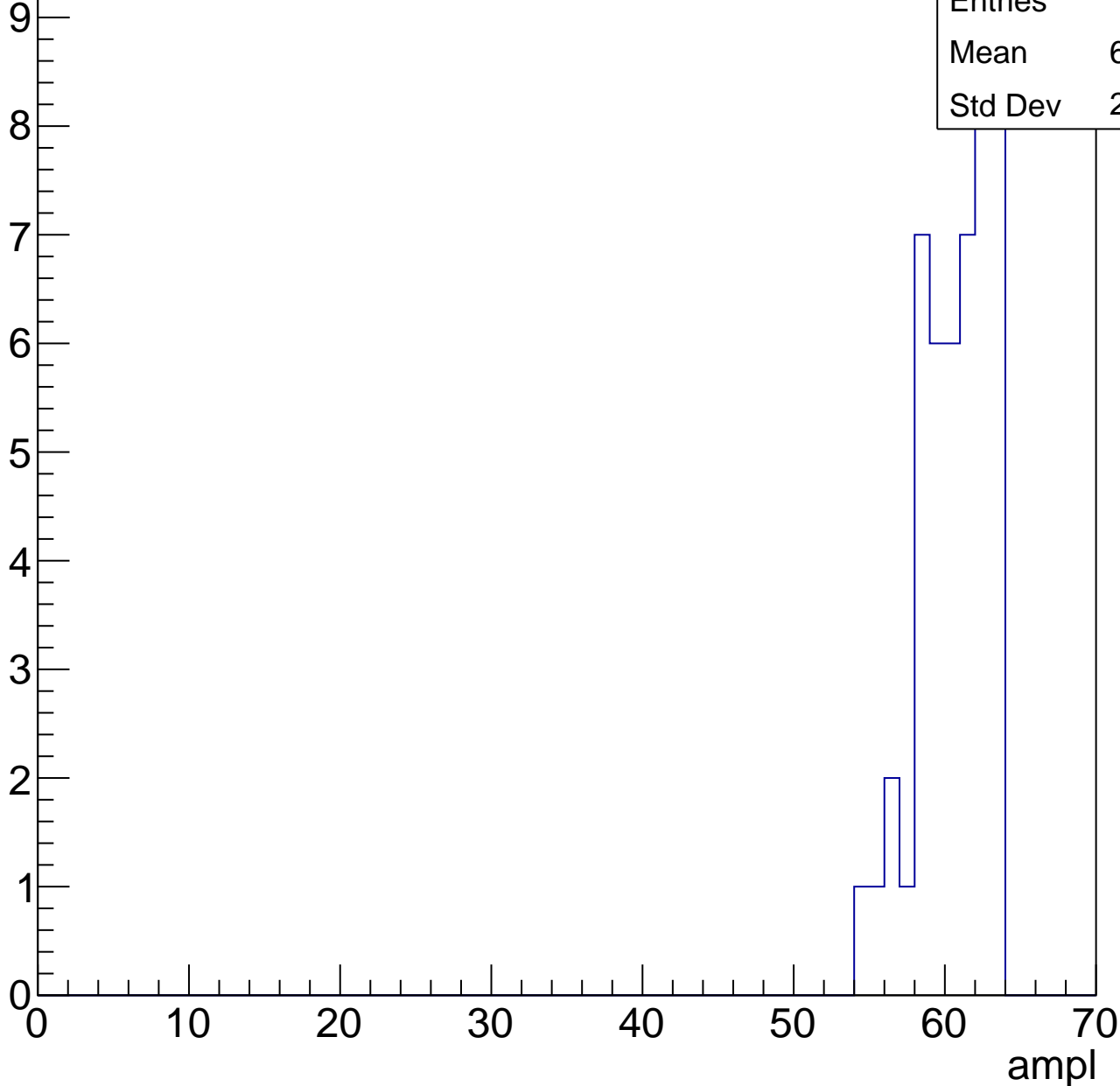


# B1L103S, U22-ch9, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

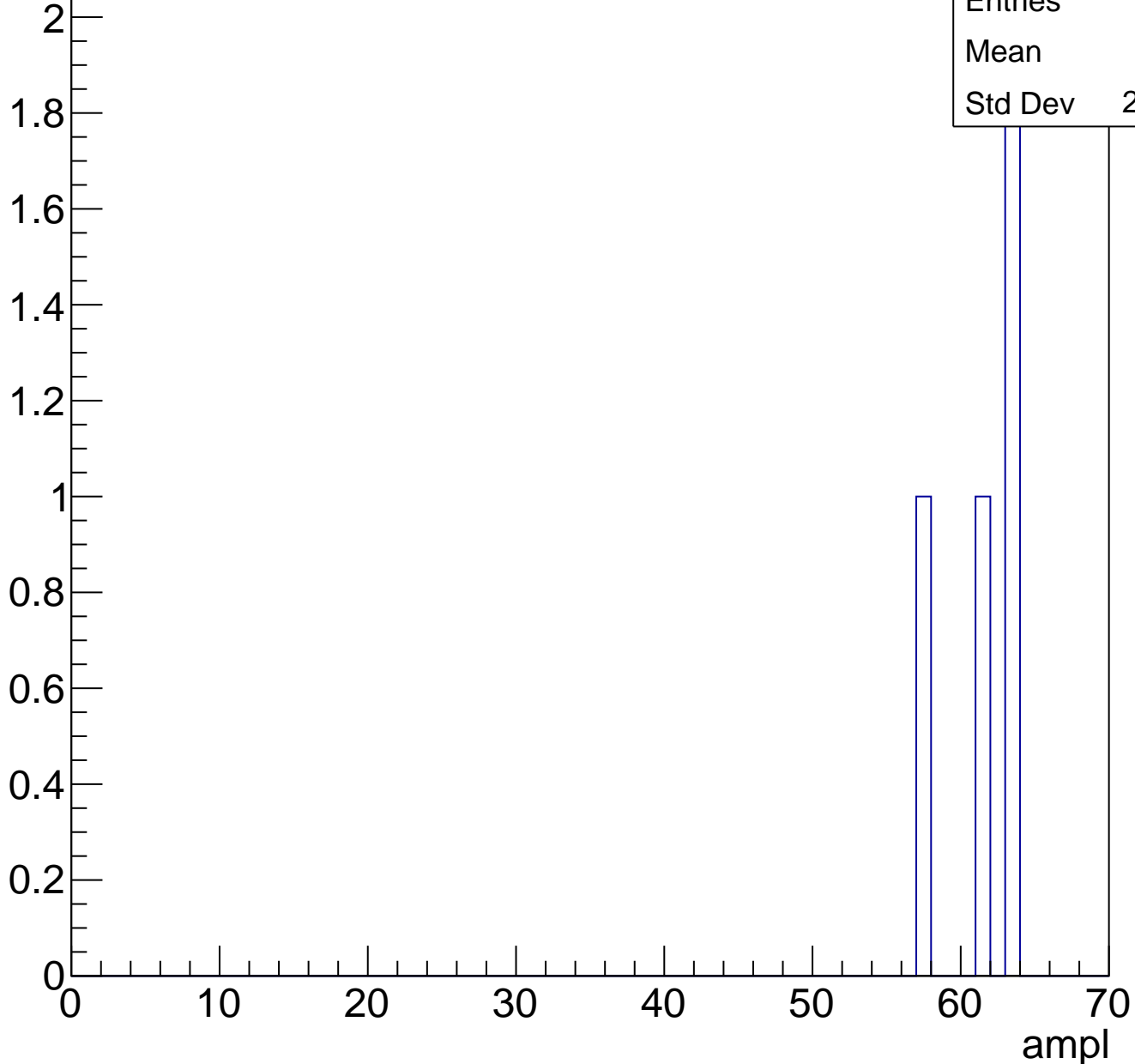
Entries	48
Mean	60.15
Std Dev	2.282



# B1L103S, U22-ch9, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch9, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch10, adc0

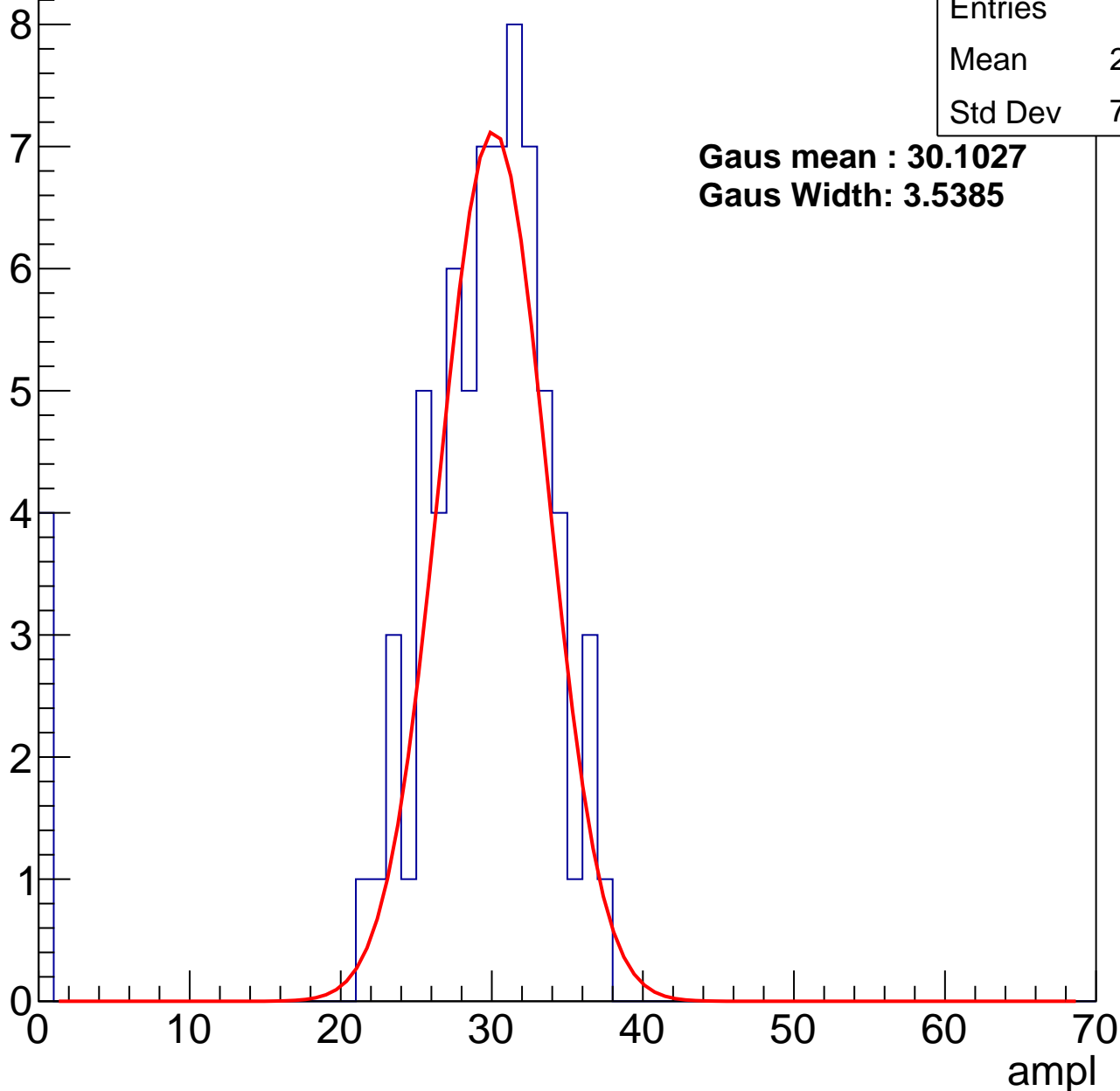
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	27.85
Std Dev	7.574

**Gaus mean : 30.1027**

**Gaus Width: 3.5385**



# B1L103S, U22-ch10, adc1

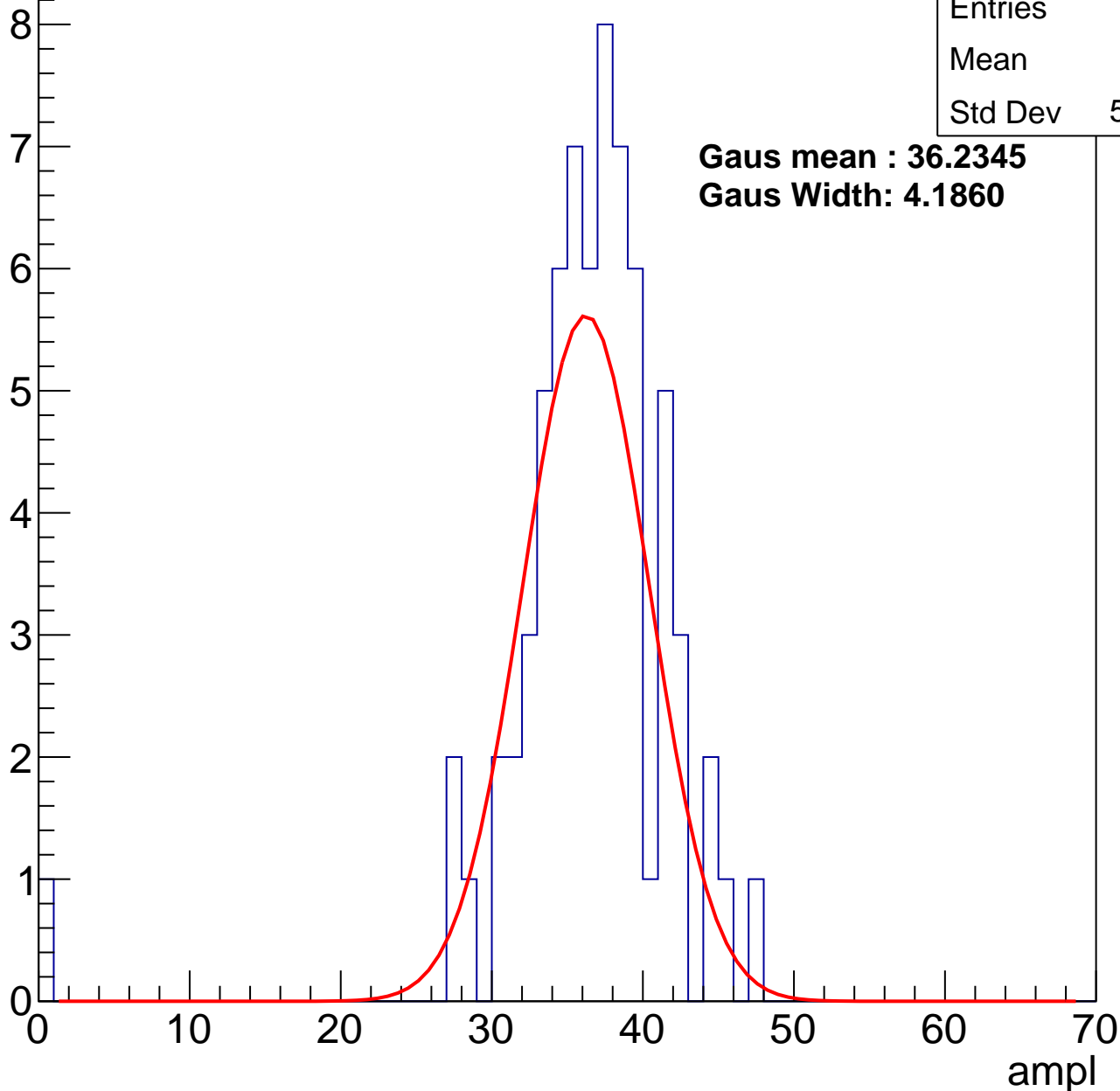
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.9
Std Dev	5.942

**Gaus mean : 36.2345**

**Gaus Width: 4.1860**



# B1L103S, U22-ch10, adc2

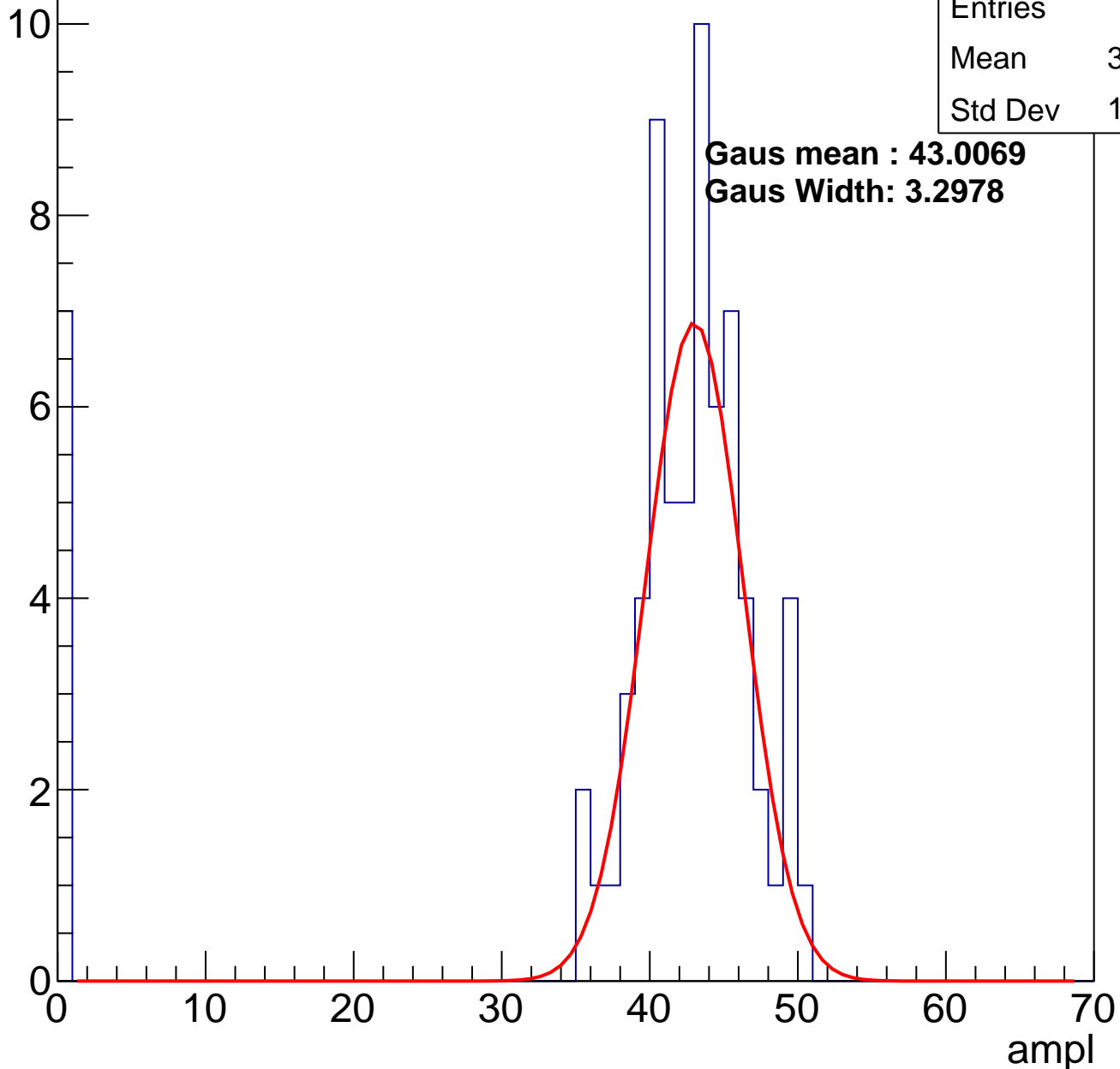
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	72
Mean	38.46
Std Dev	13.04

**Gaus mean : 43.0069**

**Gaus Width: 3.2978**

Entry

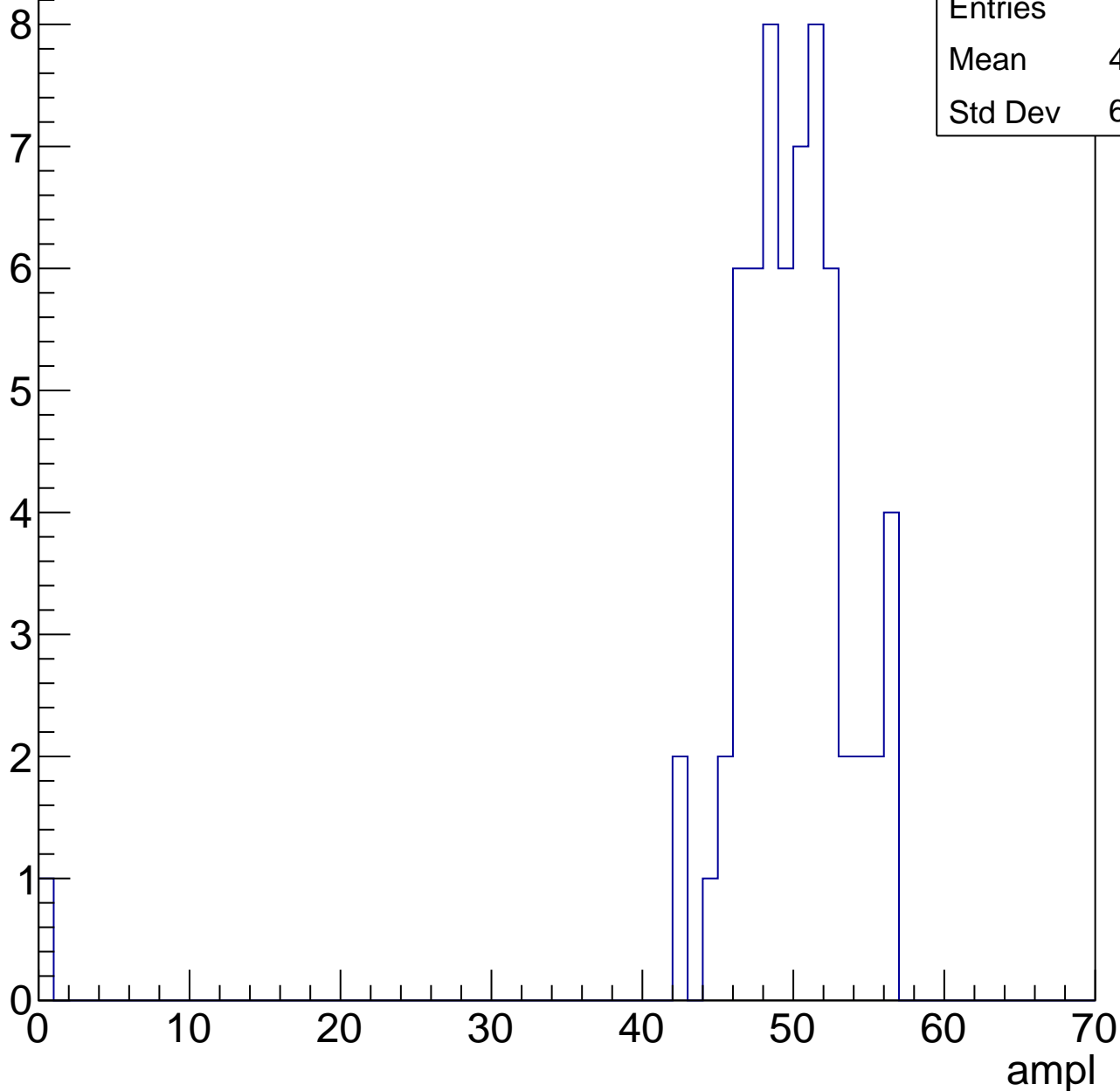


# B1L103S, U22-ch10, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.76
Std Dev	6.994

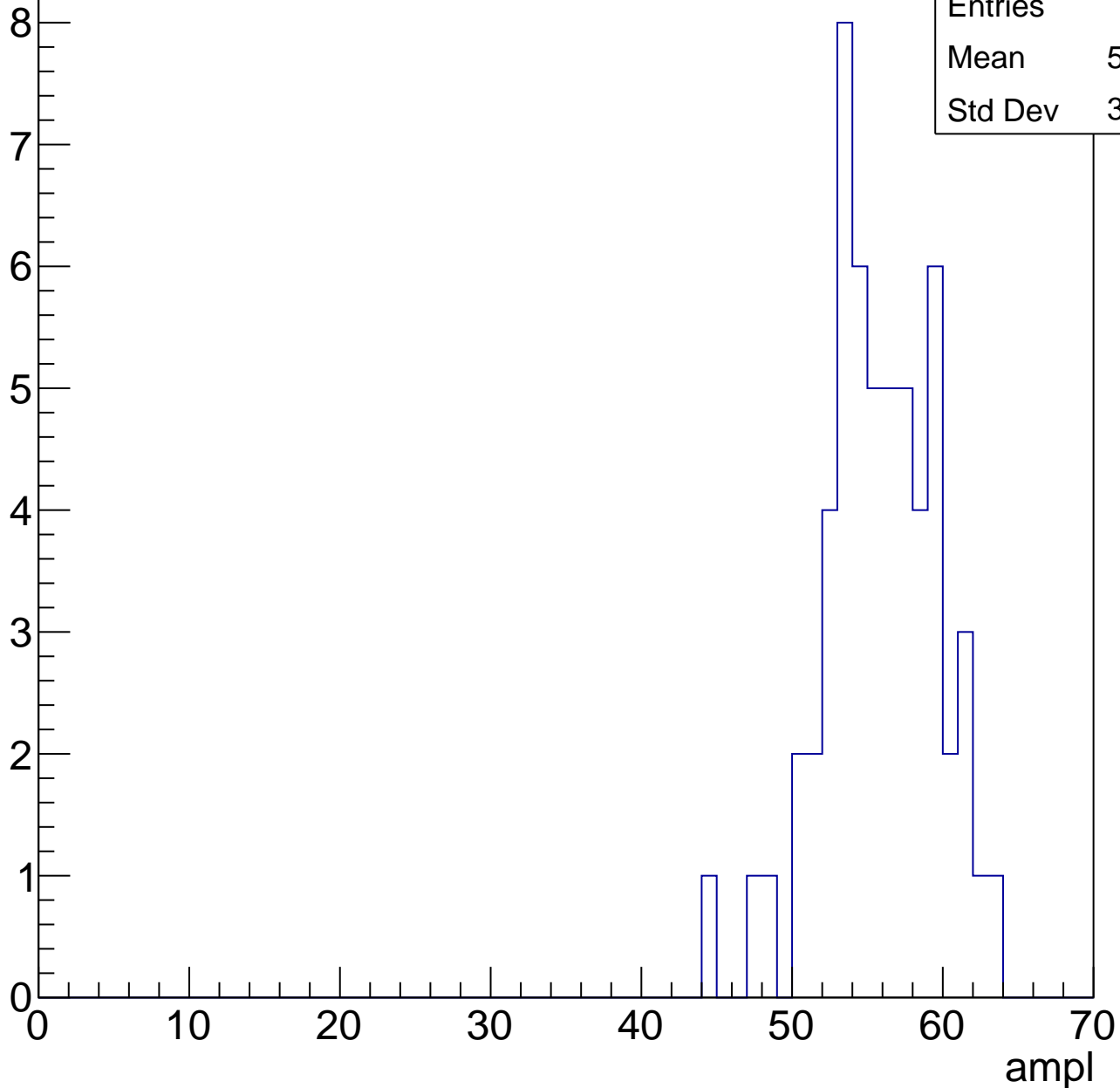


# B1L103S, U22-ch10, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.28
Std Dev	3.778

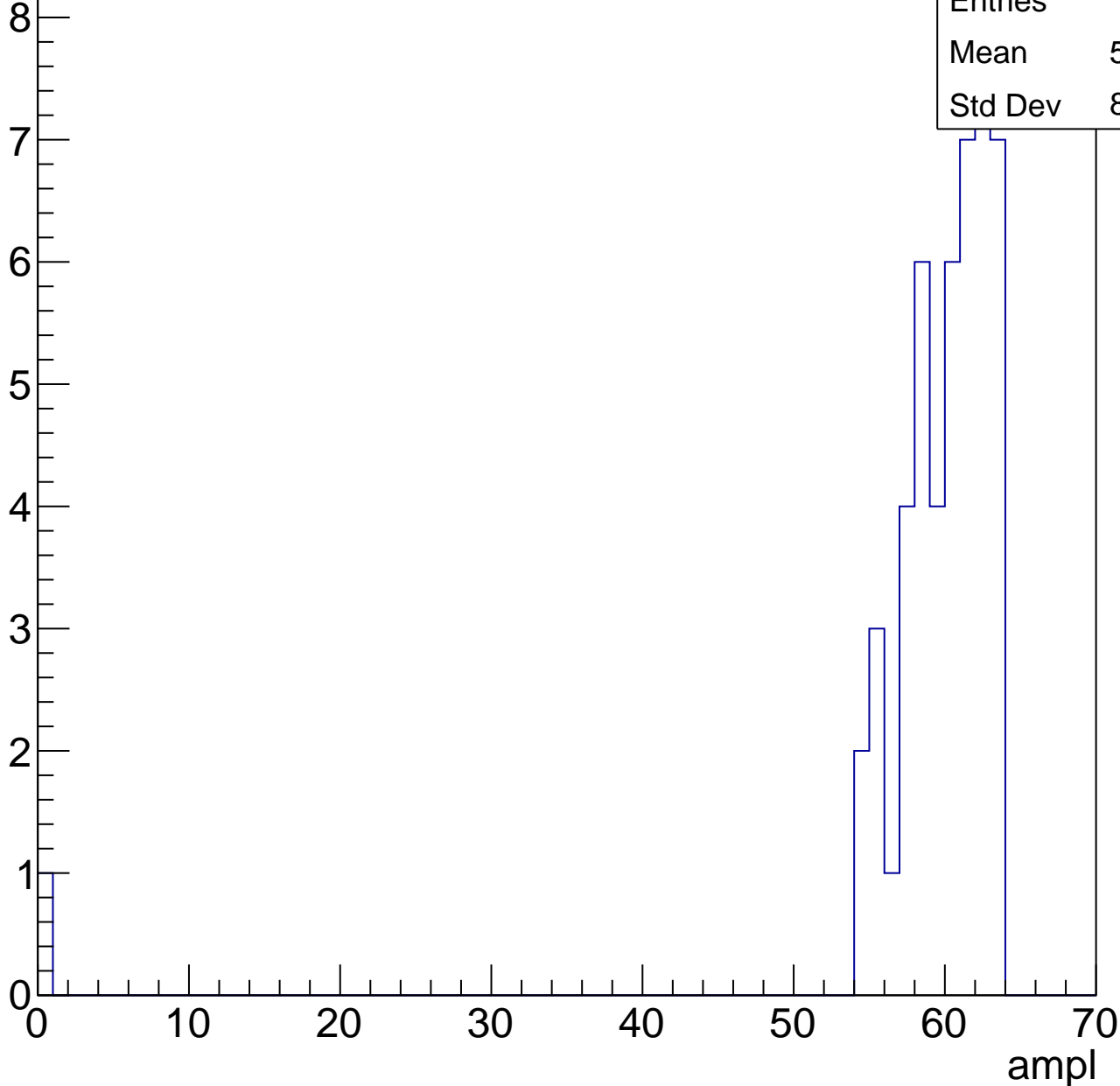


# B1L103S, U22-ch10, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

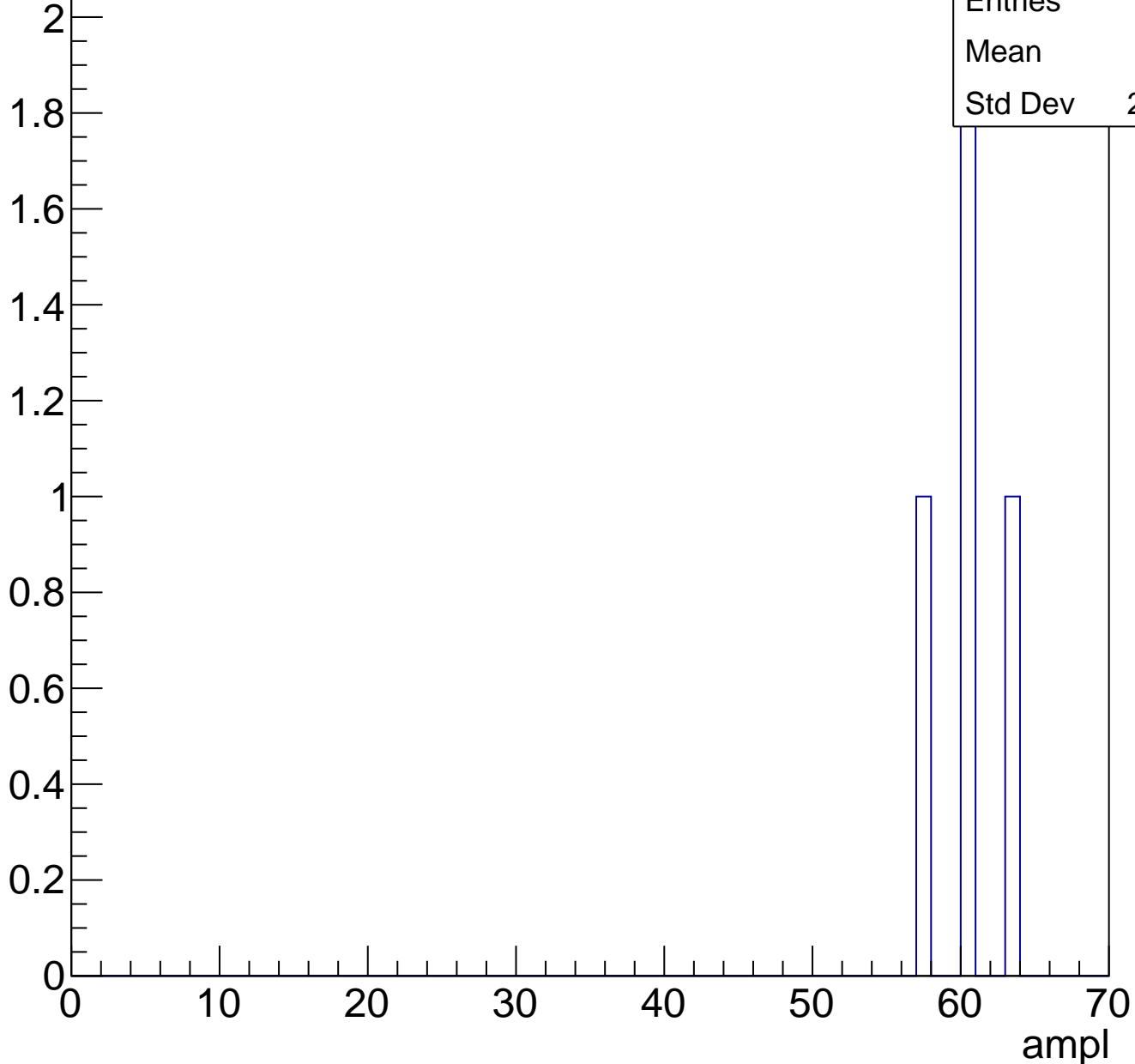
Entries	49
Mean	58.47
Std Dev	8.823



# B1L103S, U22-ch10, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch10, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35

Entry



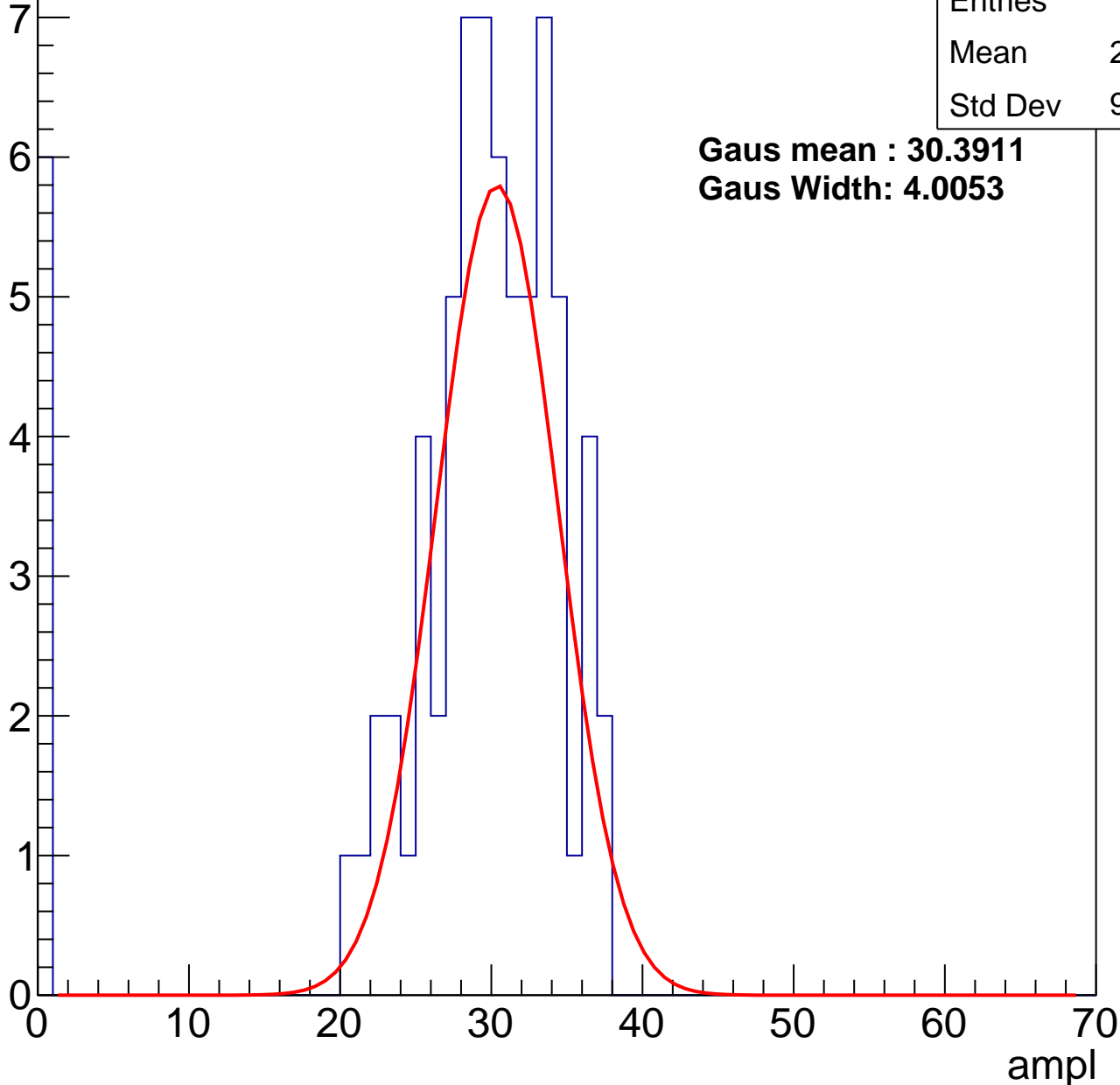
# B1L103S, U22-ch11, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	27.26
Std Dev	9.022

**Gaus mean : 30.3911**  
**Gaus Width: 4.0053**



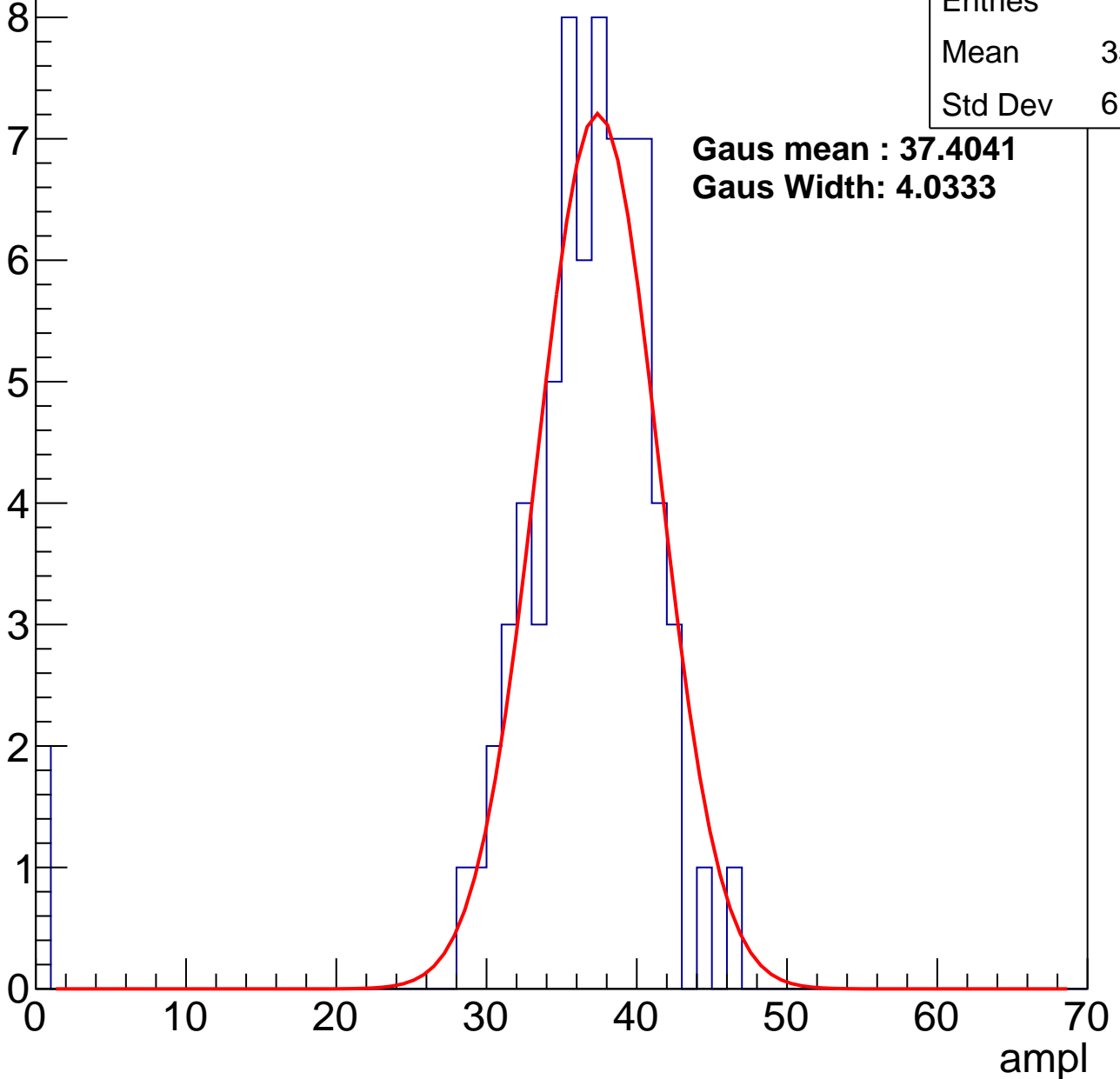
# B1L103S, U22-ch11, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.59
Std Dev	6.965

**Gaus mean : 37.4041**  
**Gaus Width: 4.0333**



# B1L103S, U22-ch11, adc2

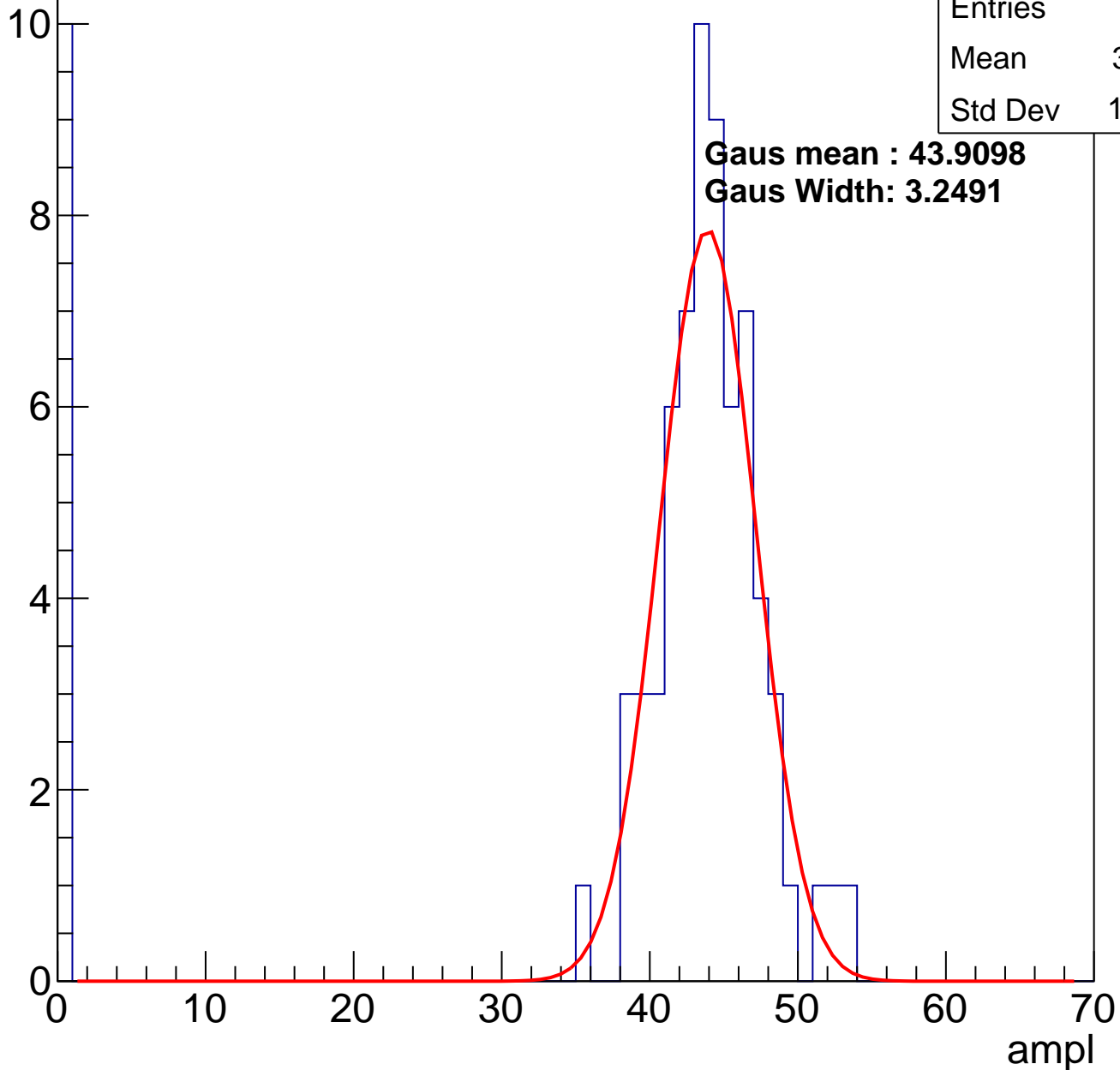
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	37.91
Std Dev	15.08

**Gaus mean : 43.9098**

**Gaus Width: 3.2491**

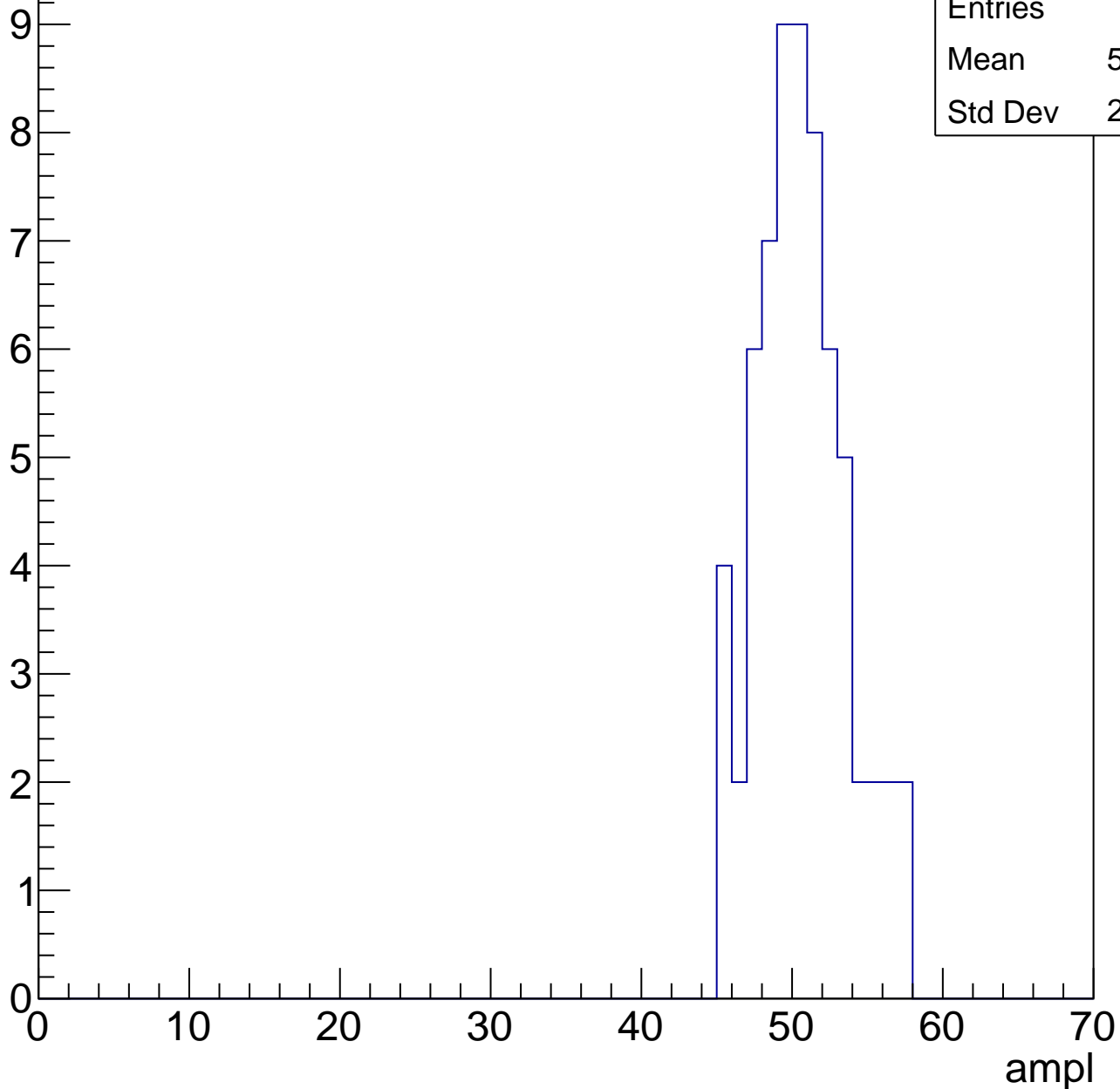
Entry



# B1L103S, U22-ch11, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



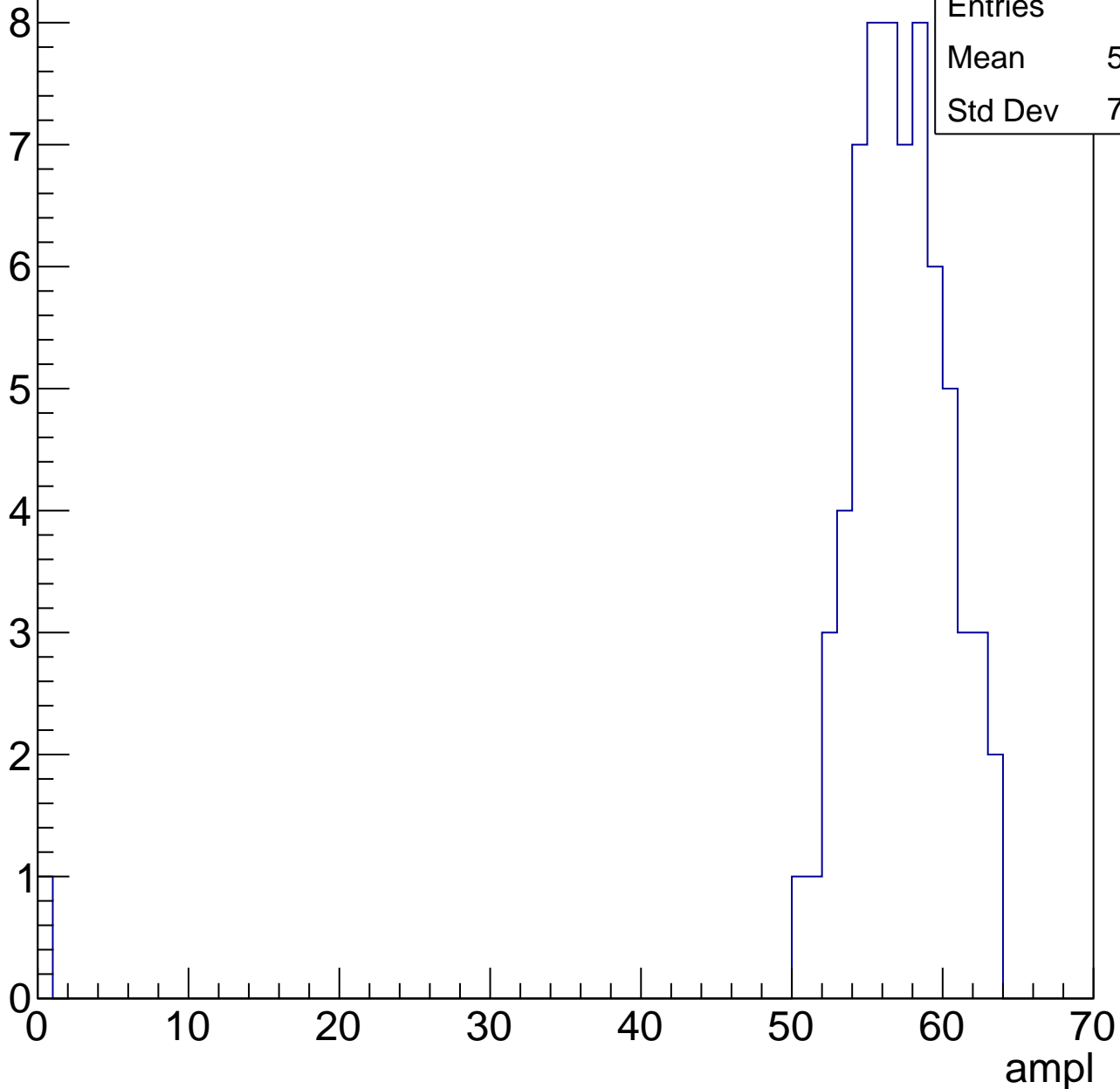
Entries	64
Mean	50.16
Std Dev	2.933

# B1L103S, U22-ch11, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	55.93
Std Dev	7.502

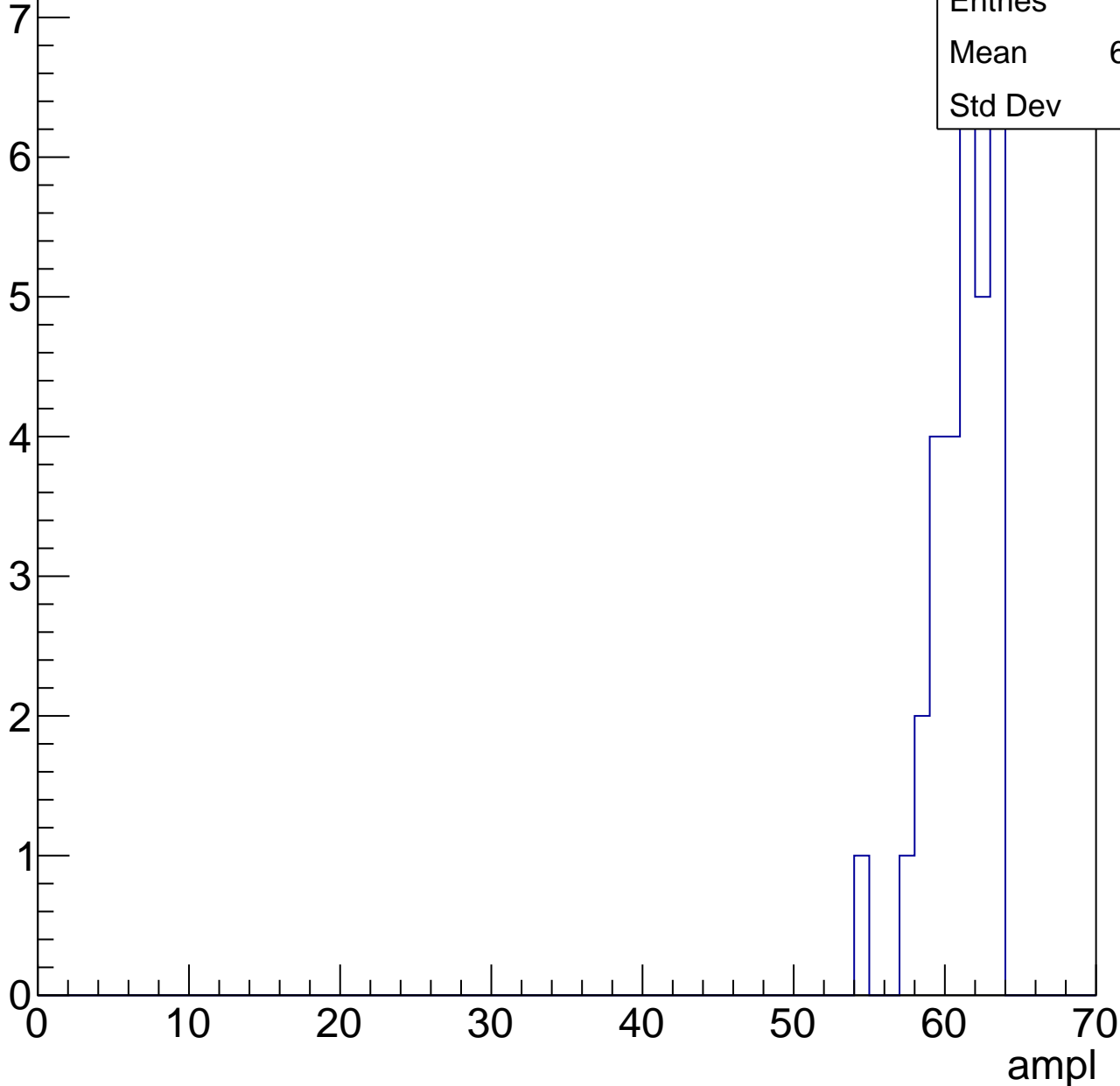


# B1L103S, U22-ch11, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

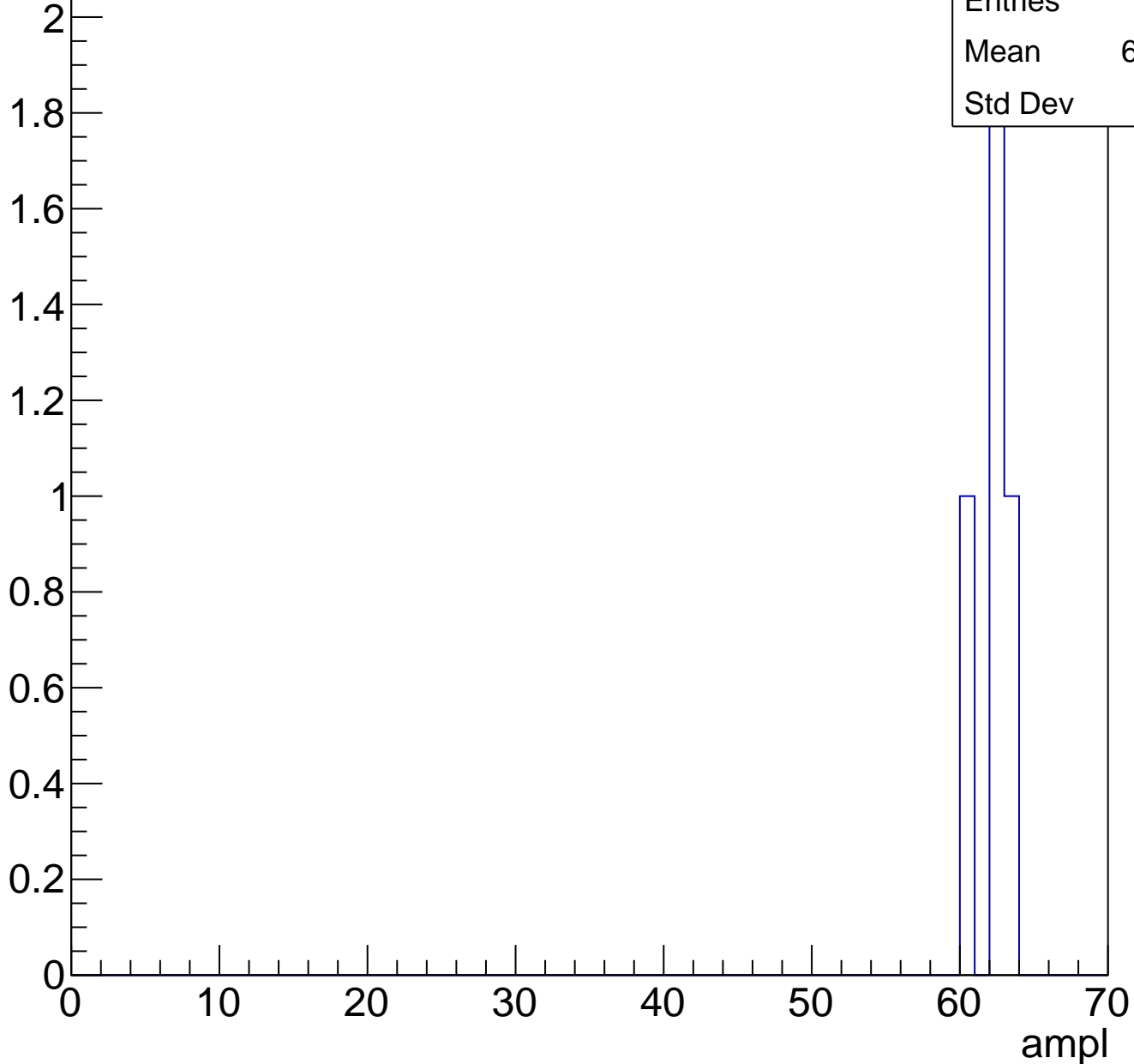
Entries	31
Mean	60.68
Std Dev	2.07



# B1L103S, U22-ch11, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch11, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

# B1L103S, U22-ch12, adc0

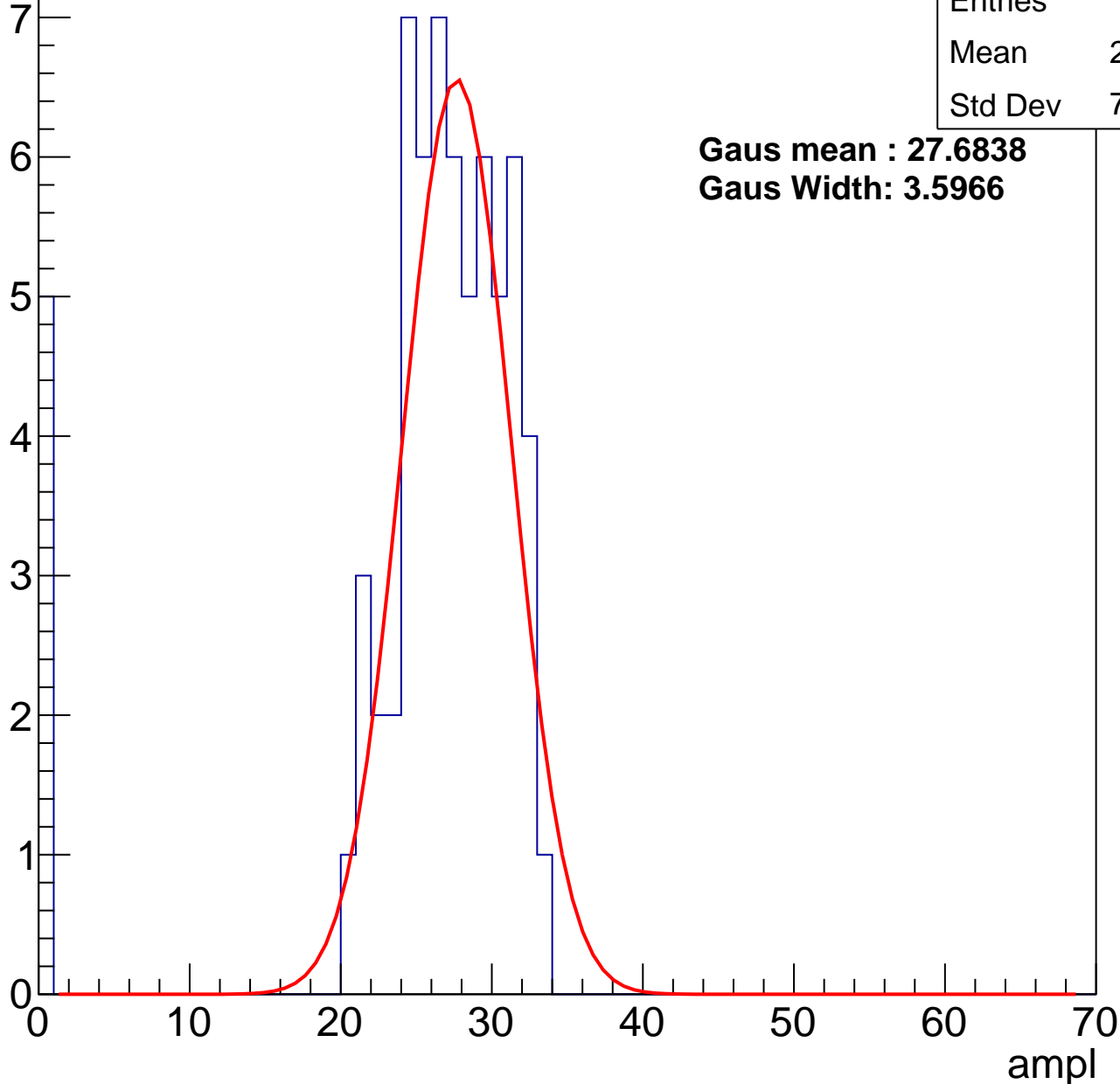
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	24.94
Std Dev	7.789

**Gaus mean : 27.6838**

**Gaus Width: 3.5966**



# B1L103S, U22-ch12, adc1

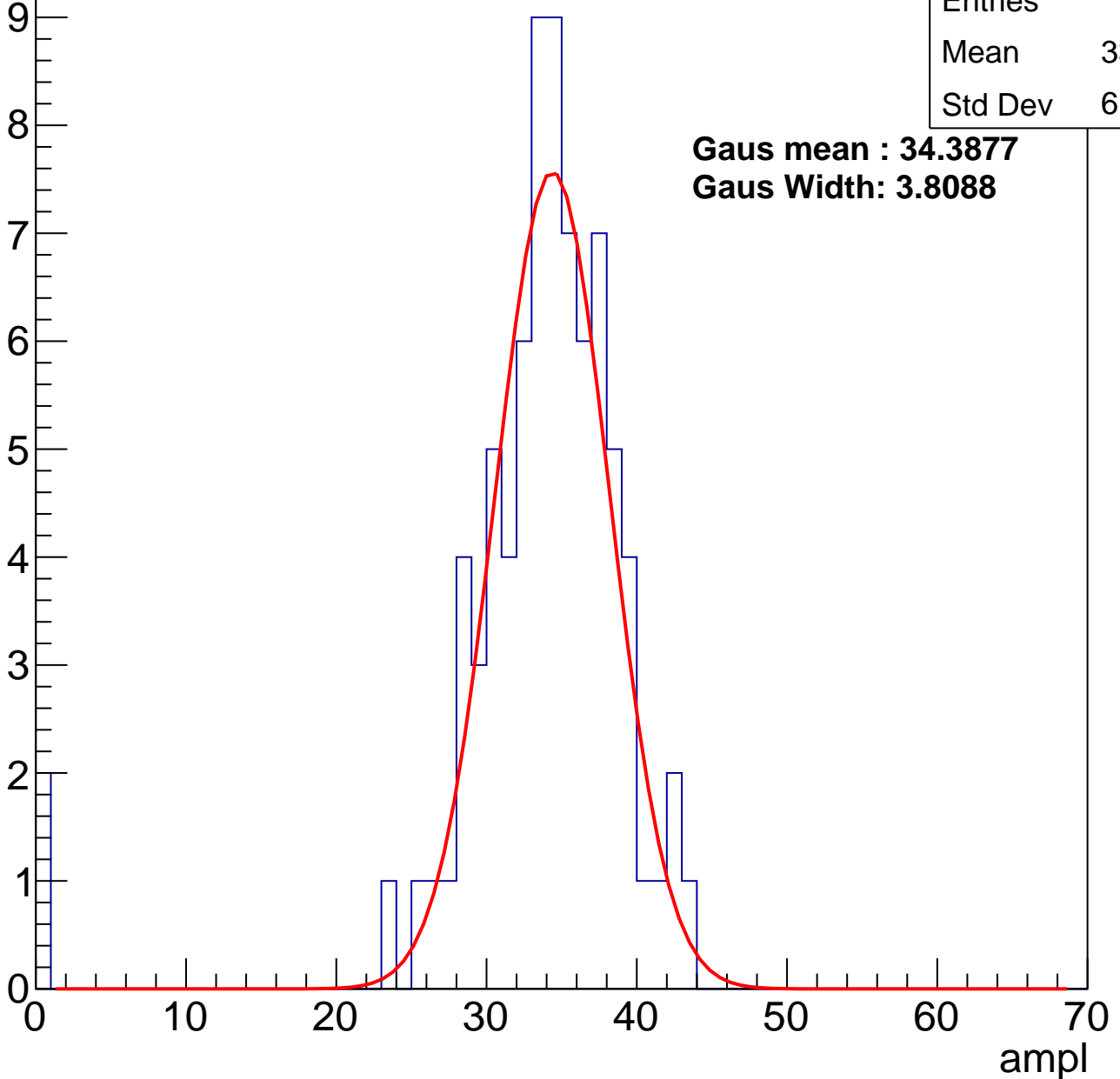
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	80
Mean	33.04
Std Dev	6.604

**Gaus mean : 34.3877**

**Gaus Width: 3.8088**



# B1L103S, U22-ch12, adc2

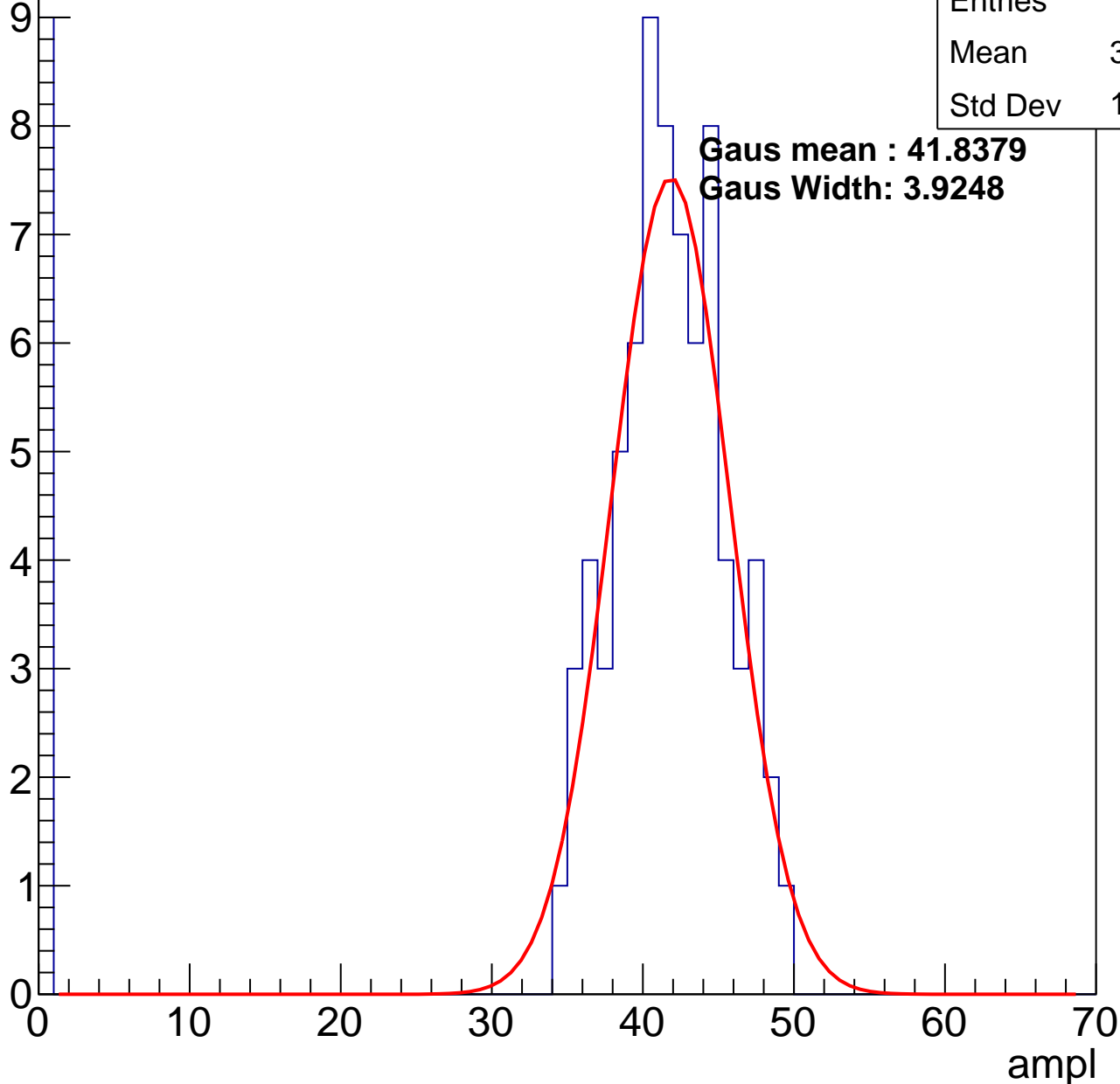
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	83
Mean	36.88
Std Dev	13.29

**Gaus mean : 41.8379**

**Gaus Width: 3.9248**

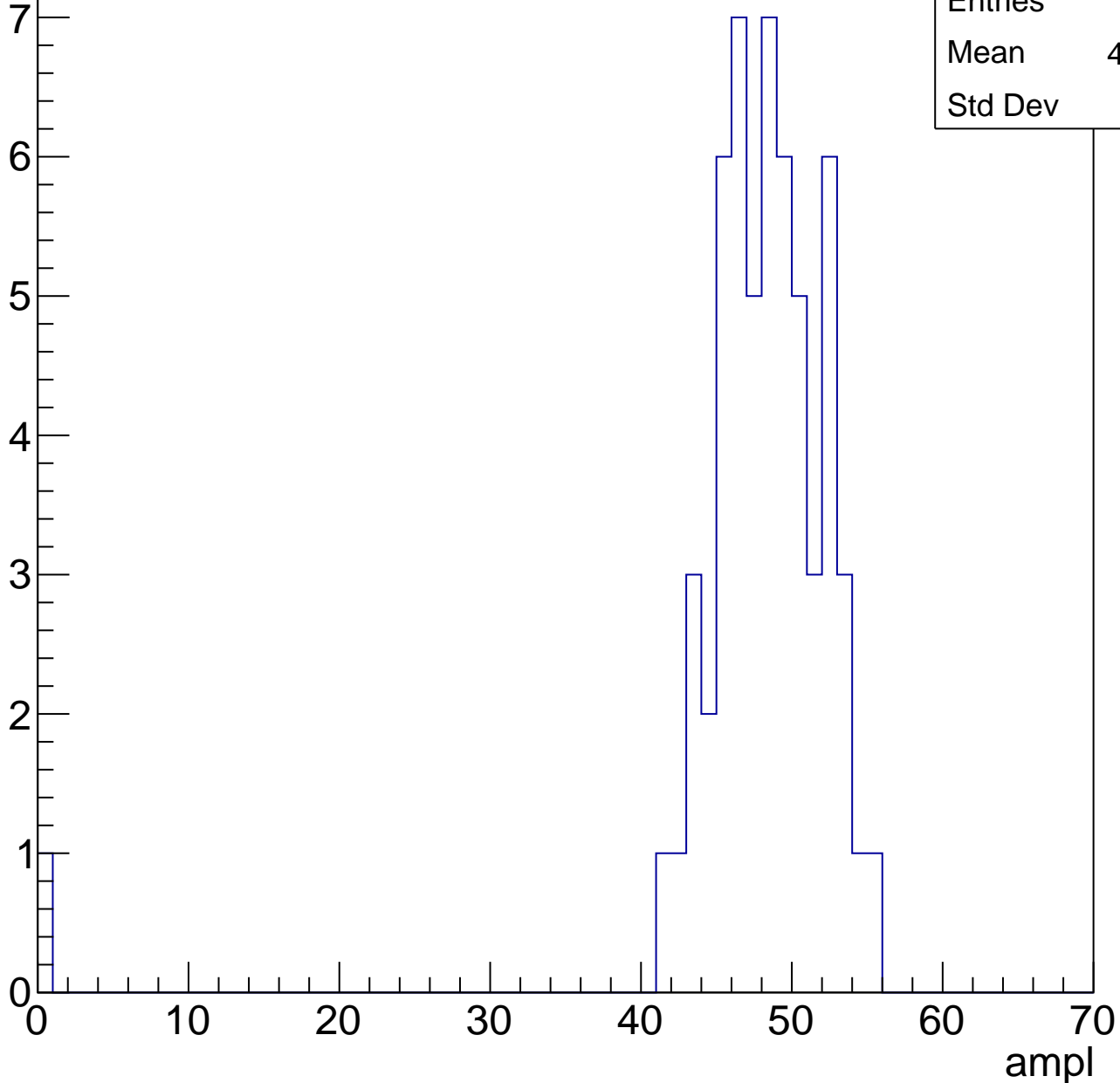


# B1L103S, U22-ch12, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.24
Std Dev	7.02

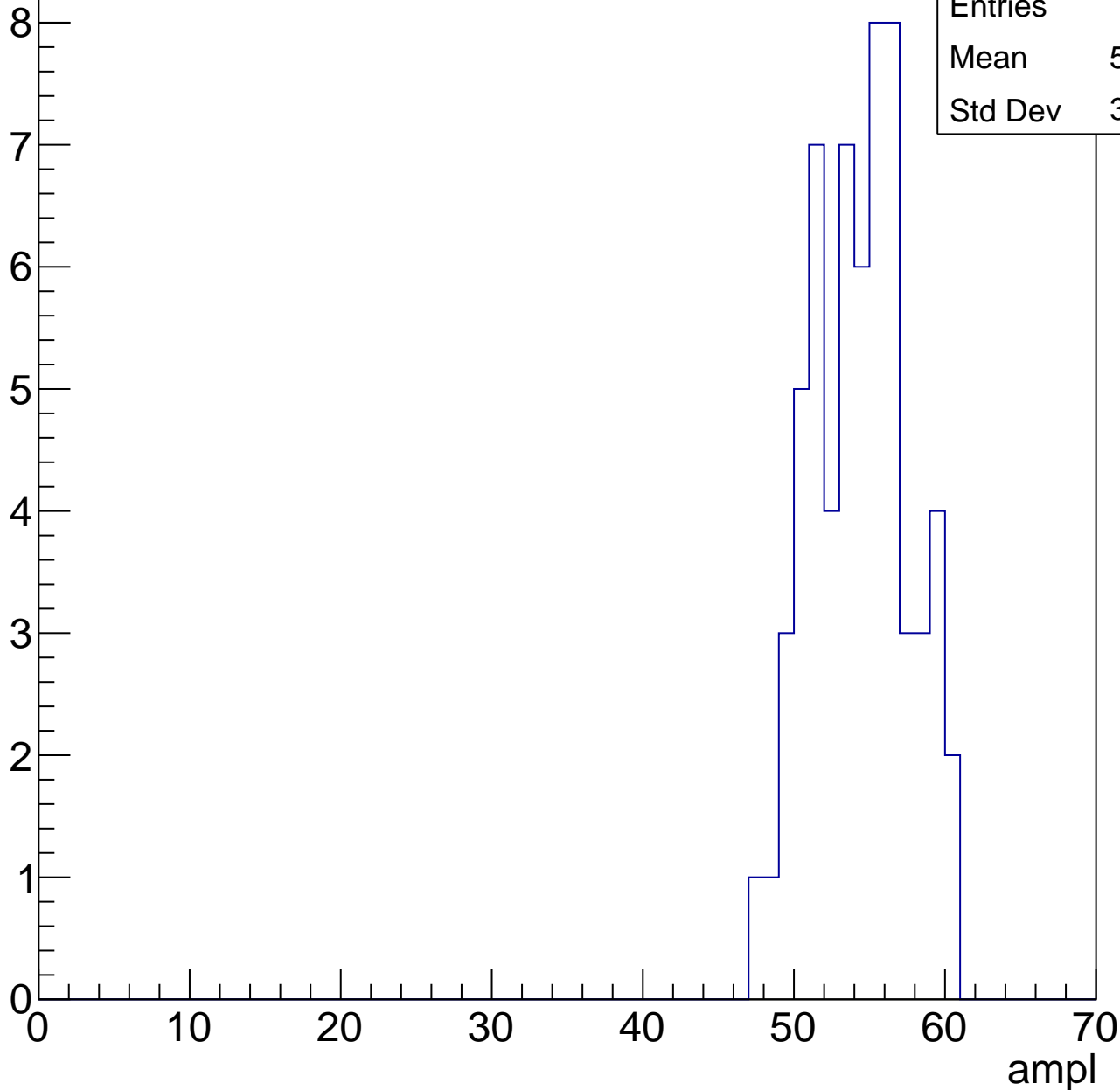


# B1L103S, U22-ch12, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	53.89
Std Dev	3.142

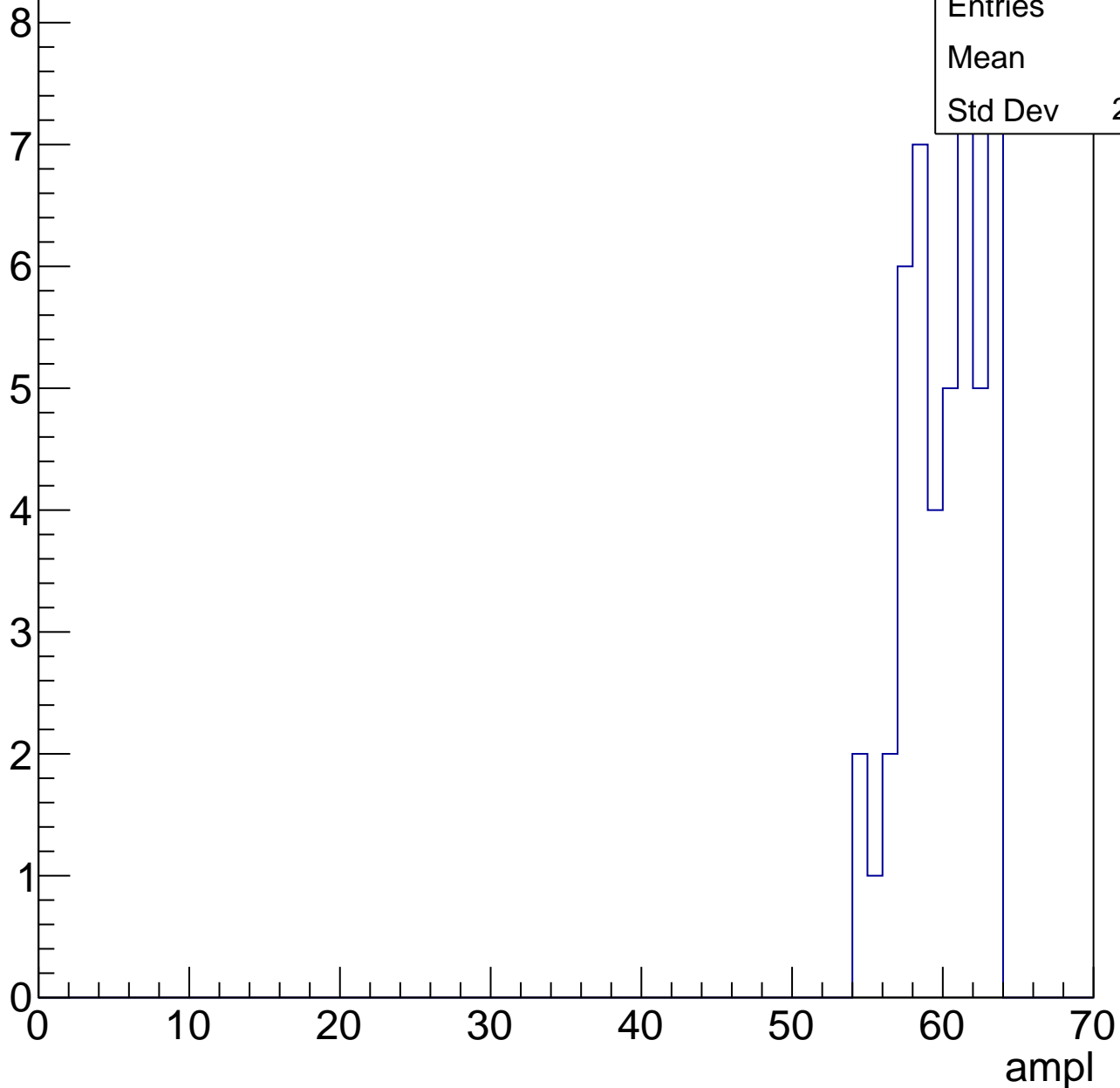


# B1L103S, U22-ch12, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

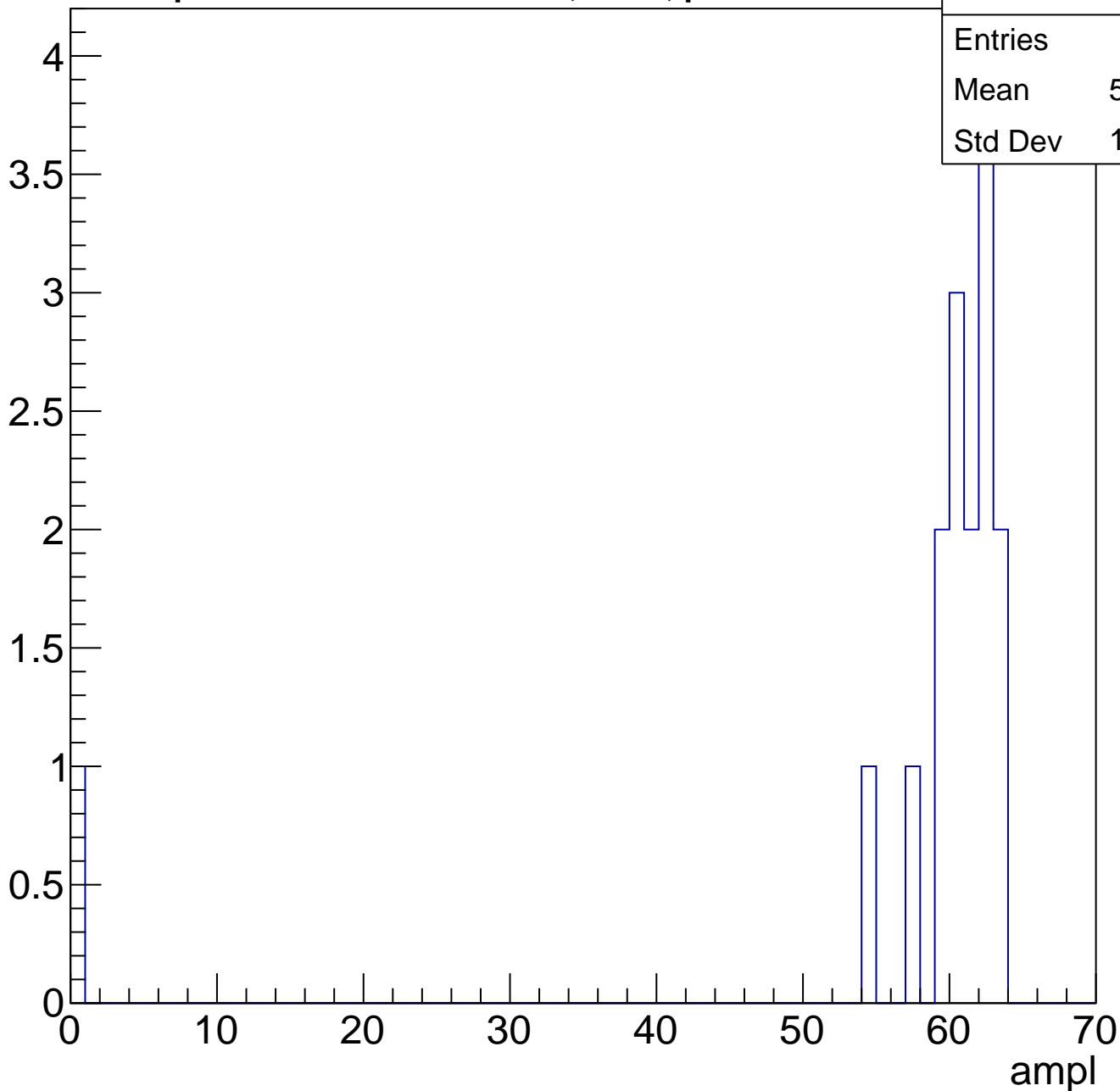
Entries	48
Mean	59.6
Std Dev	2.531



# B1L103S, U22-ch12, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch12, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



# B1L103S, U22-ch13, adc0

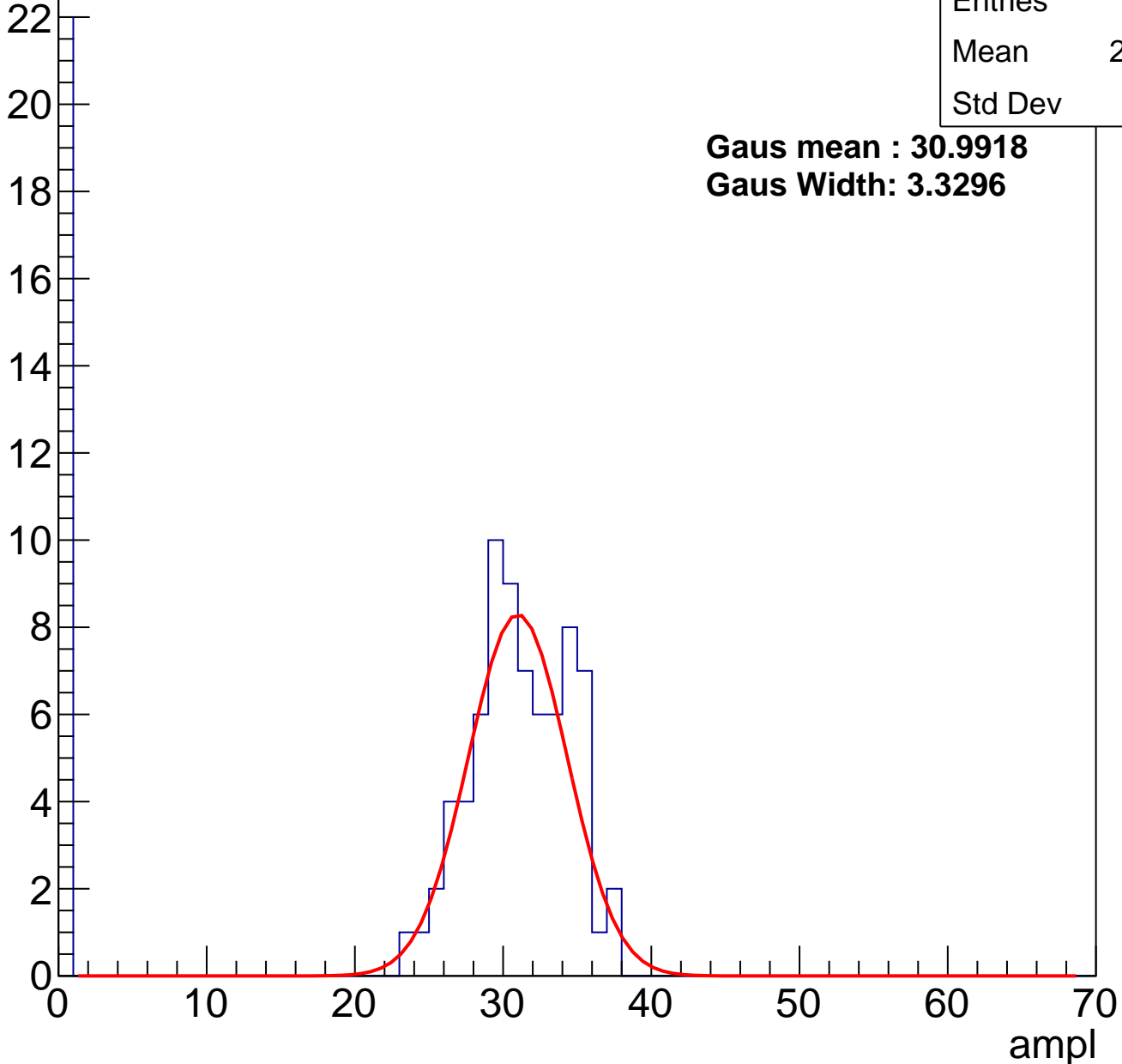
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	96
Mean	23.66
Std Dev	13.2

**Gaus mean : 30.9918**

**Gaus Width: 3.3296**

Entry



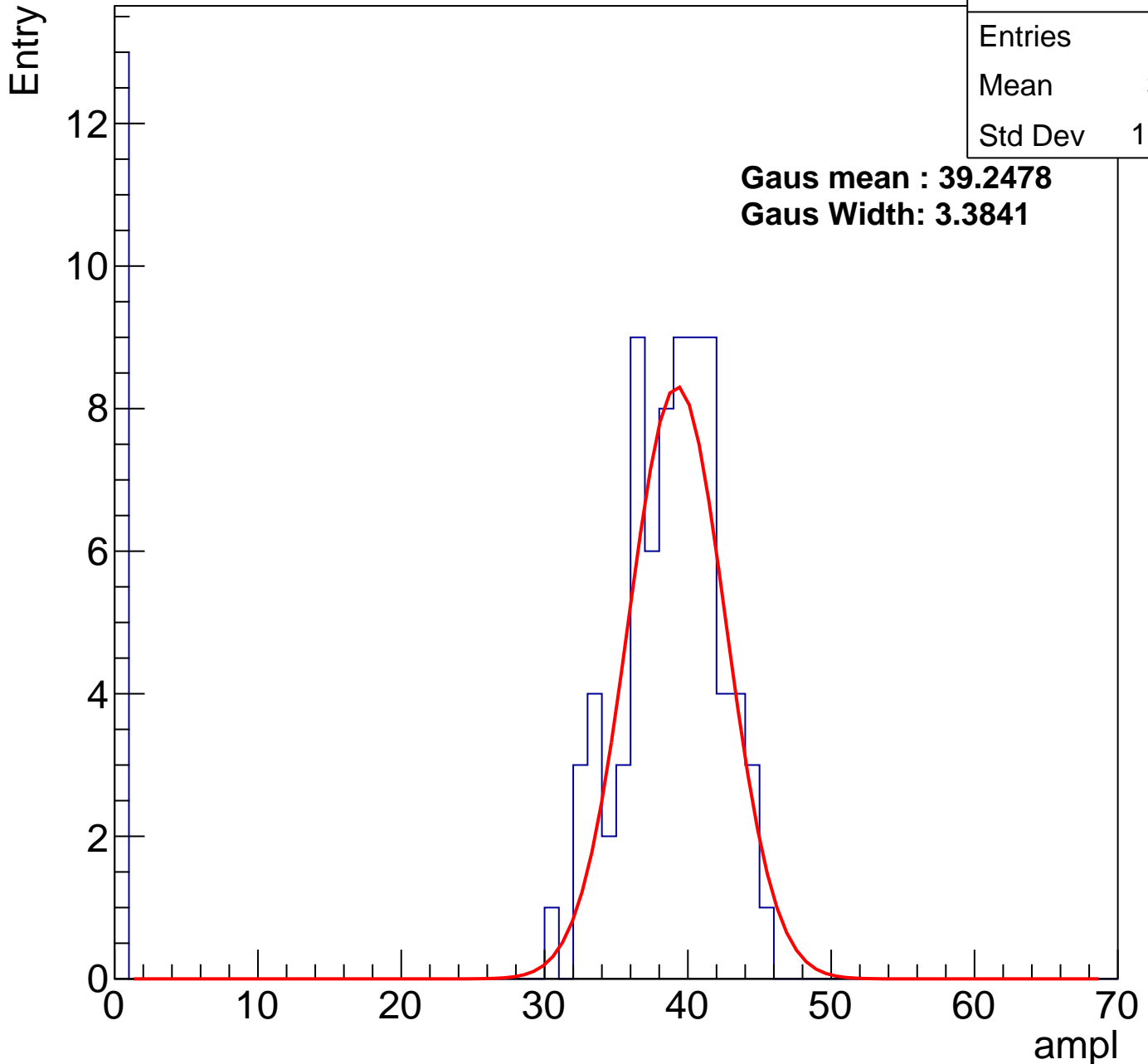
# B1L103S, U22-ch13, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	88
Mean	32.7
Std Dev	13.95

**Gaus mean : 39.2478**

**Gaus Width: 3.3841**



# B1L103S, U22-ch13, adc2

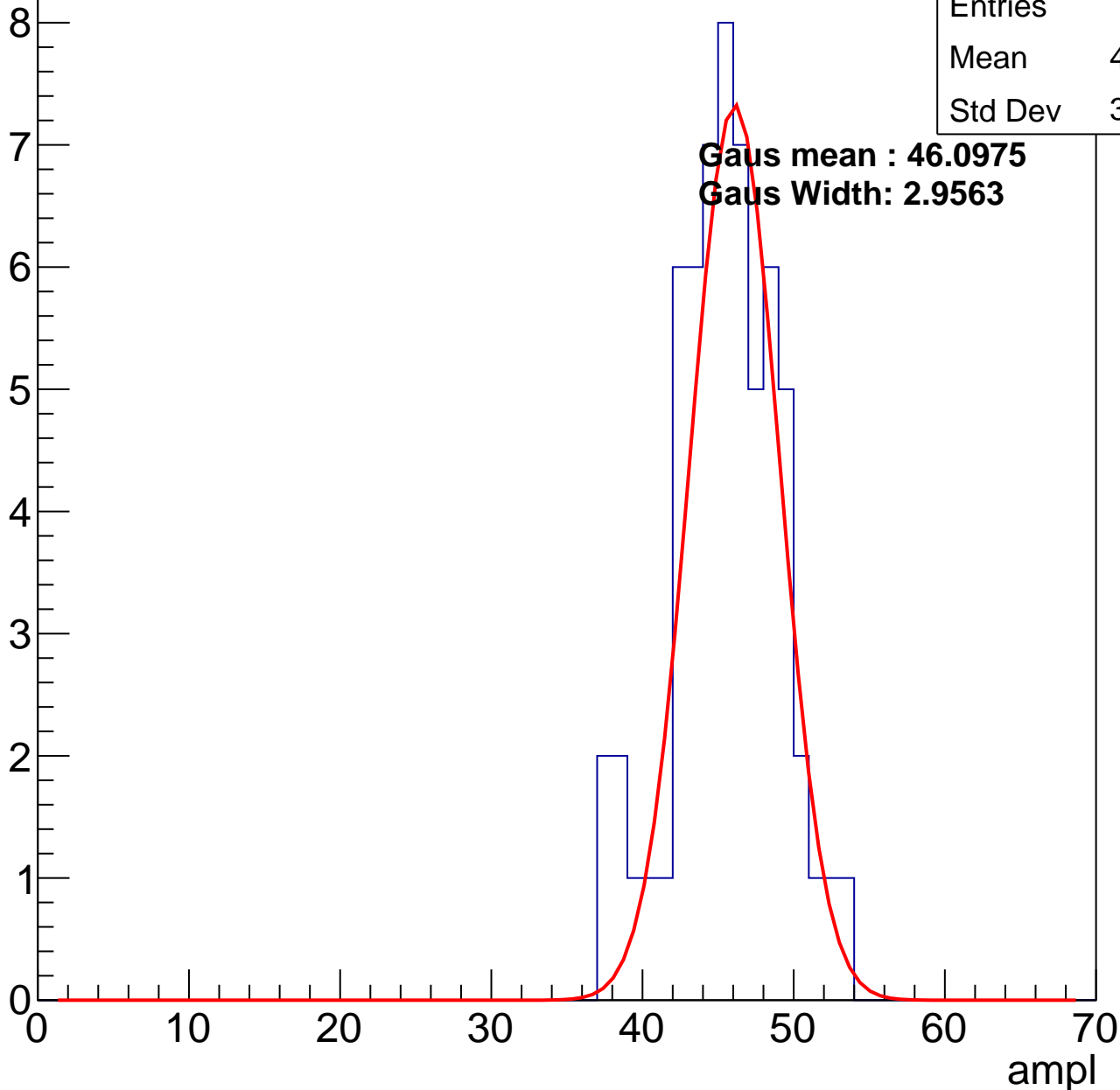
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	45.06
Std Dev	3.473

**Gaus mean : 46.0975**

**Gaus Width: 2.9563**

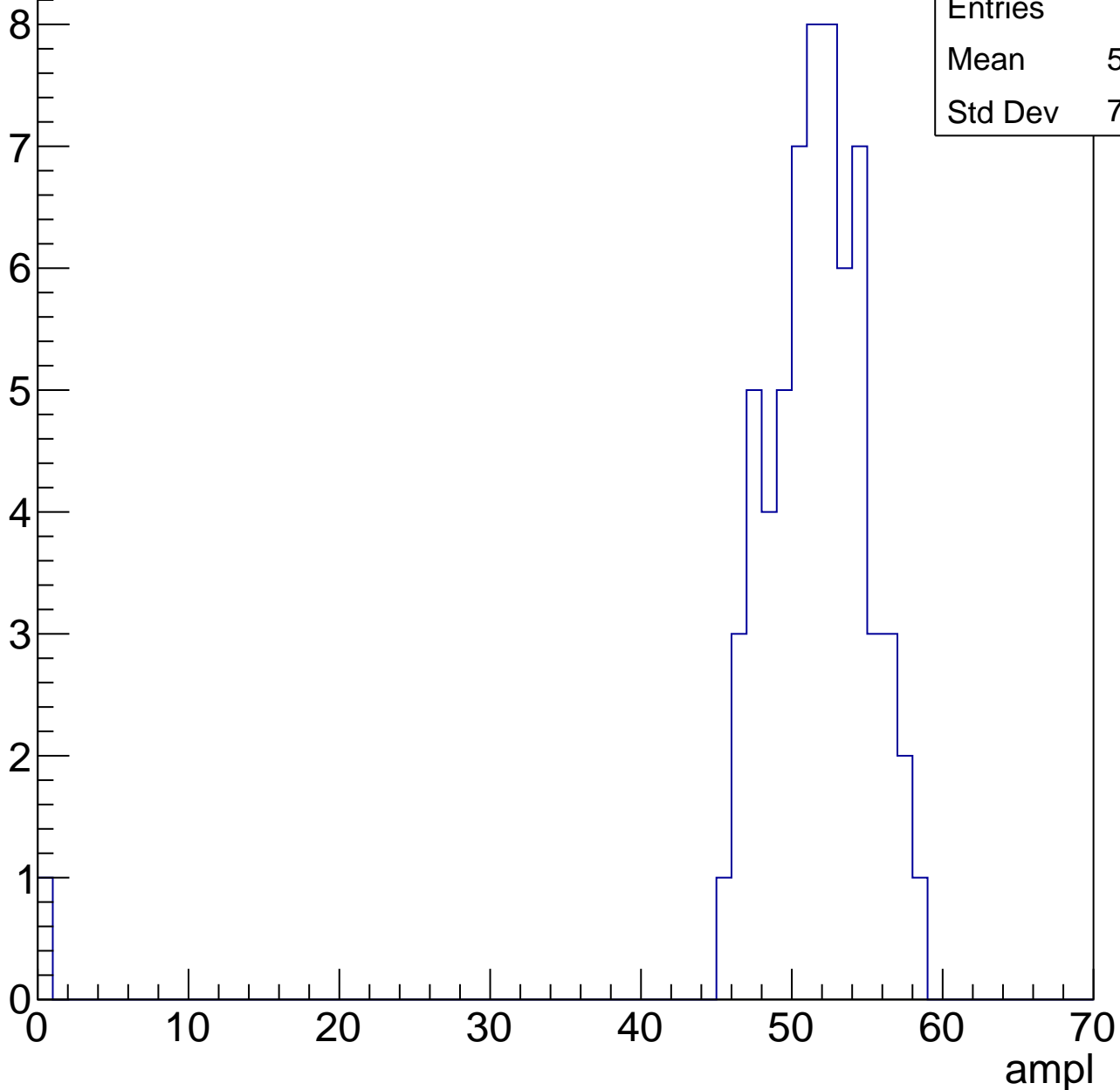


# B1L103S, U22-ch13, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	50.47
Std Dev	7.044

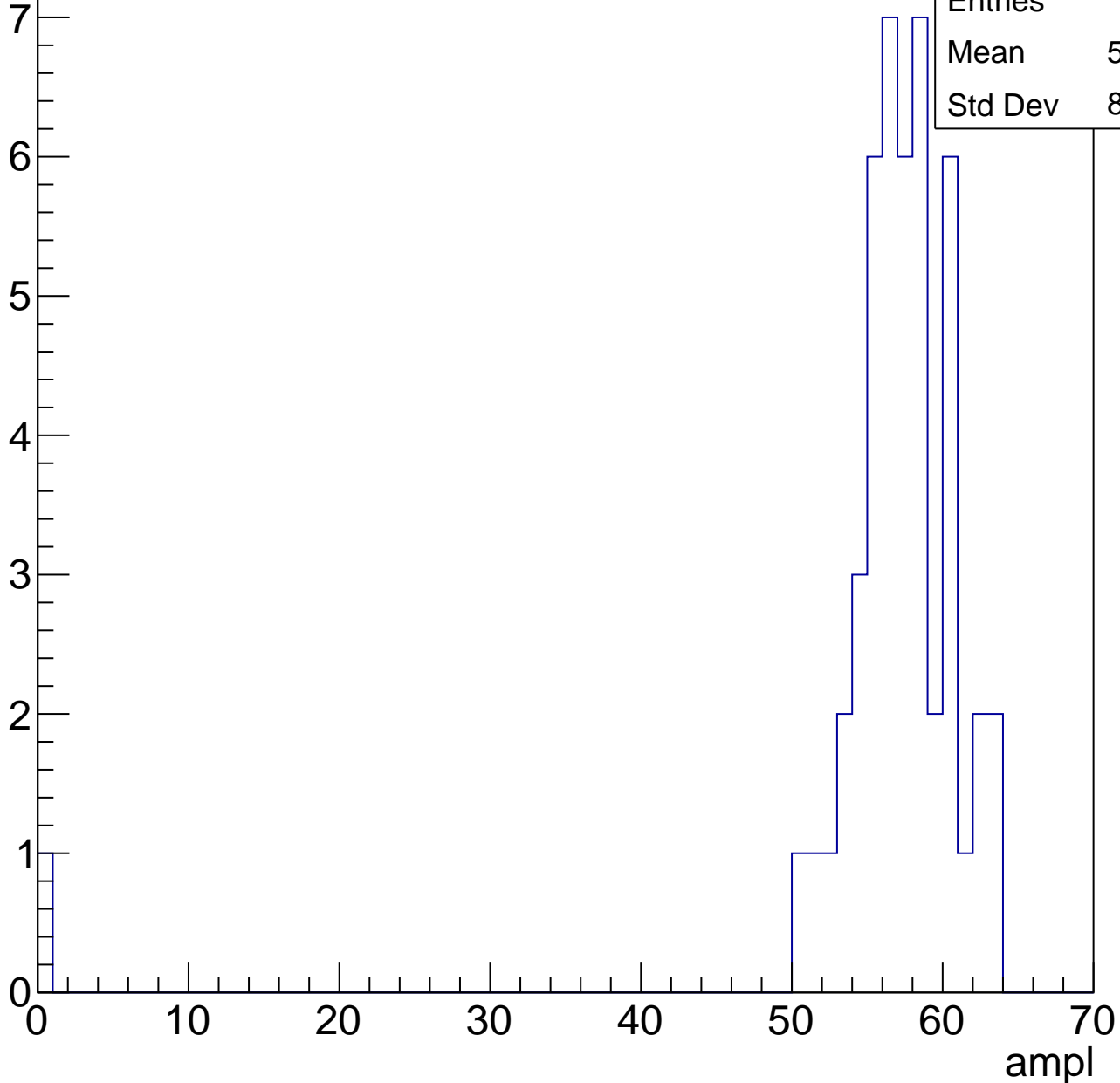


# B1L103S, U22-ch13, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.83
Std Dev	8.649

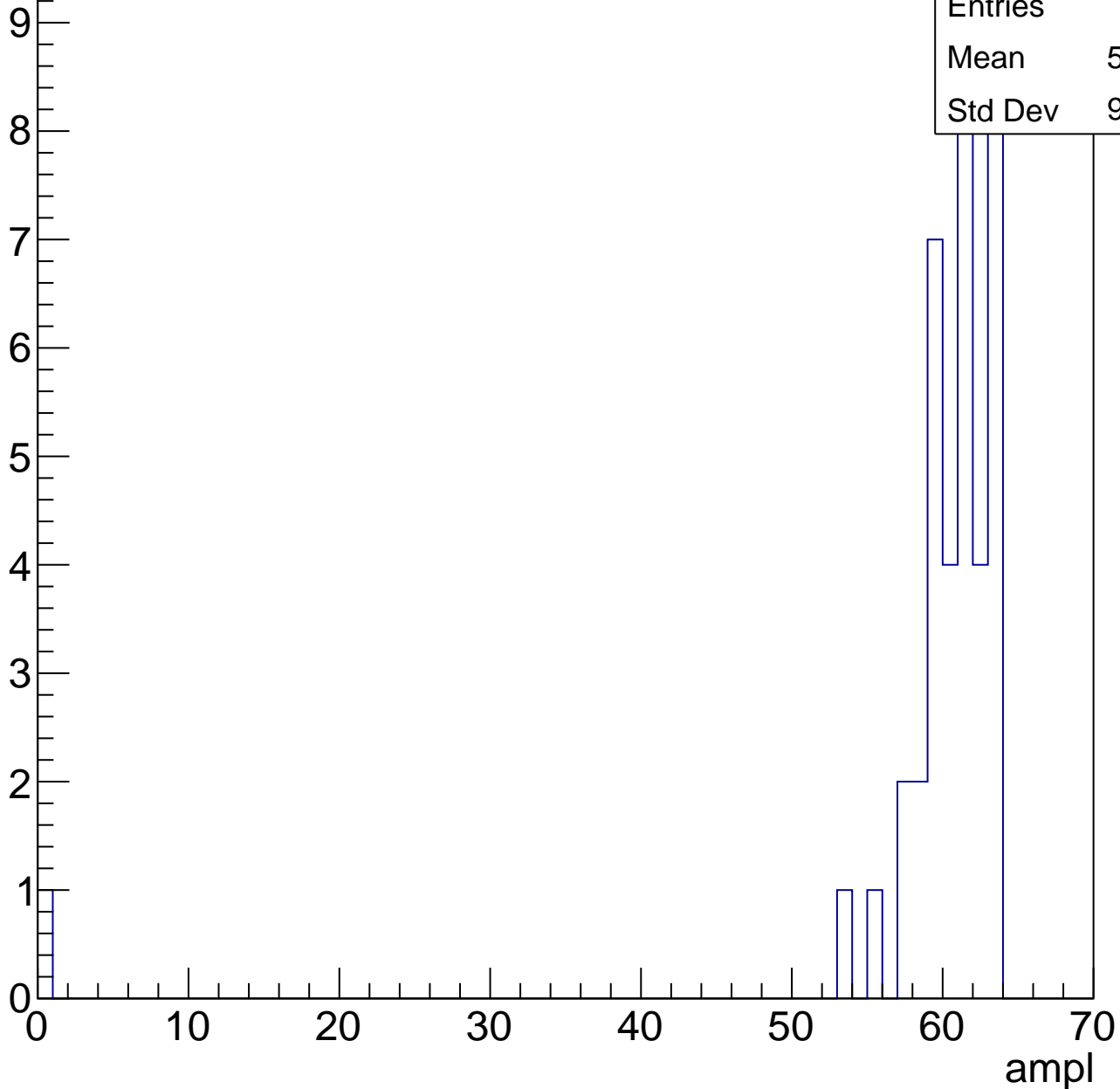


# B1L103S, U22-ch13, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

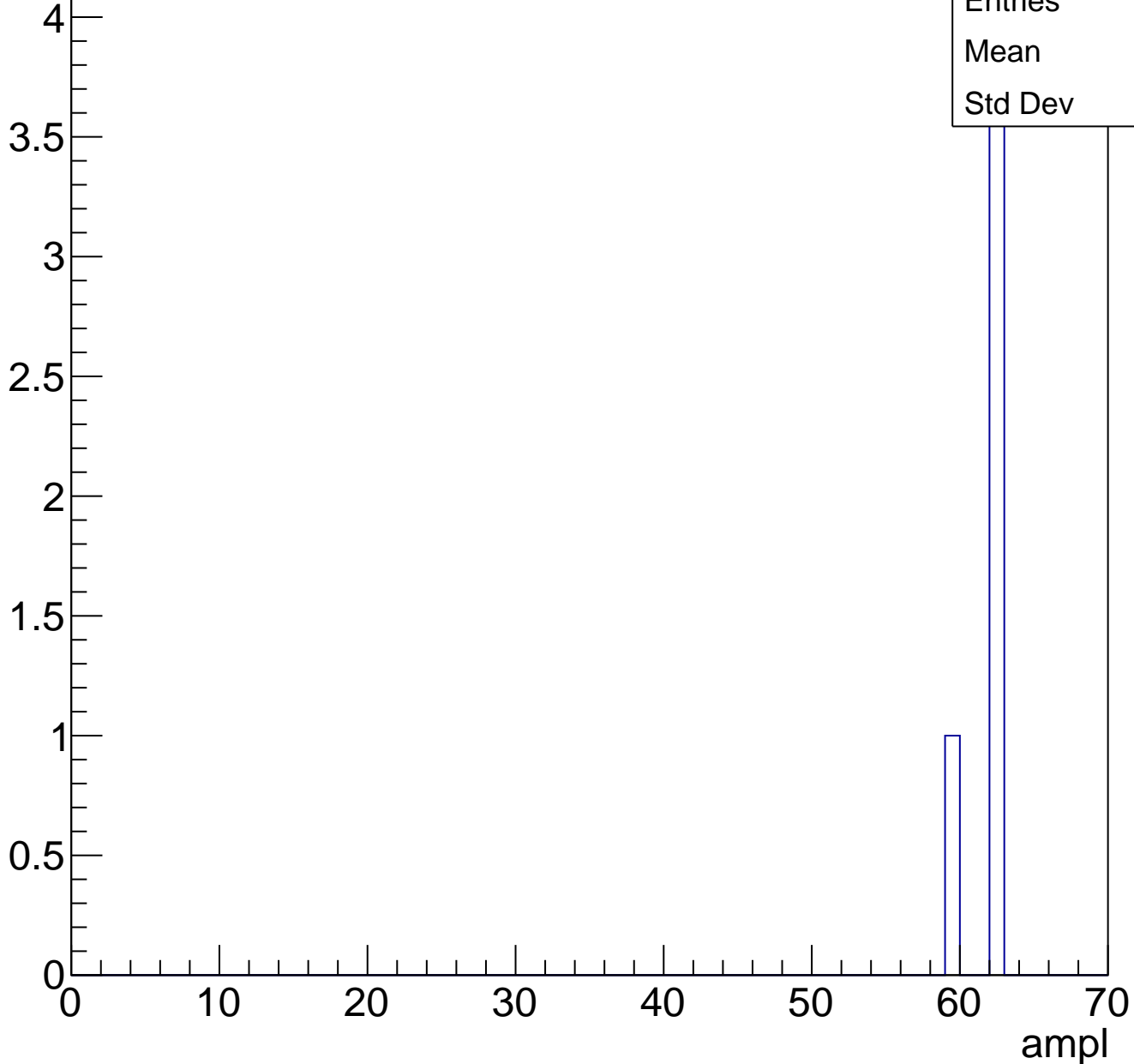
Entries	39
Mean	58.77
Std Dev	9.799



# B1L103S, U22-ch13, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch13, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.211
Std Dev	5.136

Entry



# B1L103S, U22-ch14, adc0

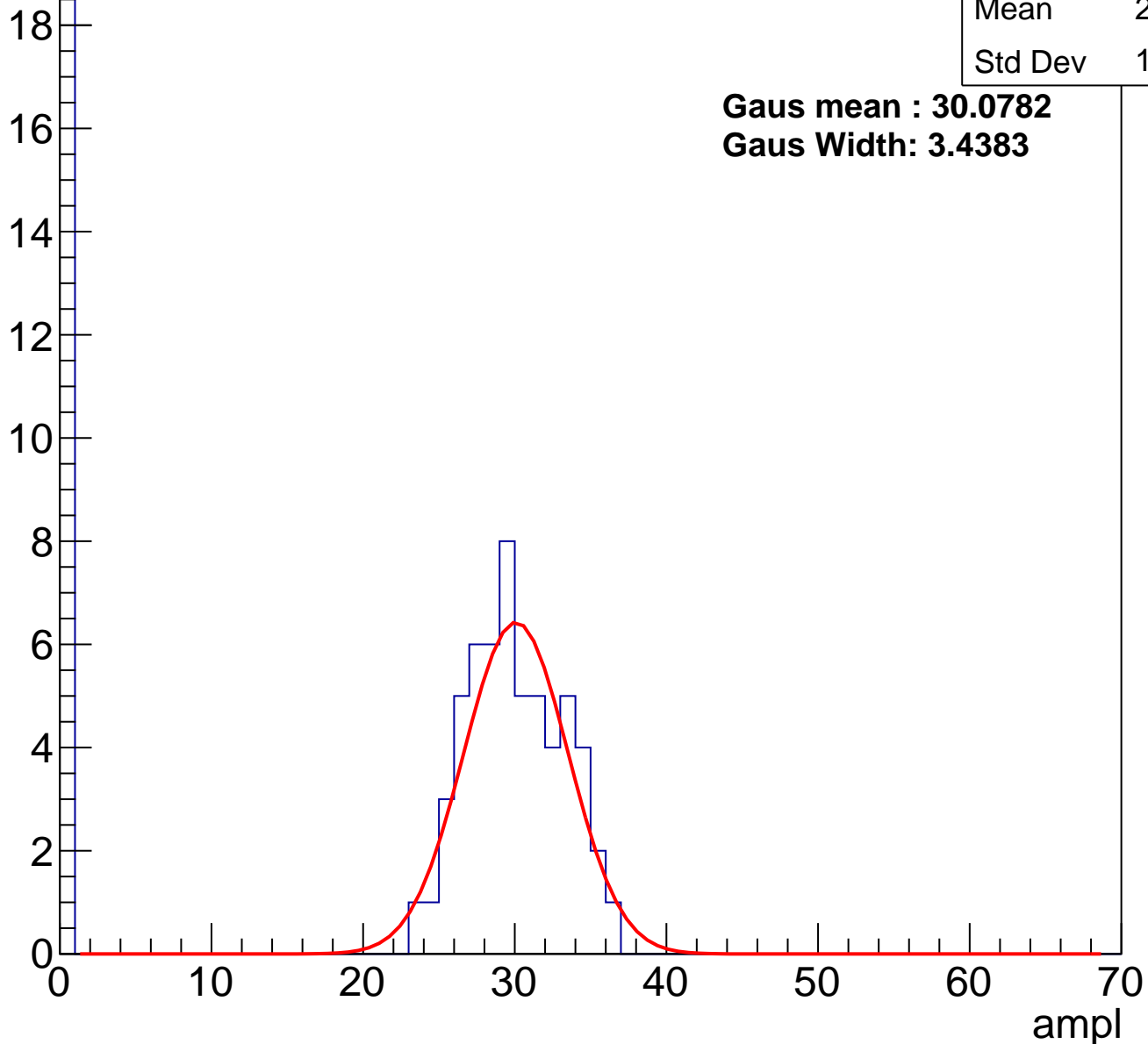
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	75
Mean	22.05
Std Dev	13.12

**Gaus mean : 30.0782**

**Gaus Width: 3.4383**

Entry



# B1L103S, U22-ch14, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	34.35
Std Dev	8.67

**Gaus mean : 36.9608**

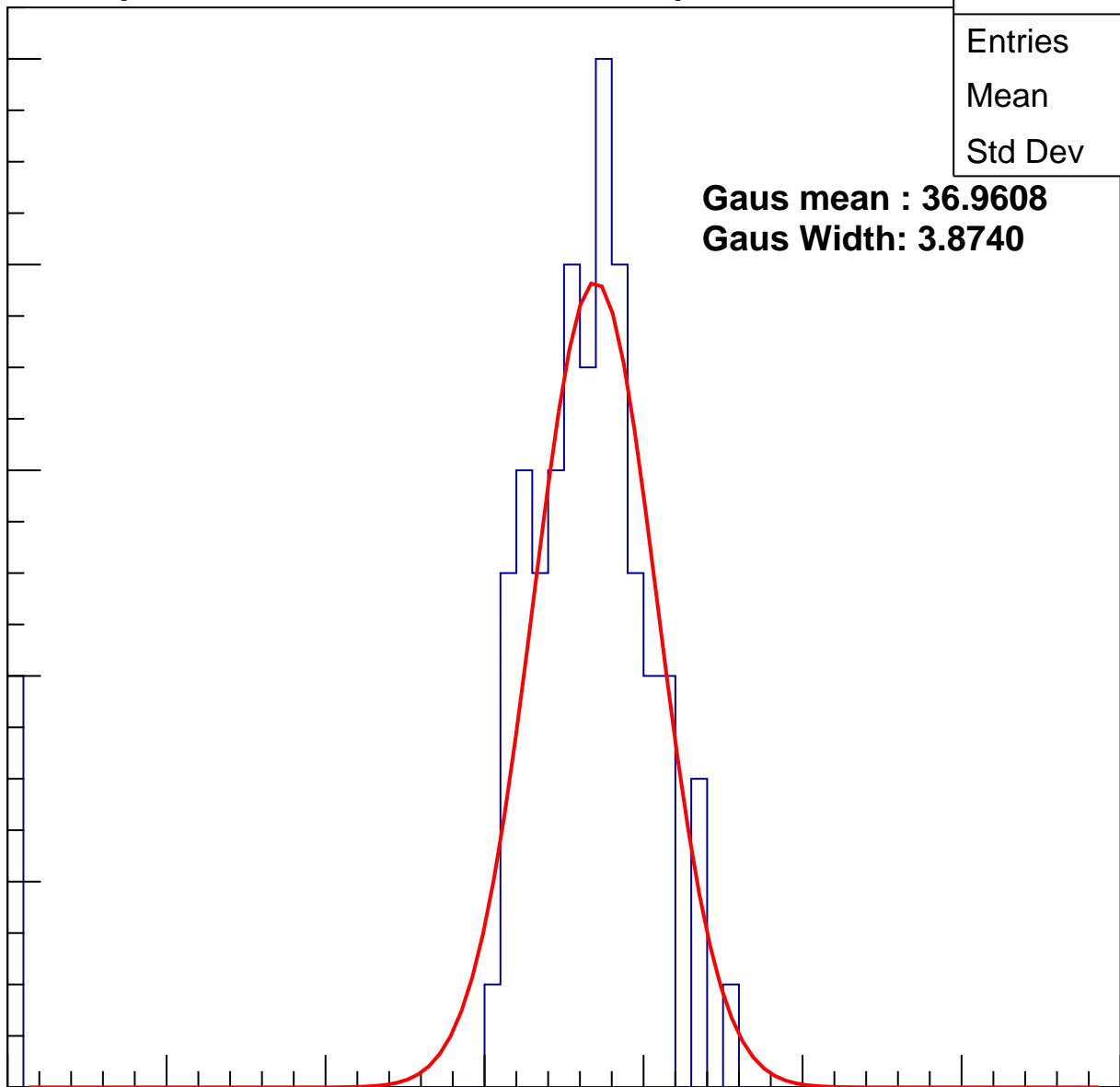
**Gaus Width: 3.8740**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch14, adc2

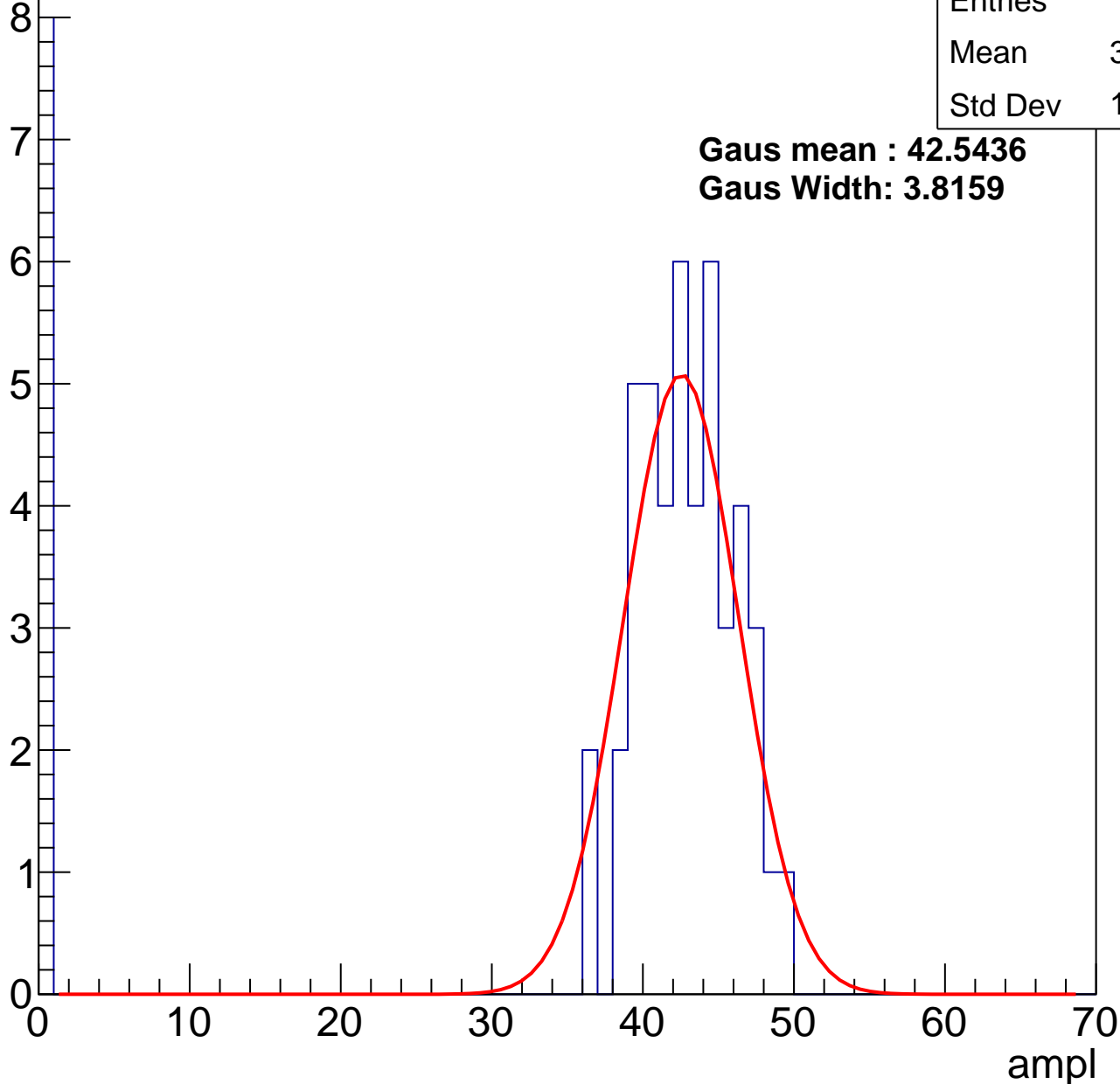
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	36.15
Std Dev	15.34

**Gaus mean : 42.5436**

**Gaus Width: 3.8159**

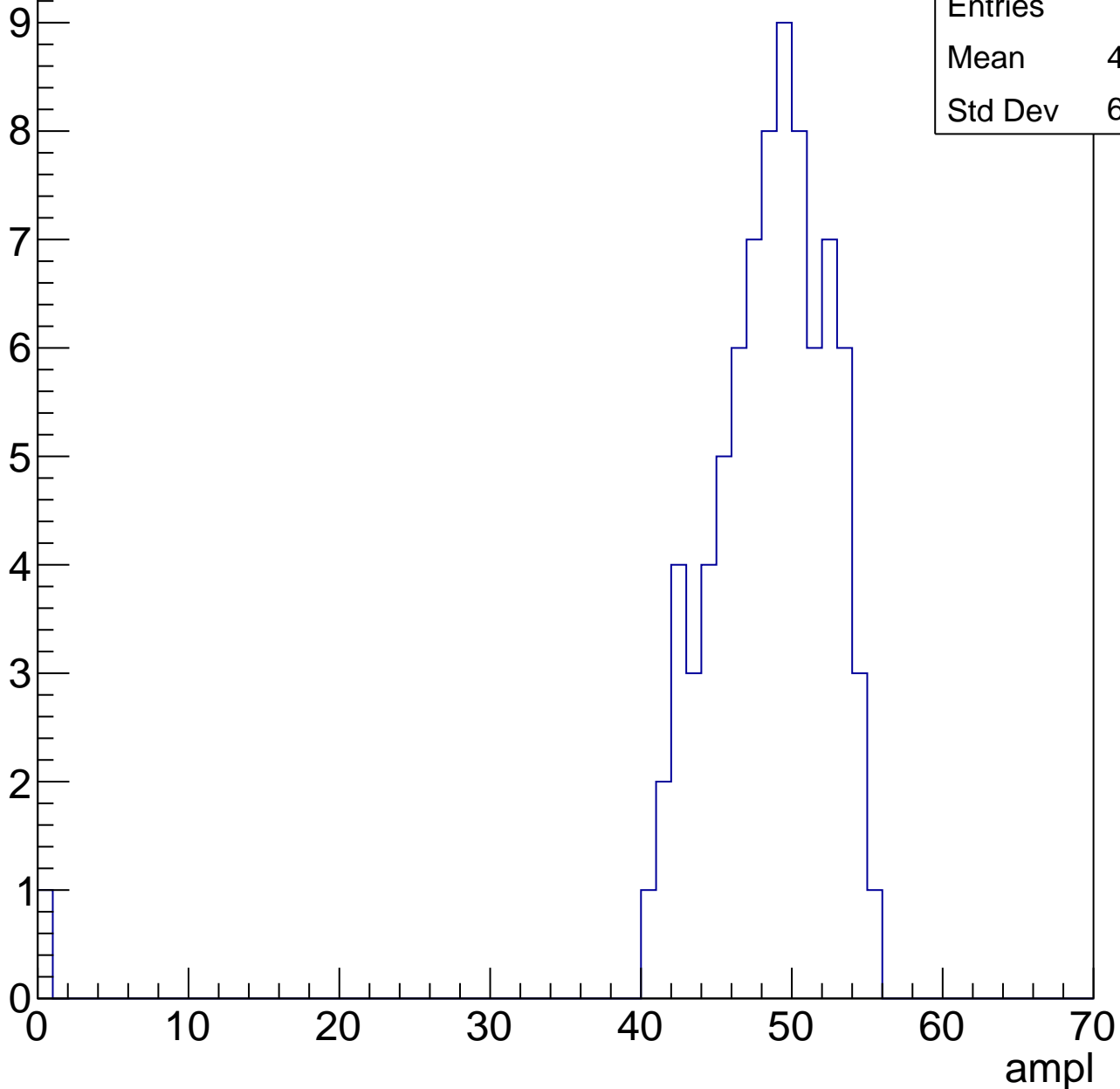


# B1L103S, U22-ch14, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	47.59
Std Dev	6.408

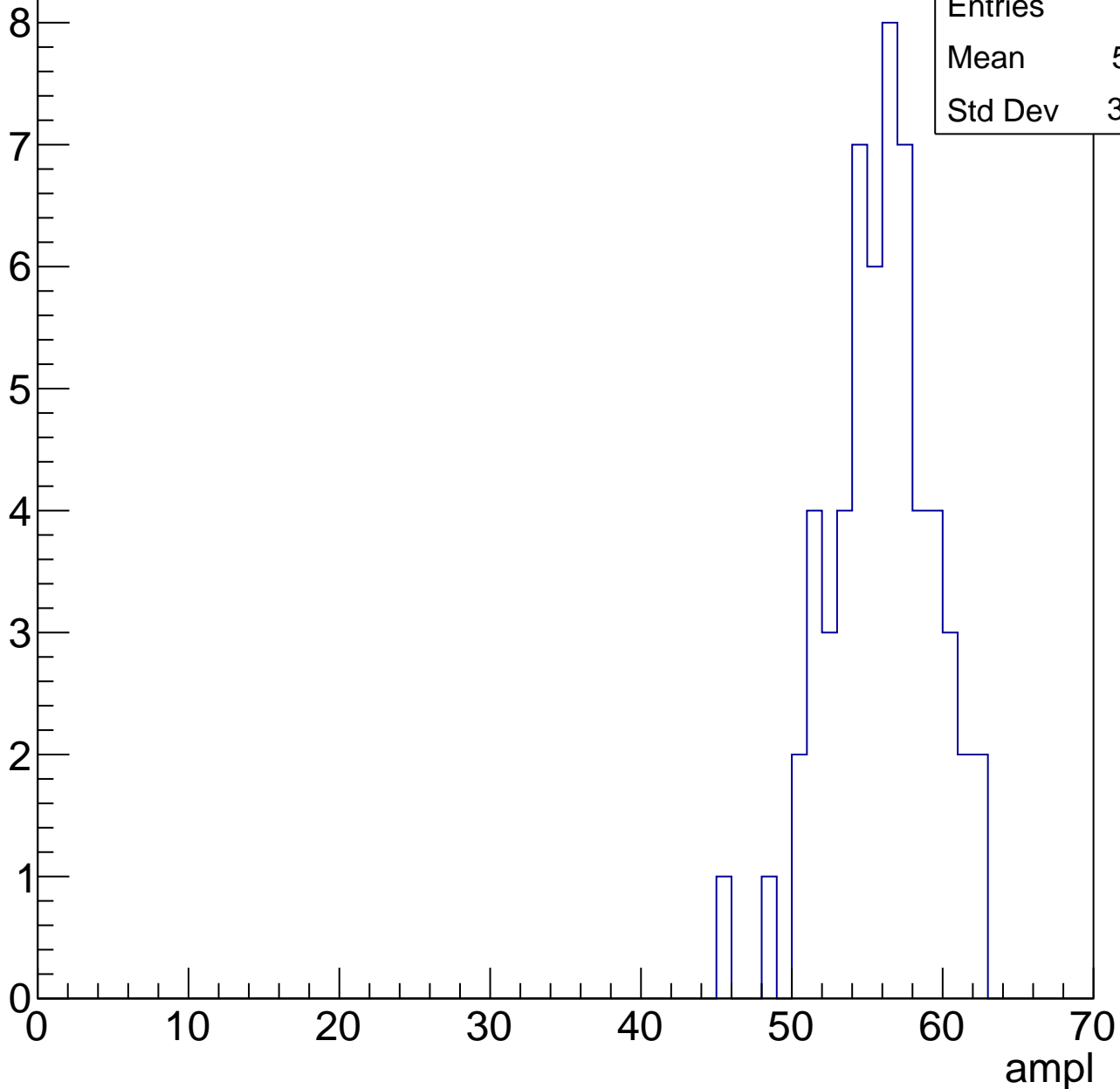


# B1L103S, U22-ch14, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	55.41
Std Dev	3.434

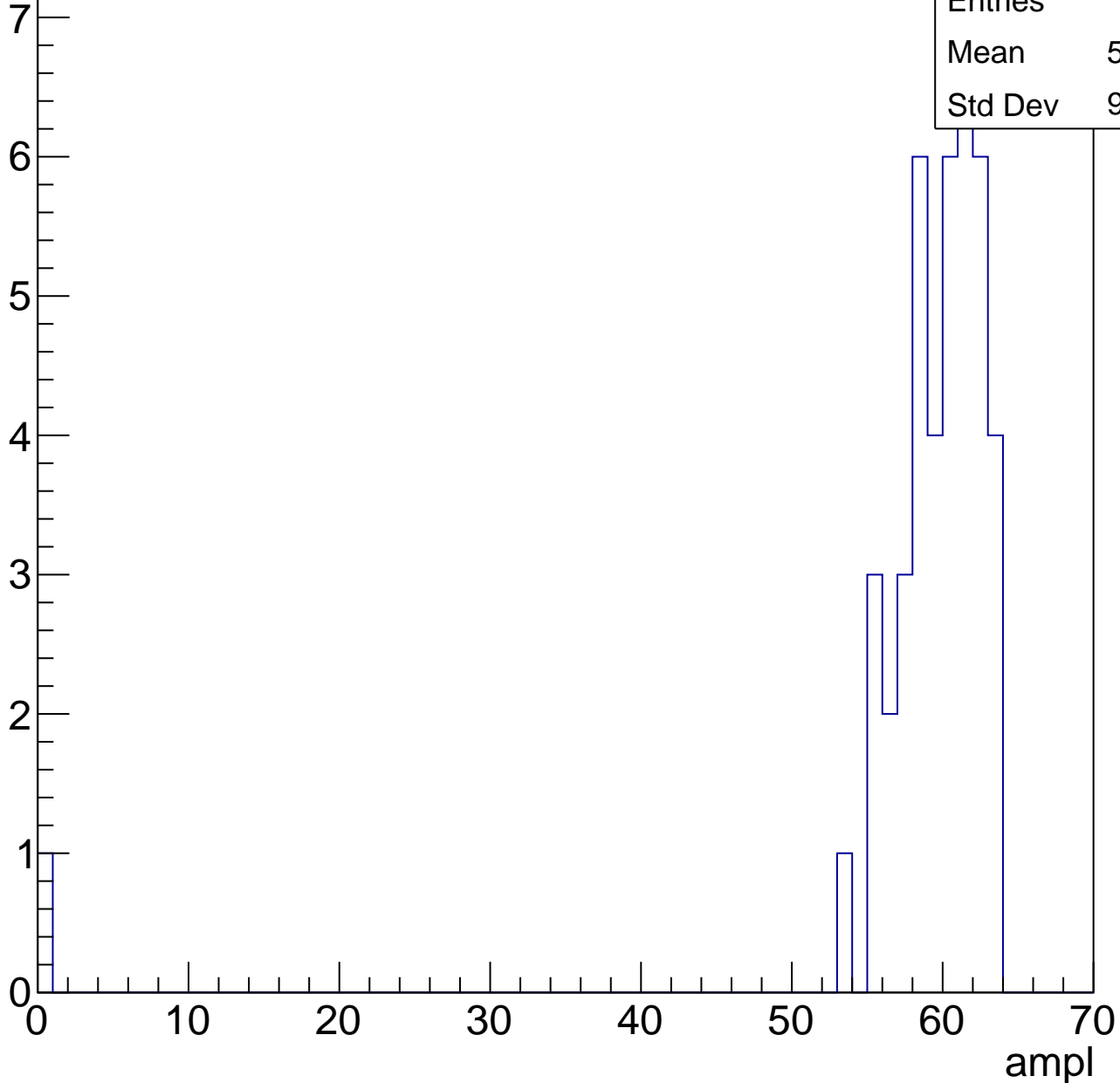


# B1L103S, U22-ch14, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.05
Std Dev	9.294

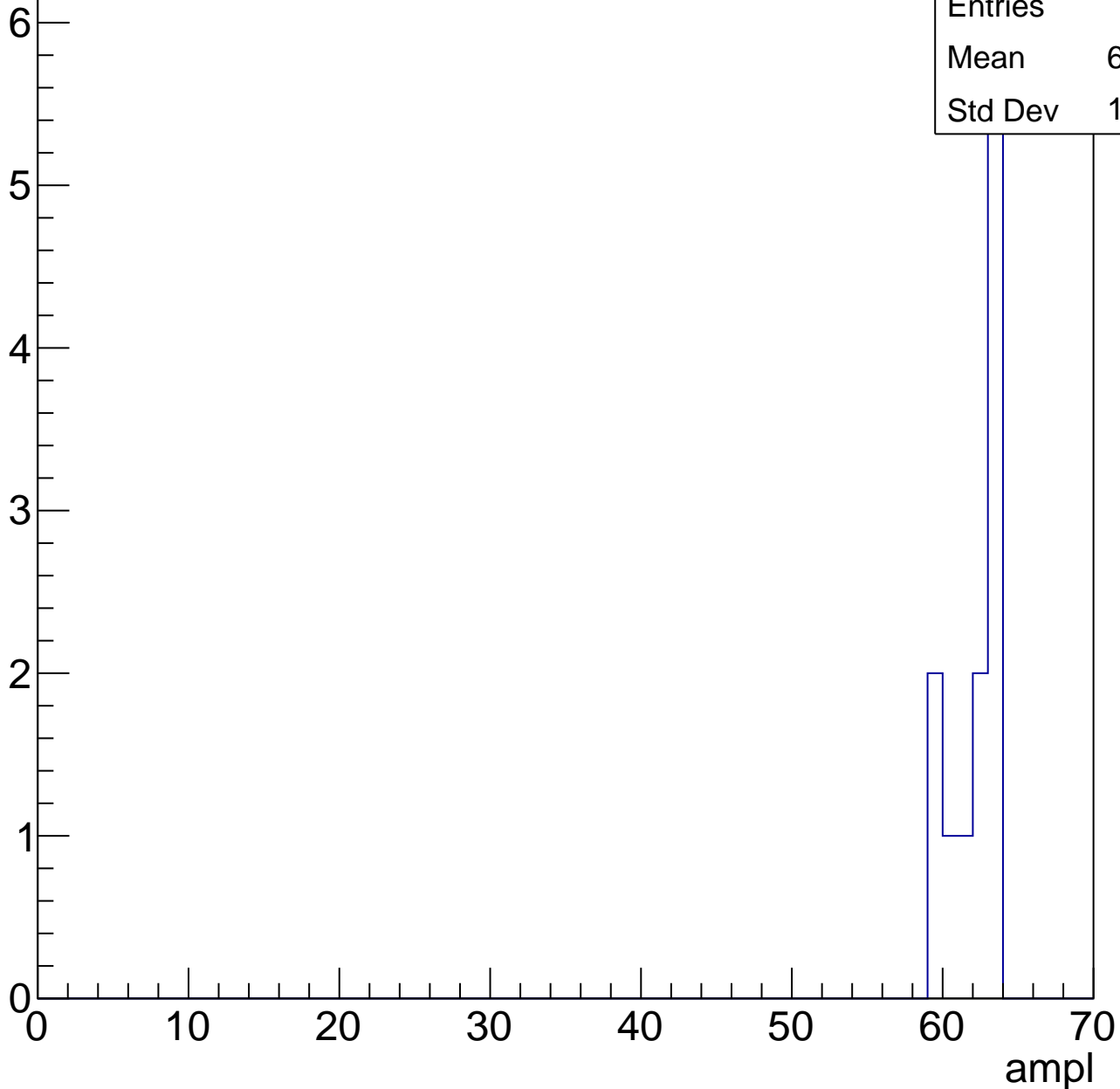


# B1L103S, U22-ch14, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.75
Std Dev	1.534





# B1L103S, U22-ch14, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch15, adc0

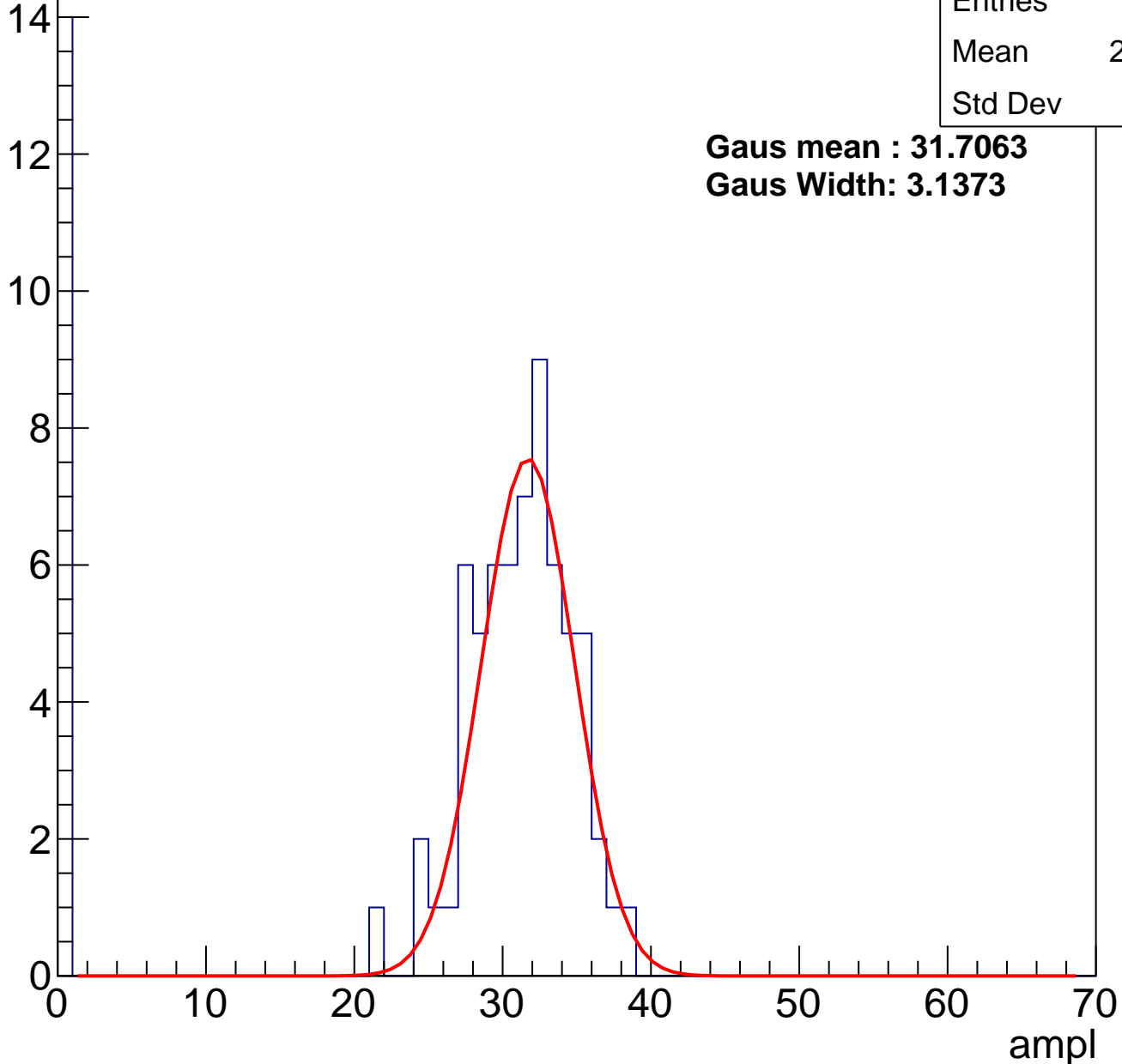
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	25.27
Std Dev	12.2

**Gaus mean : 31.7063**

**Gaus Width: 3.1373**

Entry



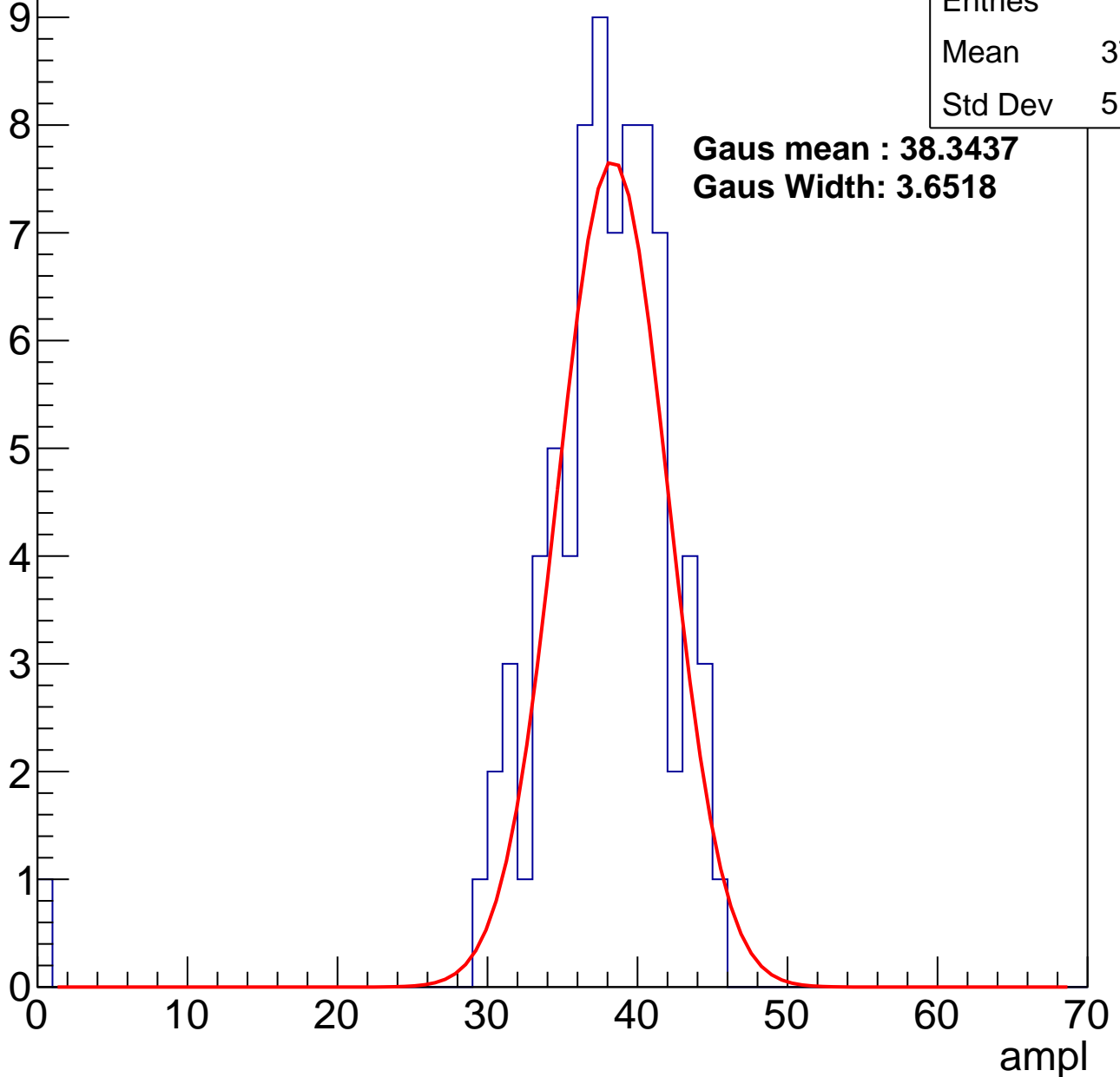
# B1L103S, U22-ch15, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	37.12
Std Dev	5.575

**Gaus mean : 38.3437**  
**Gaus Width: 3.6518**



# B1L103S, U22-ch15, adc2

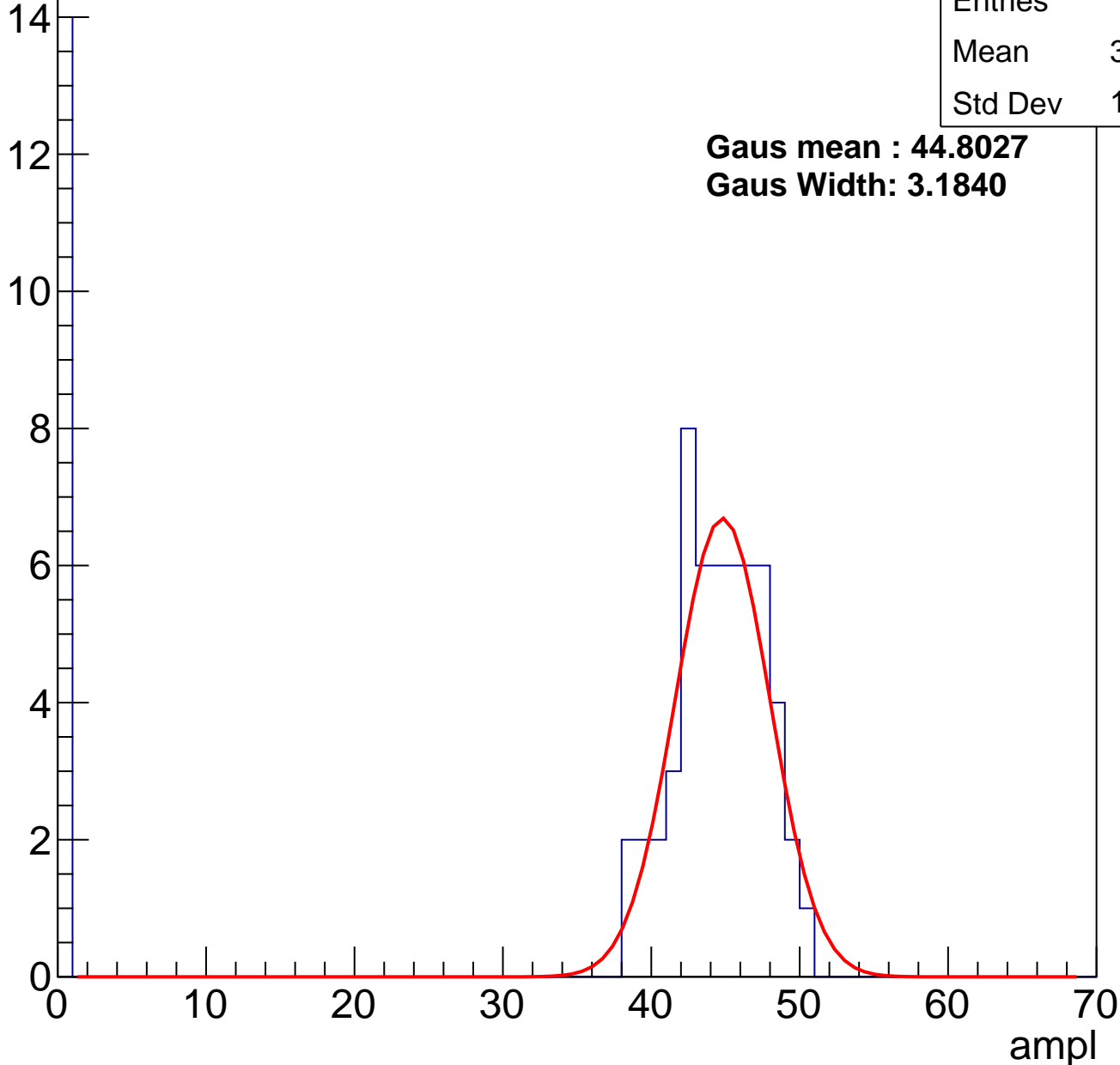
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	68
Mean	35.04
Std Dev	18.03

**Gaus mean : 44.8027**

**Gaus Width: 3.1840**

Entry

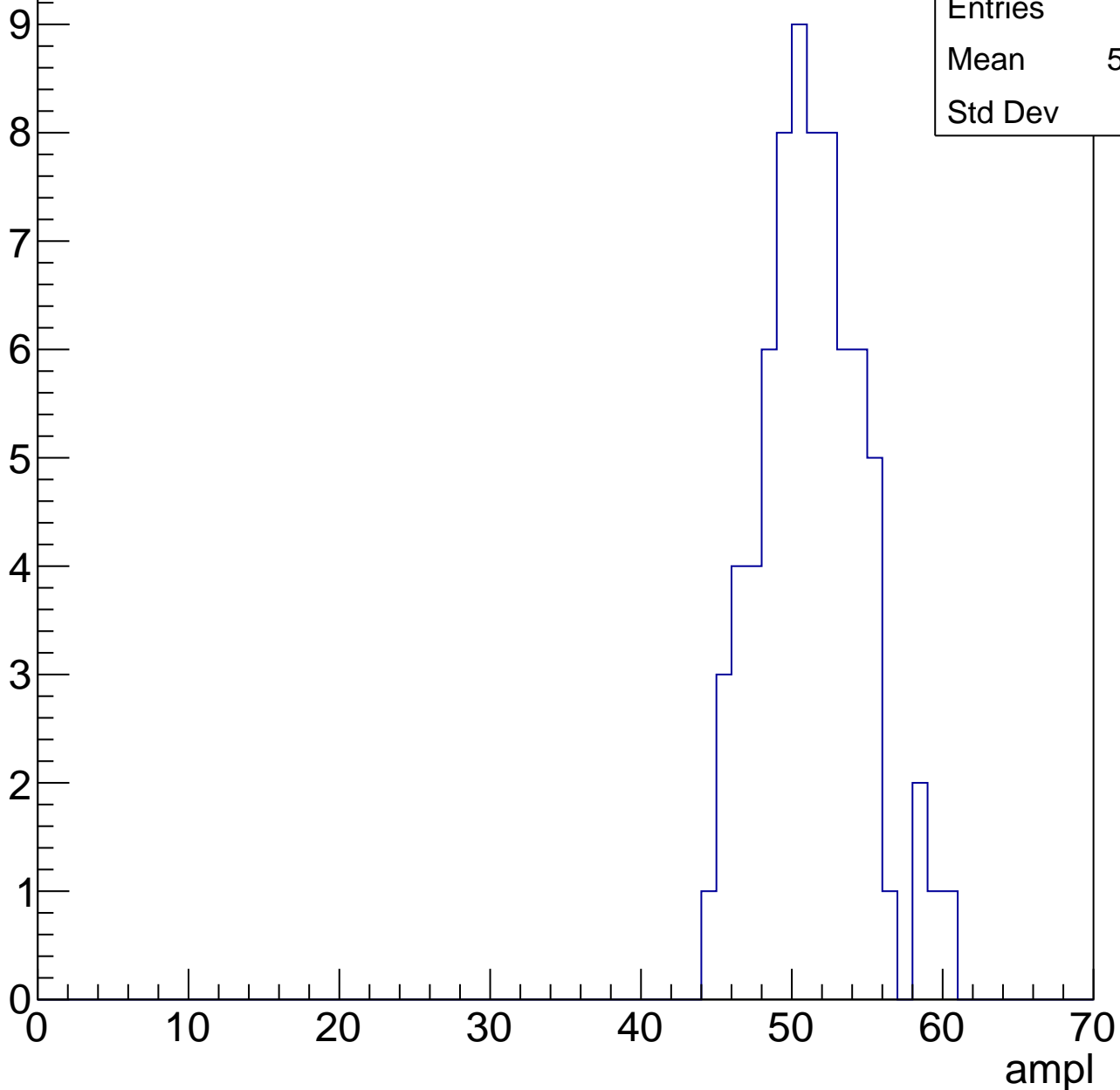


# B1L103S, U22-ch15, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	50.86
Std Dev	3.41

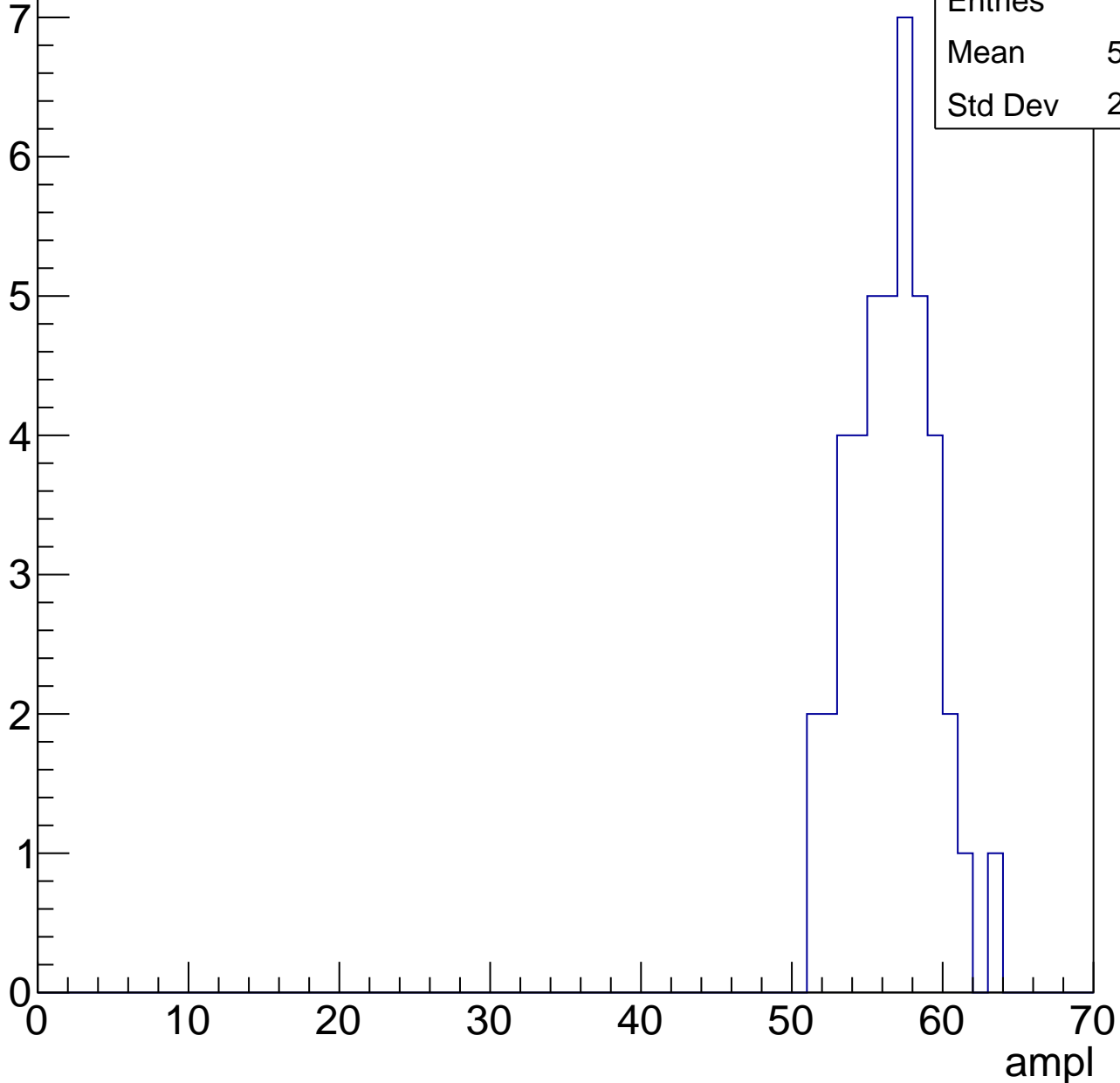


# B1L103S, U22-ch15, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	42
Mean	56.14
Std Dev	2.704

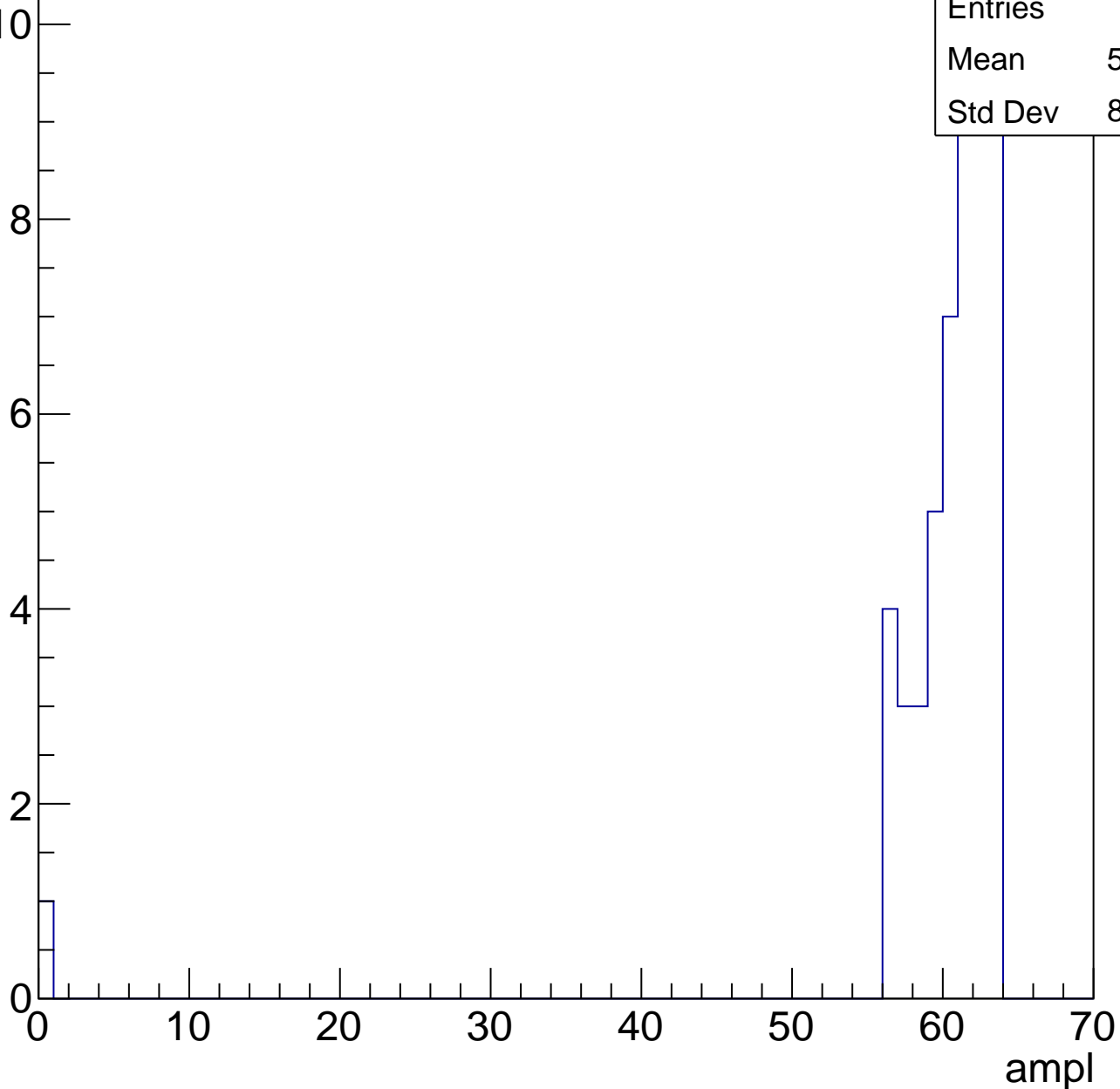


# B1L103S, U22-ch15, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	51
Mean	59.22
Std Dev	8.639



# B1L103S, U22-ch15, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	0
Mean	0
Std Dev	0



# B1L103S, U22-ch15, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



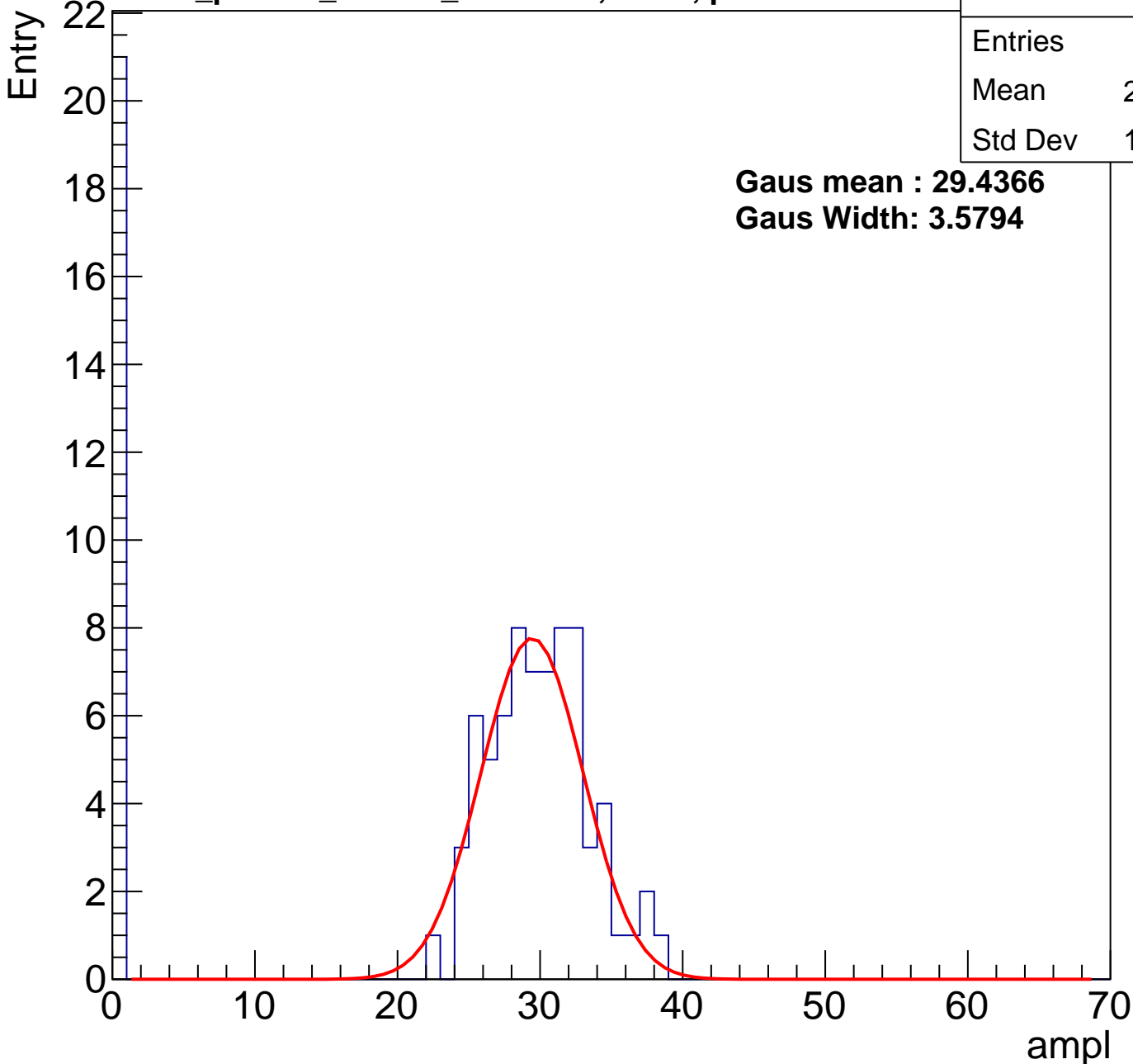
# B1L103S, U22-ch16, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	92
Mean	22.77
Std Dev	12.74

**Gaus mean : 29.4366**

**Gaus Width: 3.5794**



# B1L103S, U22-ch16, adc1

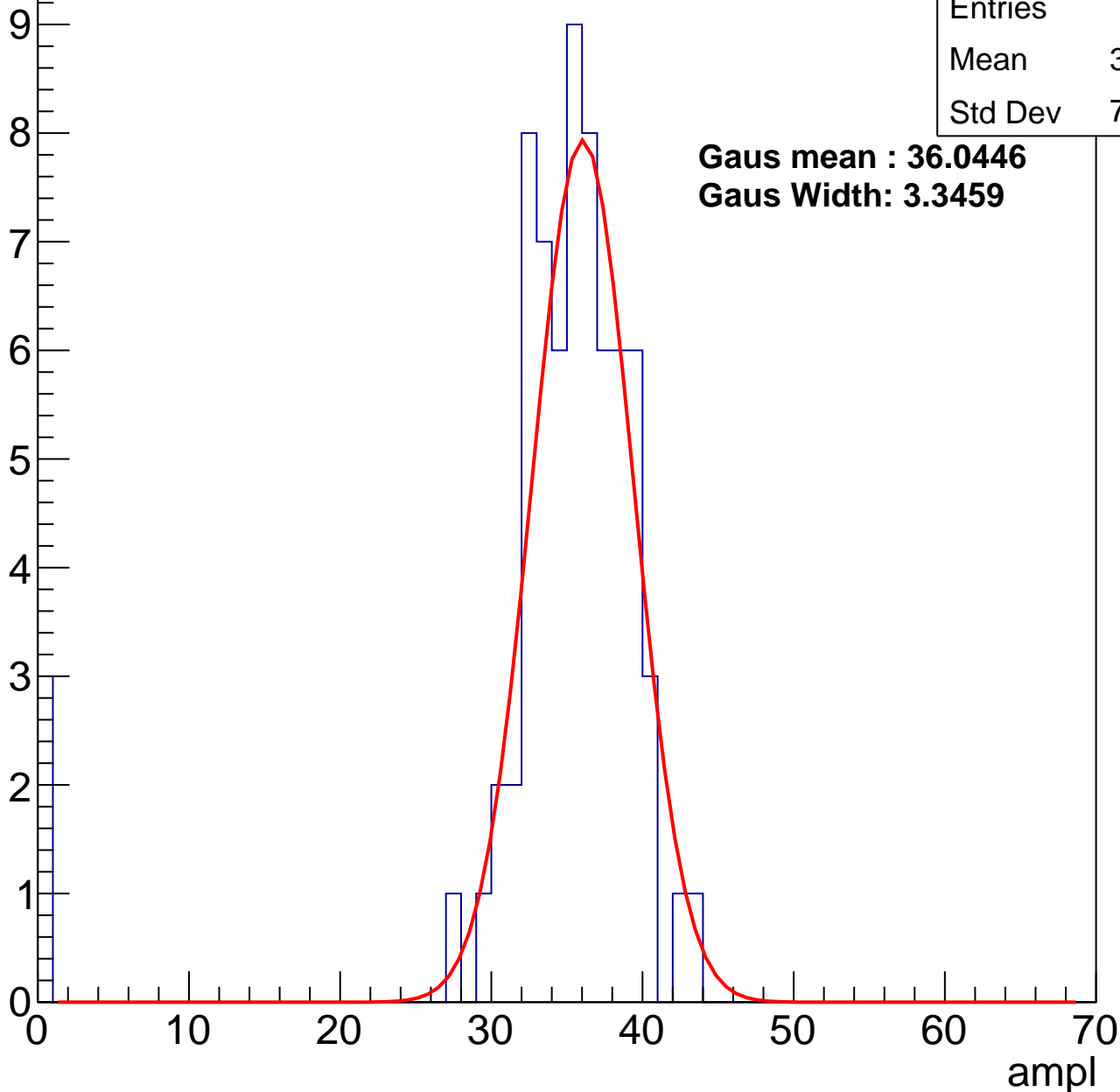
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.73
Std Dev	7.764

**Gaus mean : 36.0446**

**Gaus Width: 3.3459**



# B1L103S, U22-ch16, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	69
Mean	35.13
Std Dev	15.55

**Gaus mean : 42.5998**

**Gaus Width: 3.1033**

Entry

10

8

6

4

2

0

0

10

20

30

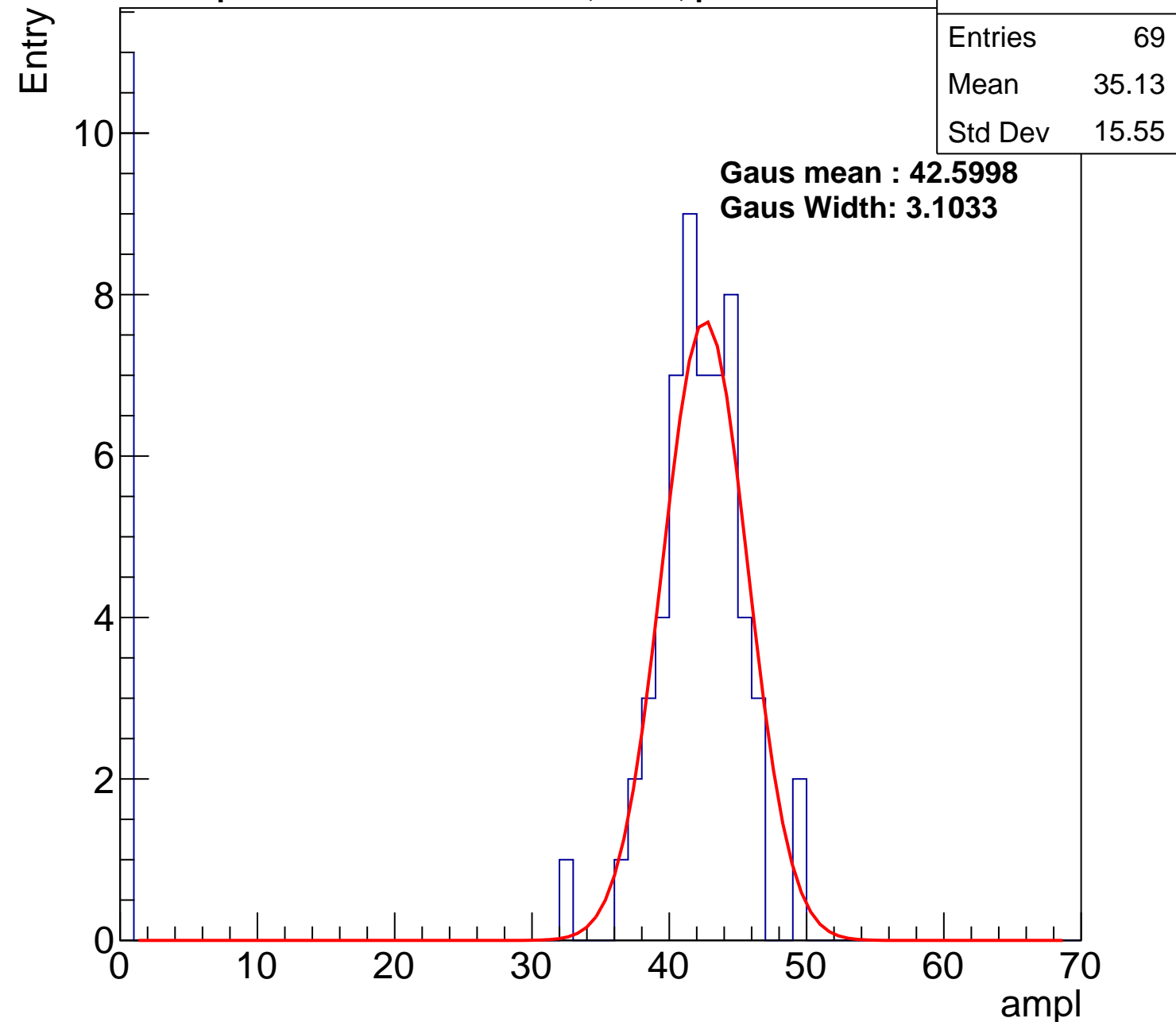
40

50

60

70

ampl



# B1L103S, U22-ch16, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	48.25
Std Dev	8.083

Entry

10

8

6

4

2

0

0

10

20

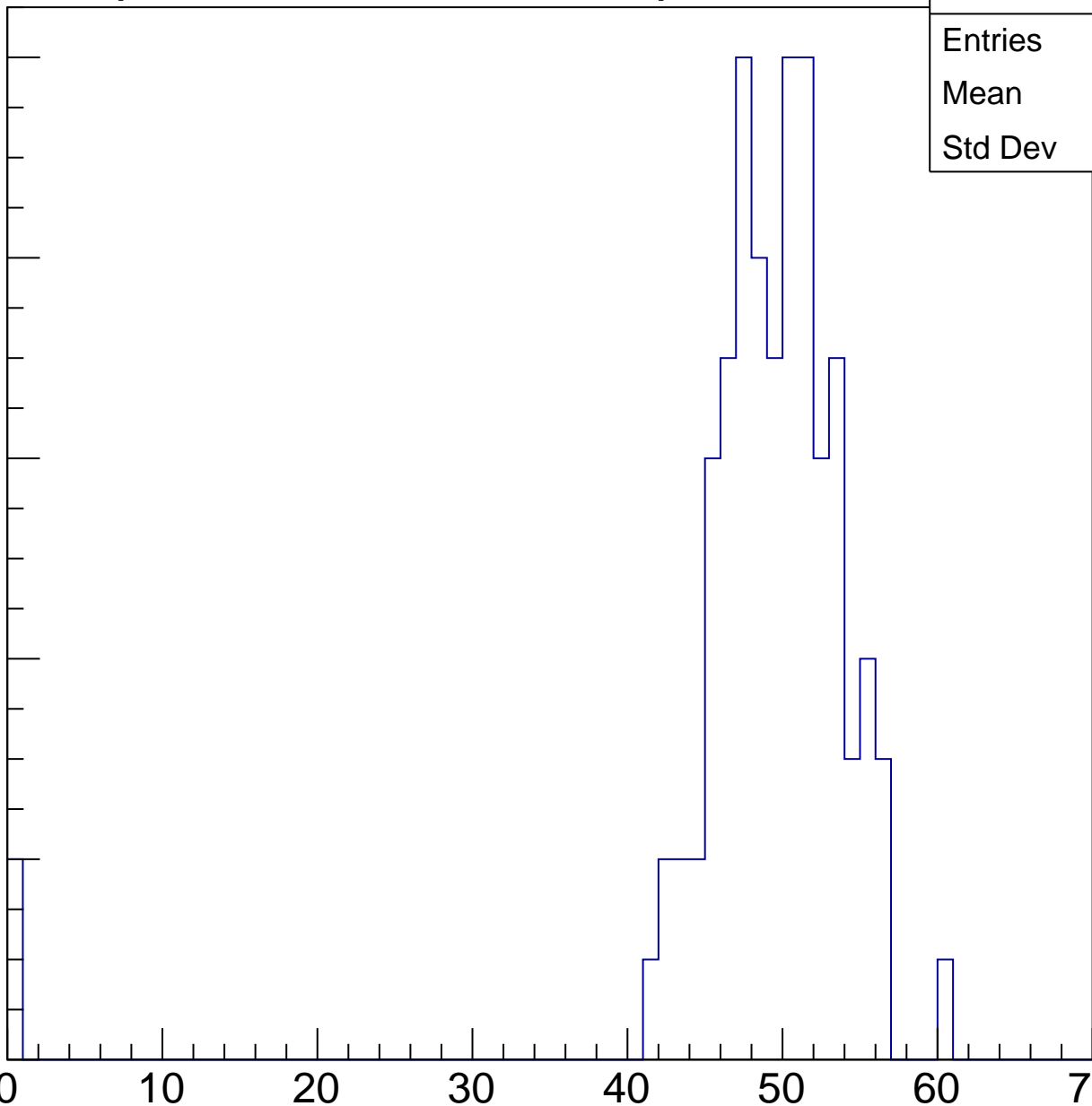
30

40

50

60

ampl

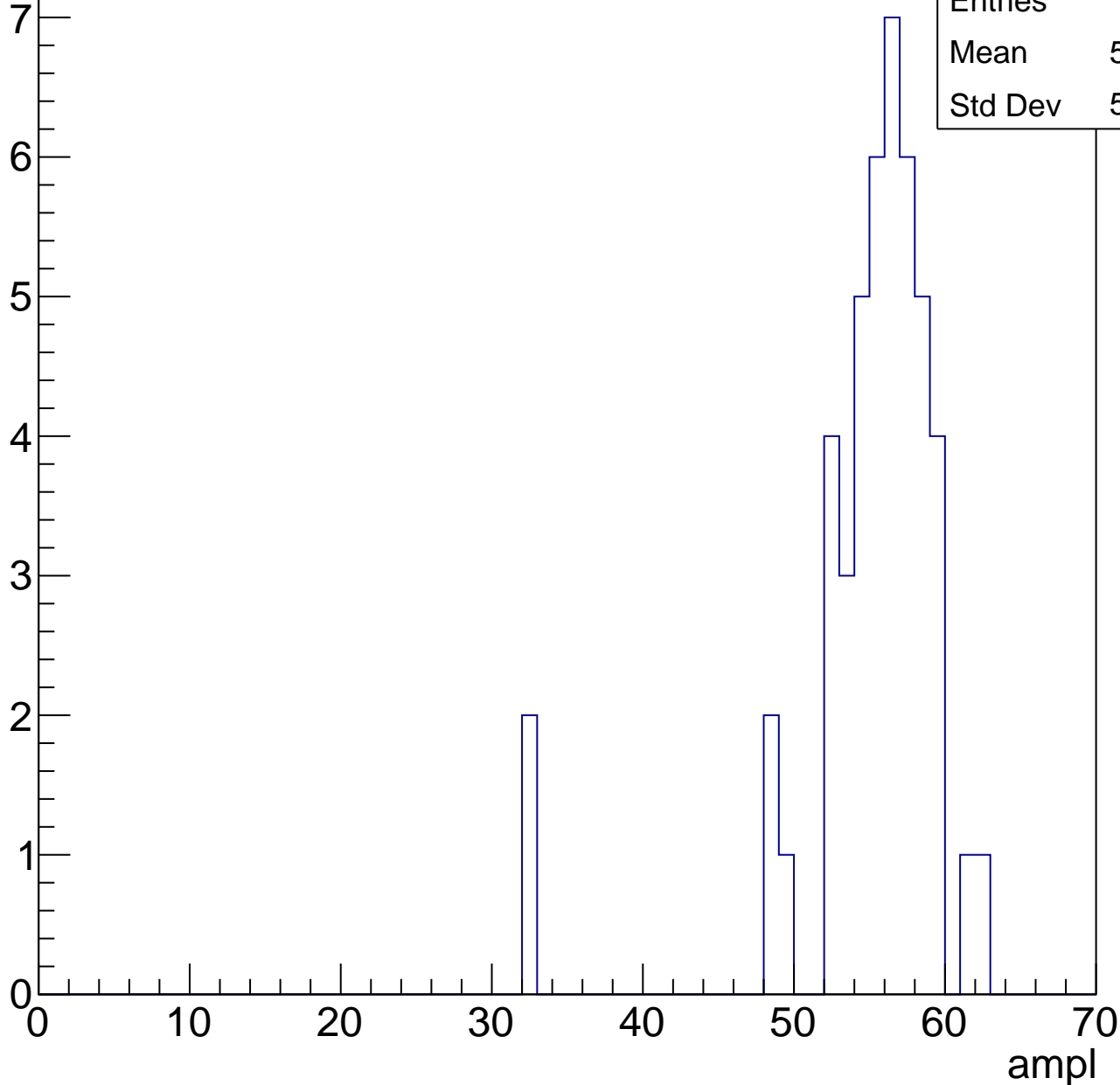


# B1L103S, U22-ch16, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

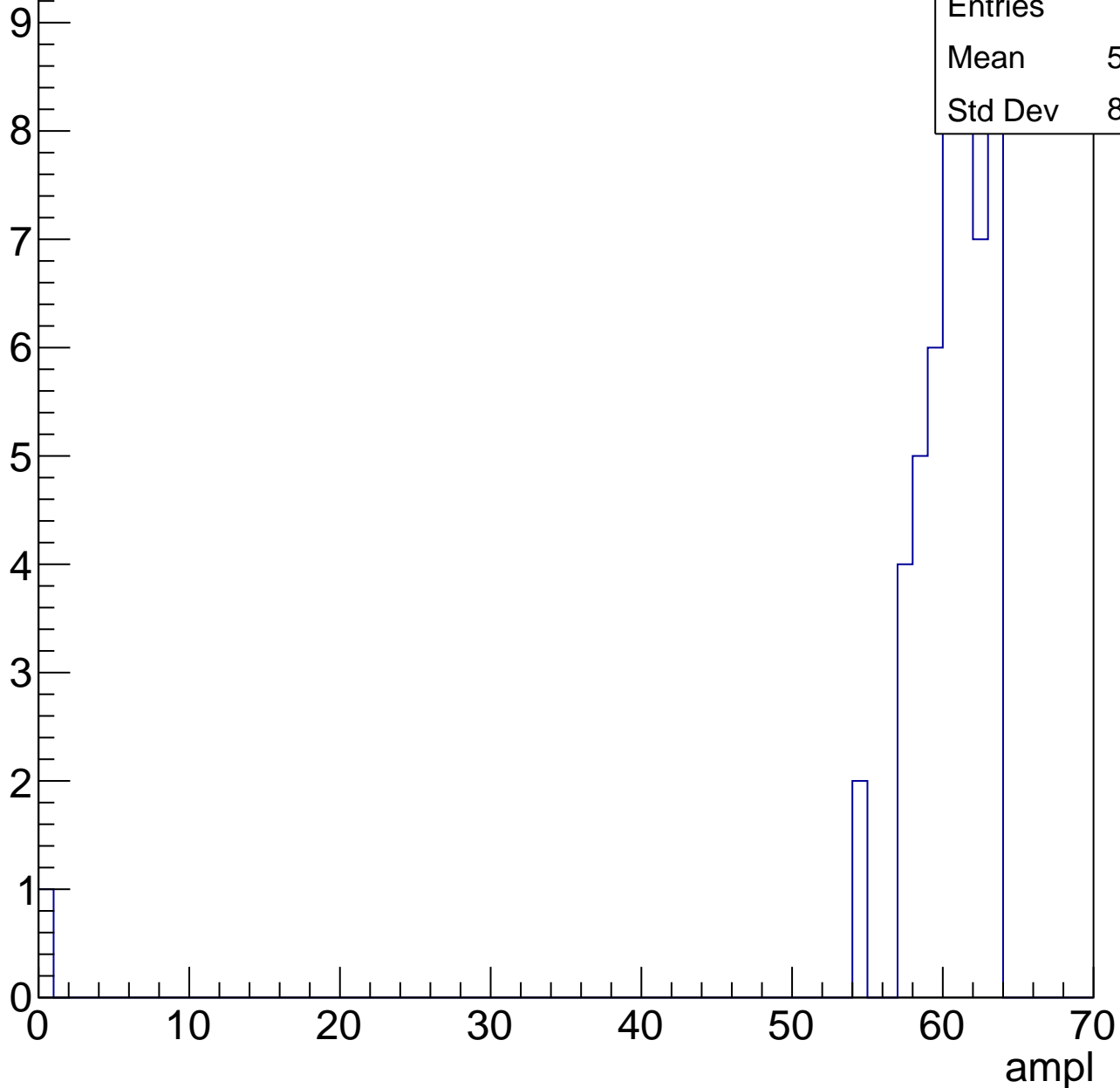
Entries	47
Mean	54.45
Std Dev	5.565



# B1L103S, U22-ch16, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch16, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch16, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch17, adc0

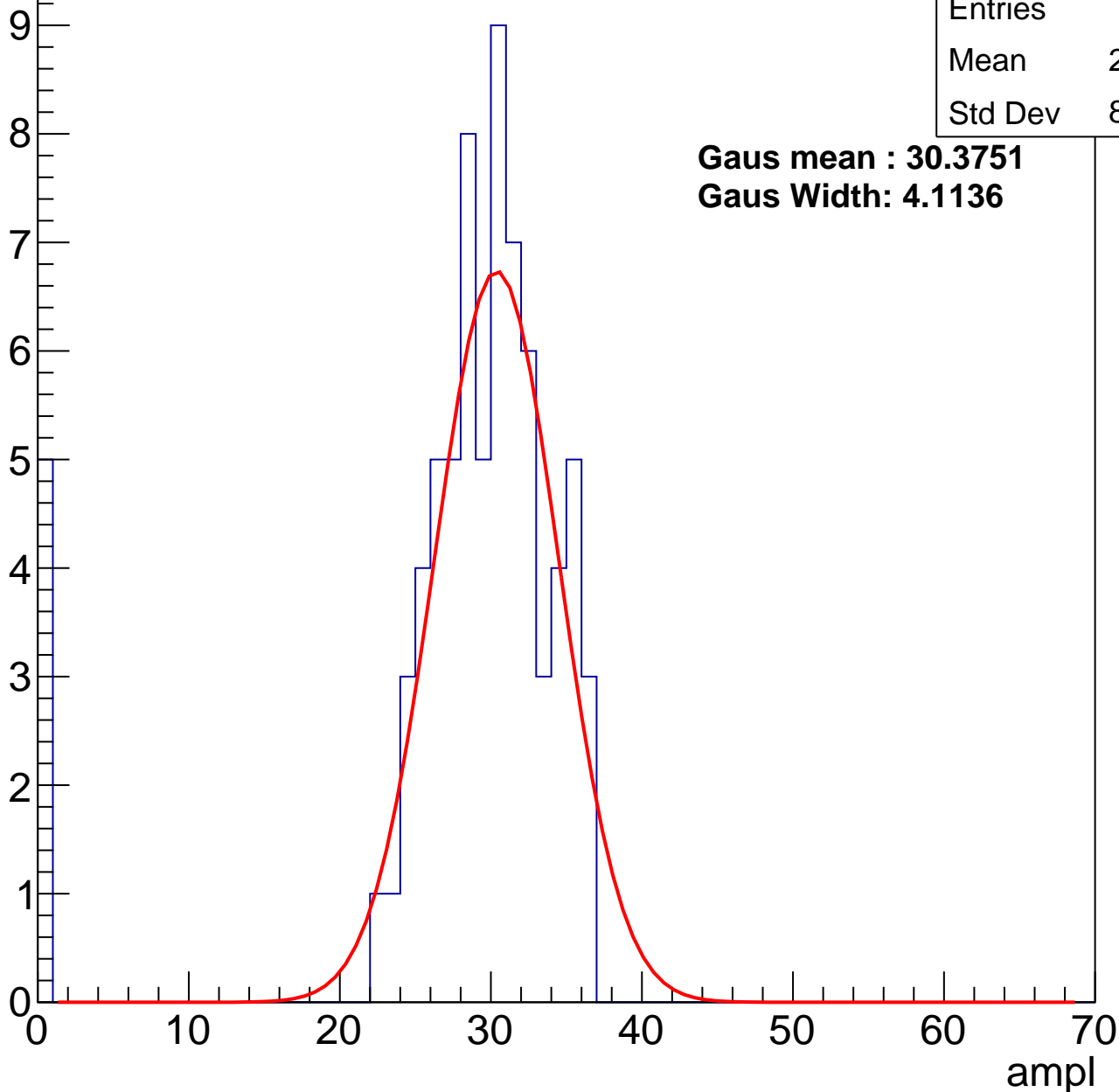
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	27.68
Std Dev	8.169

**Gaus mean : 30.3751**

**Gaus Width: 4.1136**



# B1L103S, U22-ch17, adc1

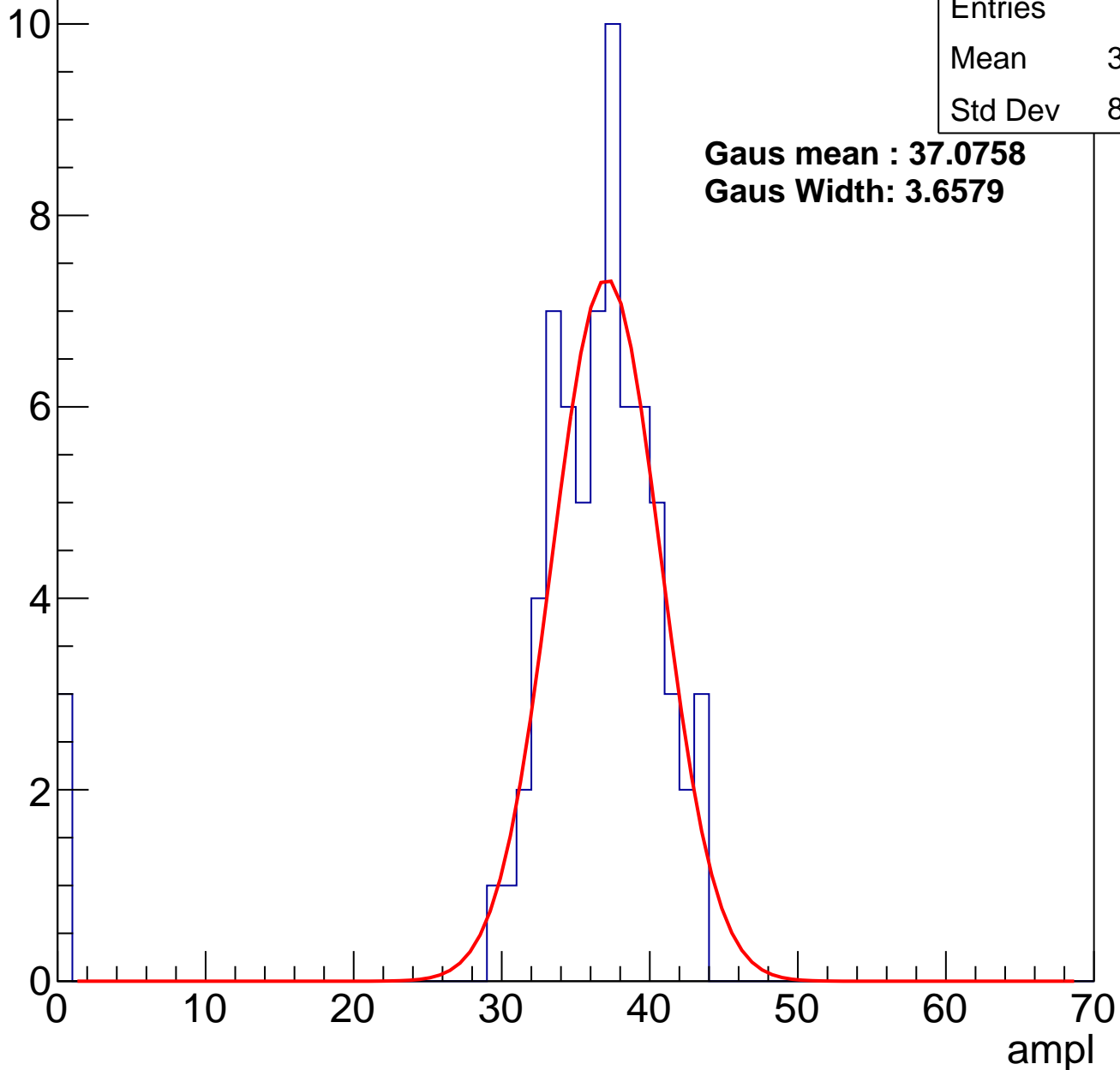
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	71
Mean	34.92
Std Dev	8.012

**Gaus mean : 37.0758**

**Gaus Width: 3.6579**

Entry



# B1L103S, U22-ch17, adc2

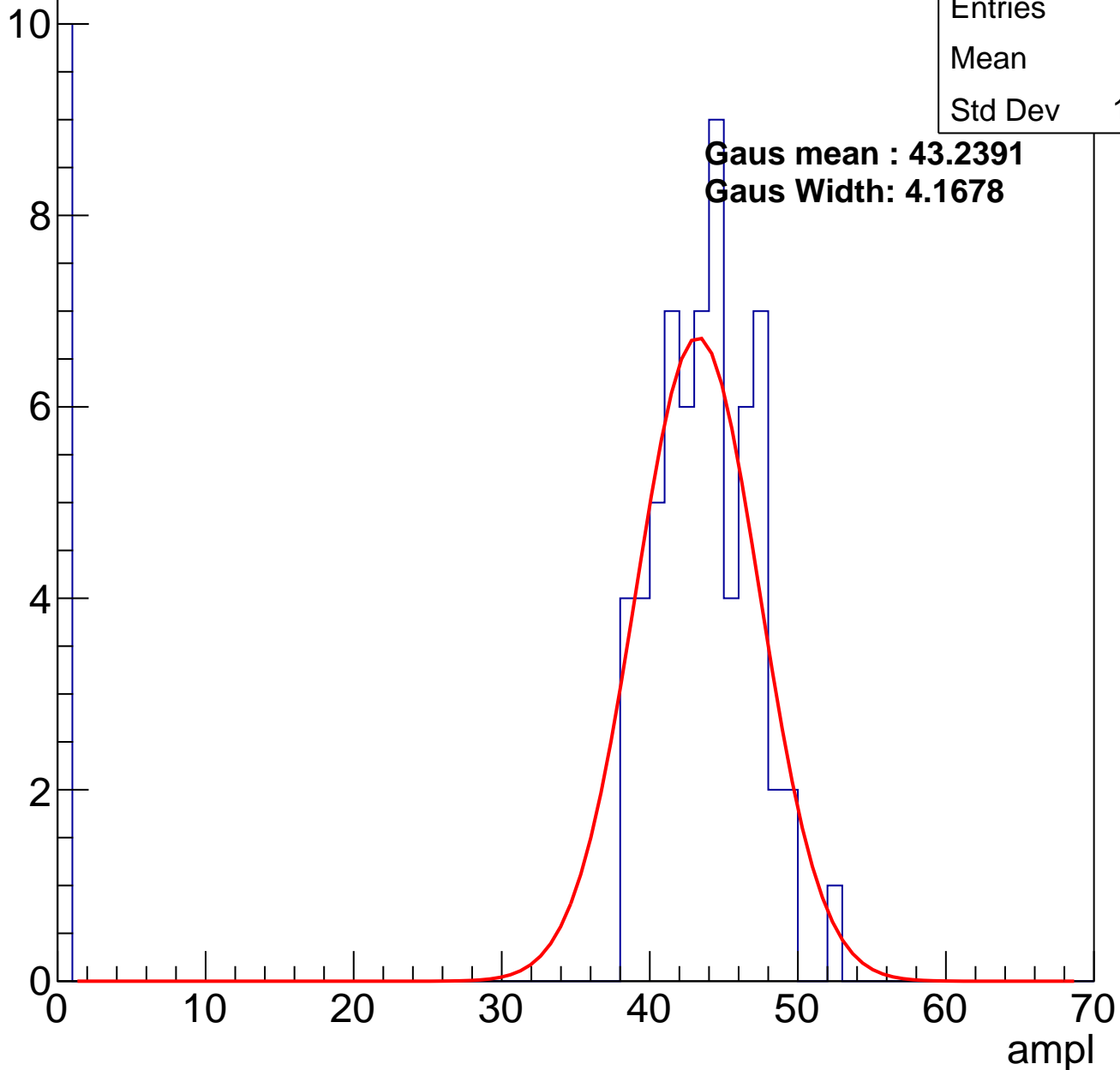
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	74
Mean	37.5
Std Dev	15.11

**Gaus mean : 43.2391**

**Gaus Width: 4.1678**

Entry



# B1L103S, U22-ch17, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	70
Mean	48.94
Std Dev	6.729

Entry

10

8

6

4

2

0

0

10

20

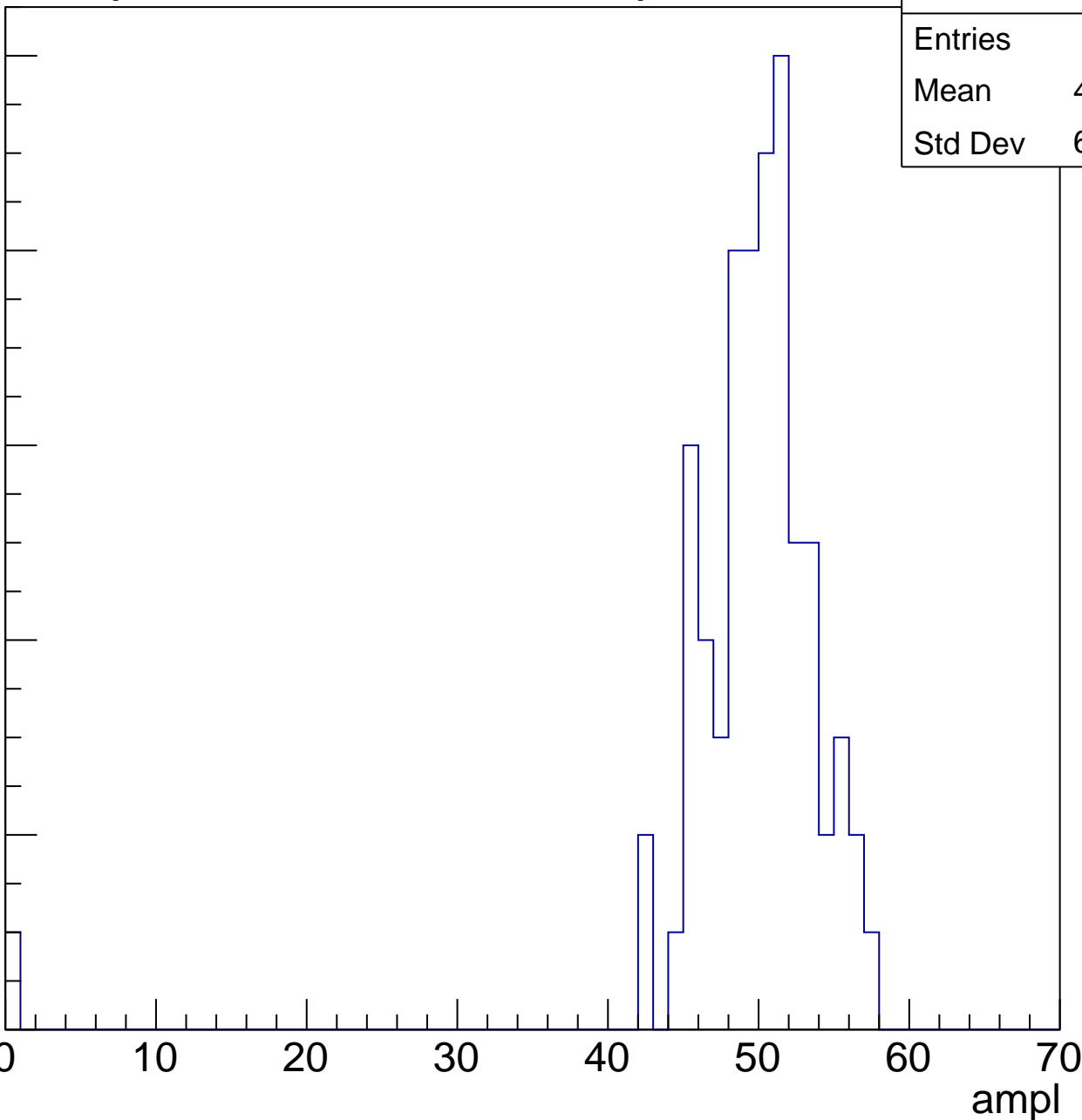
30

40

50

60

ampl

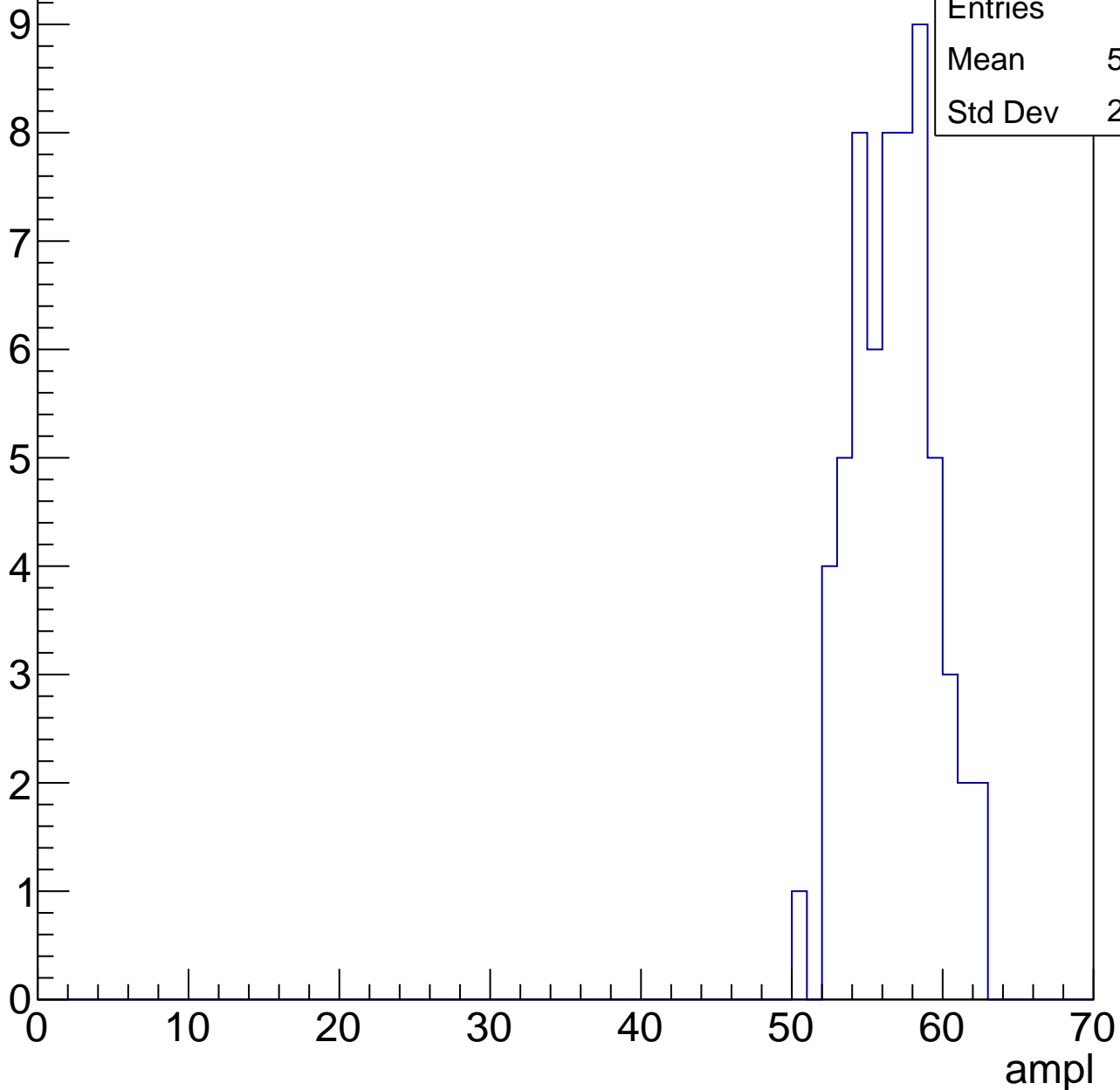


# B1L103S, U22-ch17, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	56.26
Std Dev	2.679

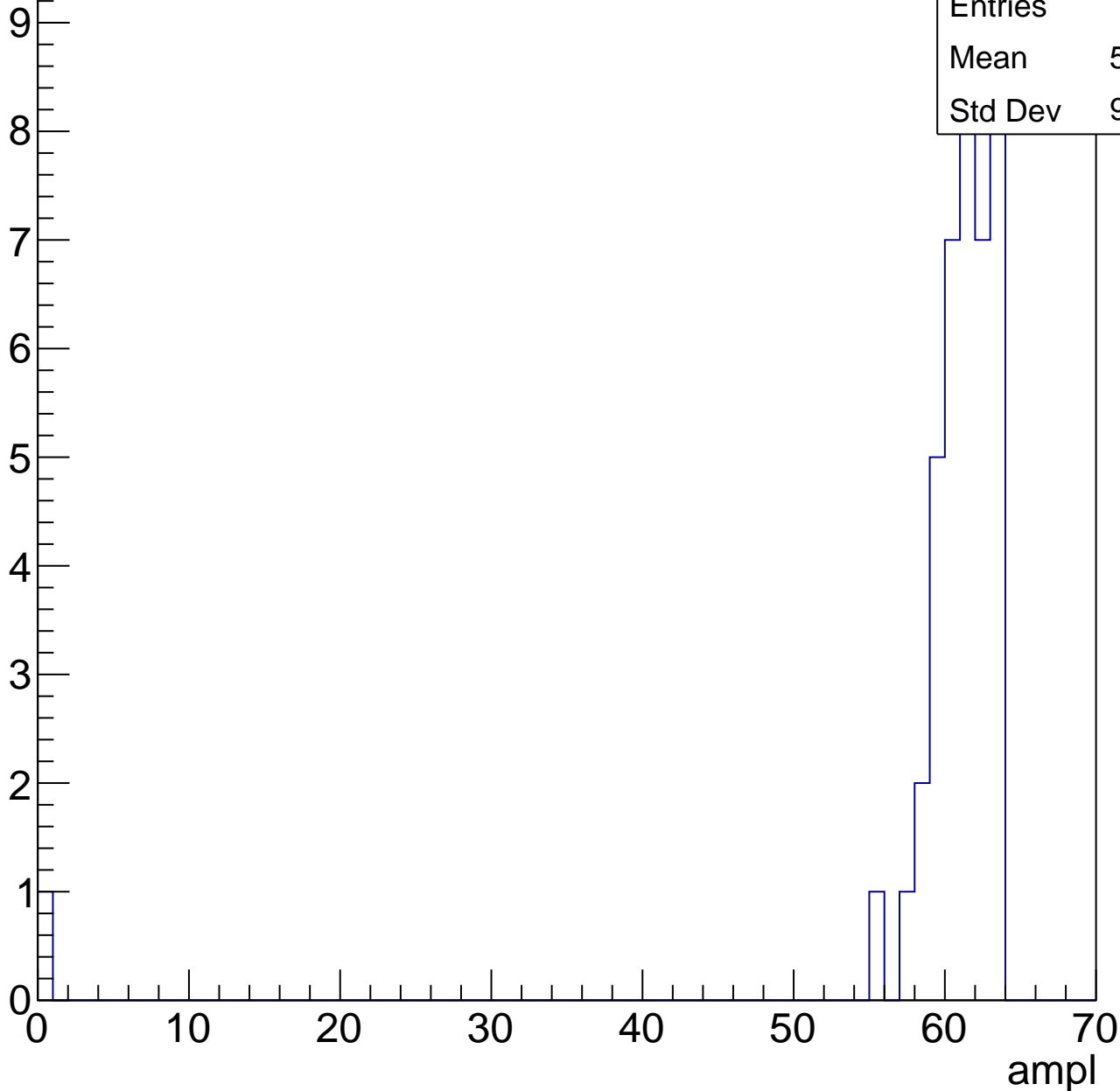


# B1L103S, U22-ch17, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	41
Mean	59.32
Std Dev	9.557



# B1L103S, U22-ch17, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch17, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



# B1L103S, U22-ch18, adc0

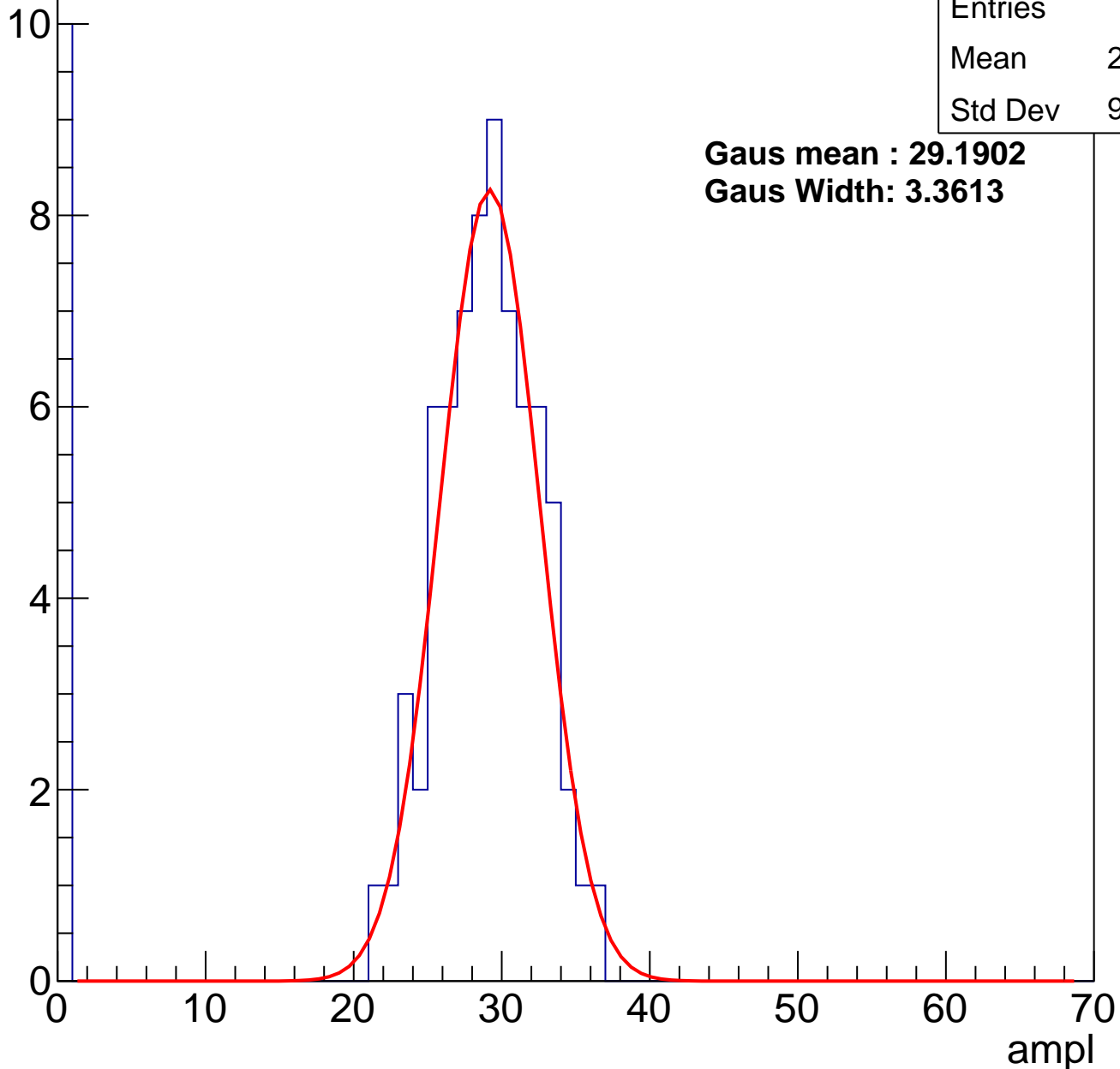
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	25.09
Std Dev	9.892

**Gaus mean : 29.1902**

**Gaus Width: 3.3613**

Entry



# B1L103S, U22-ch18, adc1

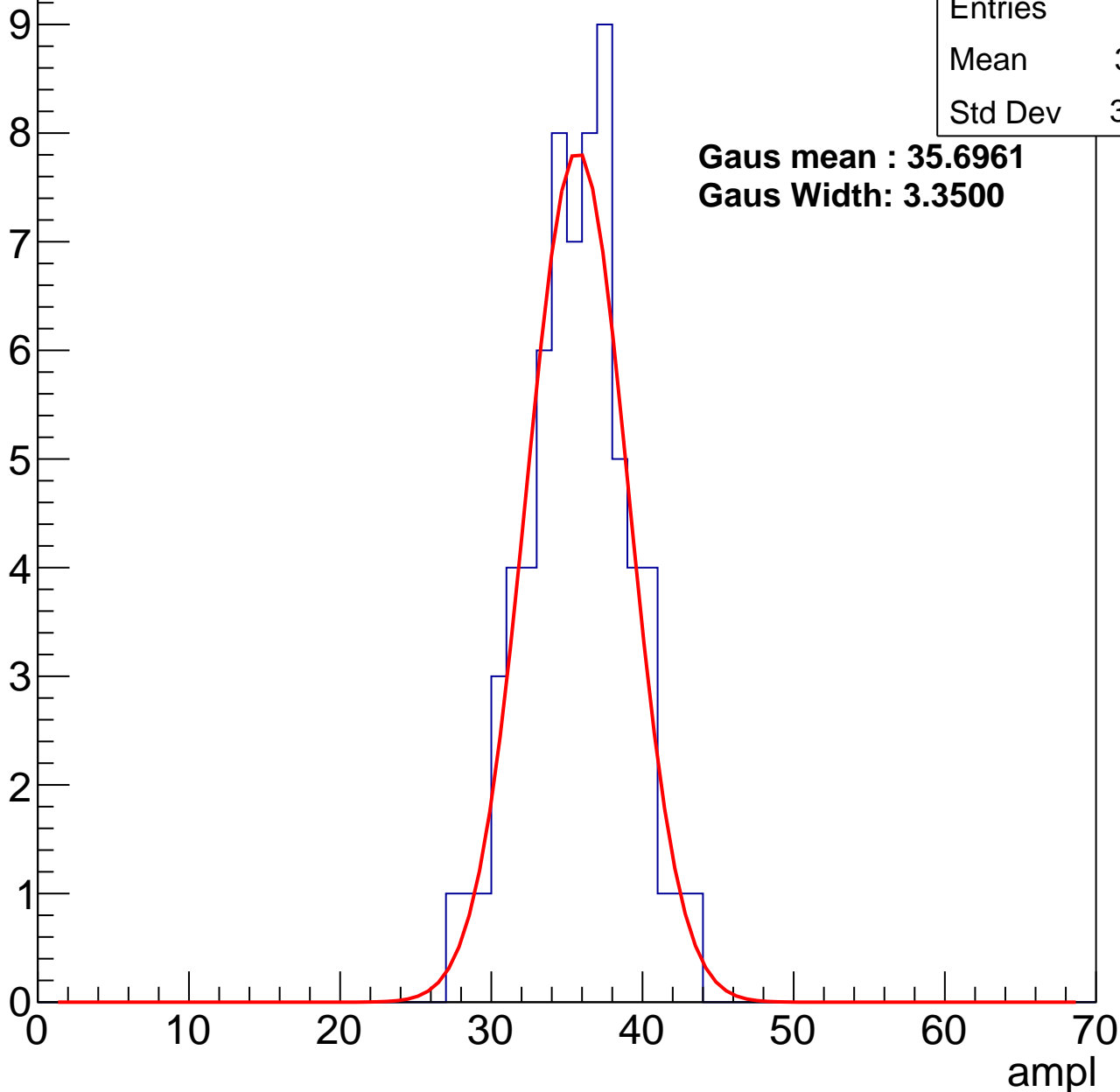
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.21
Std Dev	3.332

**Gaus mean : 35.6961**

**Gaus Width: 3.3500**



# B1L103S, U22-ch18, adc2

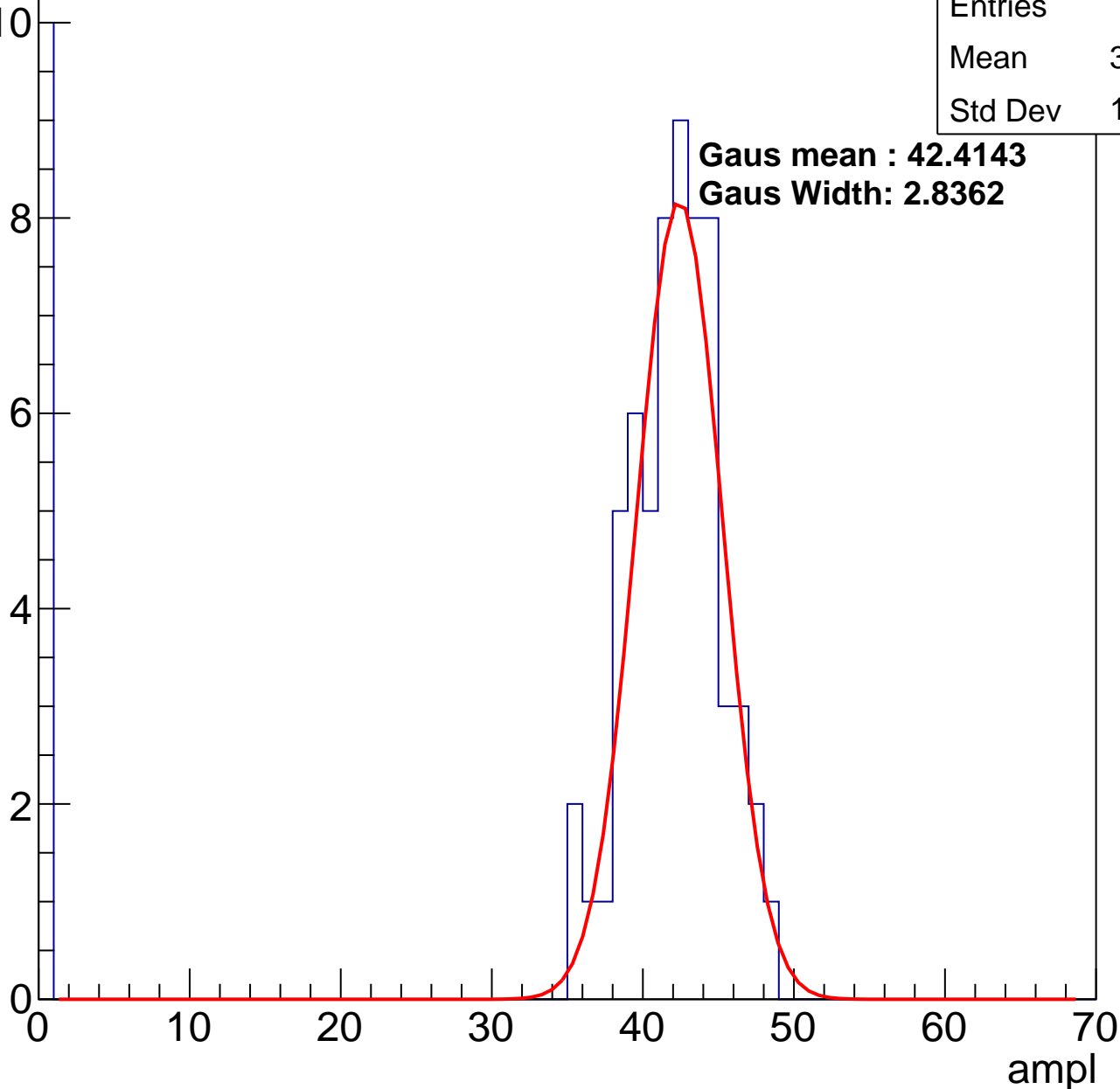
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	35.89
Std Dev	14.66

**Gaus mean : 42.4143**

**Gaus Width: 2.8362**

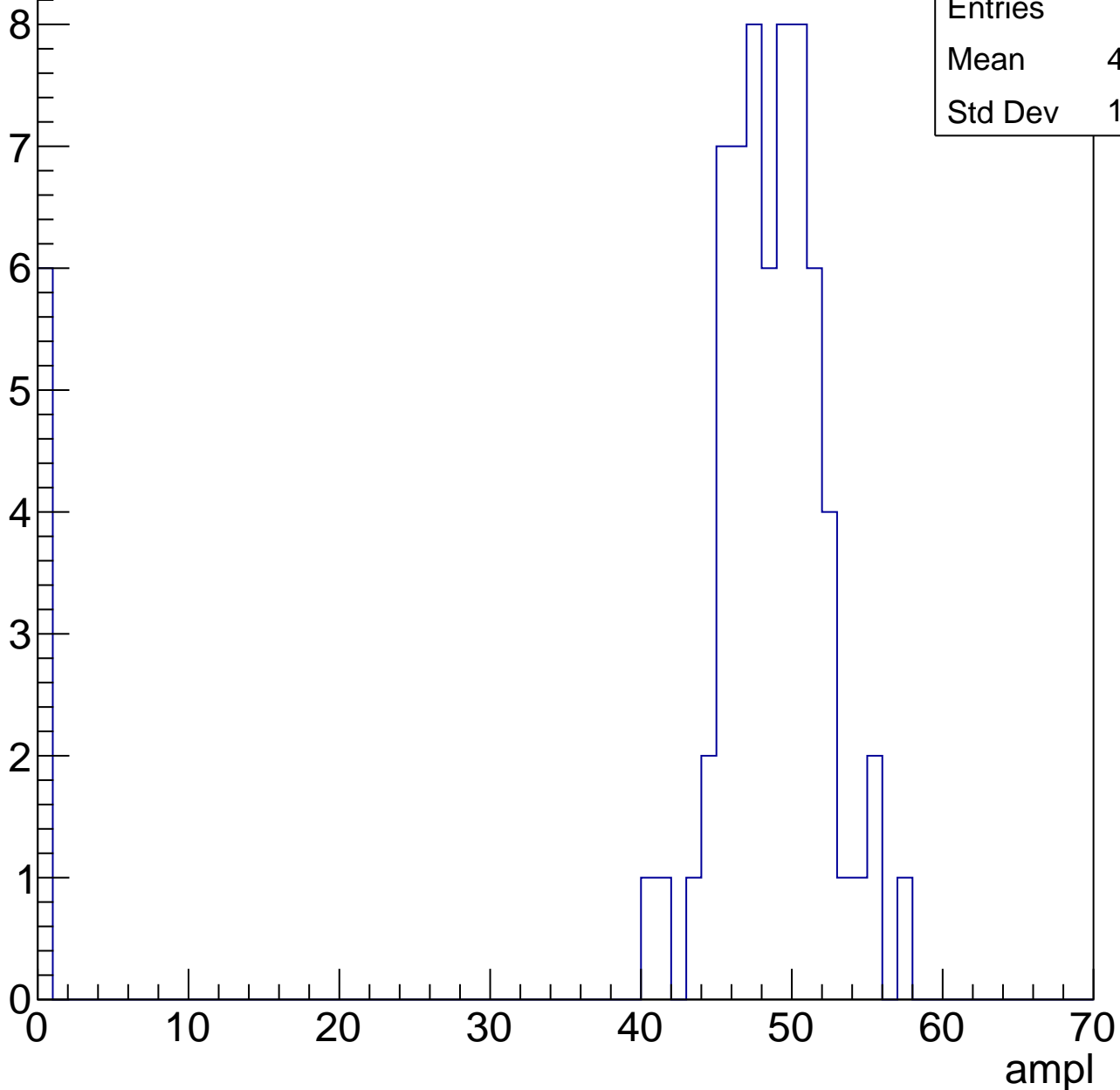


# B1L103S, U22-ch18, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

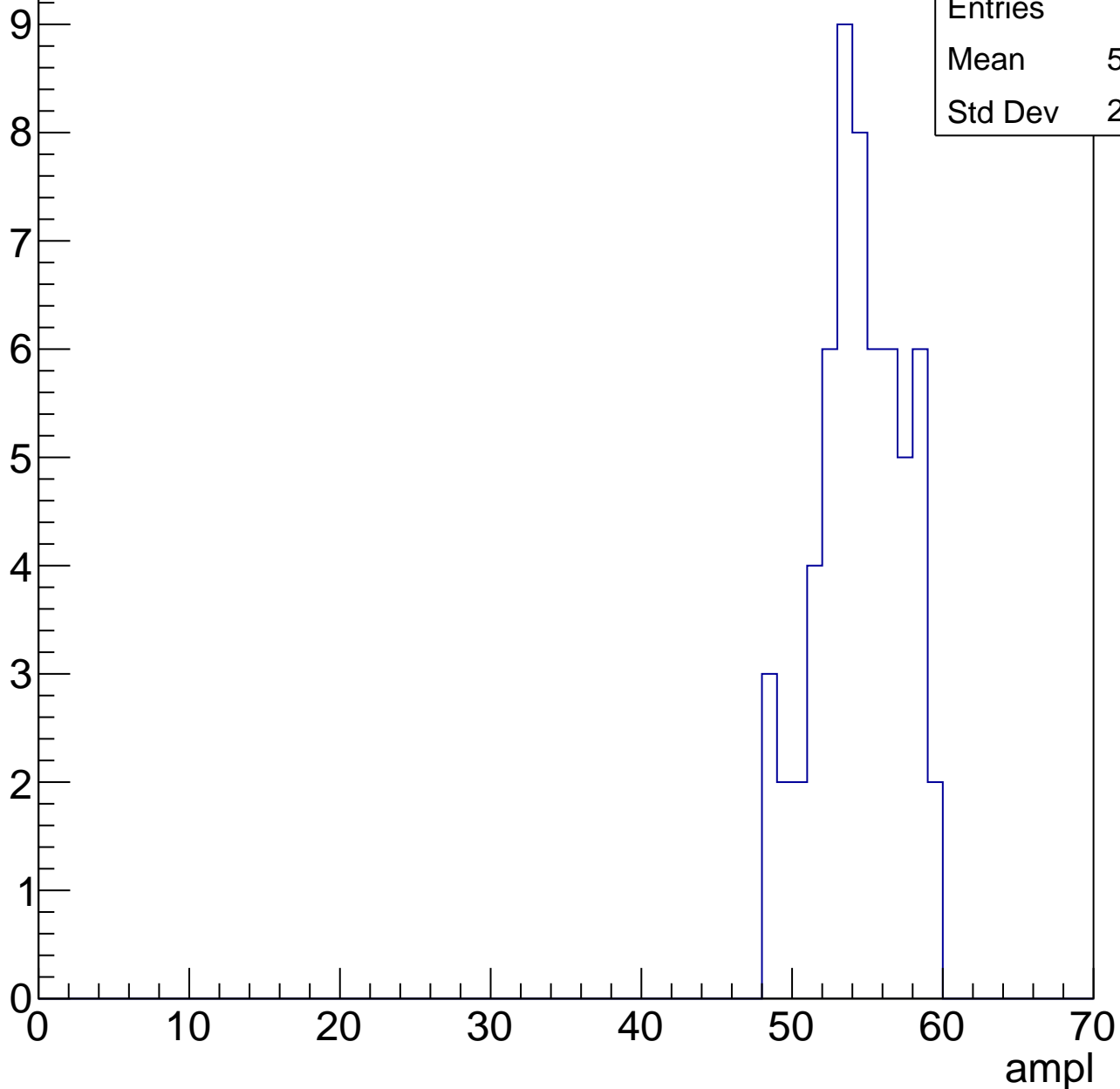
Entries	70
Mean	44.19
Std Dev	13.87



# B1L103S, U22-ch18, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

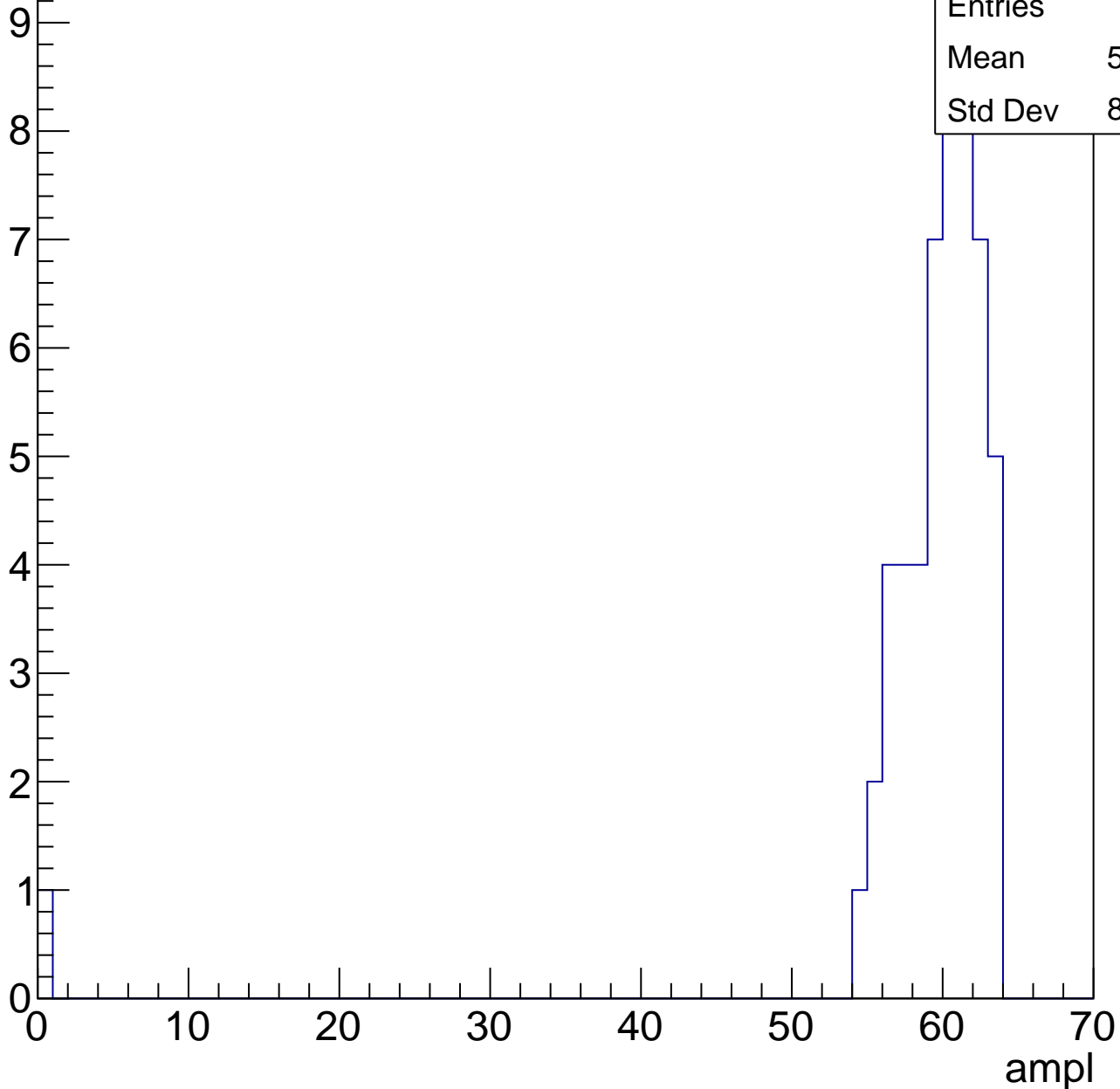
Entry



# B1L103S, U22-ch18, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

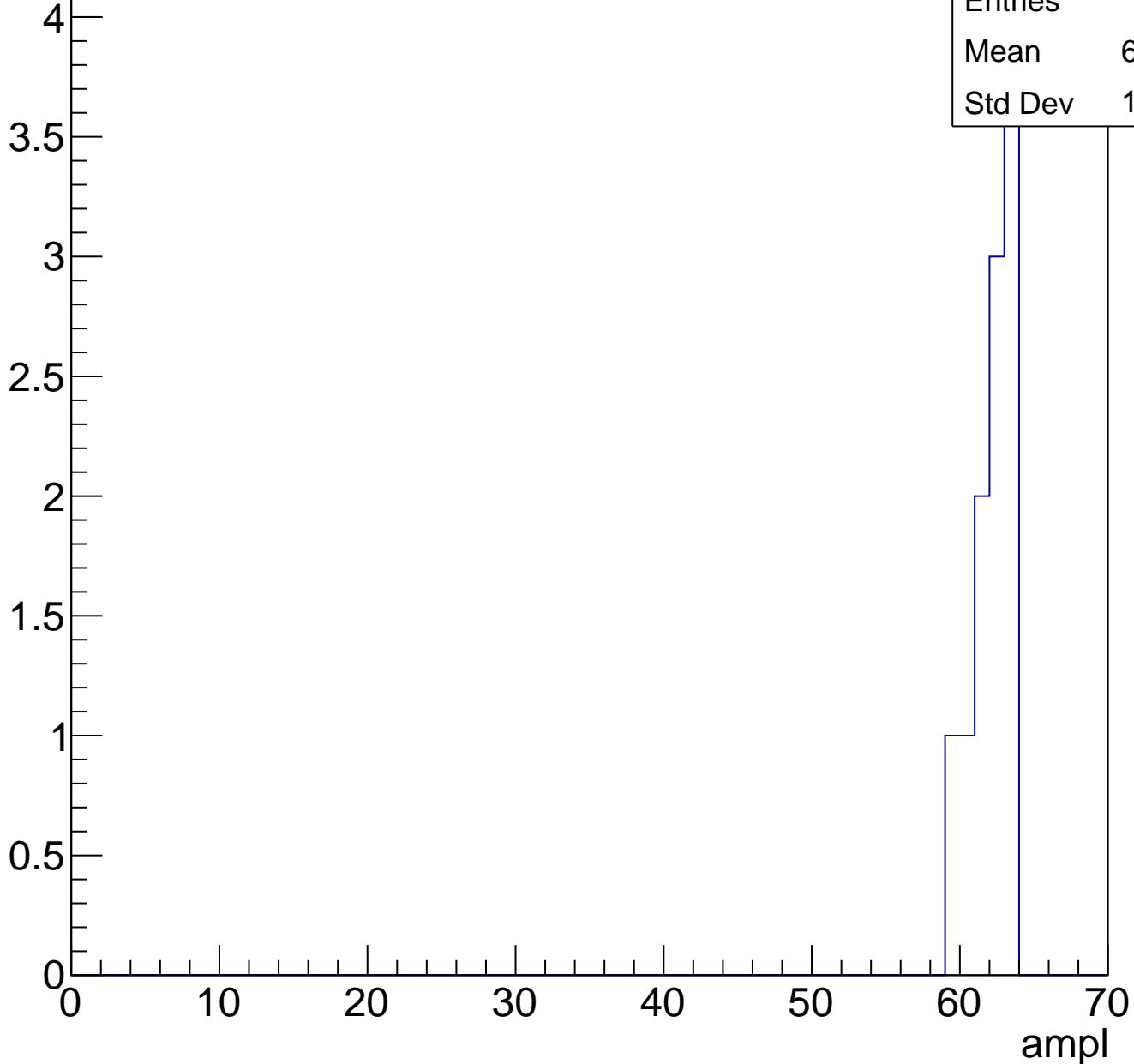
Entry



# B1L103S, U22-ch18, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

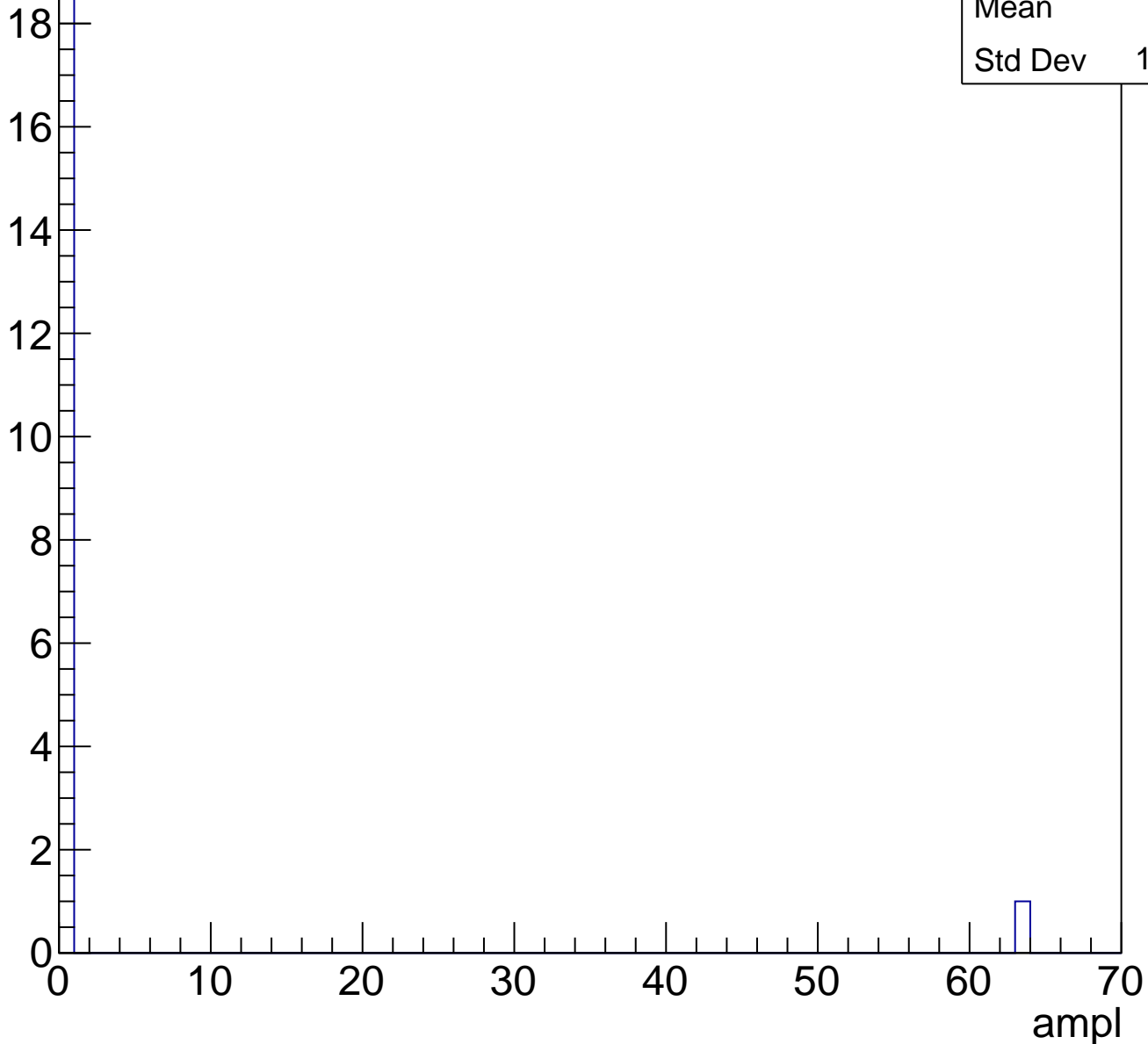




# B1L103S, U22-ch18, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	20
Mean	3.15
Std Dev	13.73

# B1L103S, U22-ch19, adc0

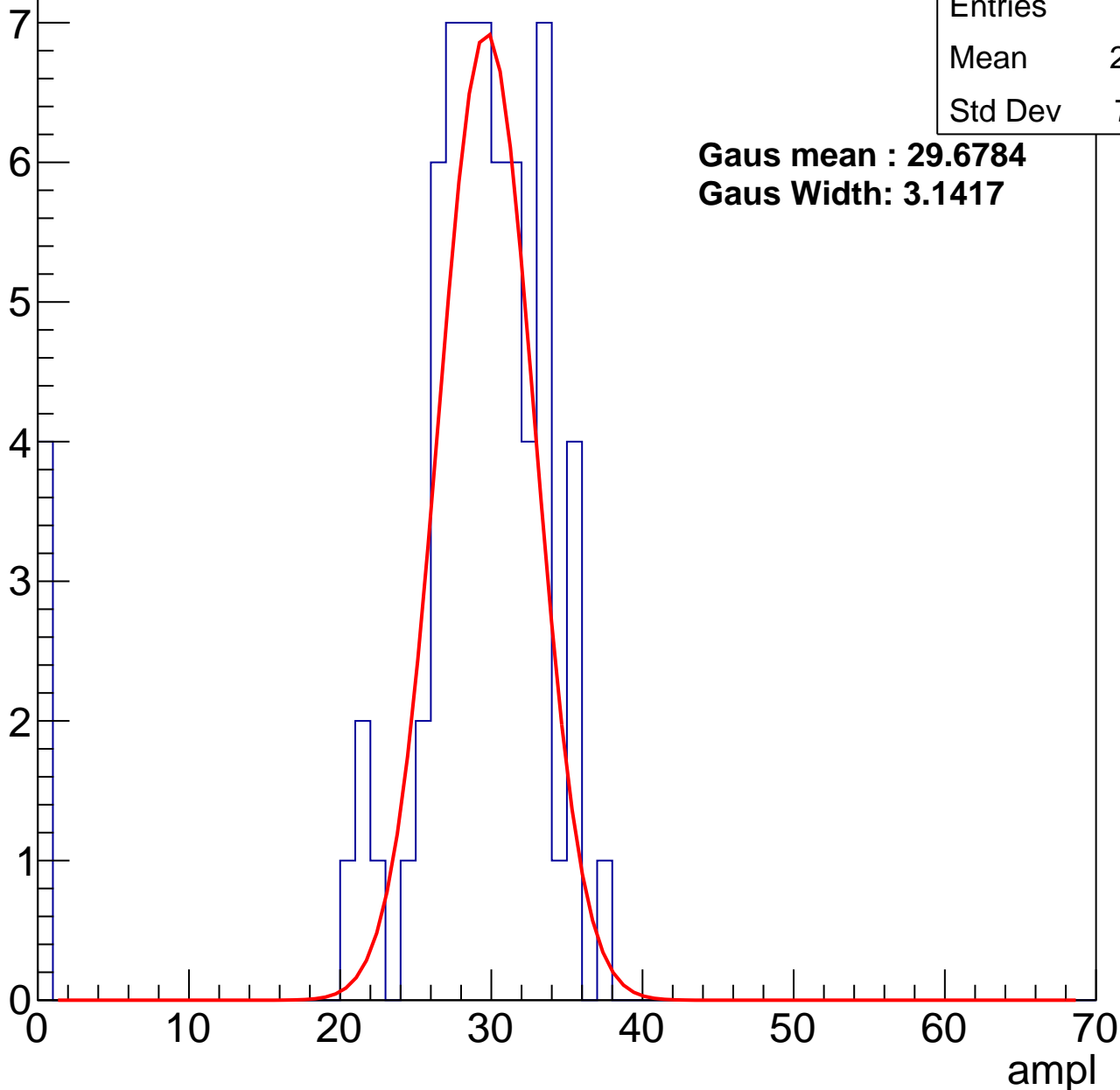
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	27.43
Std Dev	7.741

**Gaus mean : 29.6784**

**Gaus Width: 3.1417**



# B1L103S, U22-ch19, adc1

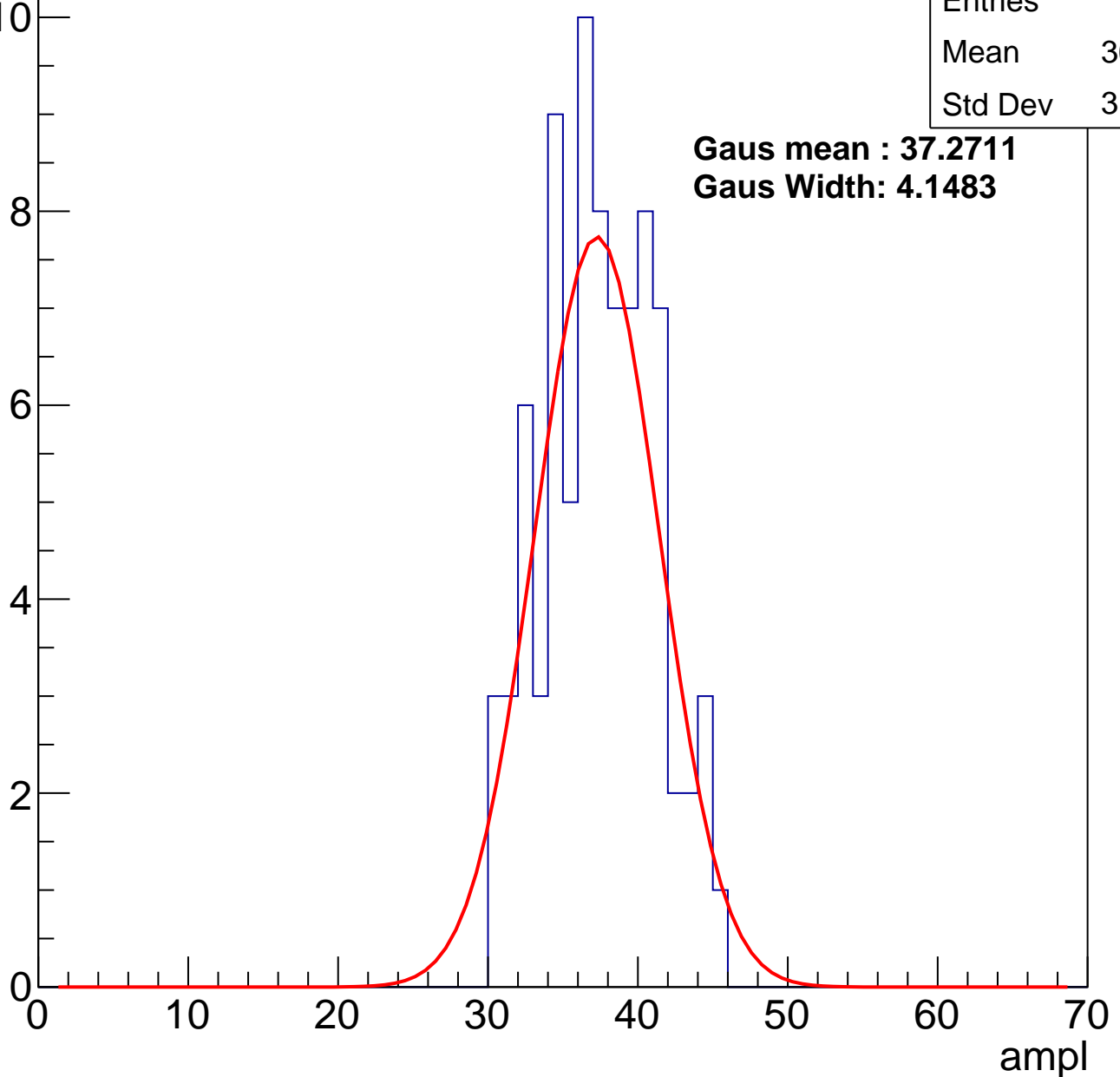
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	36.95
Std Dev	3.645

**Gaus mean : 37.2711**

**Gaus Width: 4.1483**



# B1L103S, U22-ch19, adc2

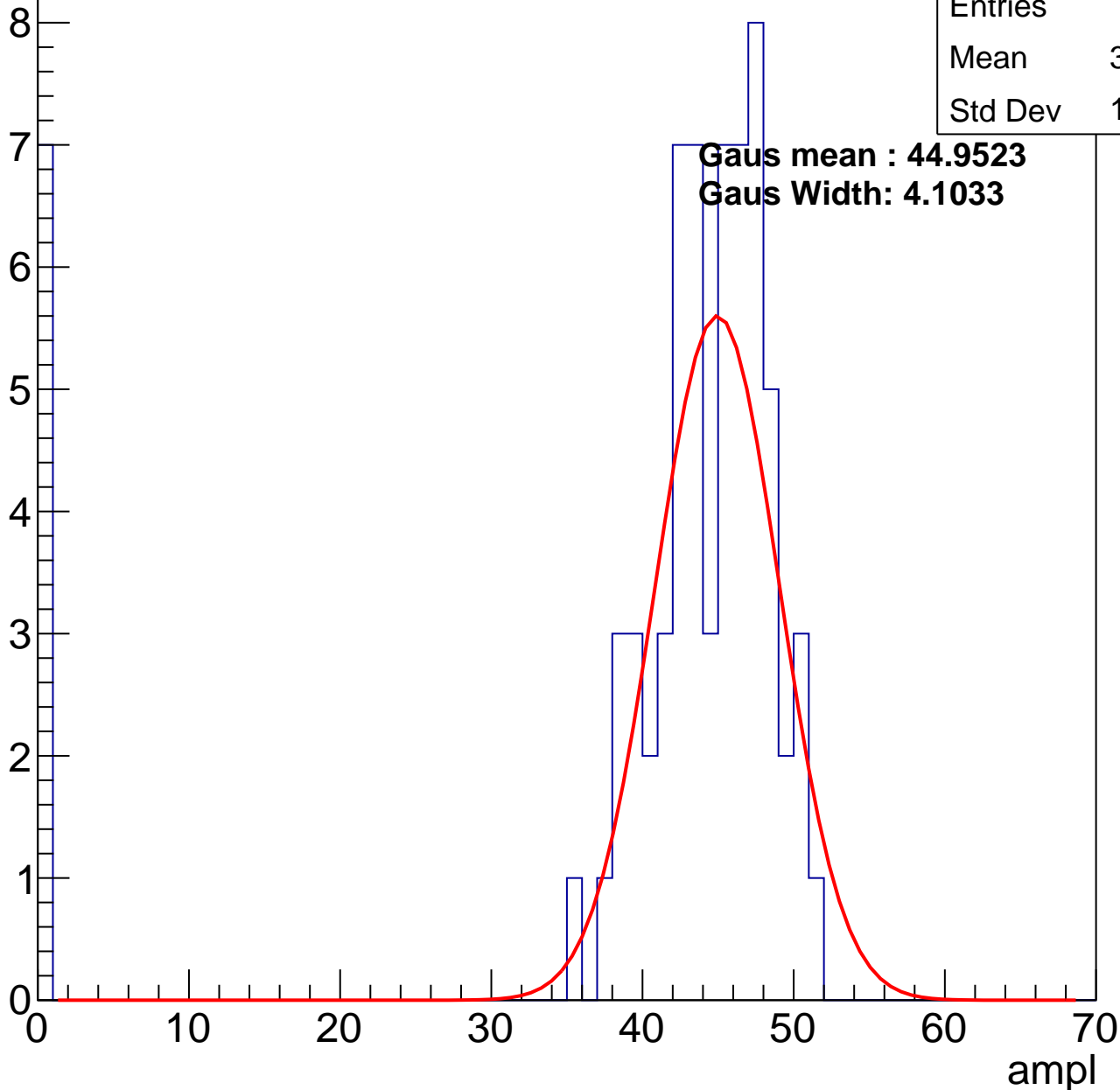
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	39.79
Std Dev	13.69

**Gaus mean : 44.9523**

**Gaus Width: 4.1033**

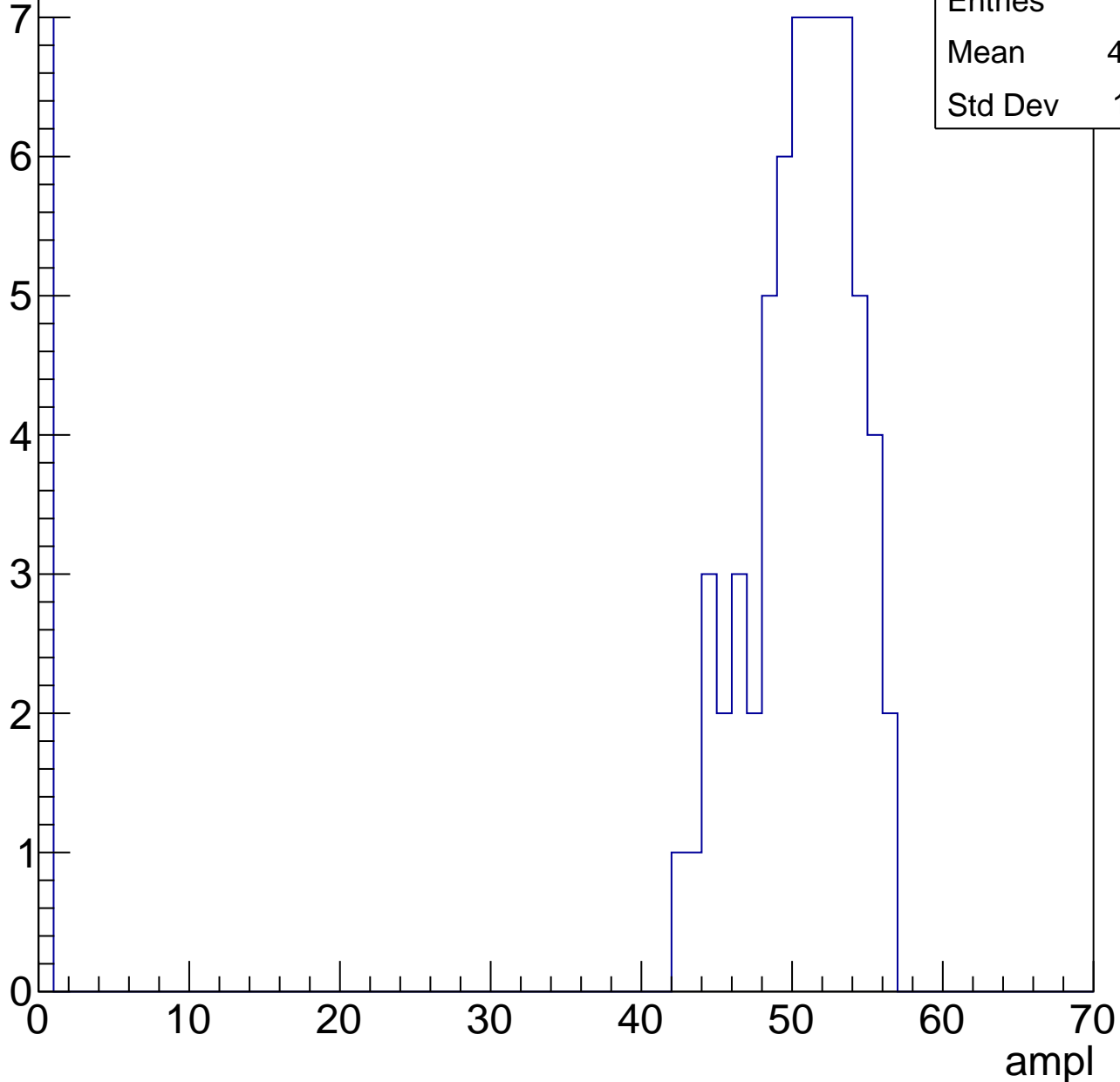


# B1L103S, U22-ch19, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	45.17
Std Dev	15.51

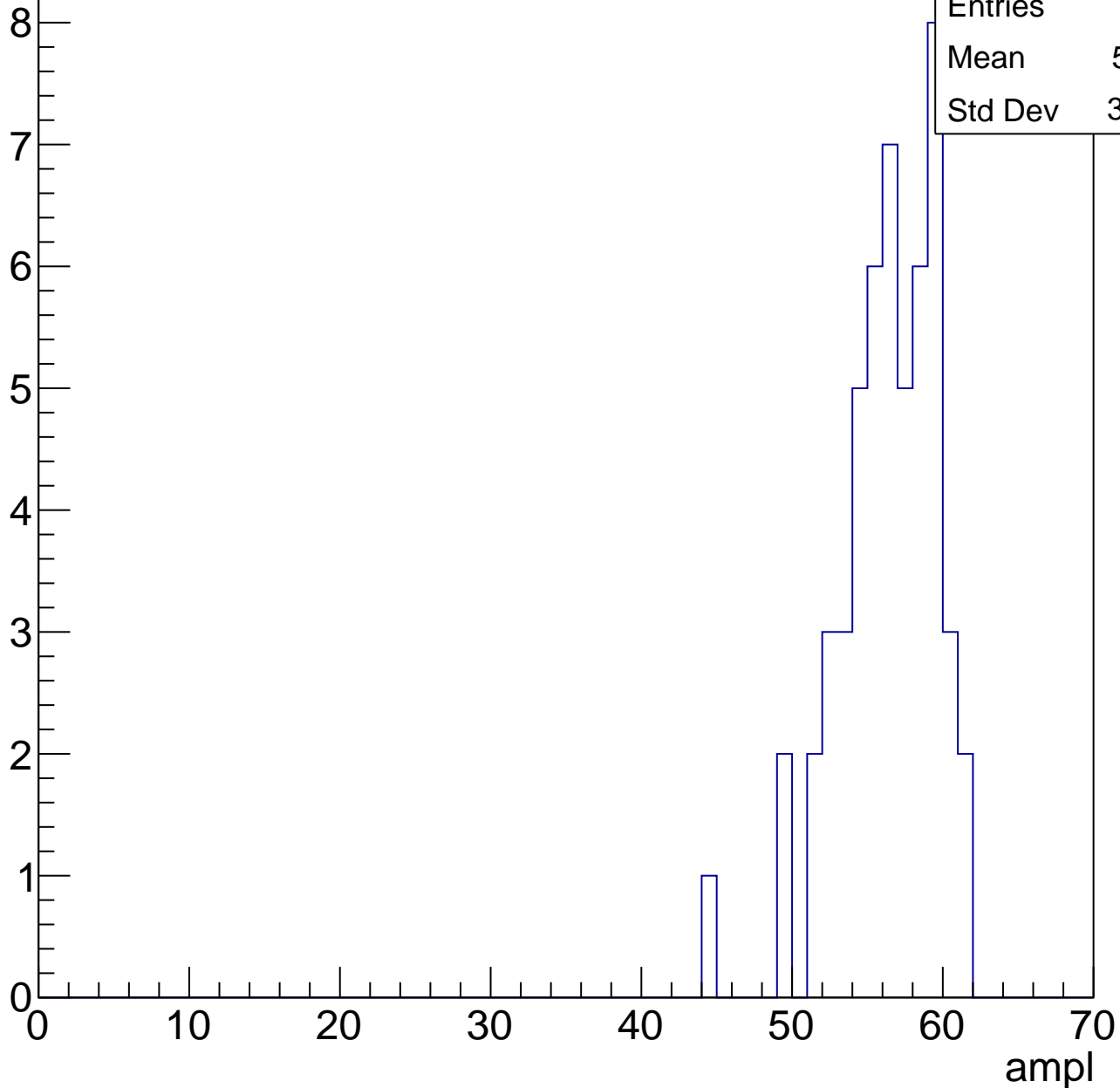


# B1L103S, U22-ch19, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	55.81
Std Dev	3.337

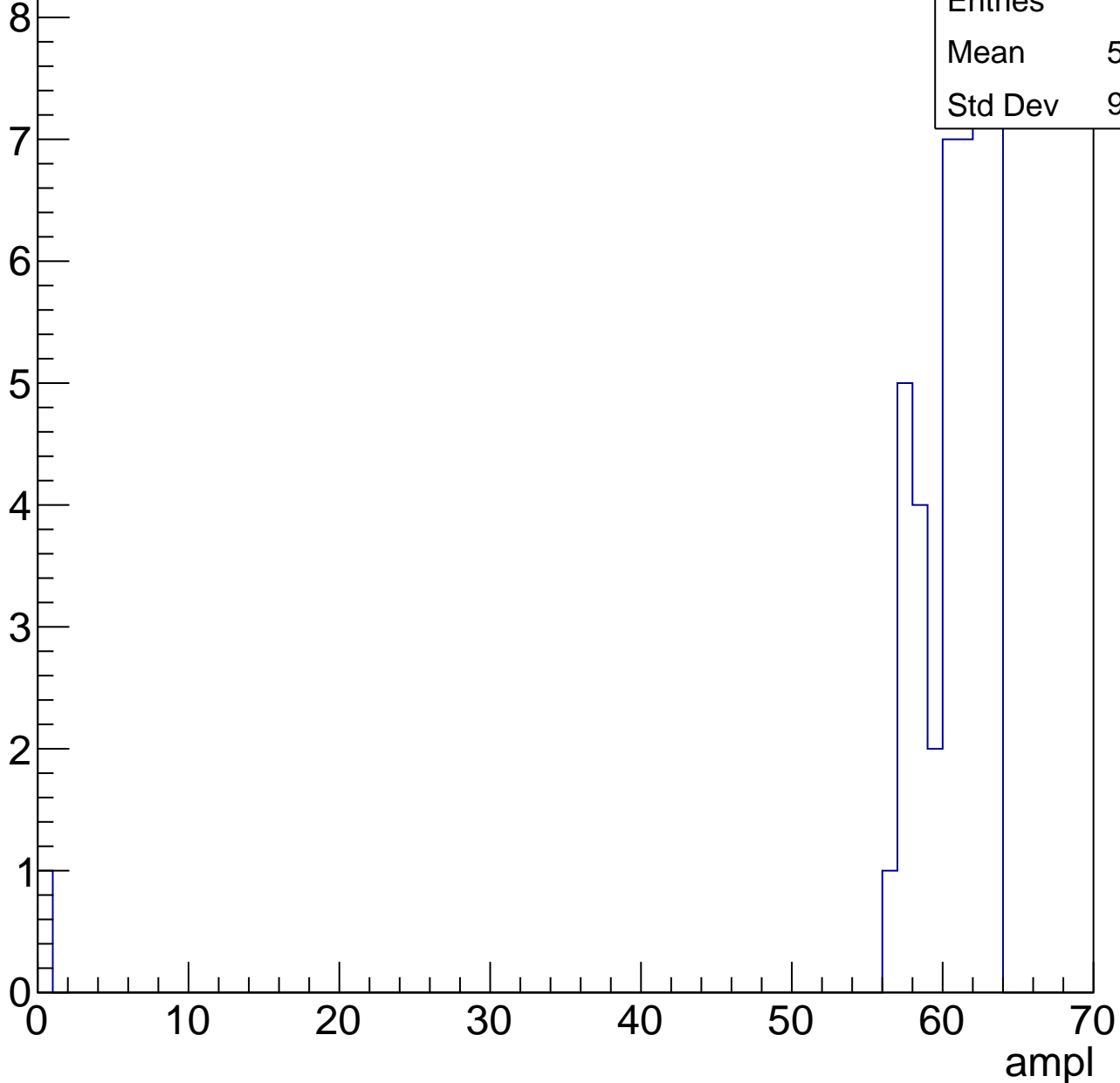


# B1L103S, U22-ch19, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	43
Mean	59.02
Std Dev	9.337



# B1L103S, U22-ch19, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch19, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch20, adc0

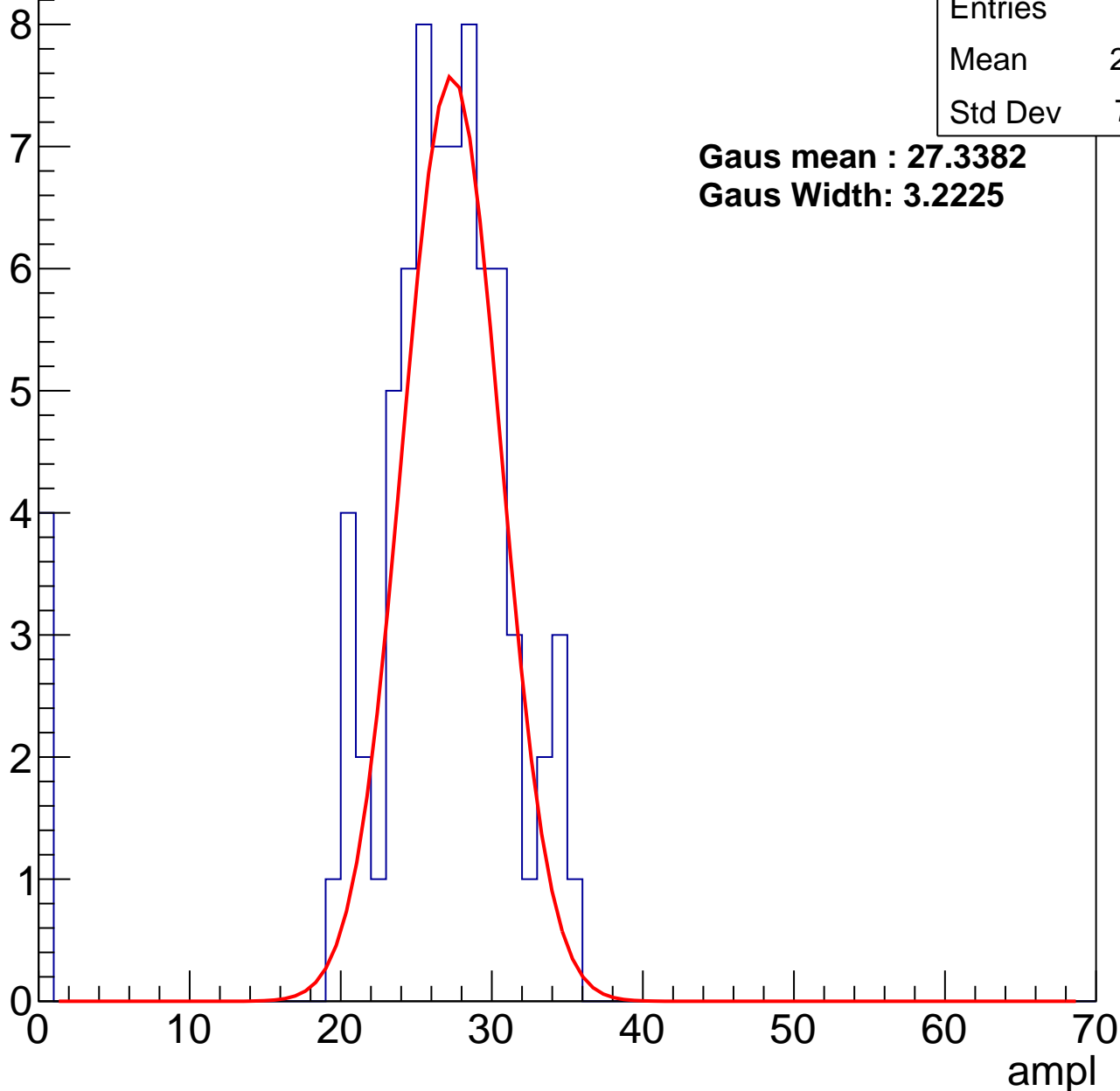
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	25.32
Std Dev	7.001

**Gaus mean : 27.3382**

**Gaus Width: 3.2225**



# B1L103S, U22-ch20, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	29.94
Std Dev	11.35

**Gaus mean : 34.4546**

**Gaus Width: 3.9089**

Entry

10

8

6

4

2

0

0

10

20

30

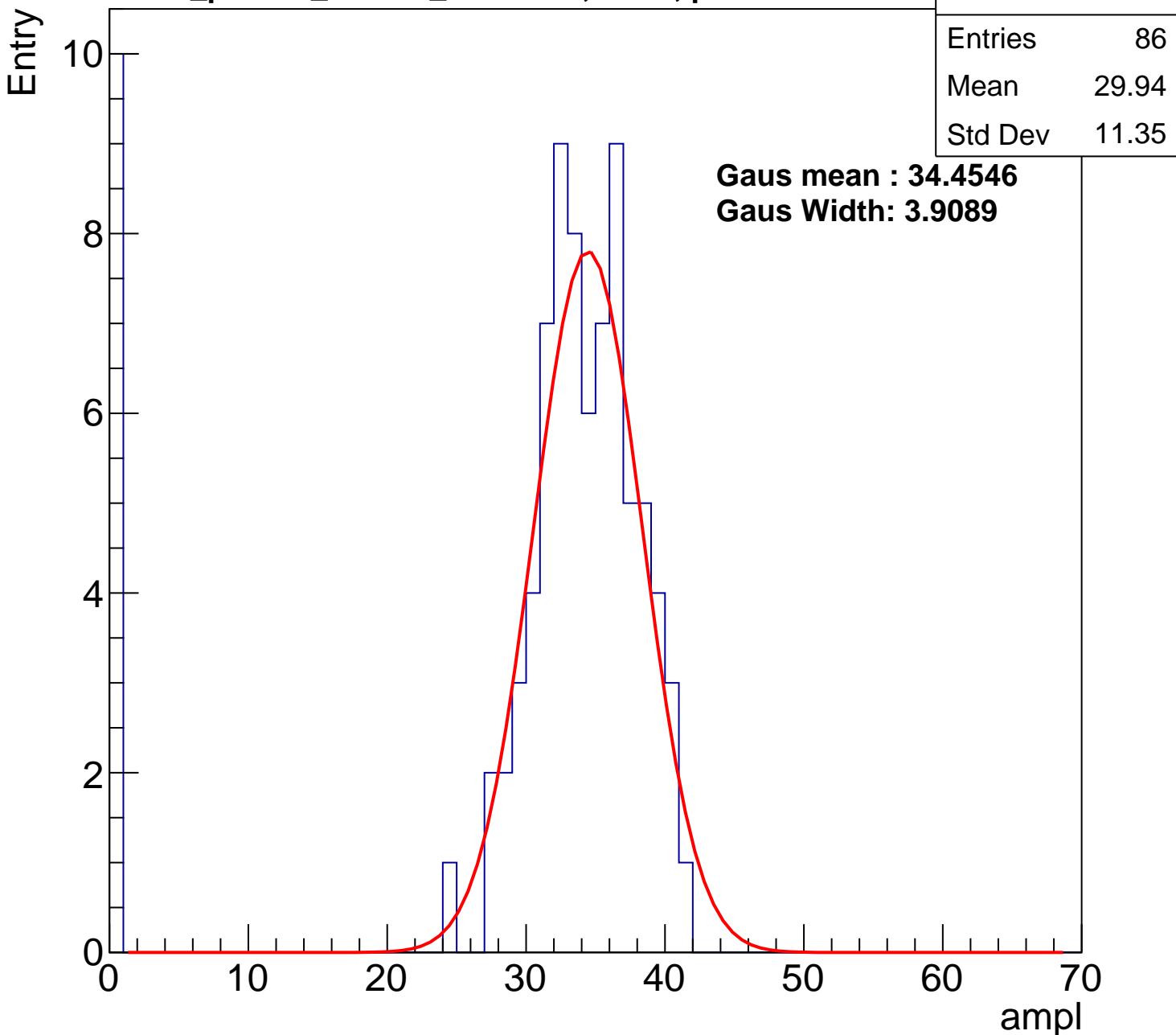
40

50

60

70

ampl

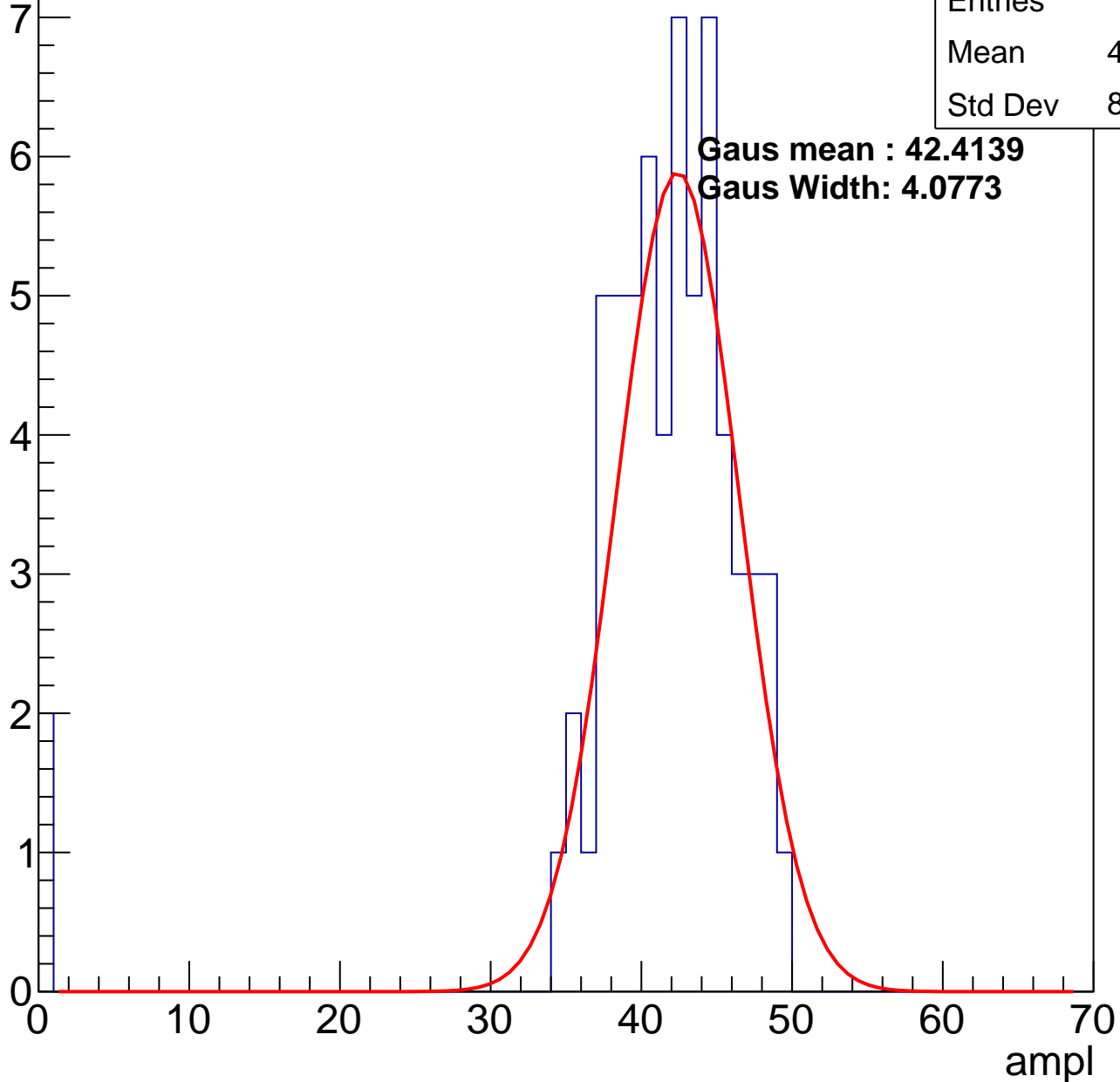


# B1L103S, U22-ch20, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	40.36
Std Dev	8.082

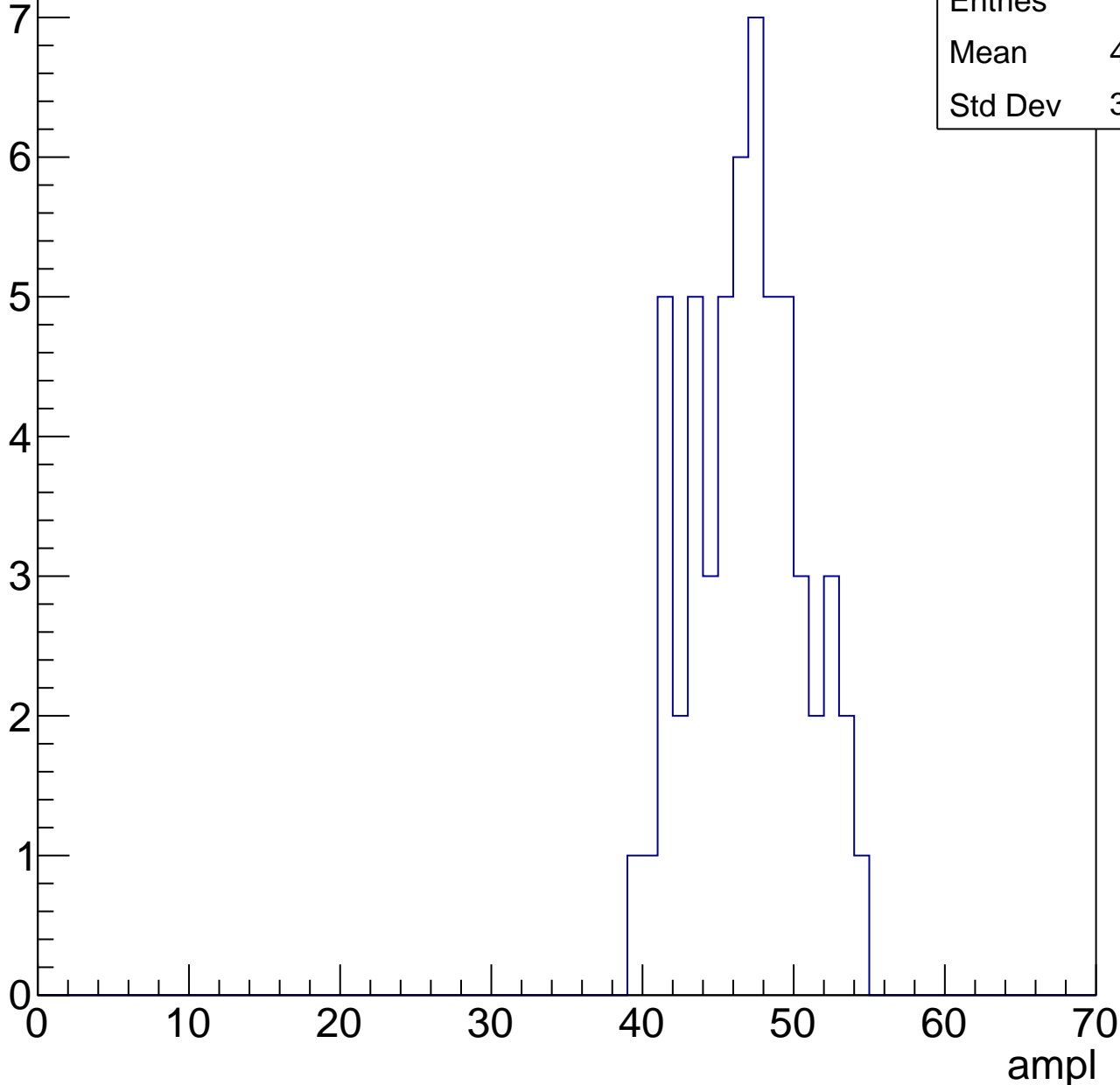


# B1L103S, U22-ch20, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	46.39
Std Dev	3.624

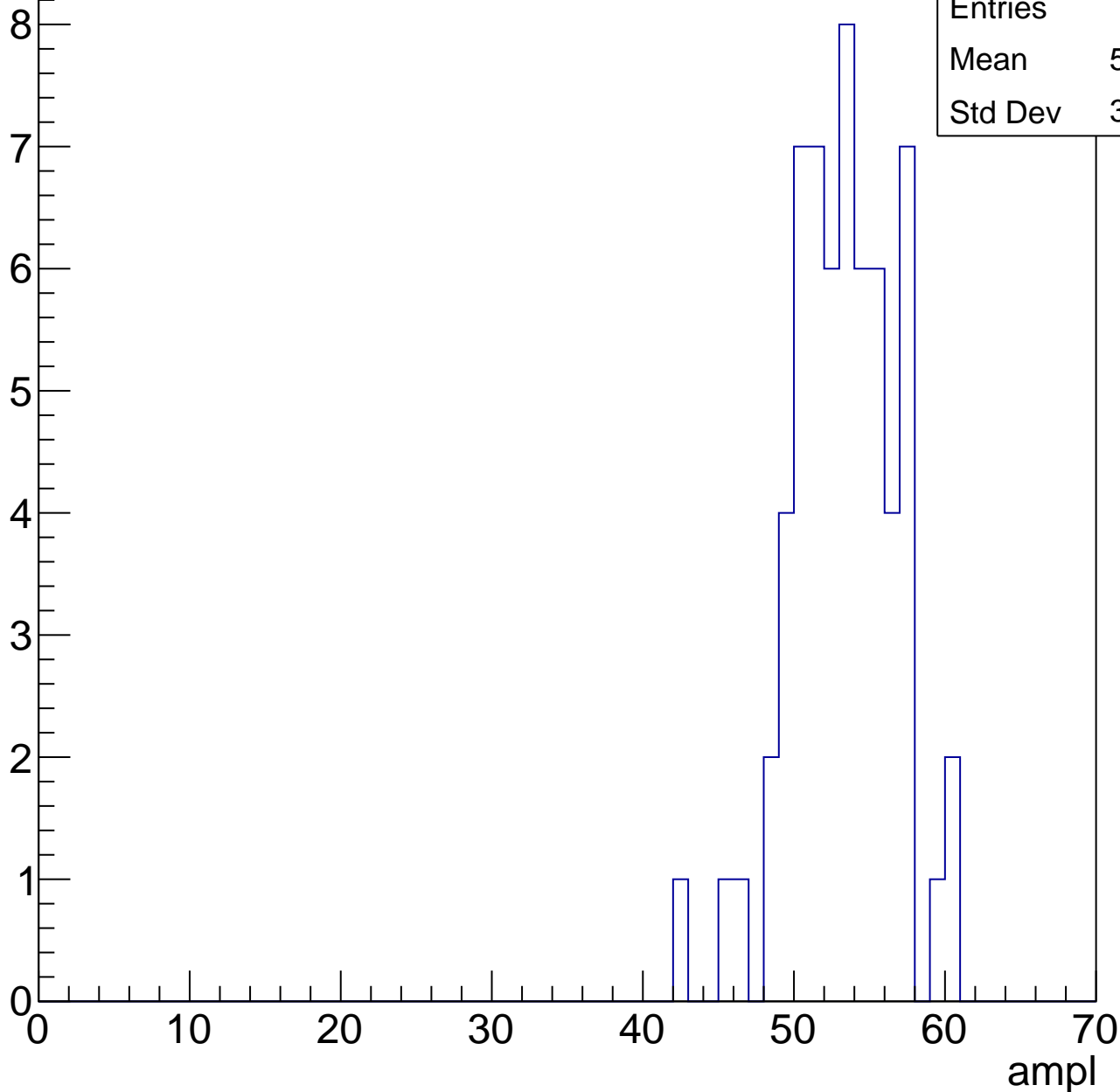


# B1L103S, U22-ch20, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.76
Std Dev	3.458

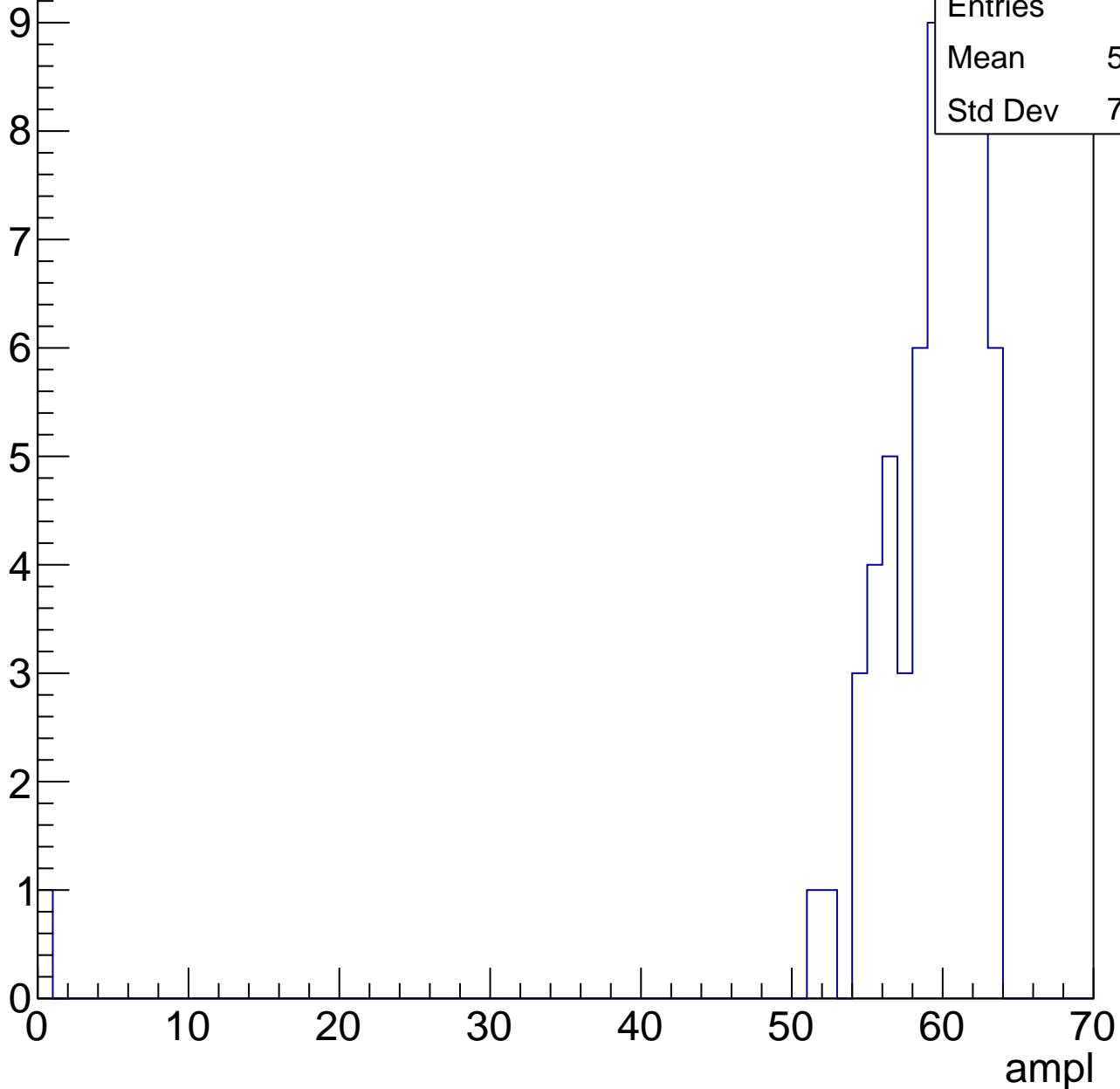


# B1L103S, U22-ch20, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	58.15
Std Dev	7.816



# B1L103S, U22-ch20, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	11
Mean	60.27
Std Dev	2.562

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch20, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch21, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	26.27
Std Dev	10.37

**Gaus mean : 30.6221**

**Gaus Width: 3.6175**

Entry

10

8

6

4

2

0

0

10

20

30

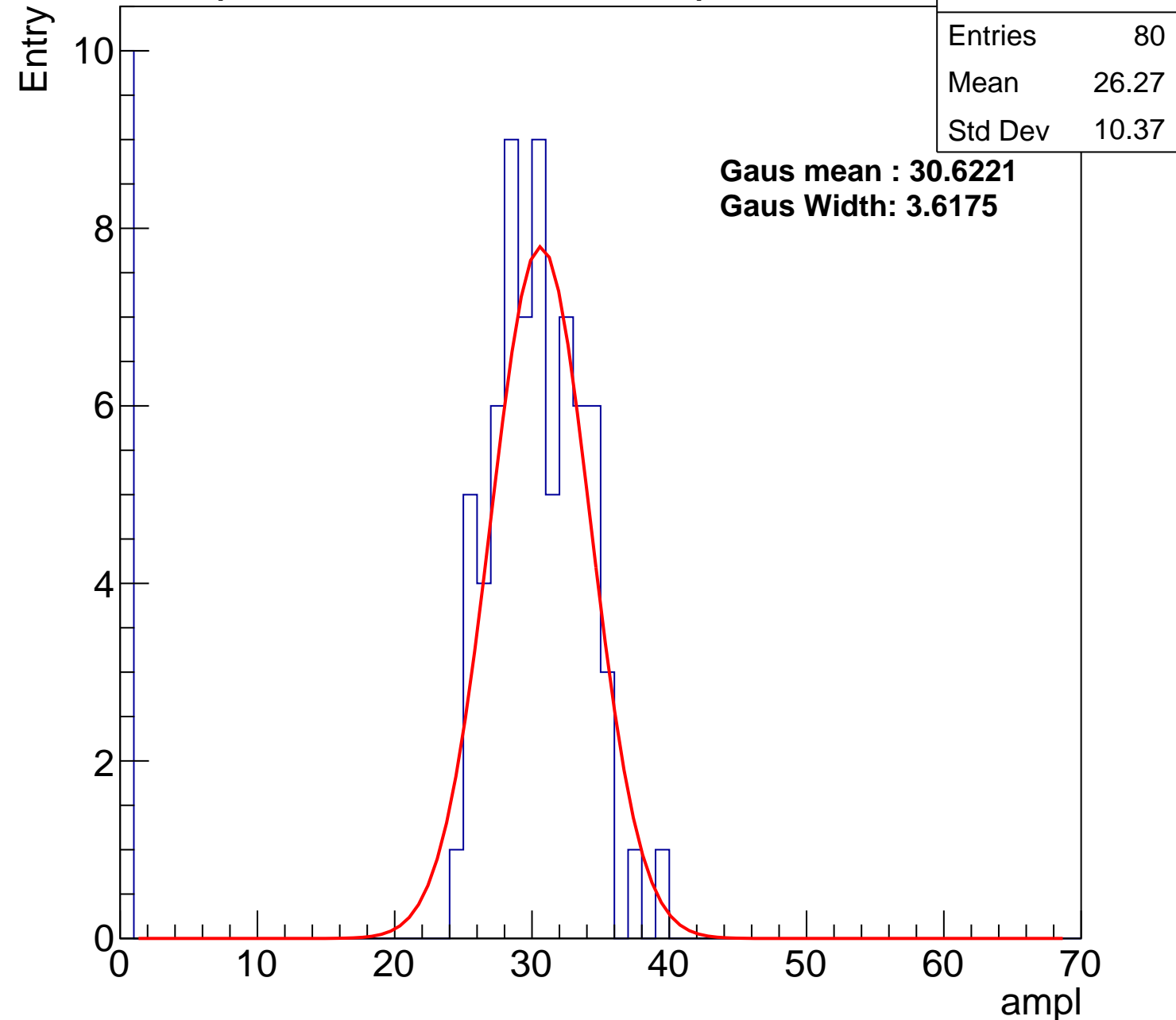
40

50

60

70

ampl



# B1L103S, U22-ch21, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	95
Mean	34.12
Std Dev	11.6

**Gaus mean : 38.1930**

**Gaus Width: 4.2049**

Entry

10

8

6

4

2

0

0

10

20

30

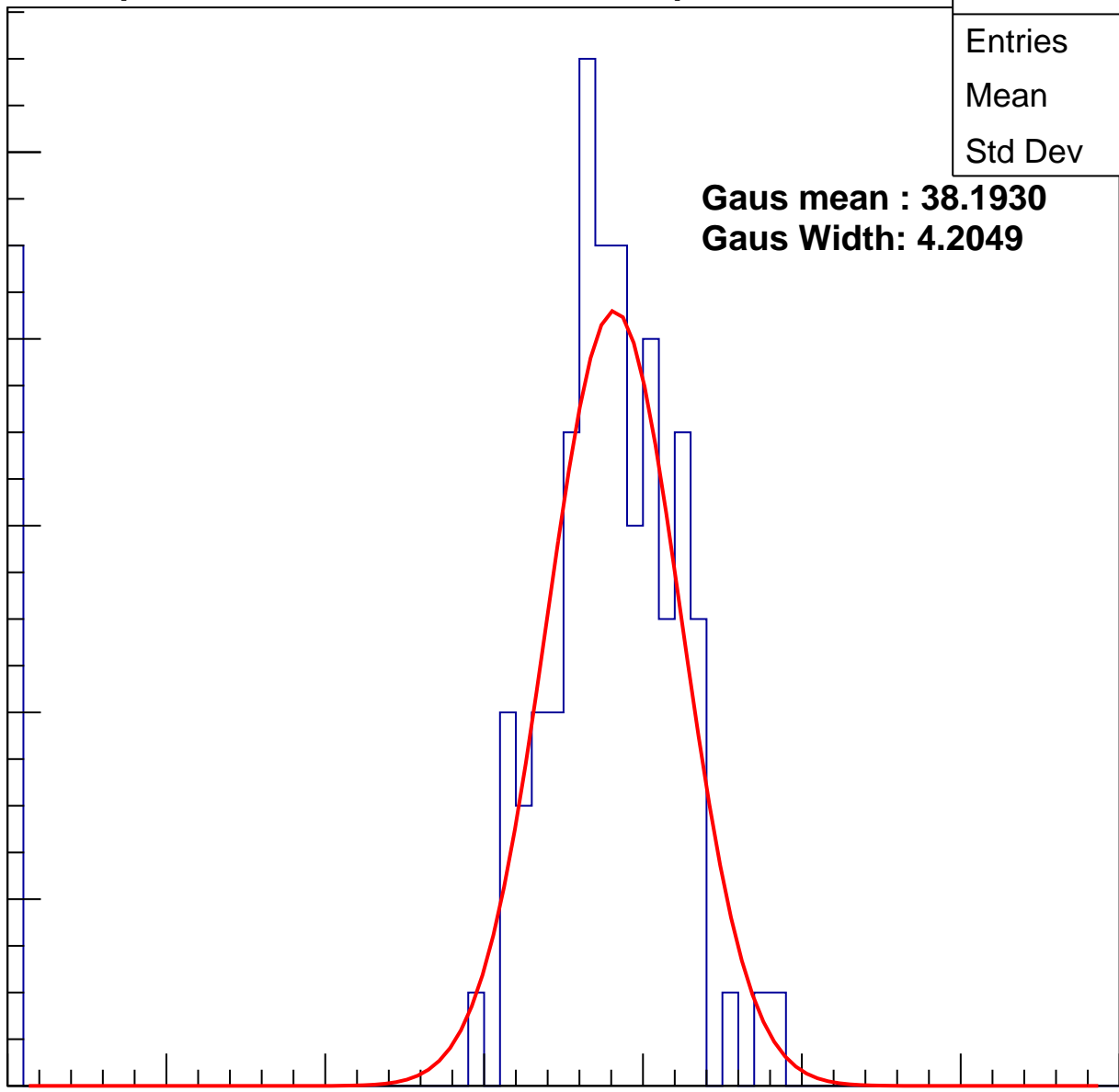
40

50

60

70

ampl



# B1L103S, U22-ch21, adc2

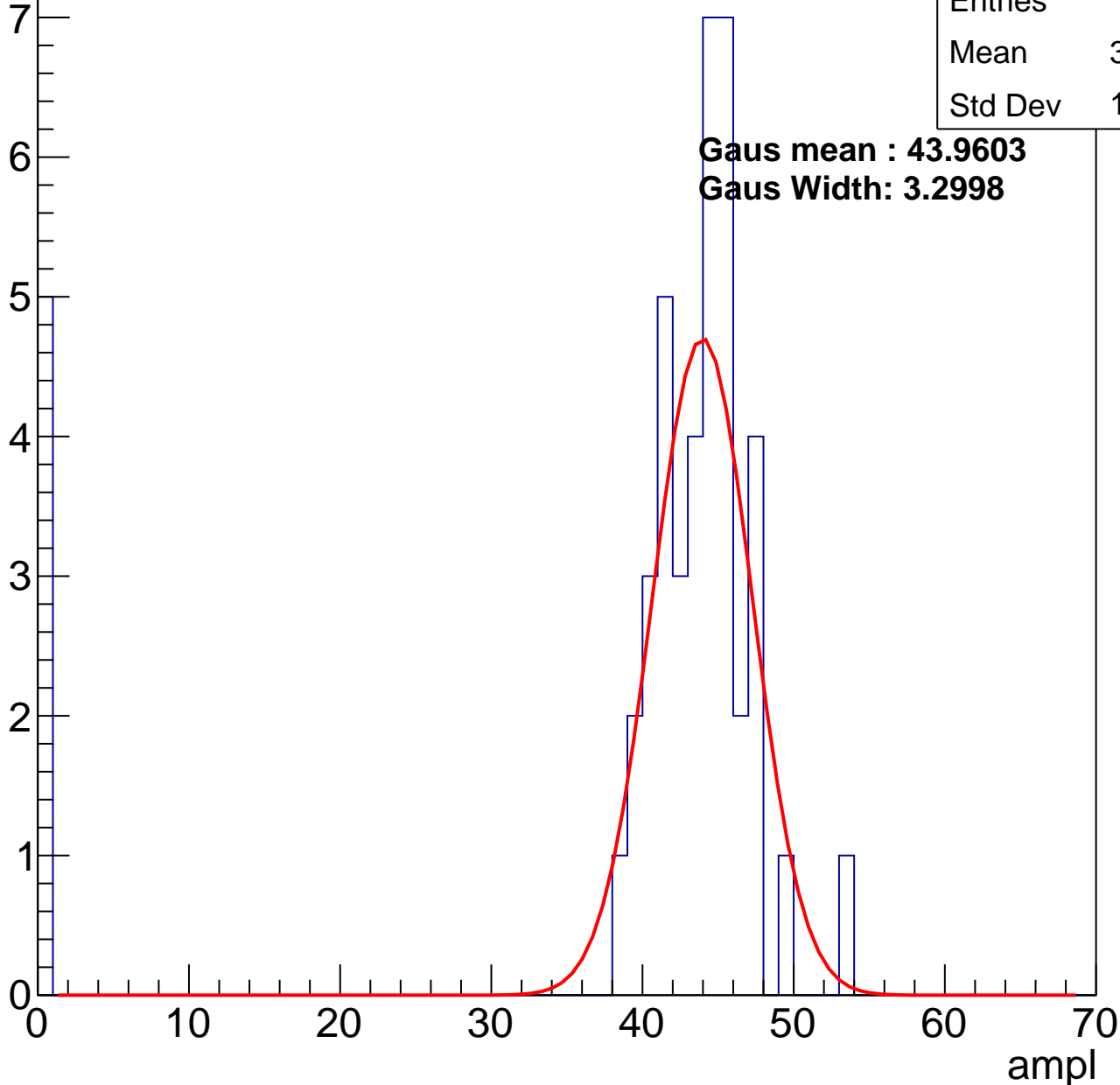
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	45
Mean	38.76
Std Dev	13.98

**Gaus mean : 43.9603**

**Gaus Width: 3.2998**

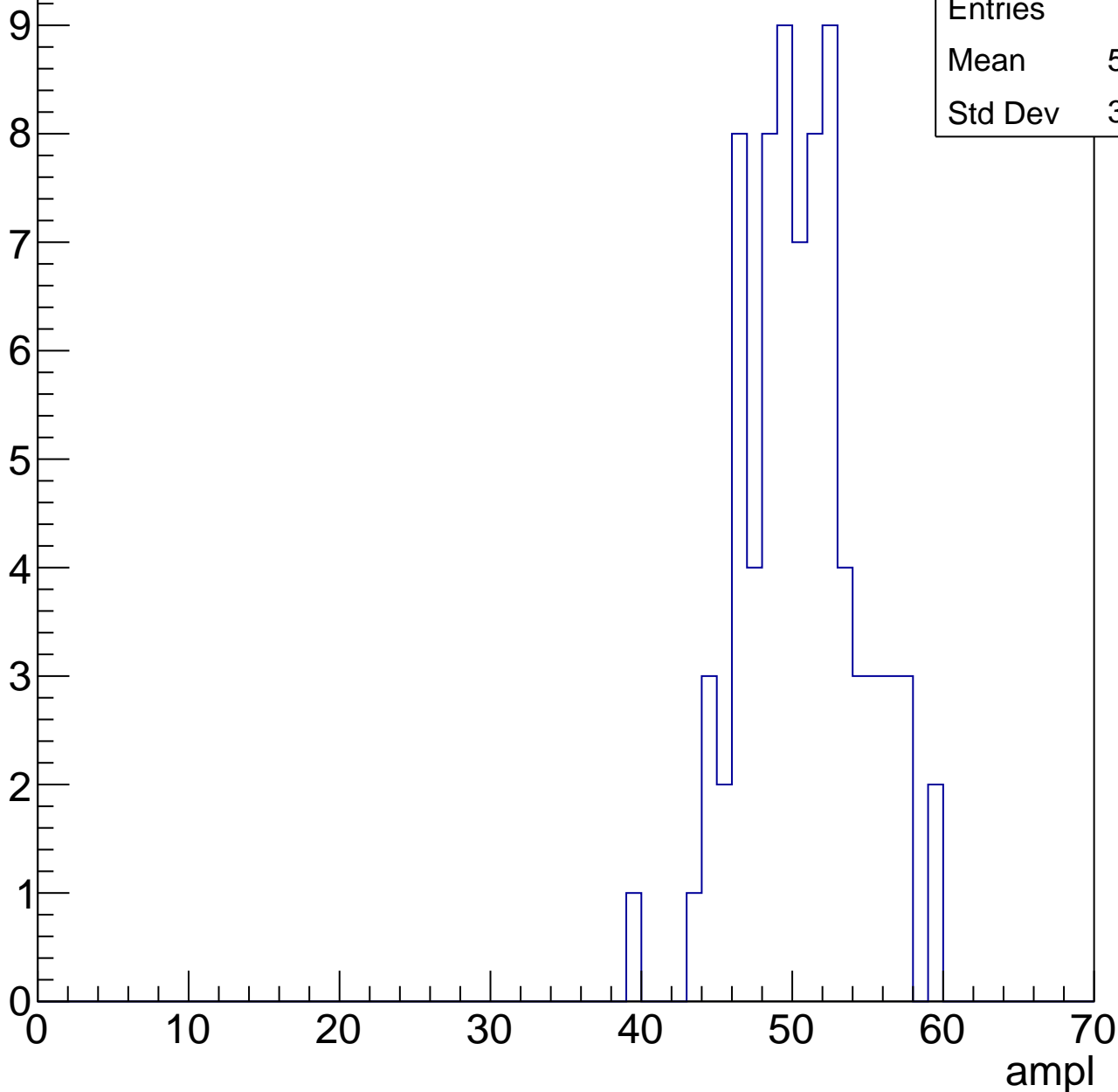


# B1L103S, U22-ch21, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	50.09
Std Dev	3.844

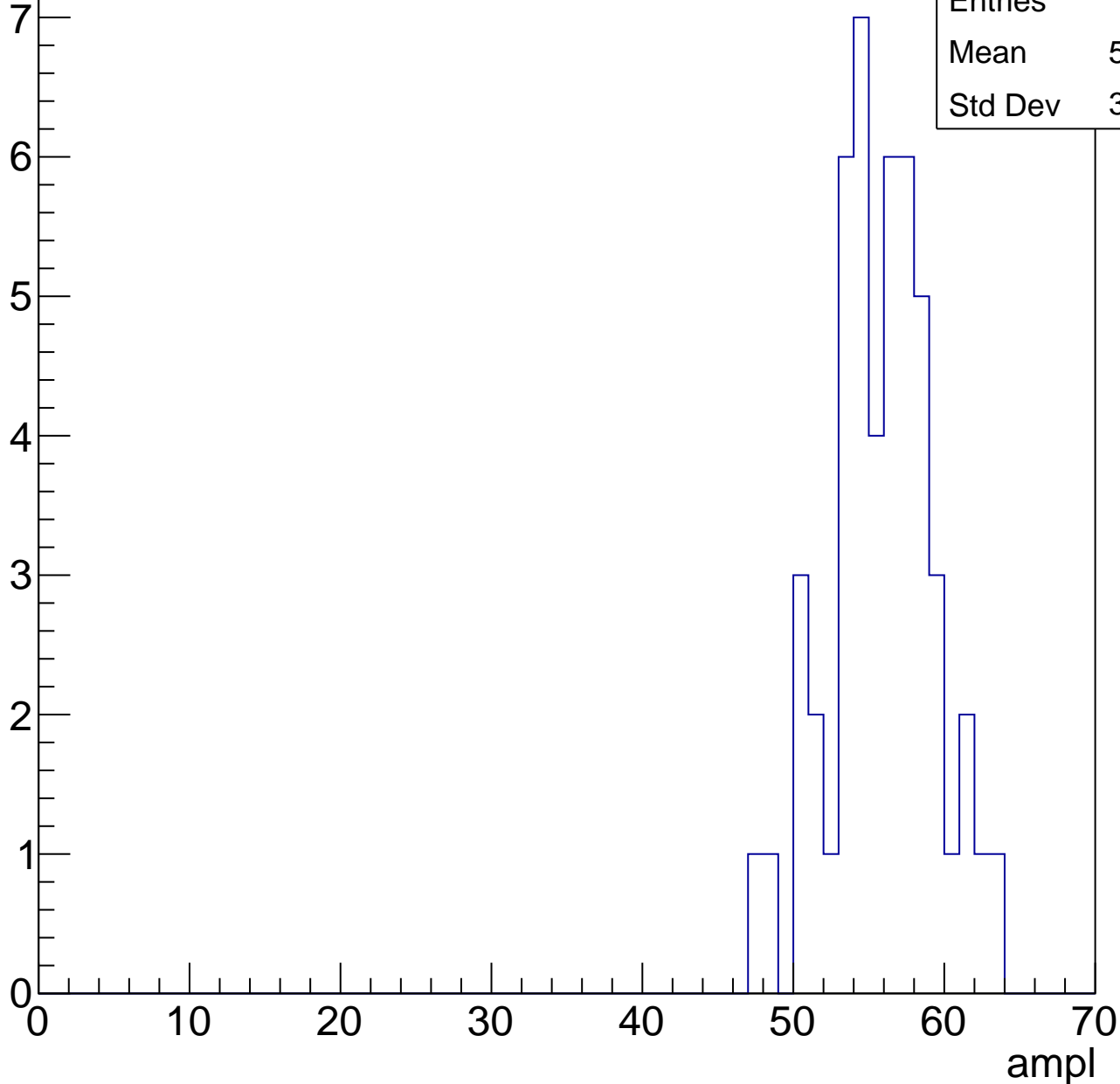


# B1L103S, U22-ch21, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	55.34
Std Dev	3.427

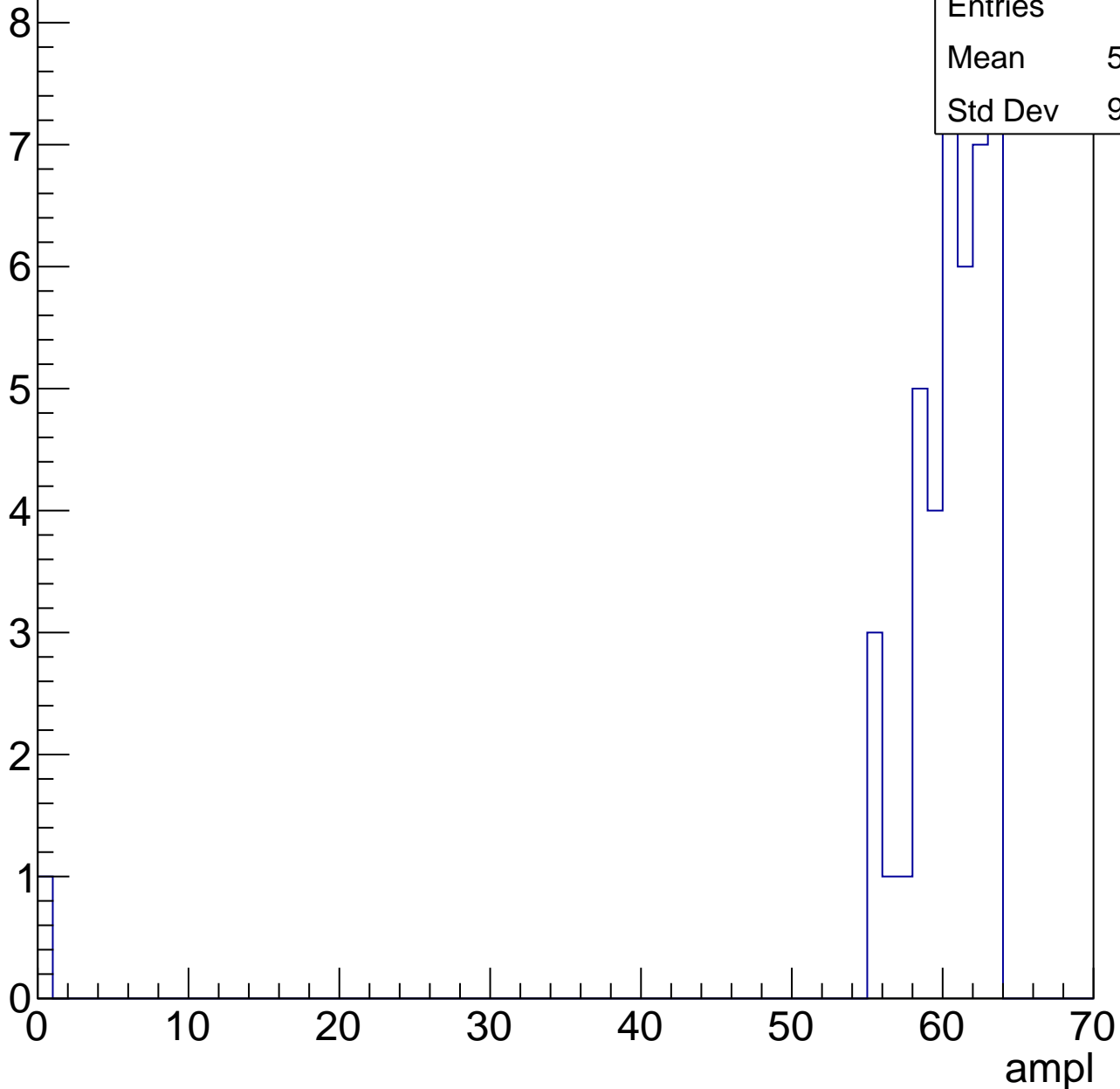


# B1L103S, U22-ch21, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.82
Std Dev	9.255



# B1L103S, U22-ch21, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

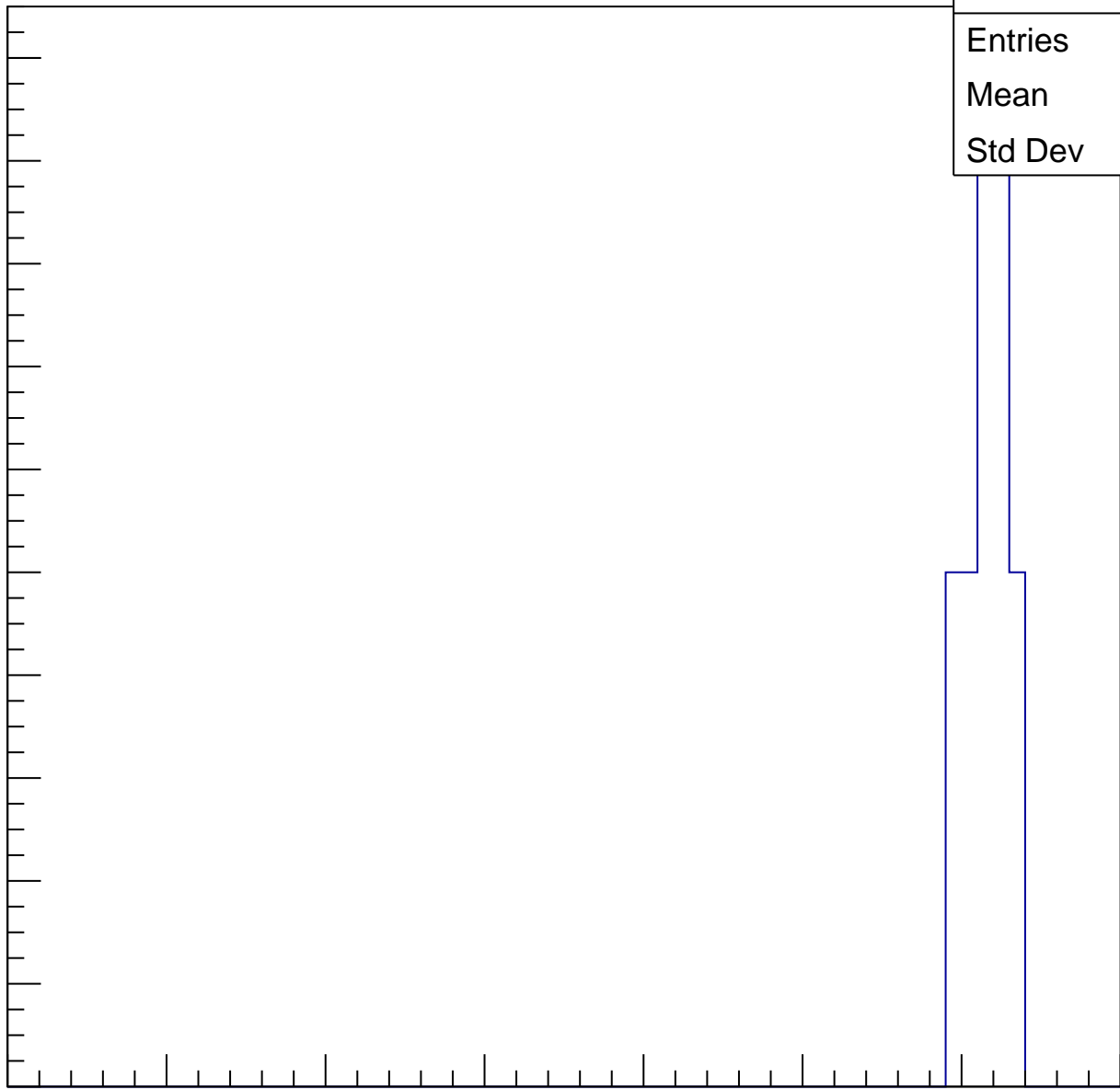
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.14
Std Dev	1.245

ampl

0 10 20 30 40 50 60 70



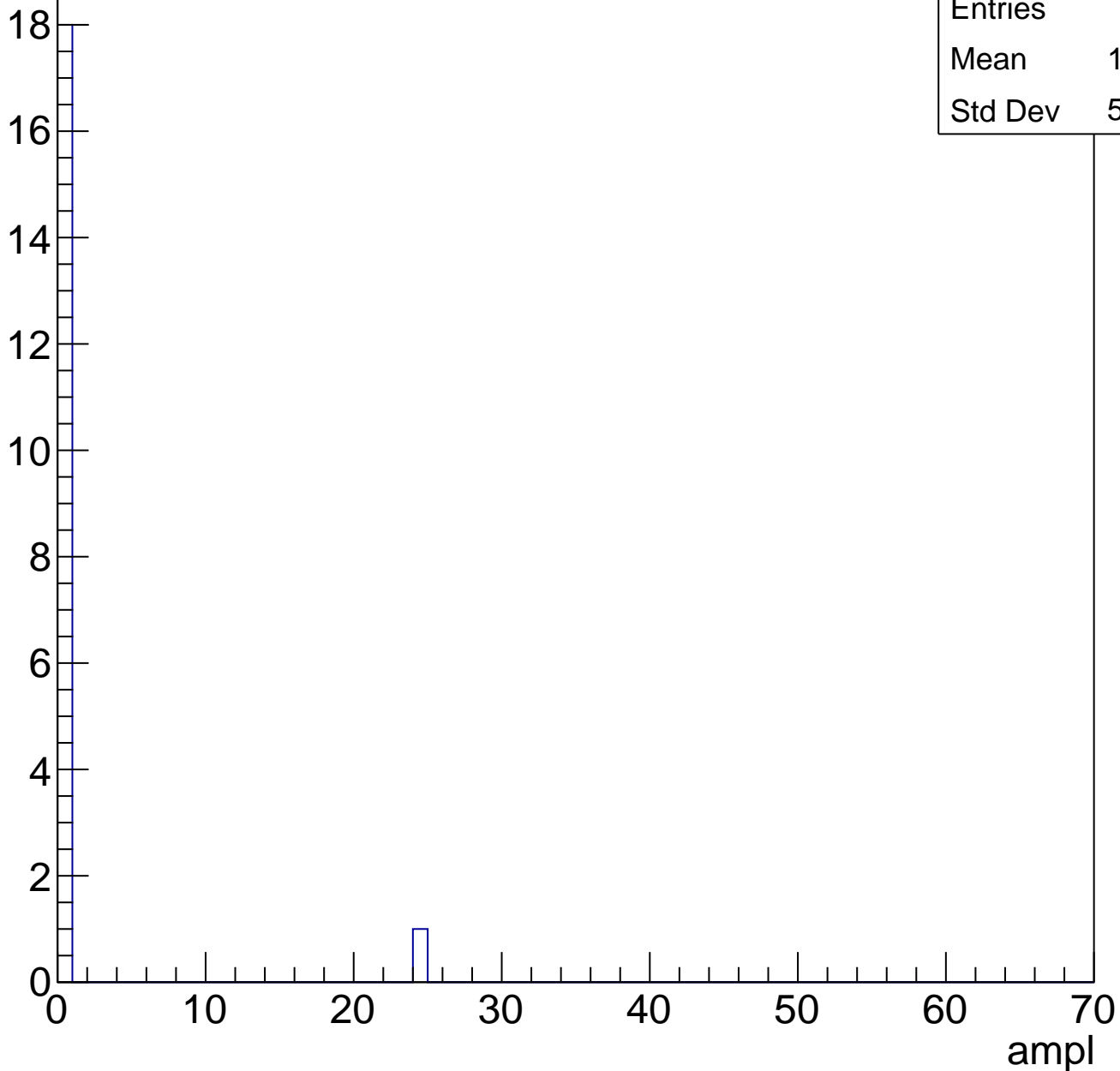


# B1L103S, U22-ch21, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.263
Std Dev	5.359

Entry



# B1L103S, U22-ch22, adc0

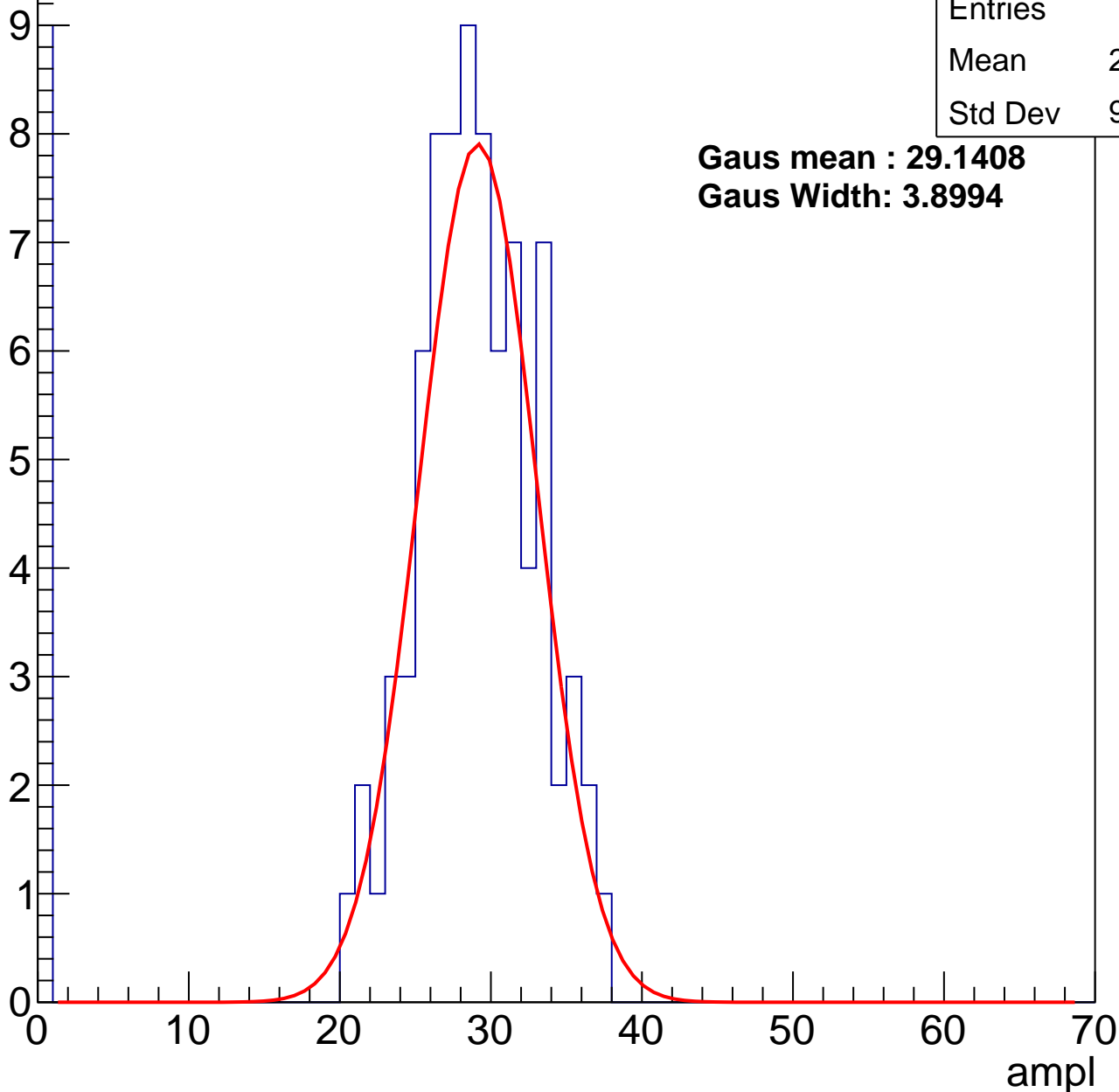
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	90
Mean	25.79
Std Dev	9.305

**Gaus mean : 29.1408**

**Gaus Width: 3.8994**



# B1L103S, U22-ch22, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	66
Mean	29.67
Std Dev	13.67

**Gaus mean : 36.7975**

**Gaus Width: 4.7035**

Entry

10

8

6

4

2

0

0

10

20

30

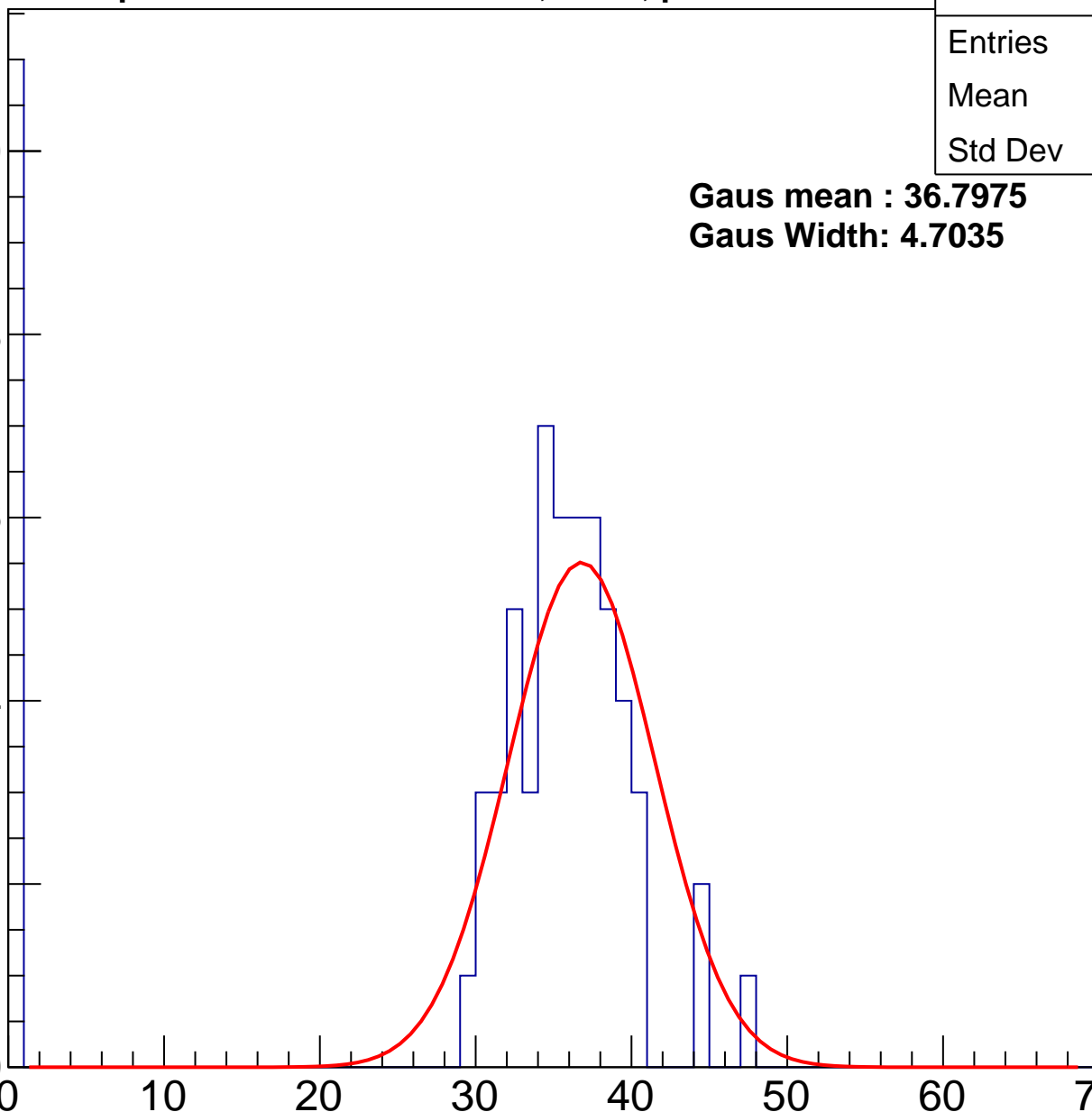
40

50

60

70

ampl



# B1L103S, U22-ch22, adc2

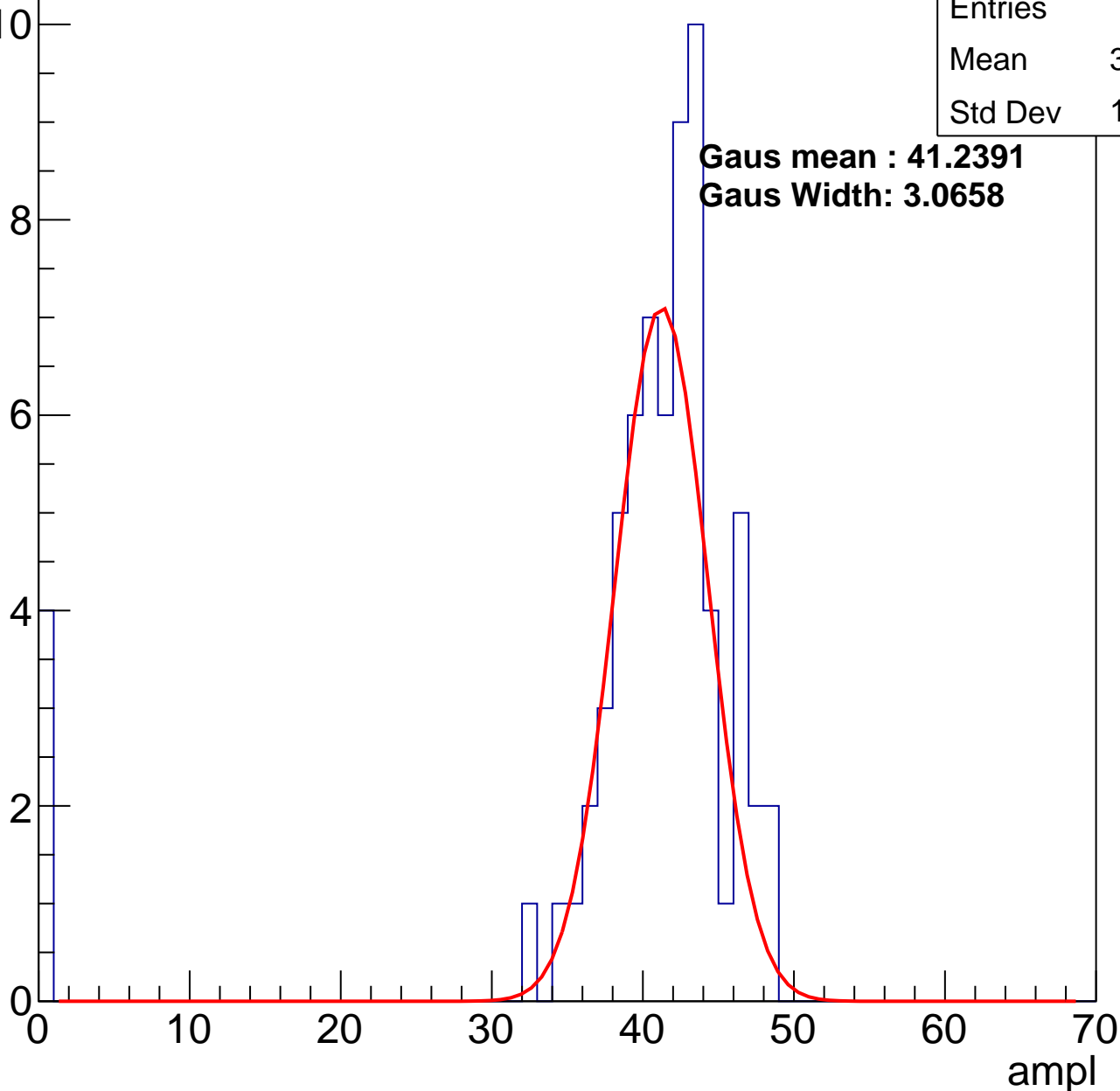
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	38.88
Std Dev	10.18

**Gaus mean : 41.2391**

**Gaus Width: 3.0658**

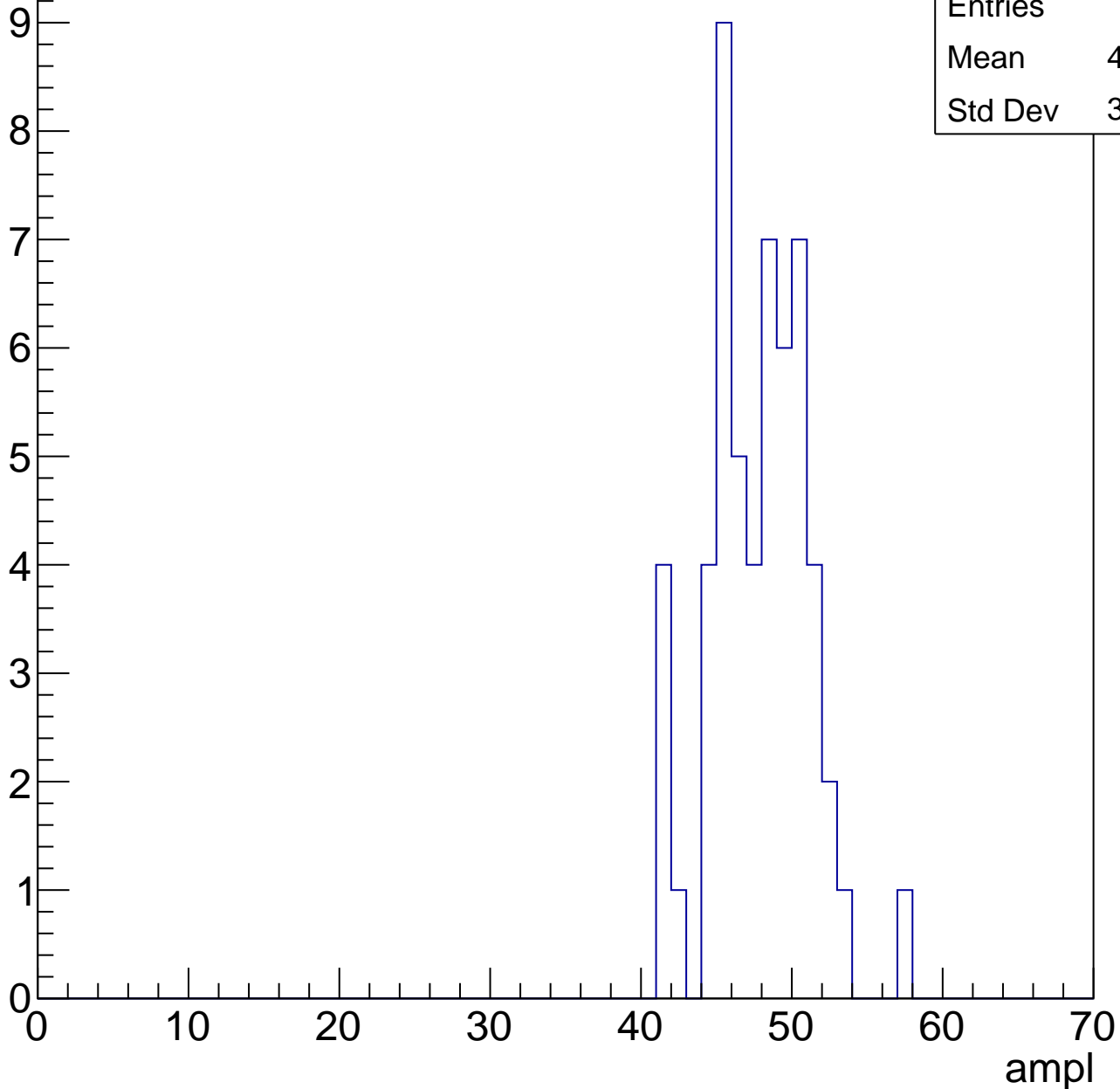


# B1L103S, U22-ch22, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.33
Std Dev	3.259

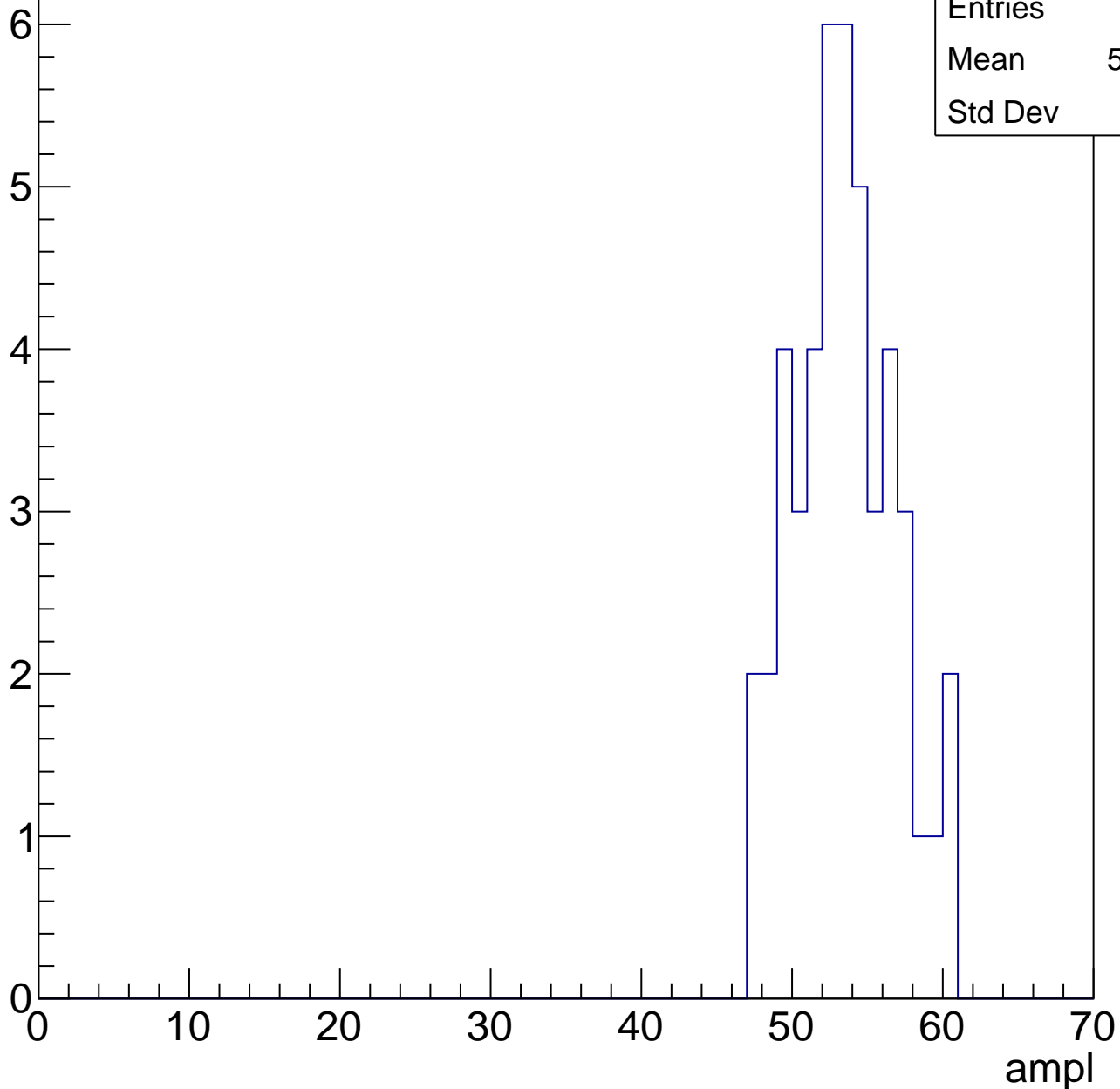


# B1L103S, U22-ch22, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	52.98
Std Dev	3.28

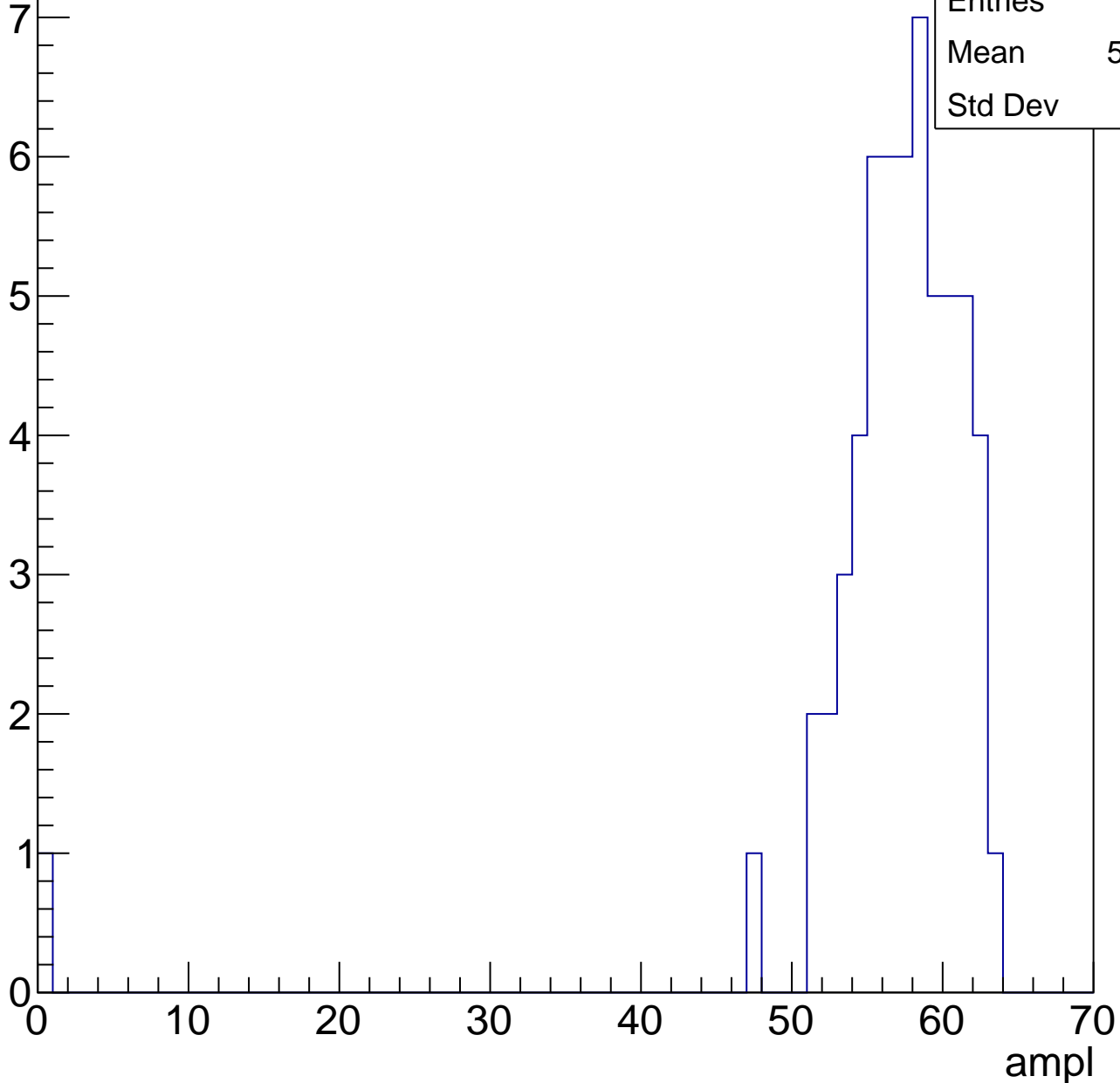


# B1L103S, U22-ch22, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.09
Std Dev	8.12

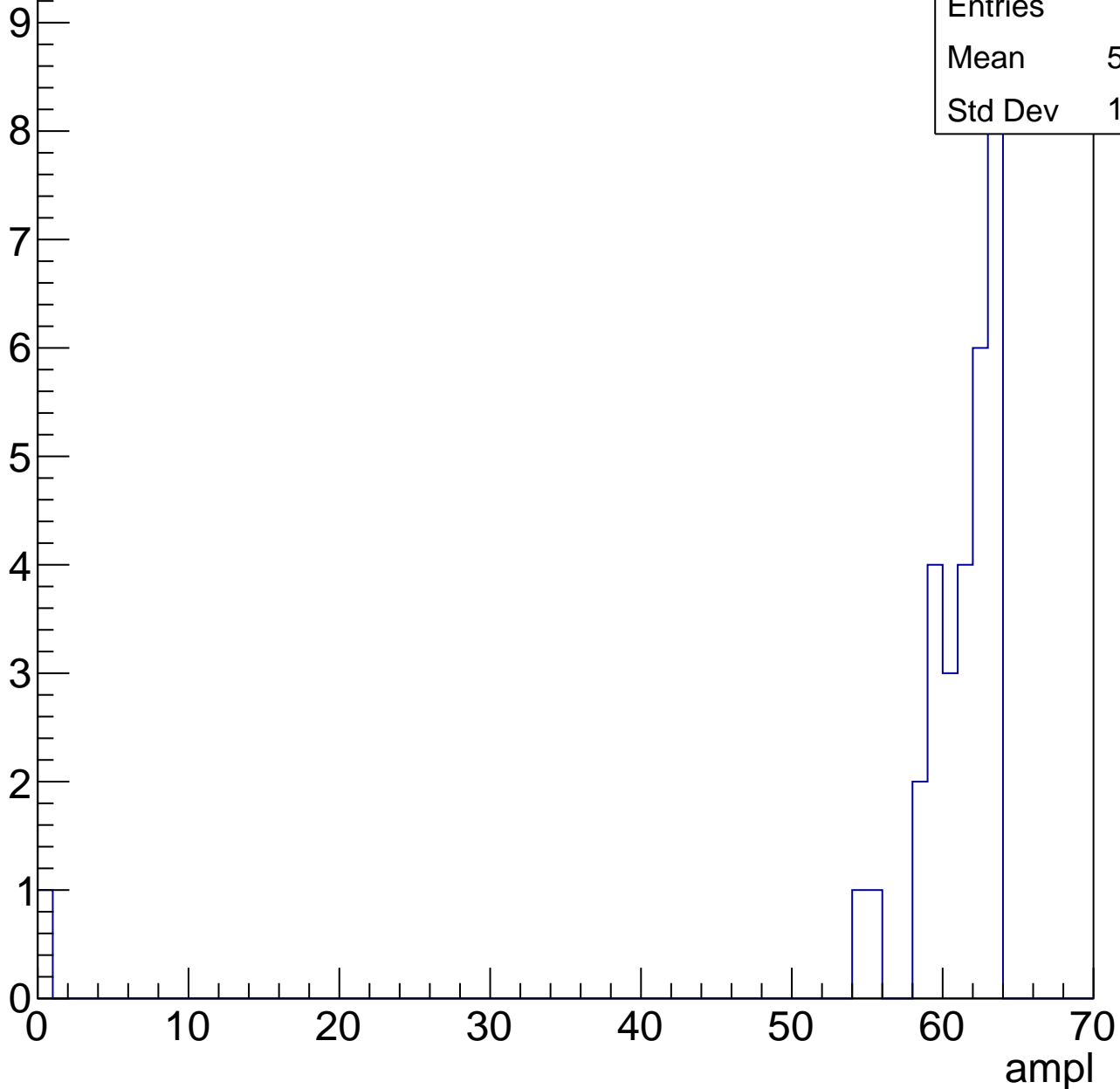


# B1L103S, U22-ch22, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	31
Mean	58.84
Std Dev	10.98



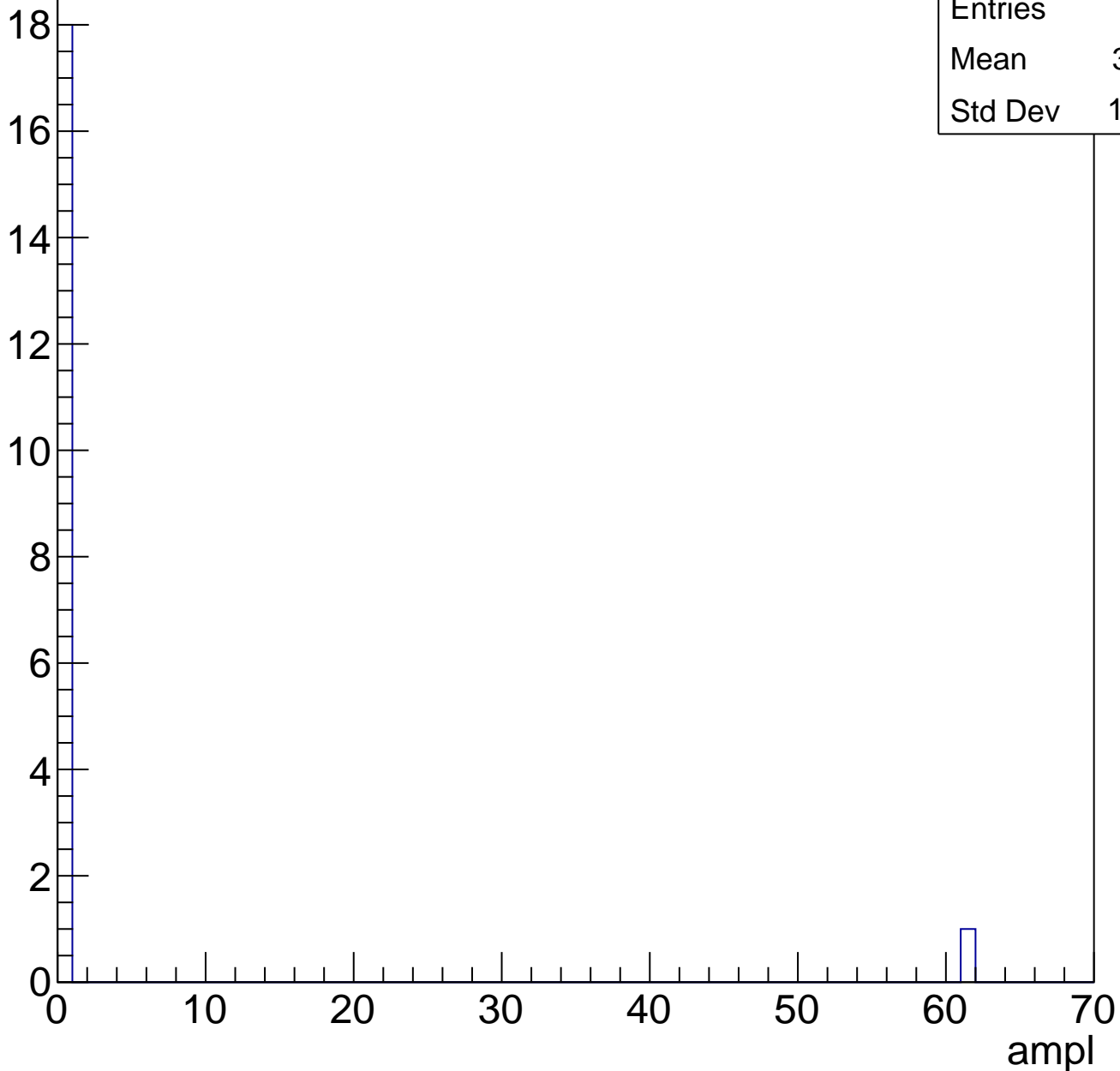


# B1L103S, U22-ch22, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	3.211
Std Dev	13.62

Entry



# B1L103S, U22-ch23, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	130
Mean	23.36
Std Dev	12.26

**Gaus mean : 29.7154**

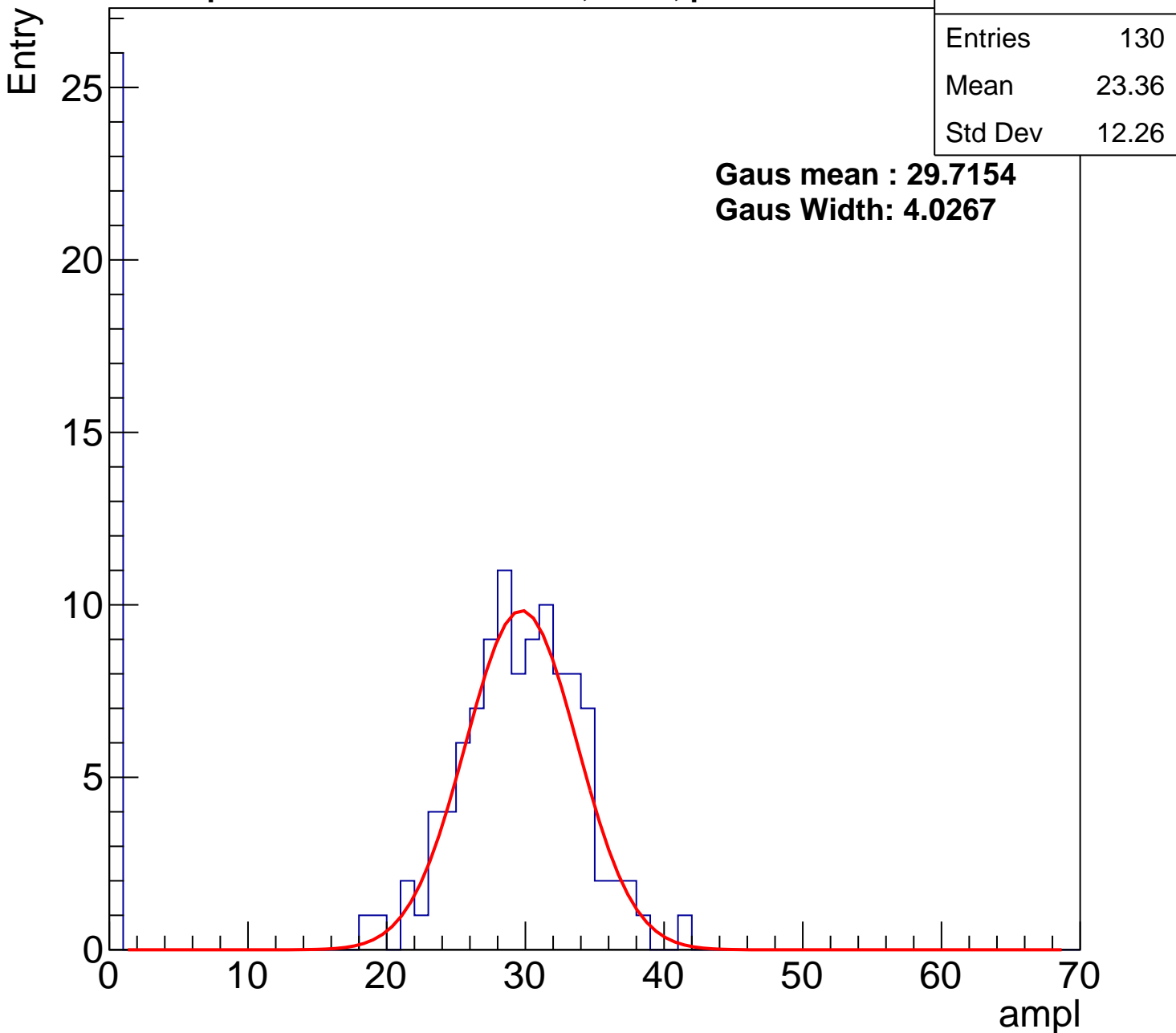
**Gaus Width: 4.0267**

Entry

25  
20  
15  
10  
5  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch23, adc1

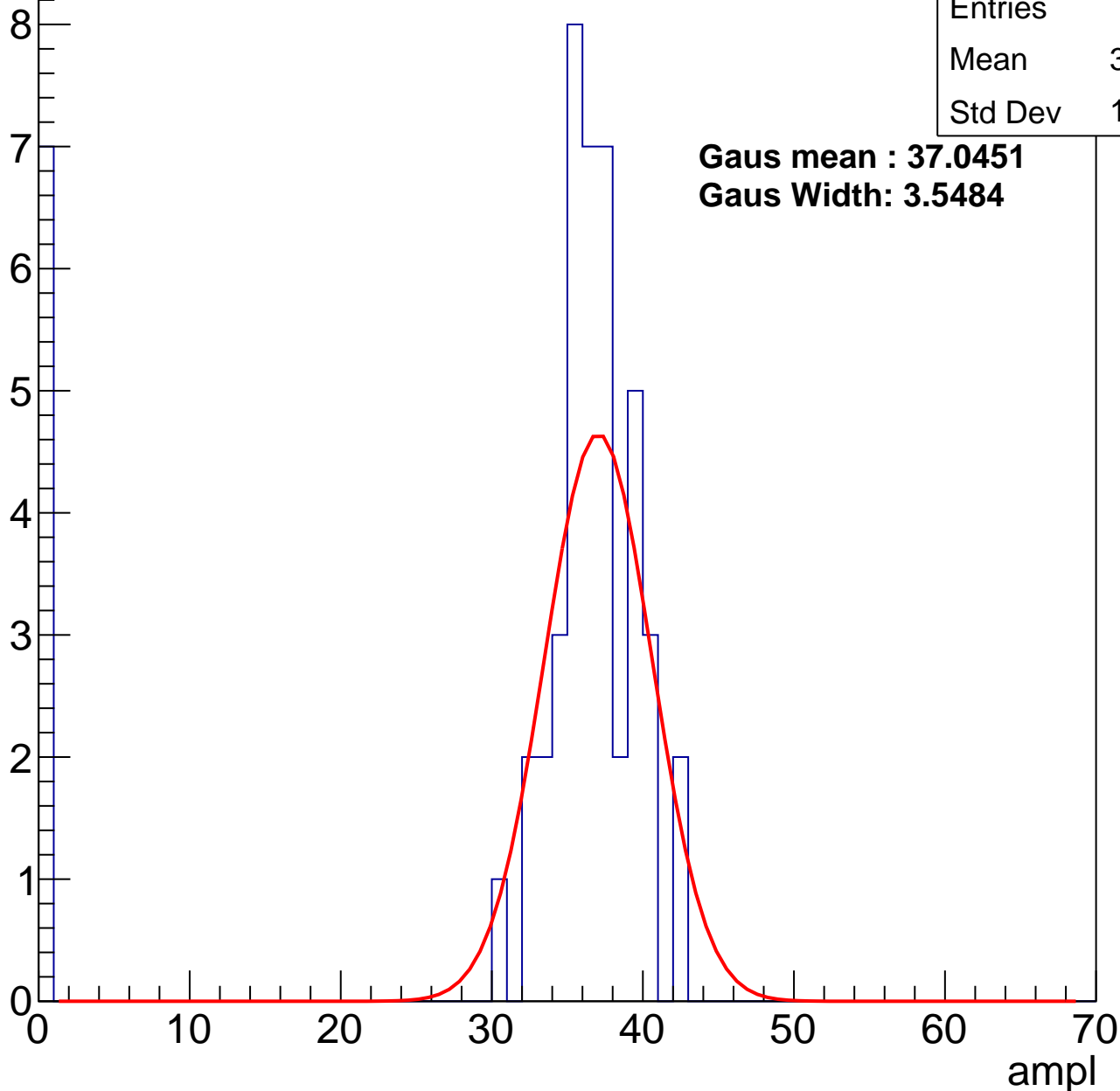
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	31.18
Std Dev	12.95

**Gaus mean : 37.0451**

**Gaus Width: 3.5484**



# B1L103S, U22-ch23, adc2

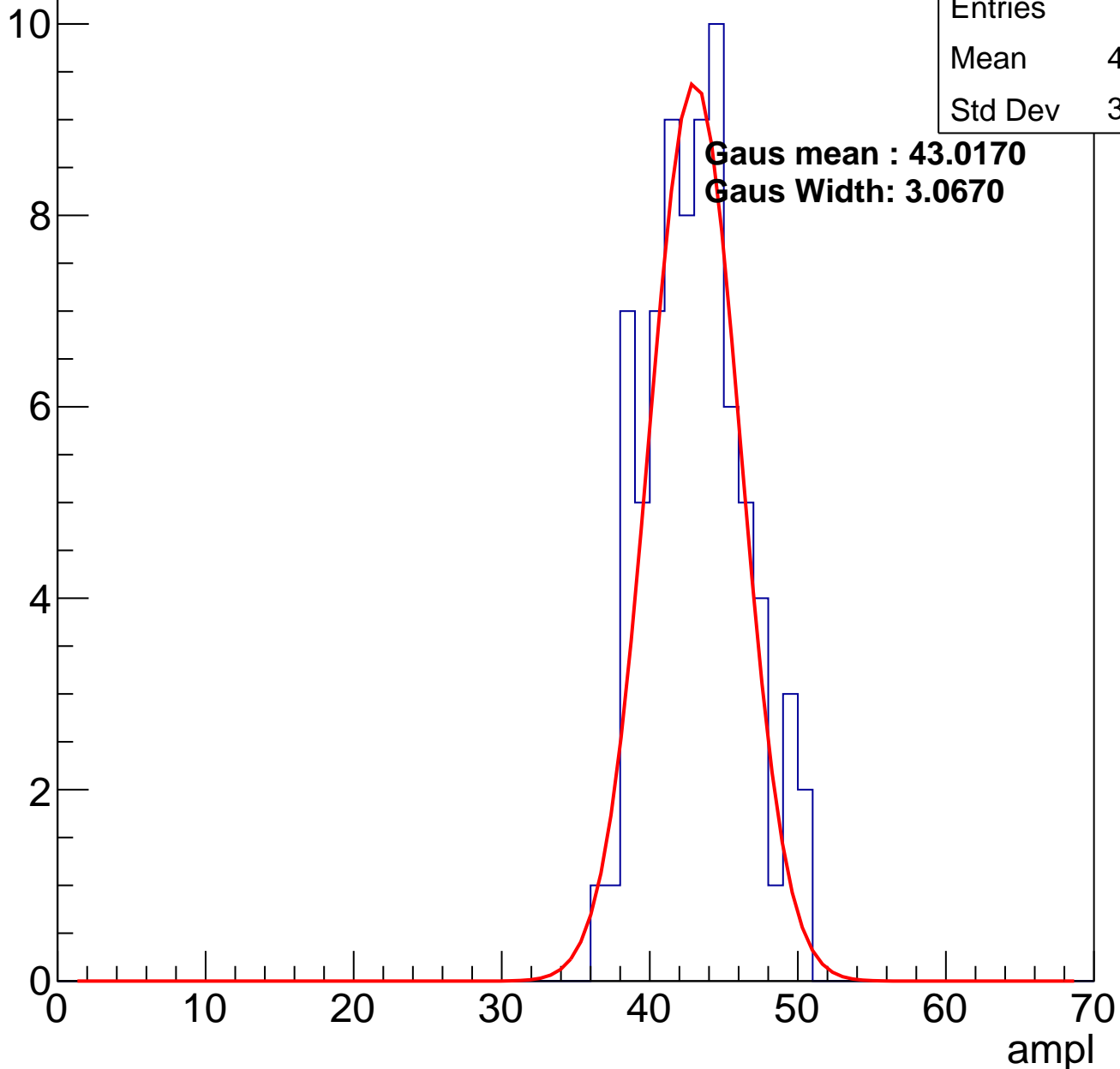
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	42.68
Std Dev	3.228

**Gaus mean : 43.0170**

**Gaus Width: 3.0670**

Entry

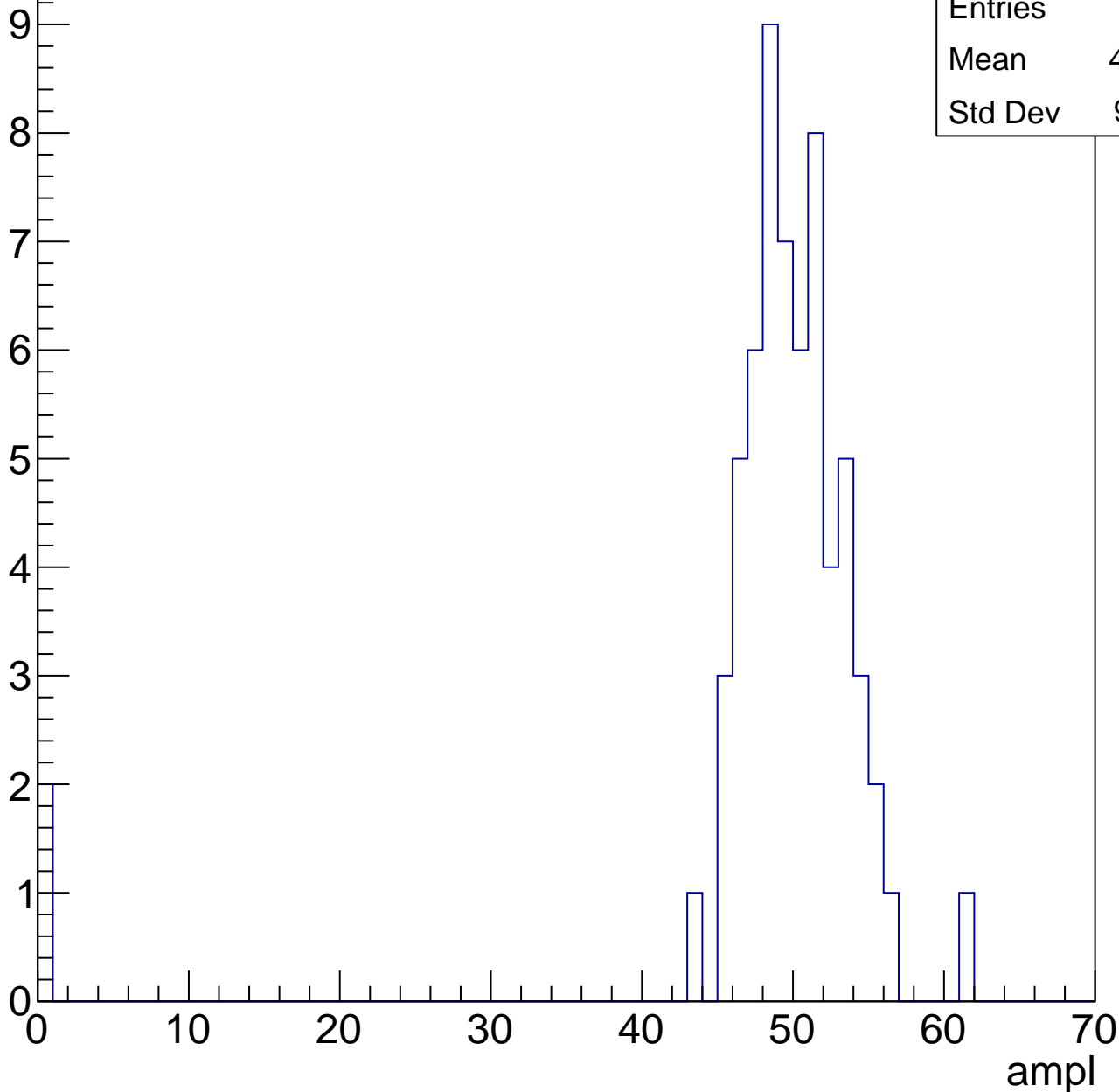


# B1L103S, U22-ch23, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.17
Std Dev	9.271

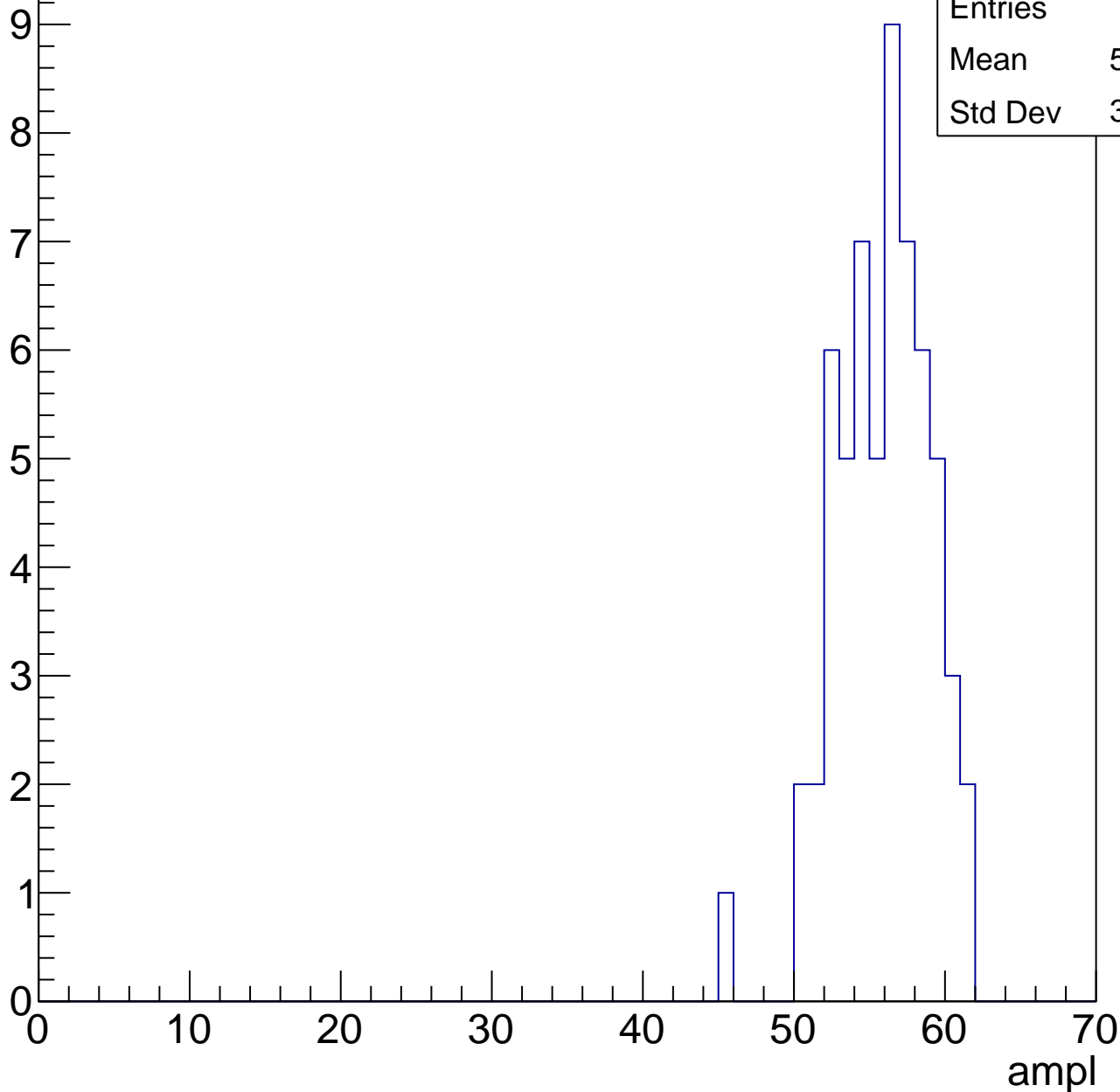


# B1L103S, U22-ch23, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.42
Std Dev	3.084

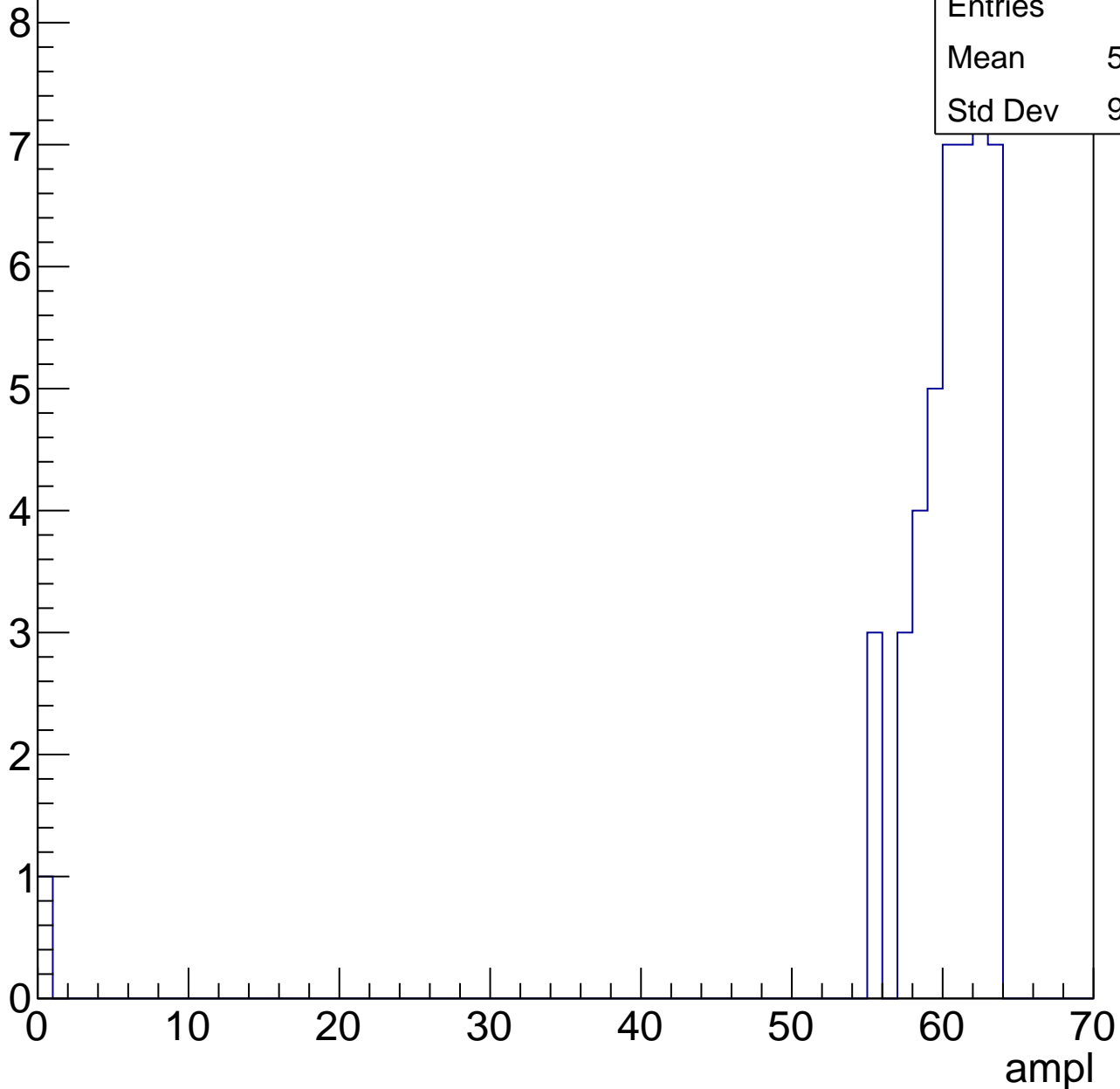


# B1L103S, U22-ch23, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	45
Mean	58.82
Std Dev	9.144



# B1L103S, U22-ch23, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



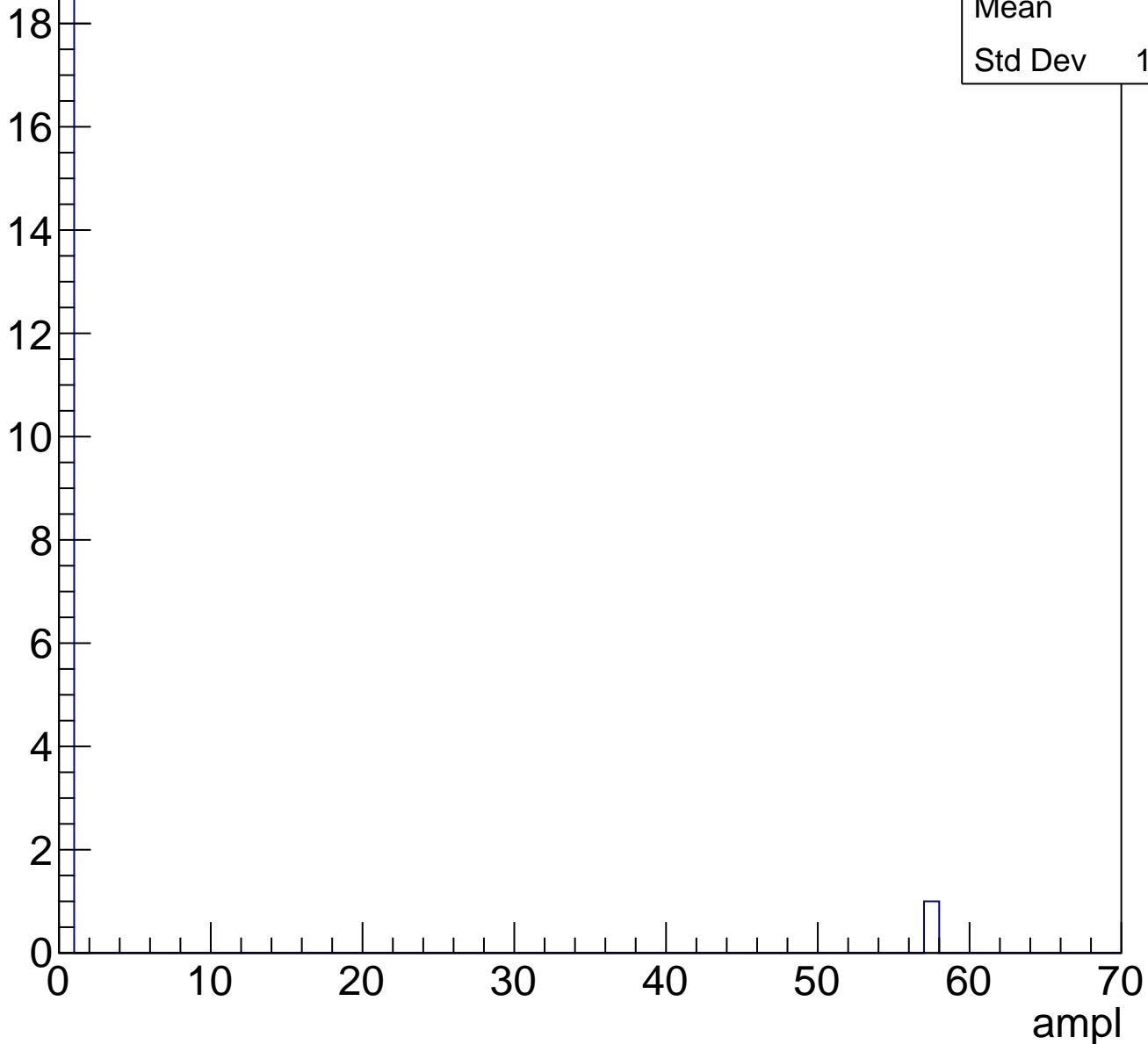


# B1L103S, U22-ch23, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	2.85
Std Dev	12.42

Entry



# B1L103S, U22-ch24, adc0

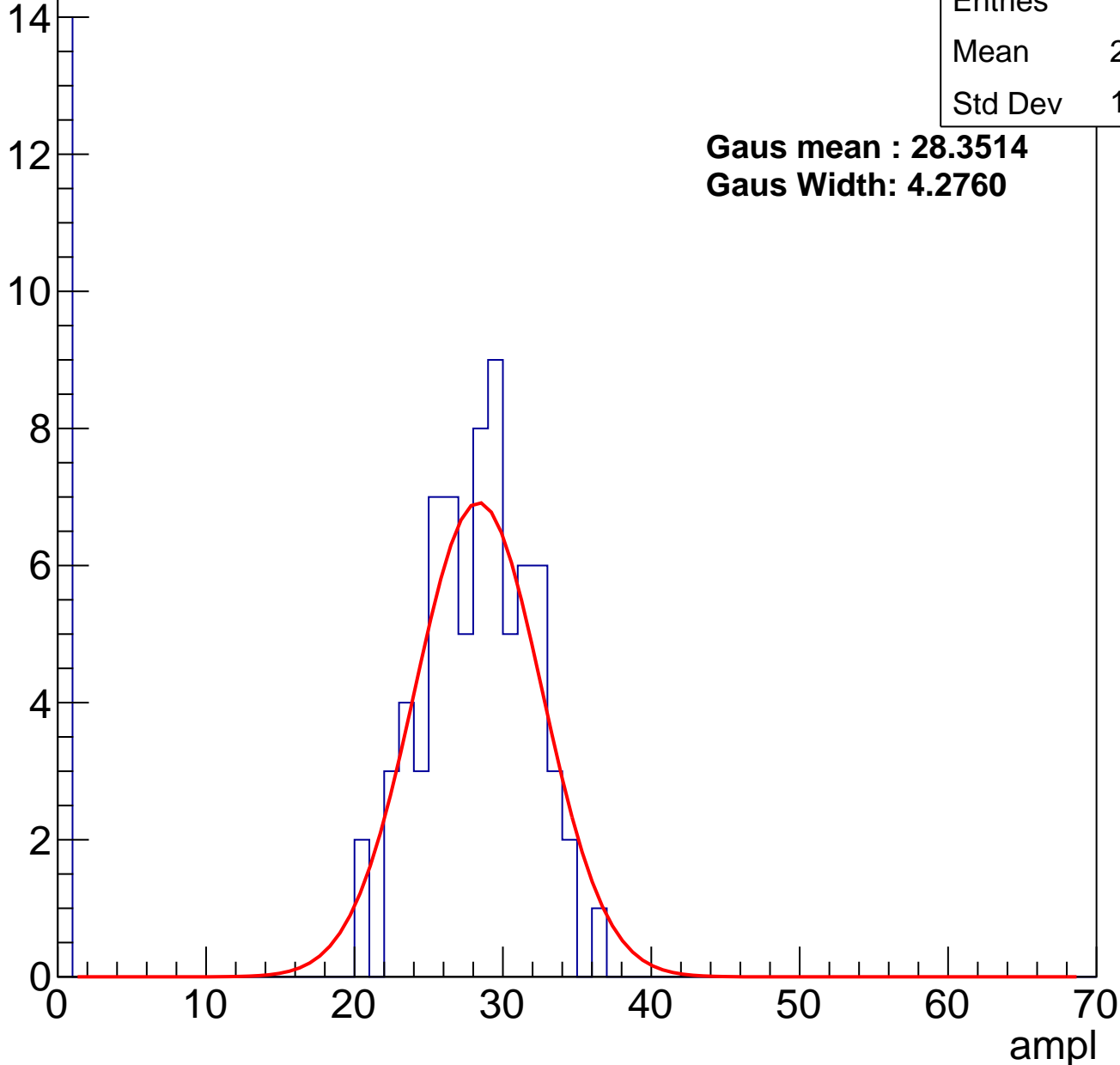
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	23.27
Std Dev	10.82

**Gaus mean : 28.3514**

**Gaus Width: 4.2760**

Entry



# B1L103S, U22-ch24, adc1

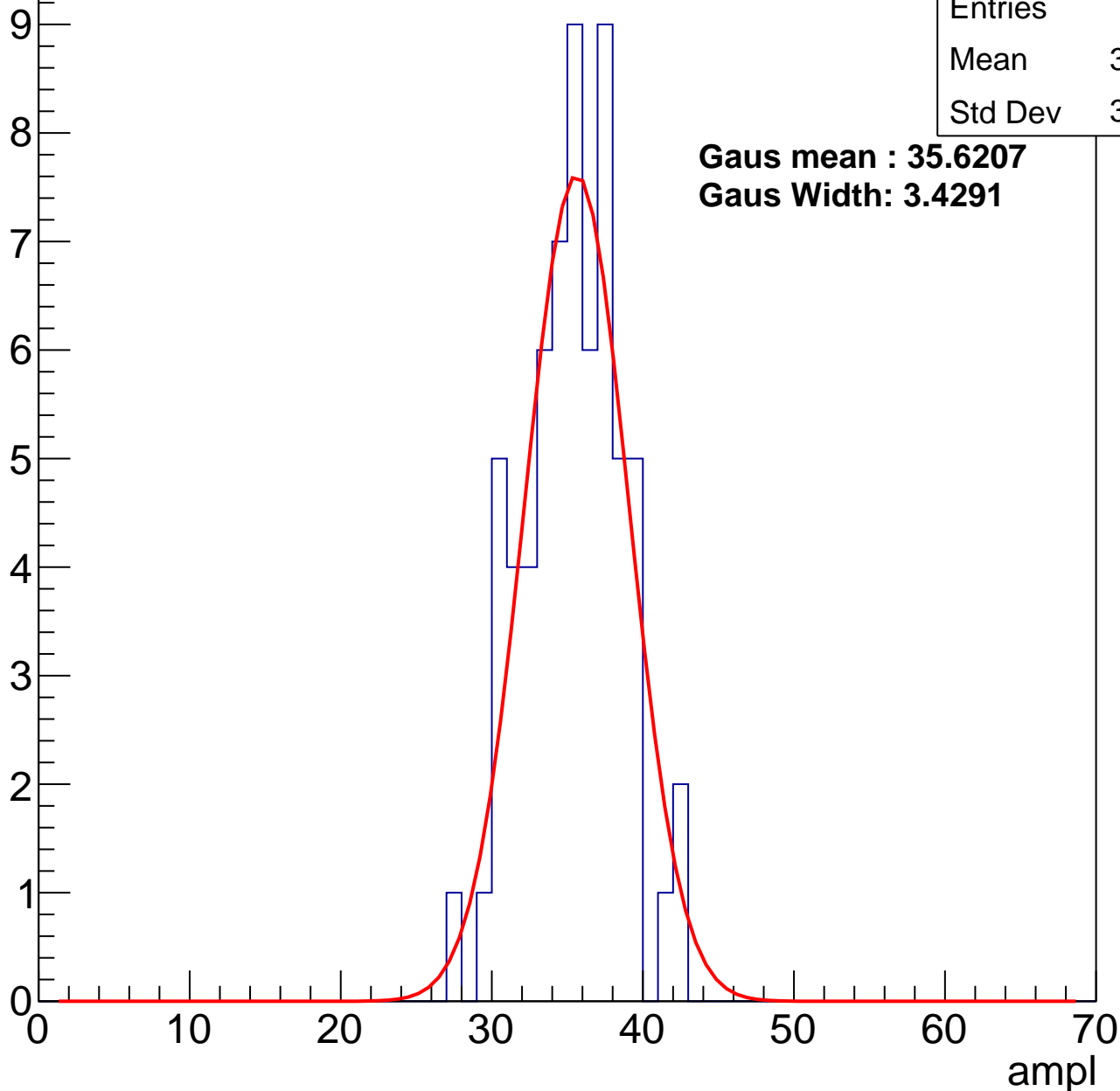
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.89
Std Dev	3.177

**Gaus mean : 35.6207**

**Gaus Width: 3.4291**



# B1L103S, U22-ch24, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	33.49
Std Dev	16.51

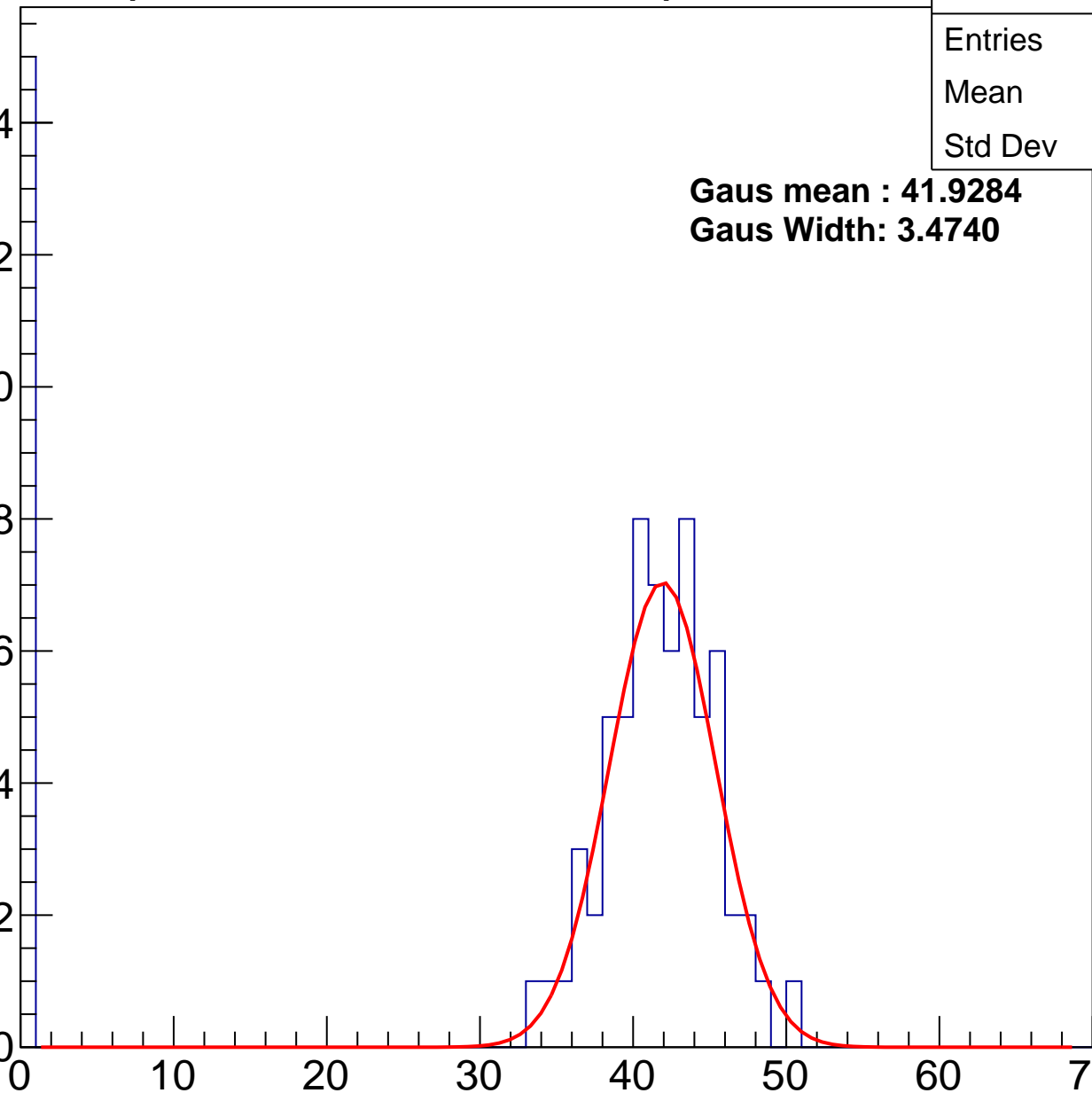
**Gaus mean : 41.9284**

**Gaus Width: 3.4740**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch24, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	63
Mean	47.37
Std Dev	3.277

Entry

10

8

6

4

2

0

0

10

20

30

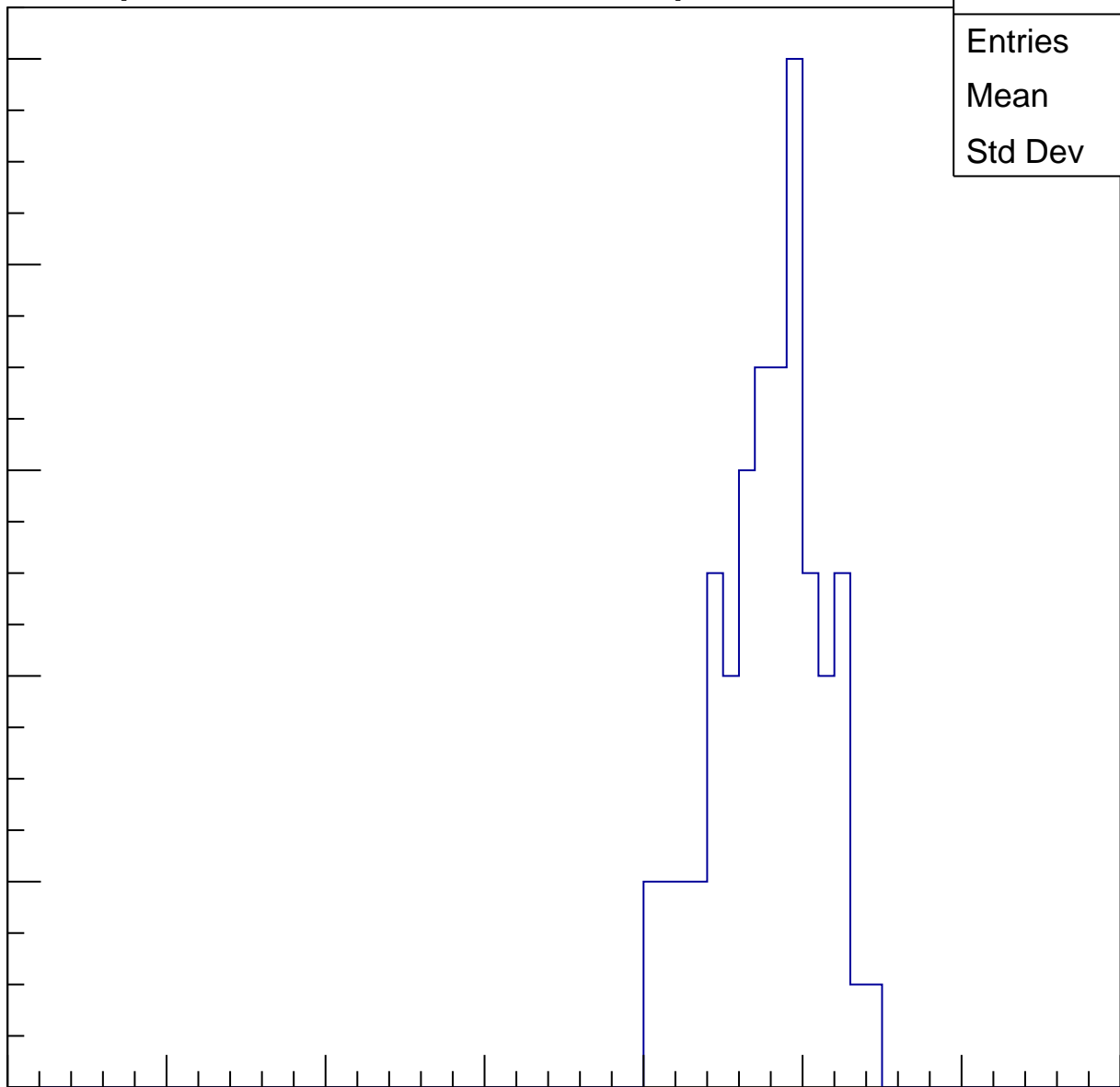
40

50

60

70

ampl



# B1L103S, U22-ch24, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

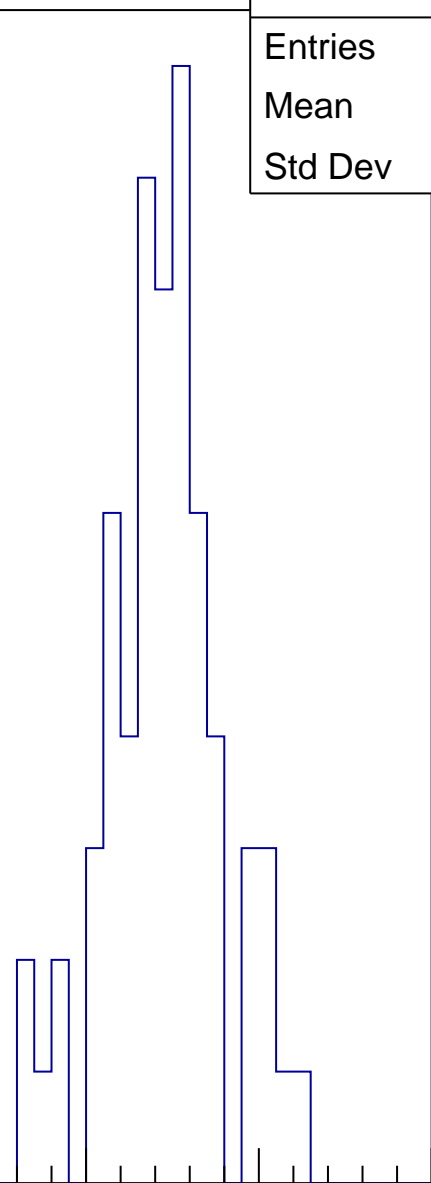
Entries	63
Mean	54
Std Dev	3.432

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

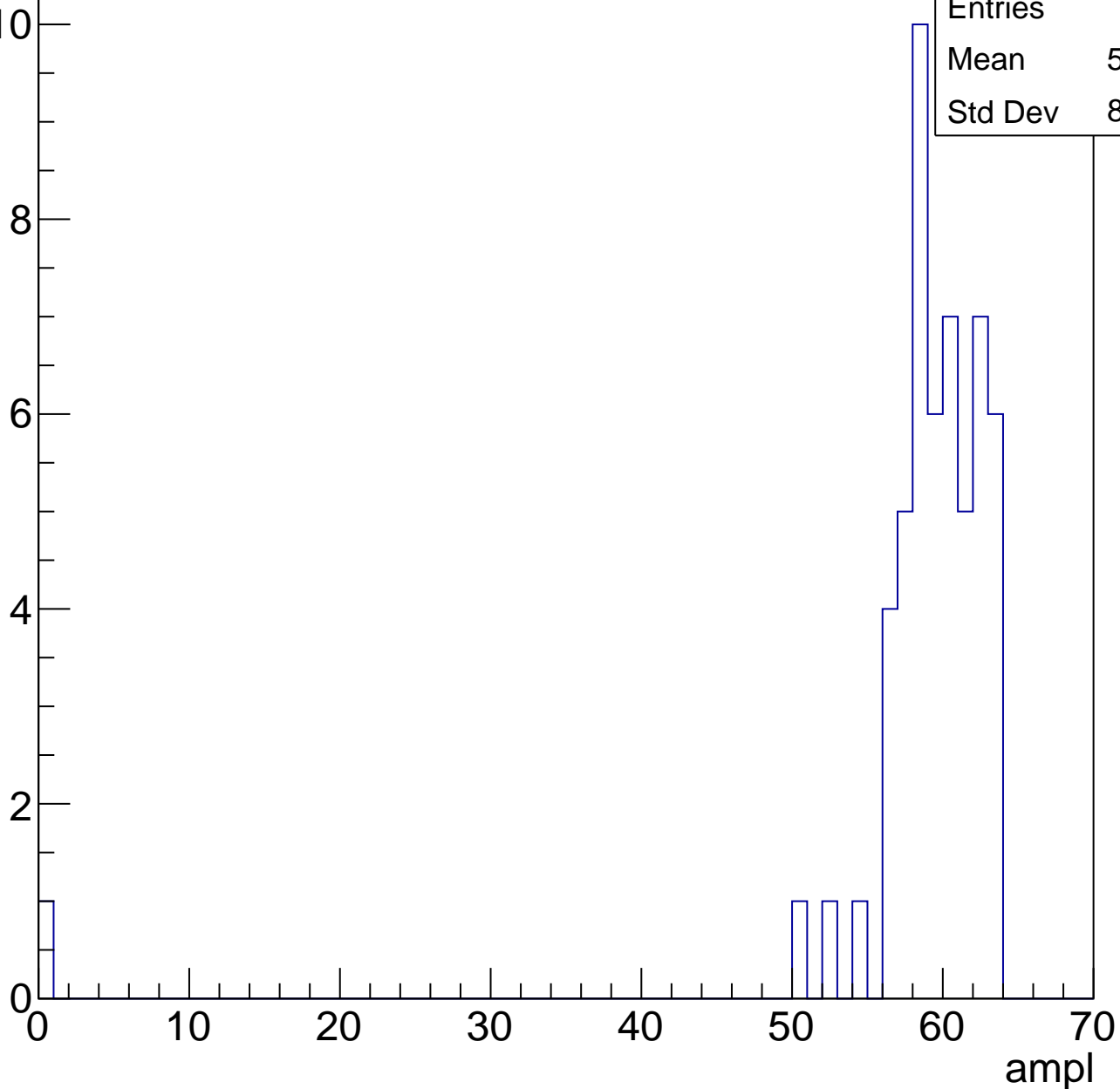


# B1L103S, U22-ch24, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

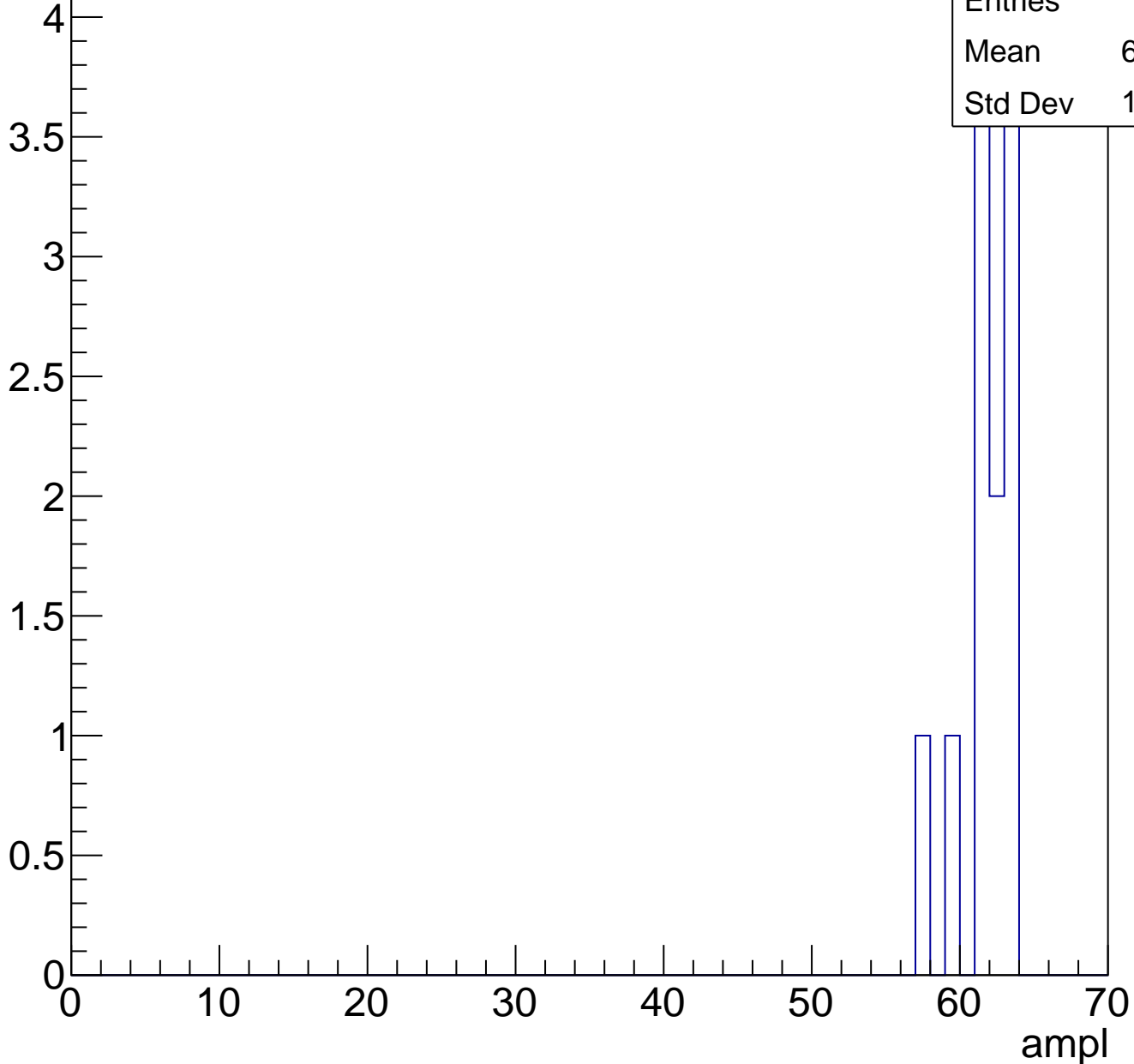
Entries	54
Mean	58.07
Std Dev	8.435



# B1L103S, U22-ch24, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch24, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch25, adc0

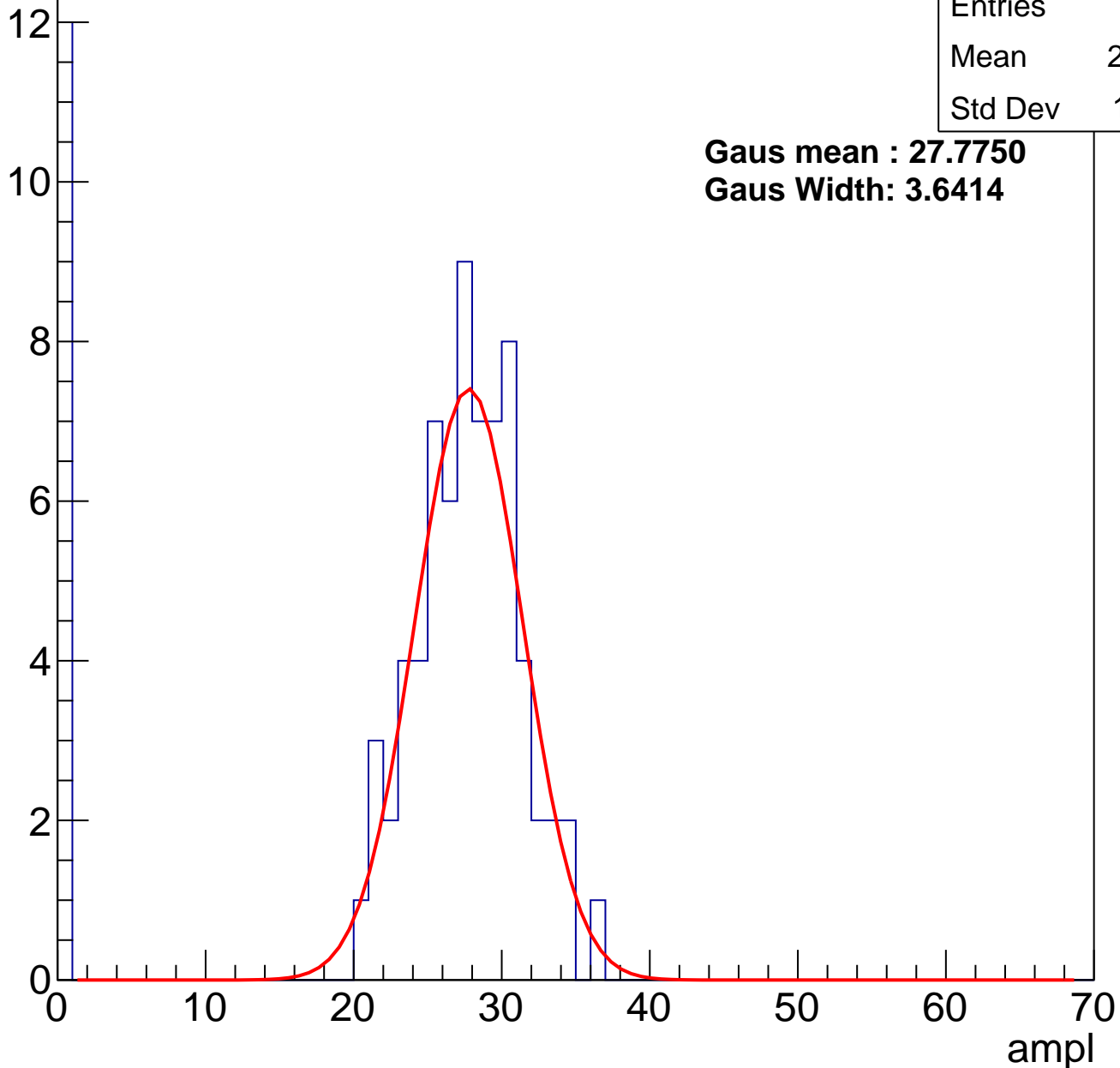
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	23.28
Std Dev	10.21

**Gaus mean : 27.7750**

**Gaus Width: 3.6414**

Entry



# B1L103S, U22-ch25, adc1

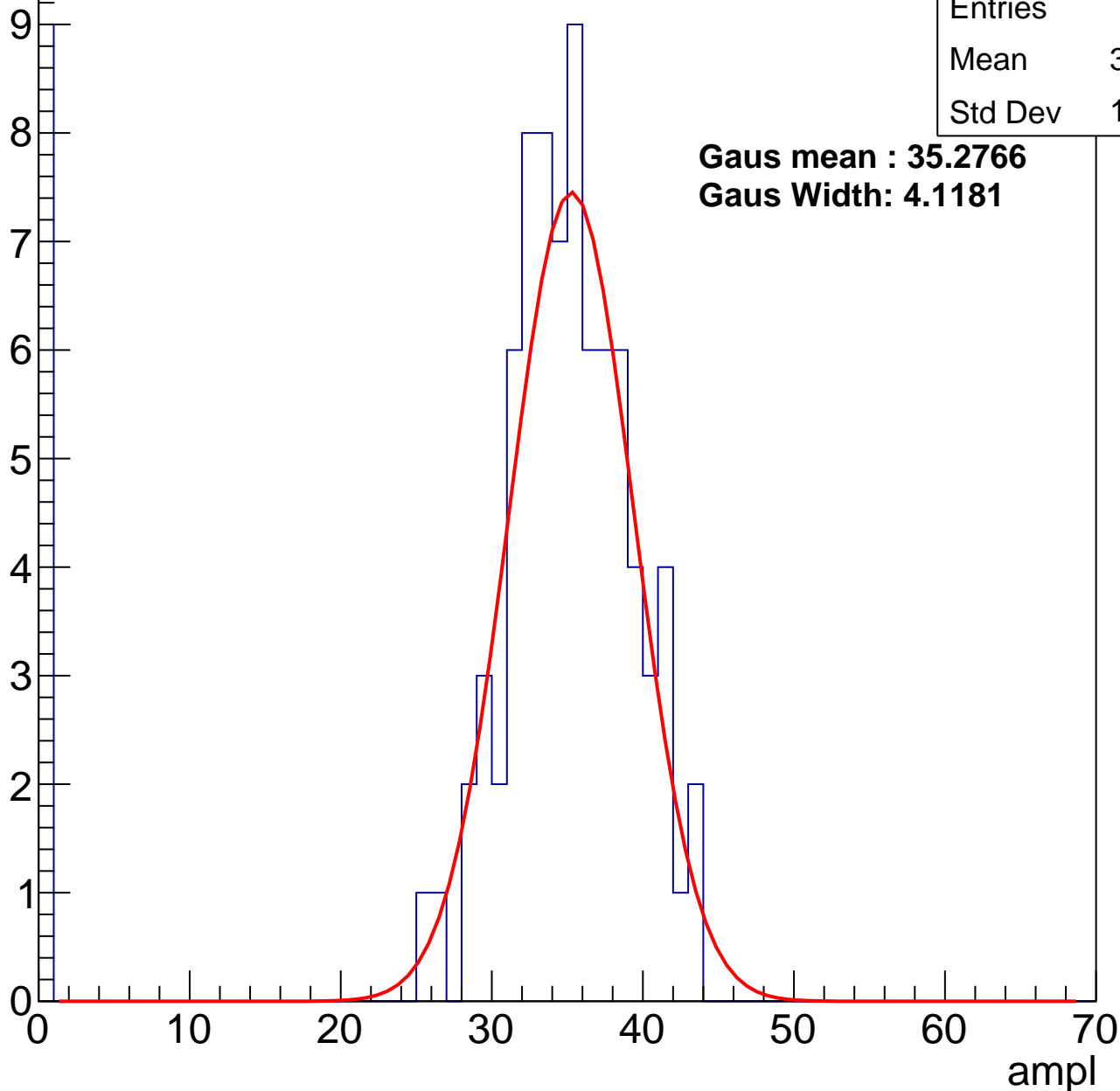
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	88
Mean	31.22
Std Dev	11.16

**Gaus mean : 35.2766**

**Gaus Width: 4.1181**



# B1L103S, U22-ch25, adc2

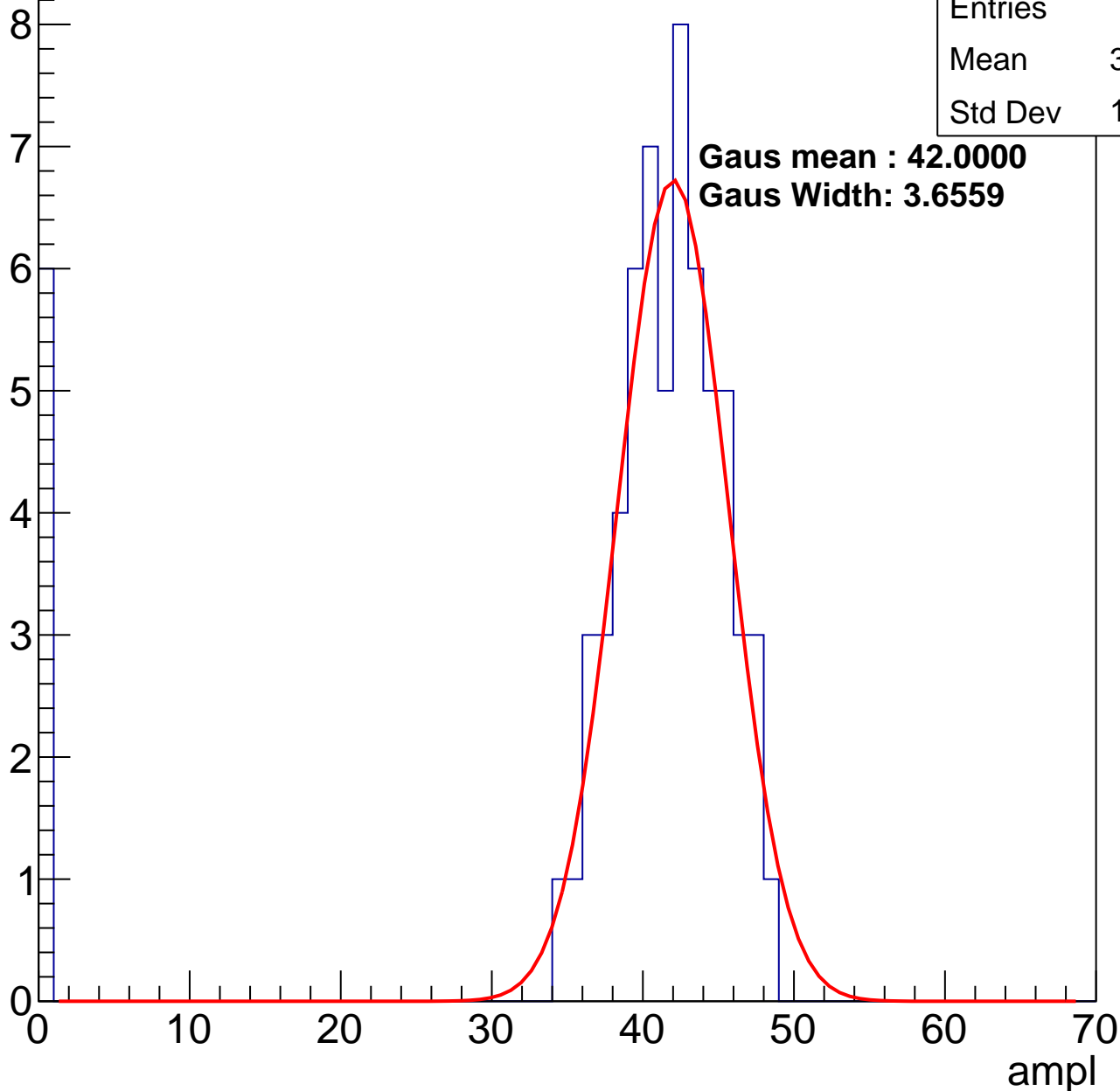
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	37.69
Std Dev	12.23

**Gaus mean : 42.0000**

**Gaus Width: 3.6559**

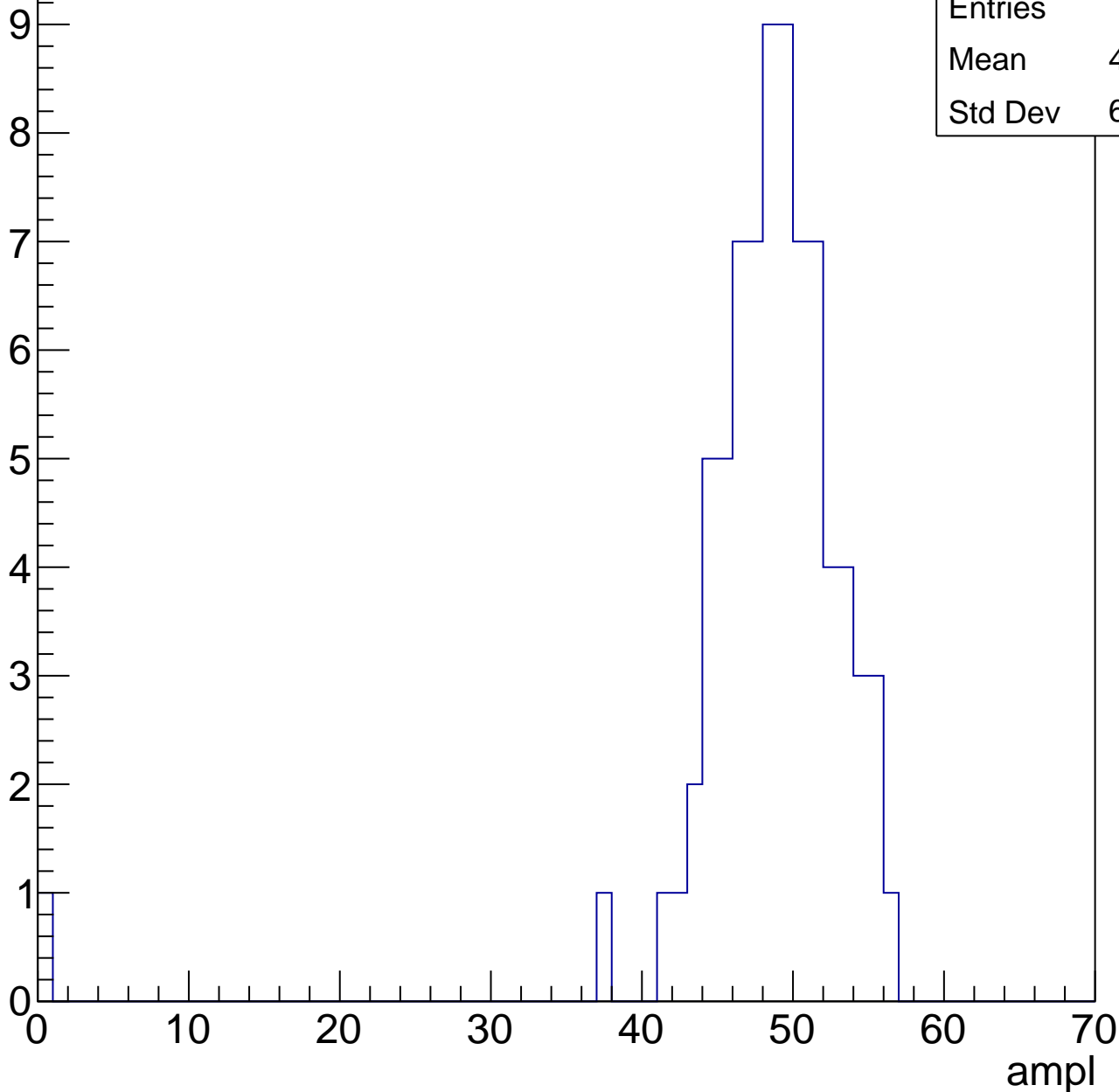


# B1L103S, U22-ch25, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.86
Std Dev	6.548

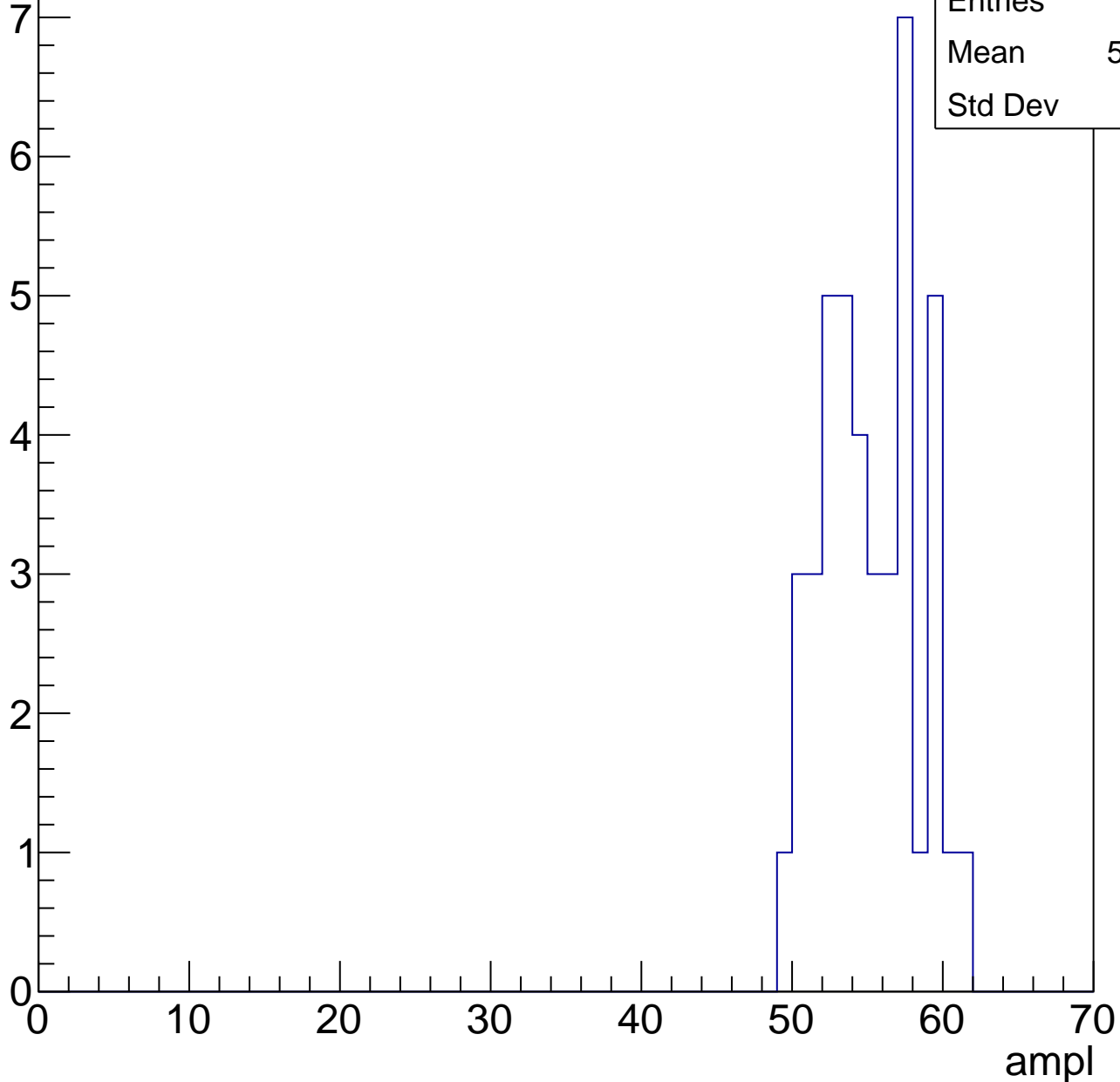


# B1L103S, U22-ch25, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	42
Mean	54.74
Std Dev	3.11

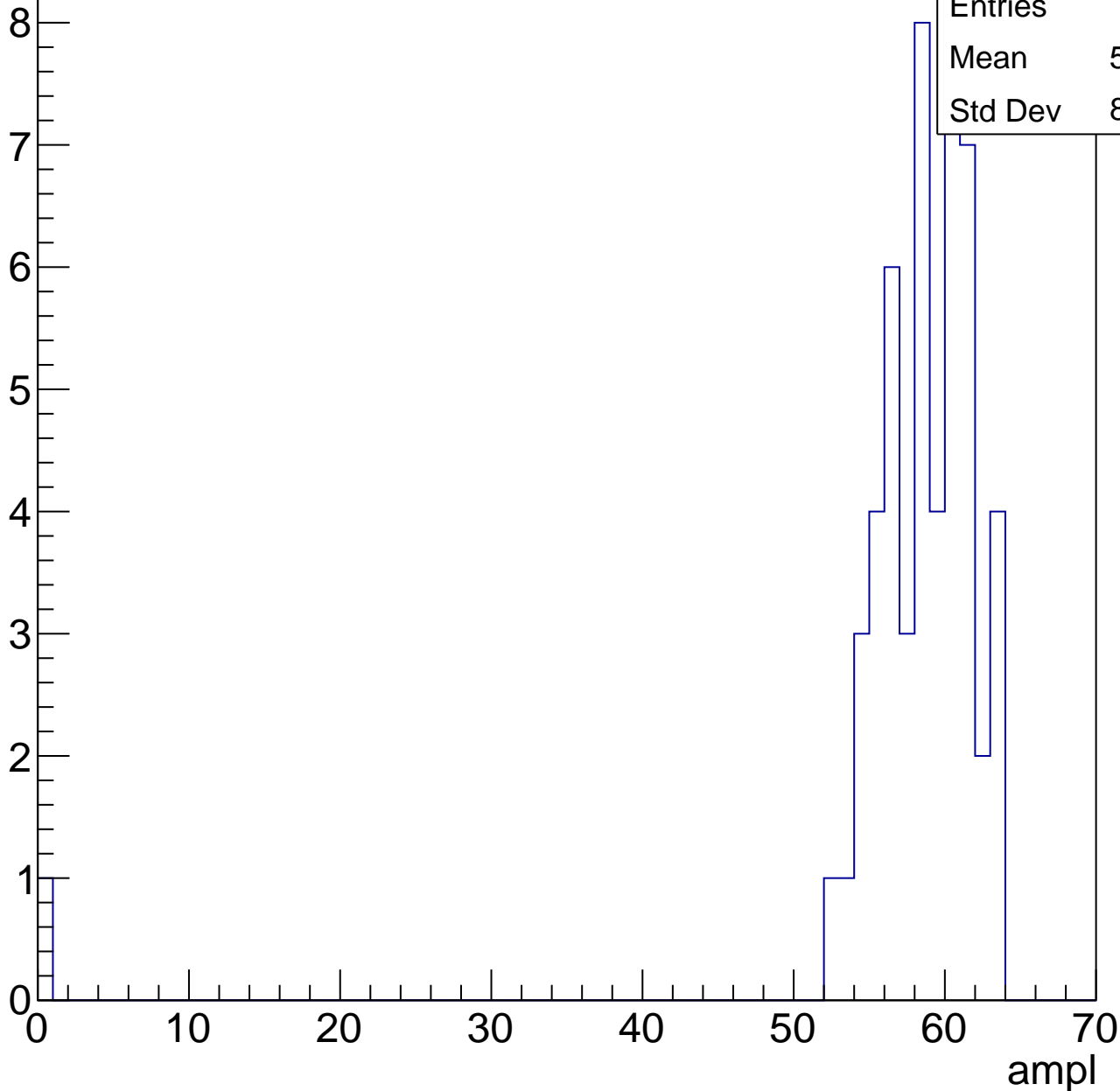


# B1L103S, U22-ch25, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	57.25
Std Dev	8.478

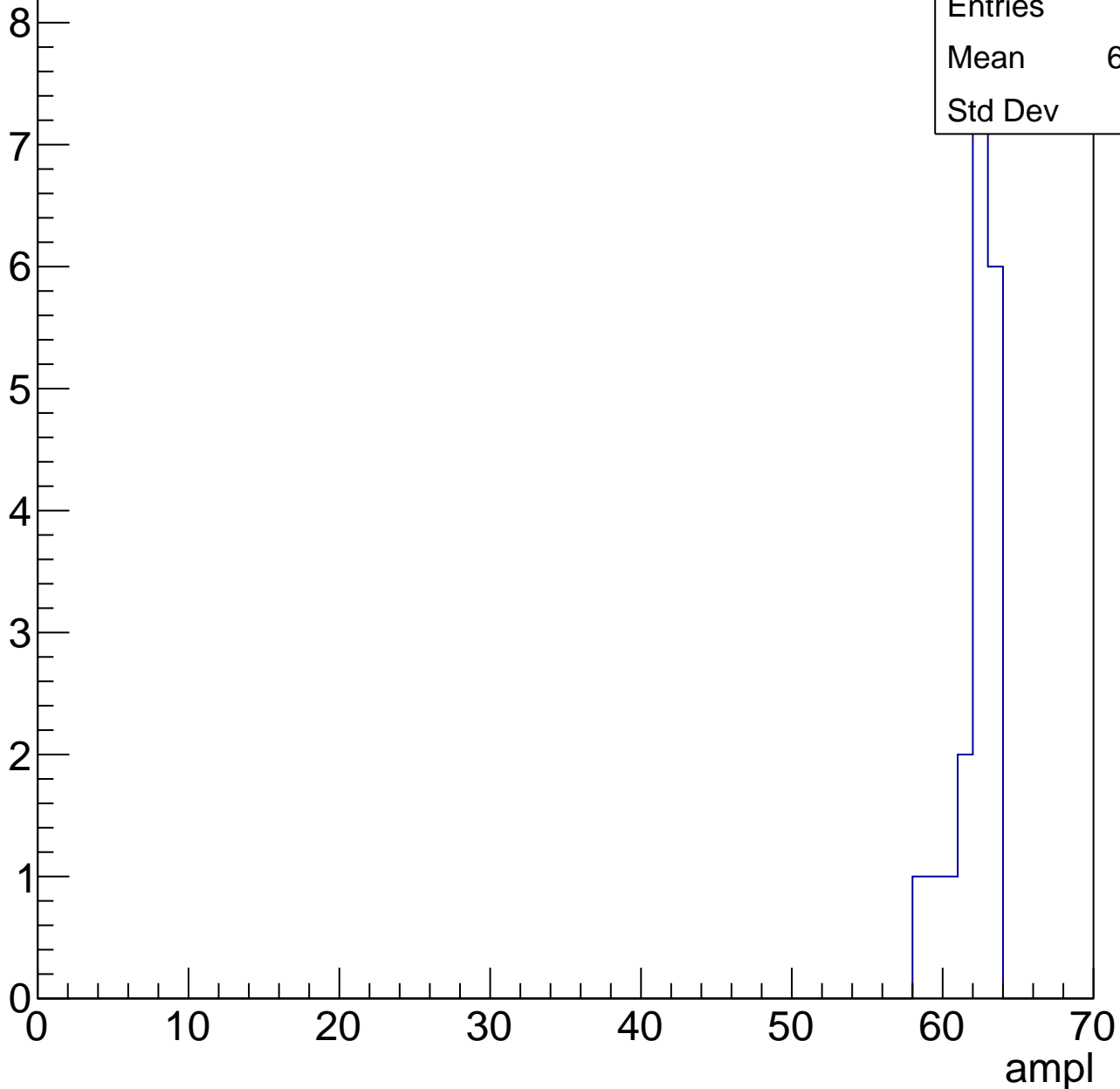


# B1L103S, U22-ch25, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	19
Mean	61.74
Std Dev	1.37





# B1L103S, U22-ch25, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch26, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	96
Mean	28.14
Std Dev	8.228

**Gaus mean : 30.1113**

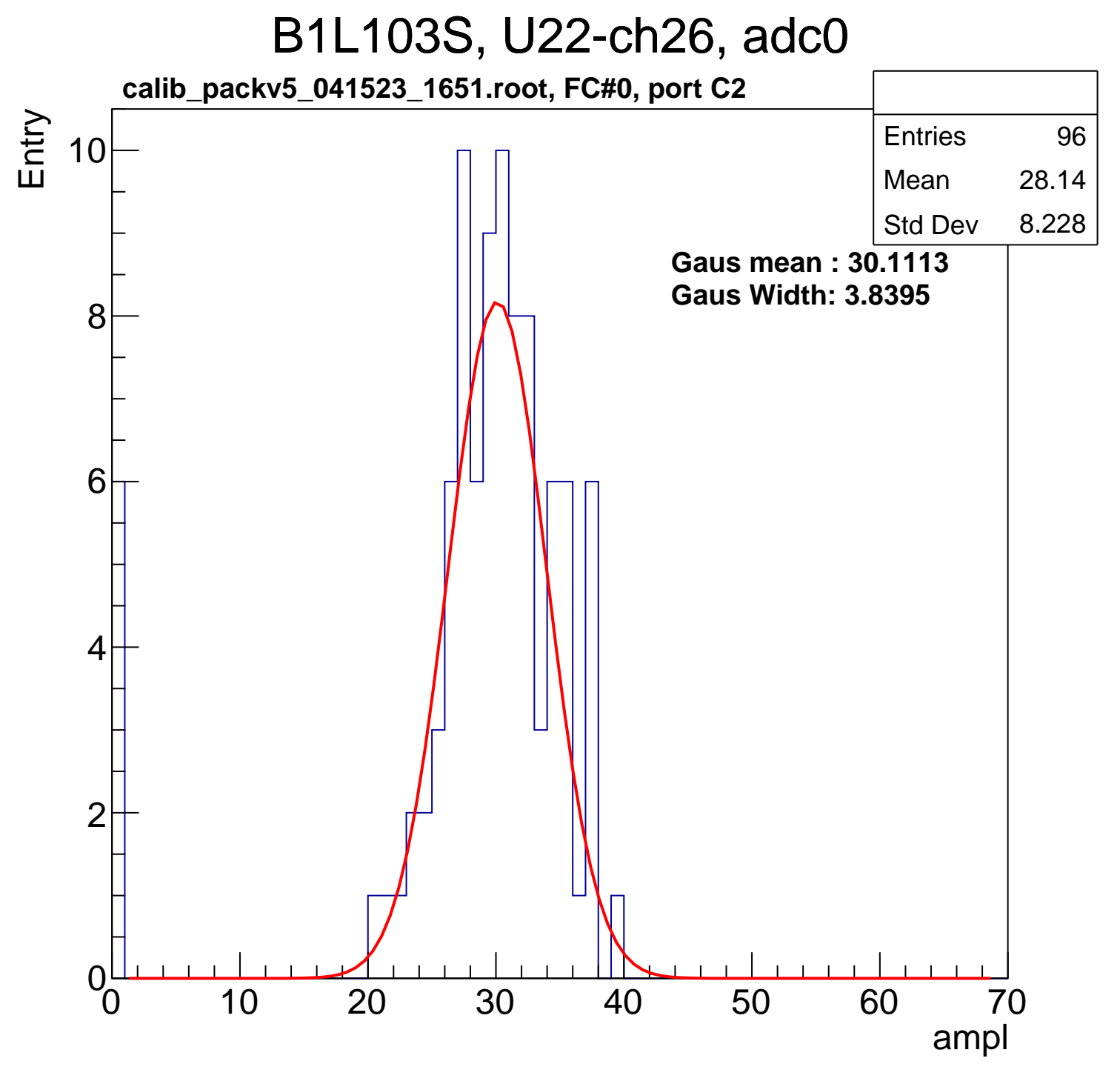
**Gaus Width: 3.8395**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch26, adc1

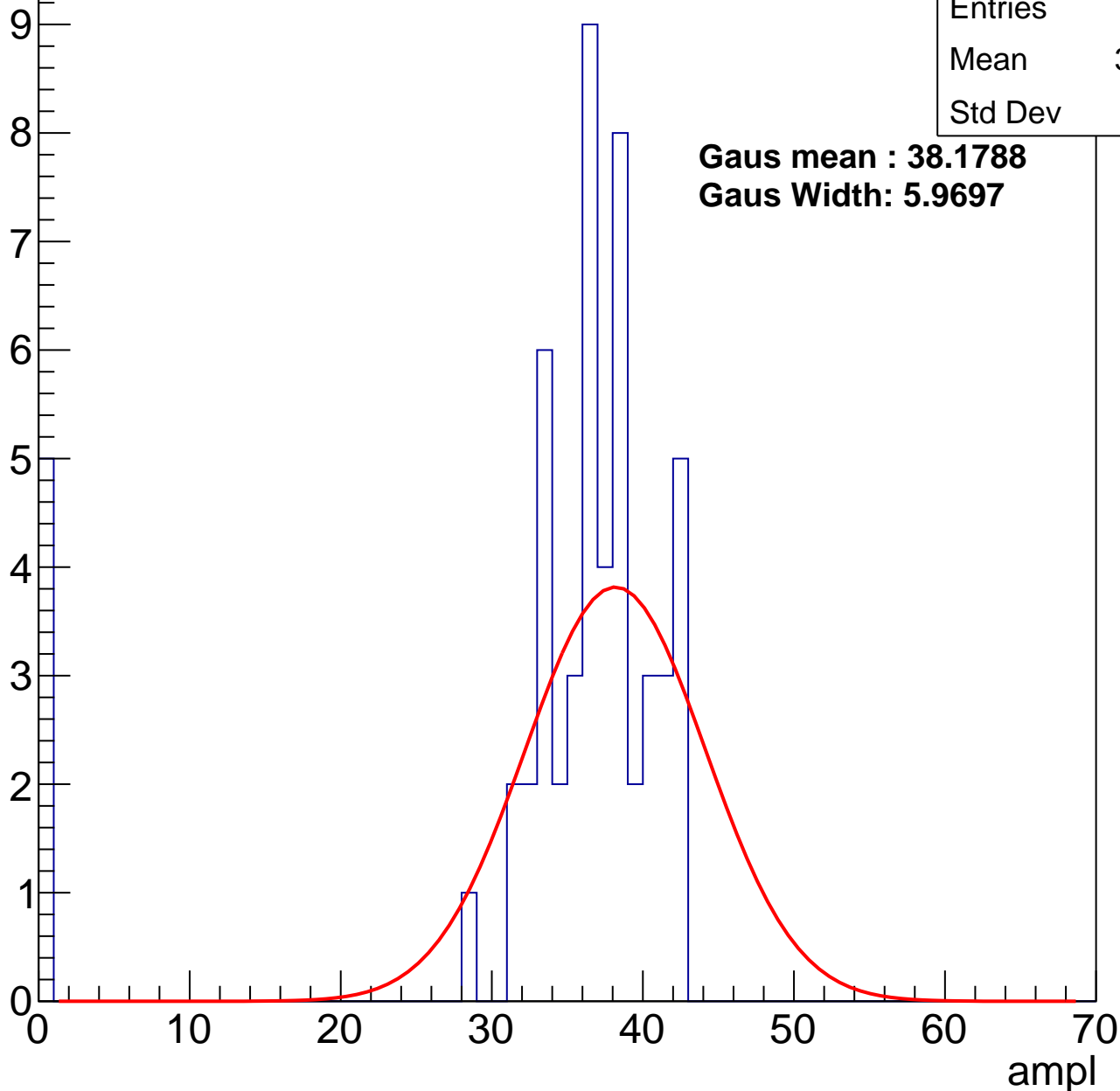
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	33.31
Std Dev	11

**Gaus mean : 38.1788**

**Gaus Width: 5.9697**



# B1L103S, U22-ch26, adc2

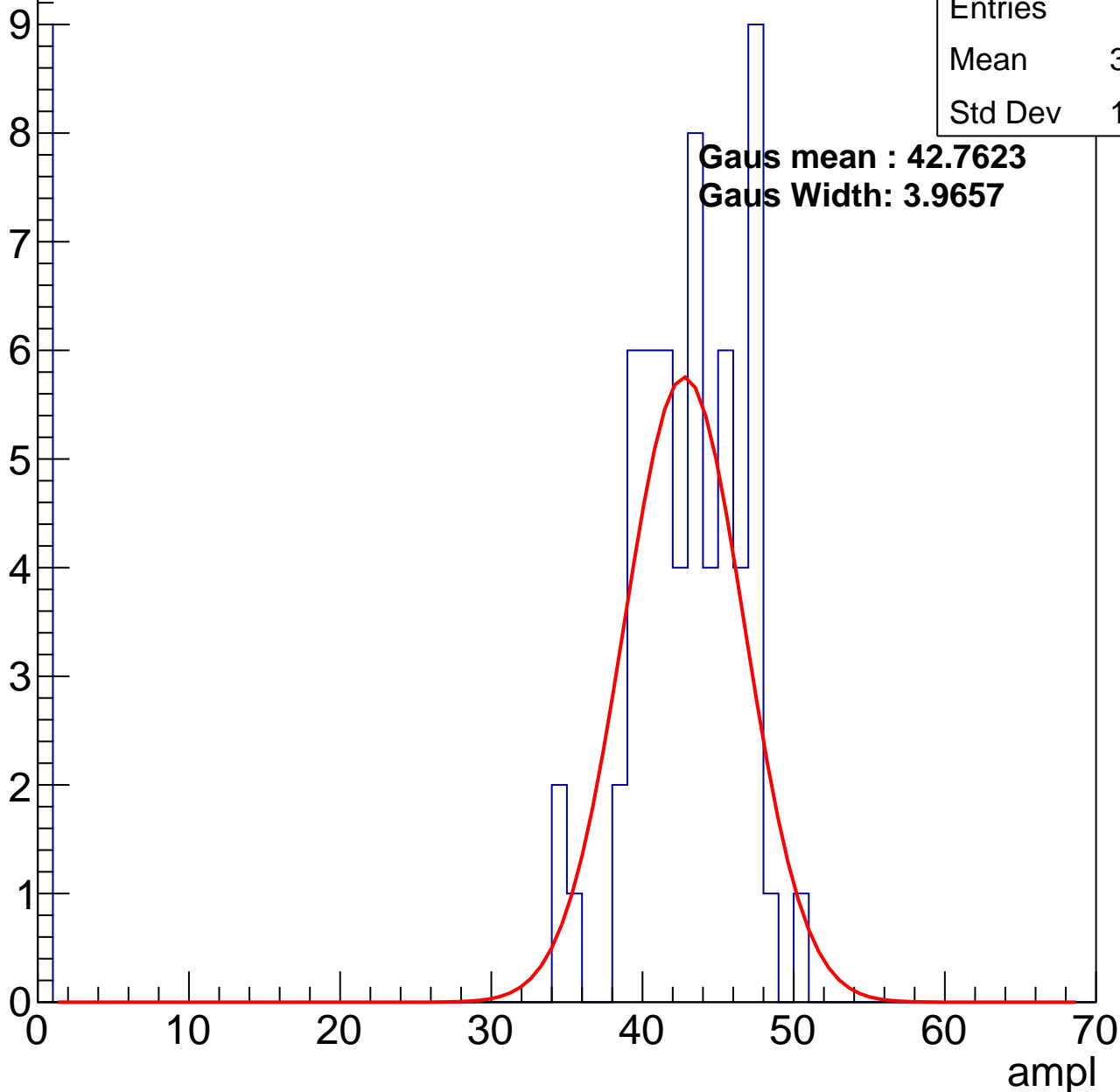
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	37.13
Std Dev	14.74

**Gaus mean : 42.7623**

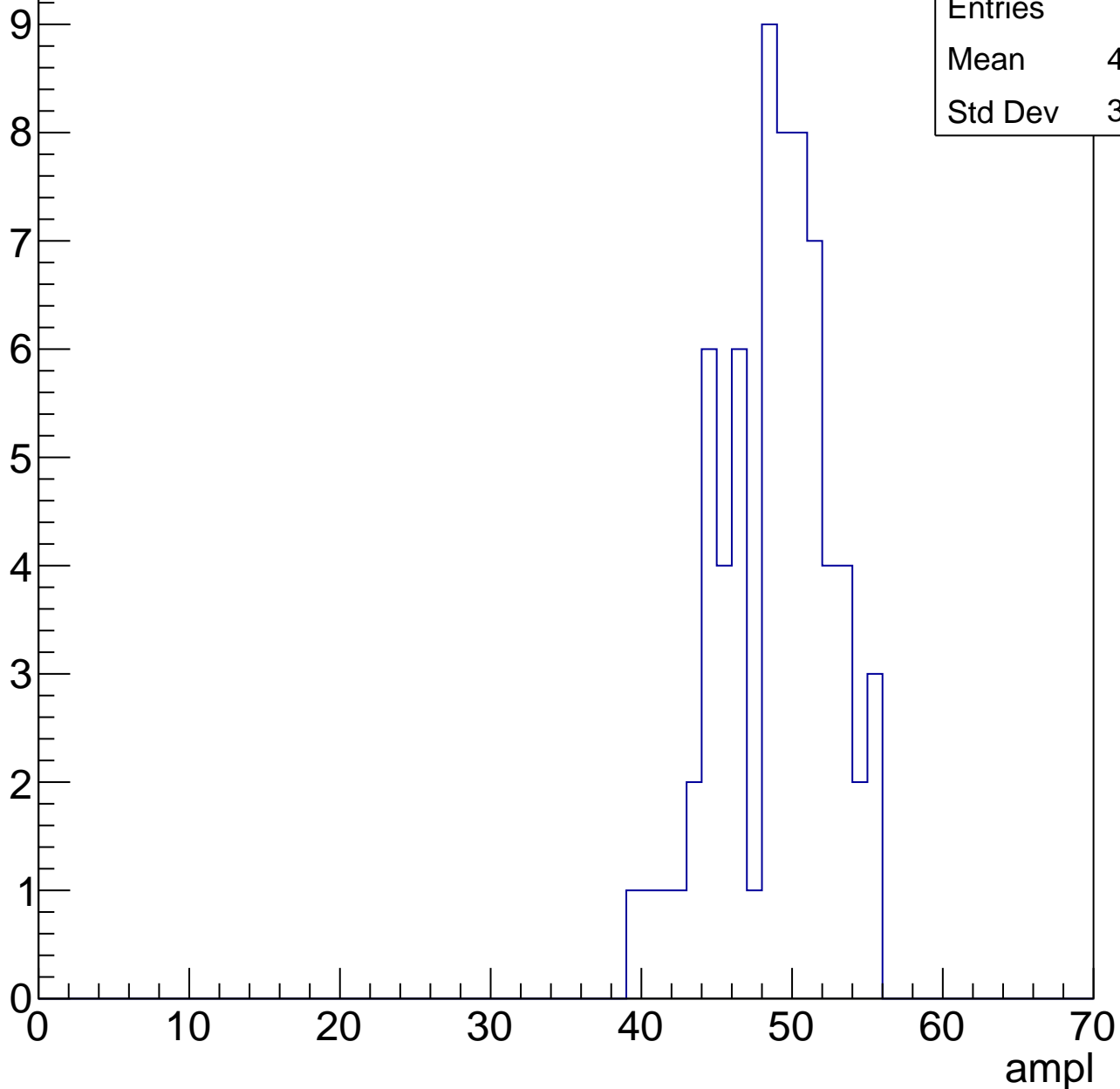
**Gaus Width: 3.9657**



# B1L103S, U22-ch26, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



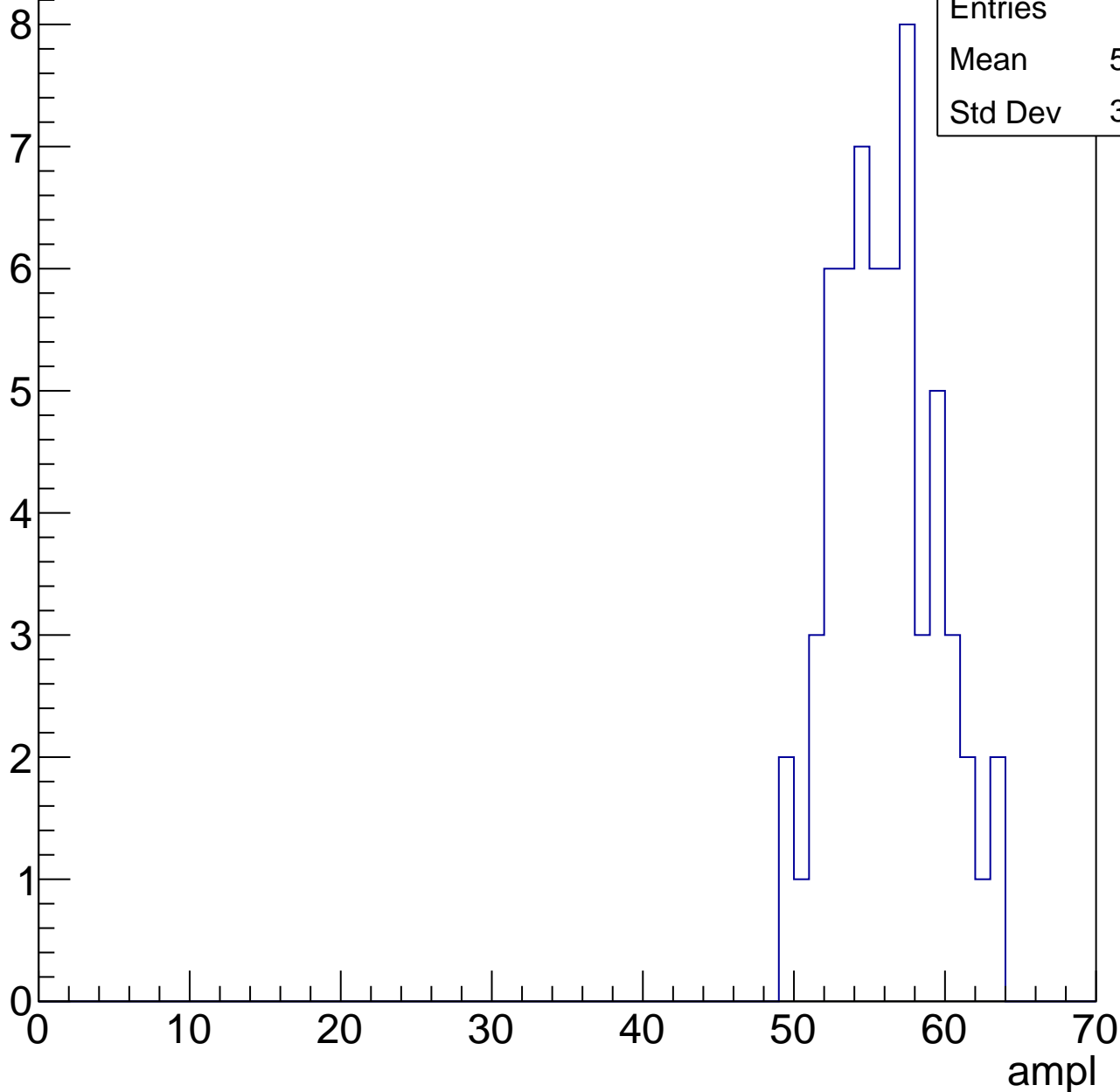
Entries	68
Mean	48.37
Std Dev	3.666

# B1L103S, U22-ch26, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	55.57
Std Dev	3.336

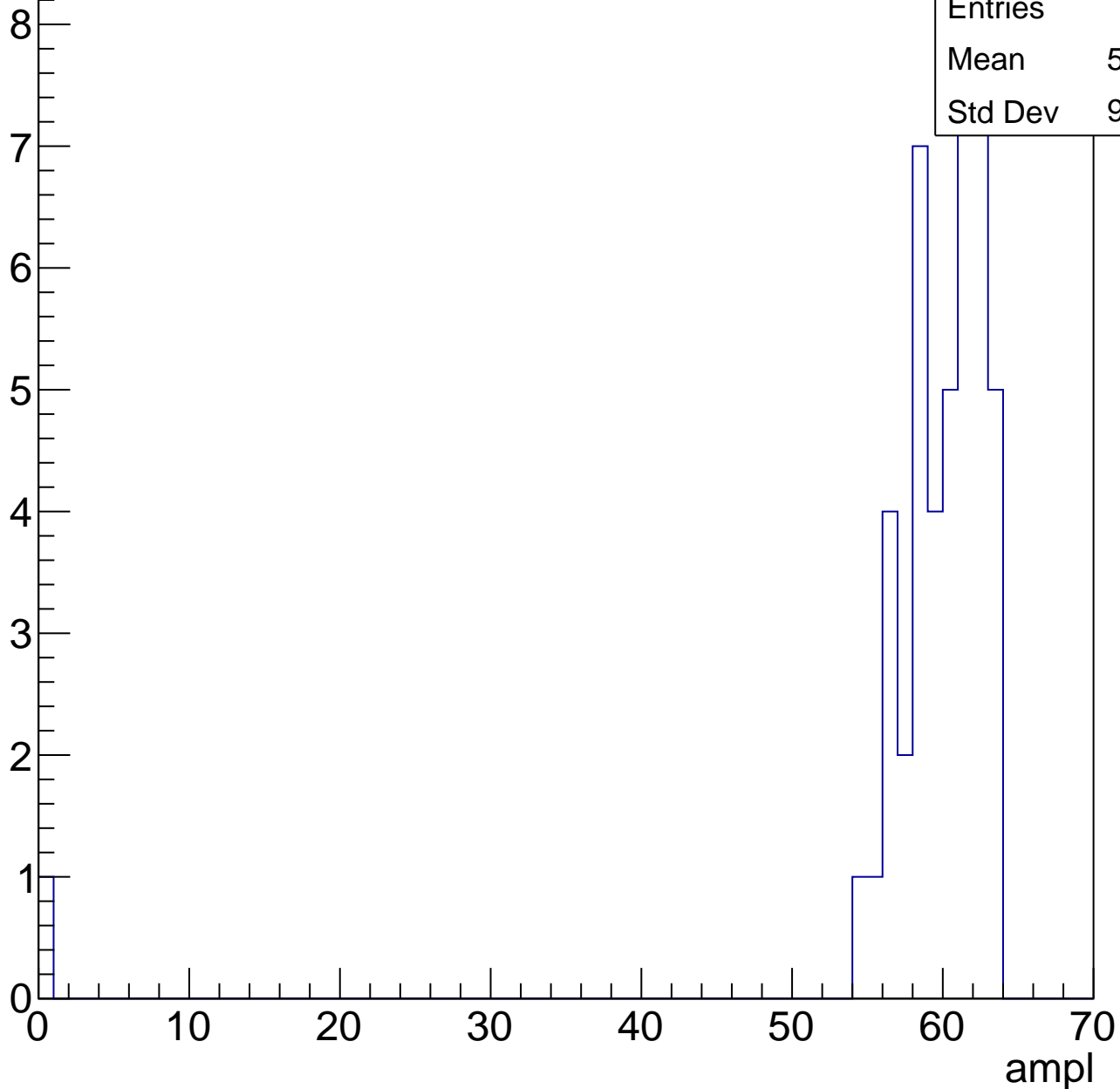


# B1L103S, U22-ch26, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

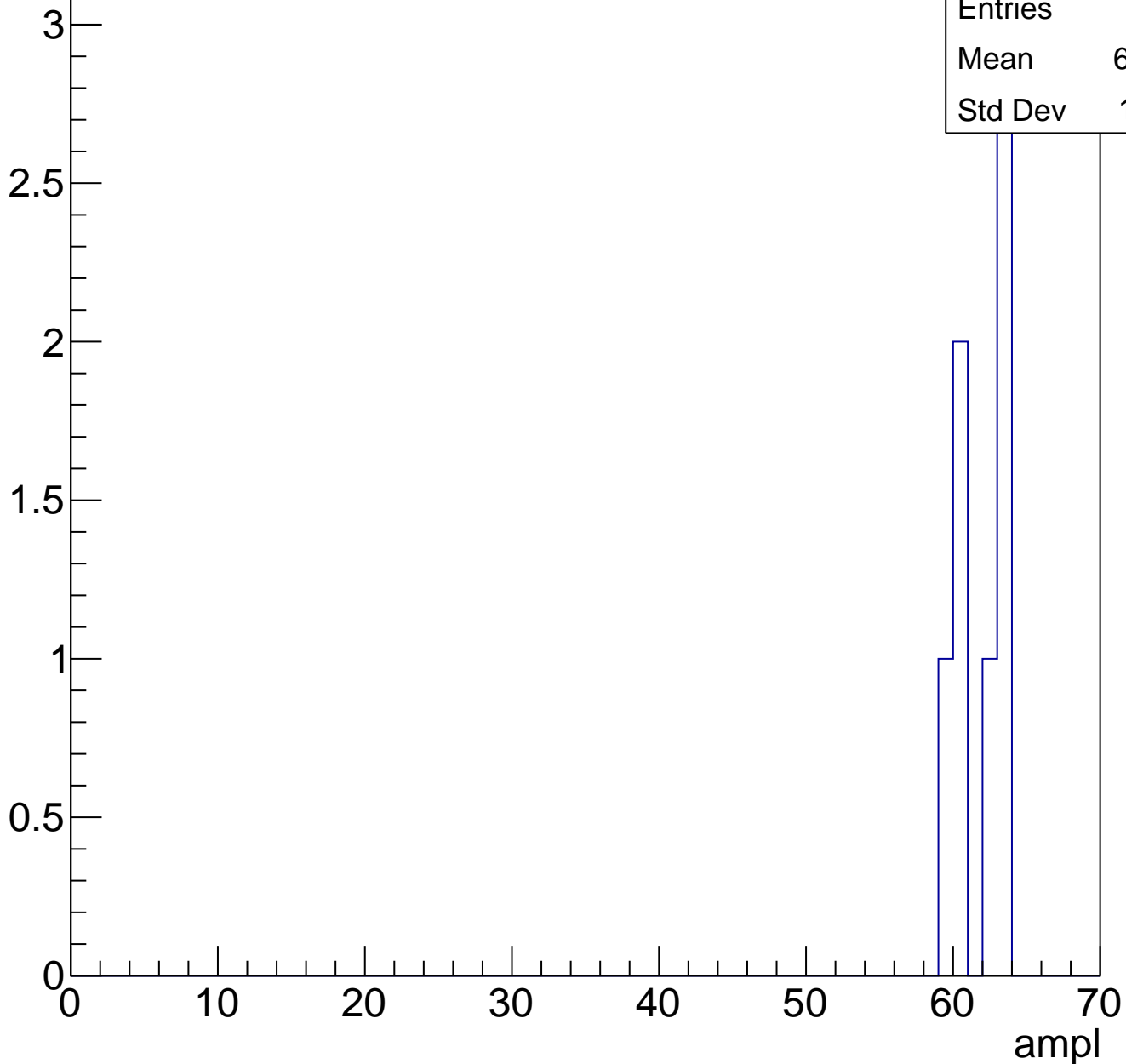
Entries	46
Mean	58.43
Std Dev	9.026



# B1L103S, U22-ch26, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch26, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch27, adc0

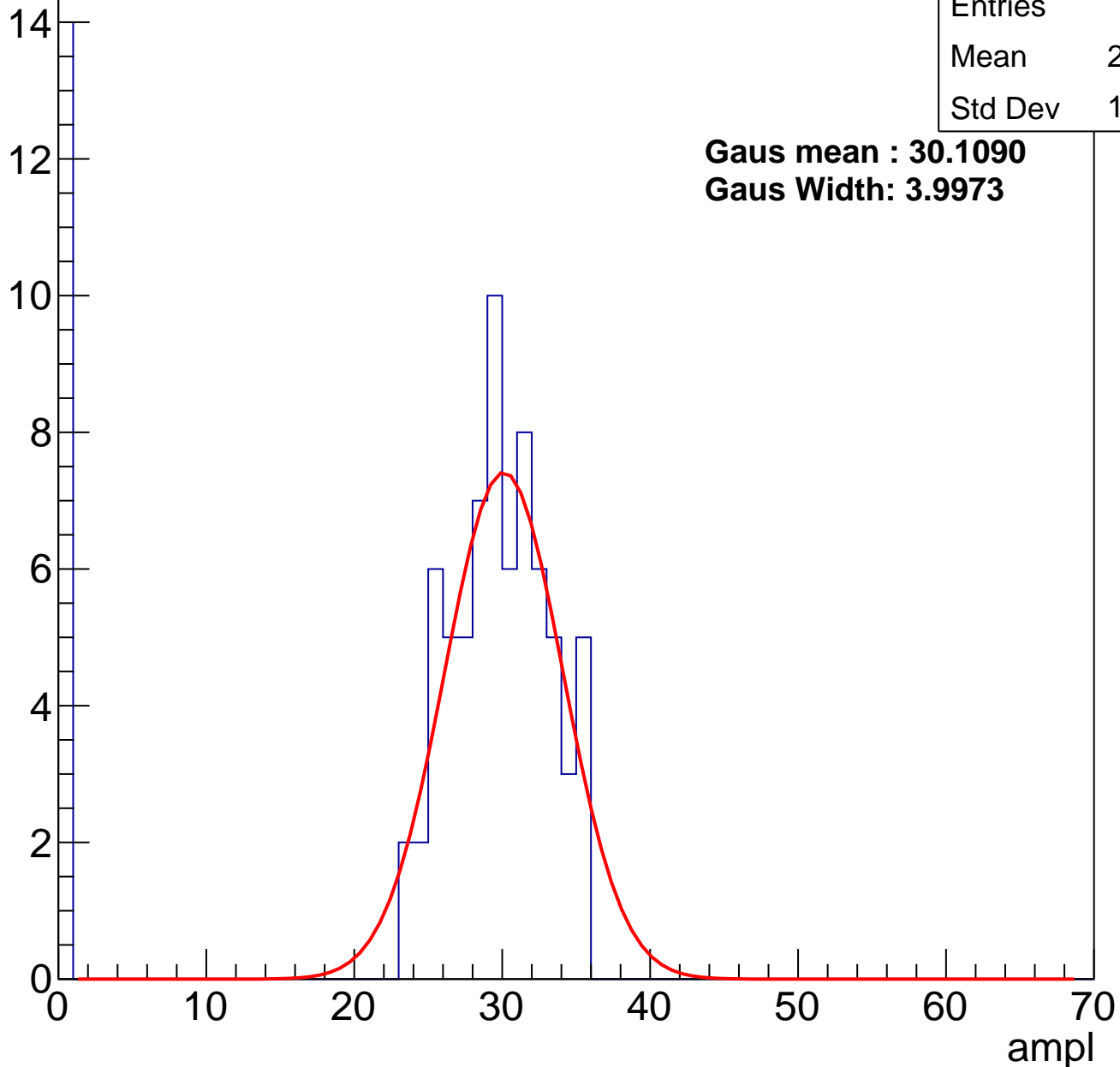
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	24.49
Std Dev	11.33

**Gaus mean : 30.1090**

**Gaus Width: 3.9973**

Entry



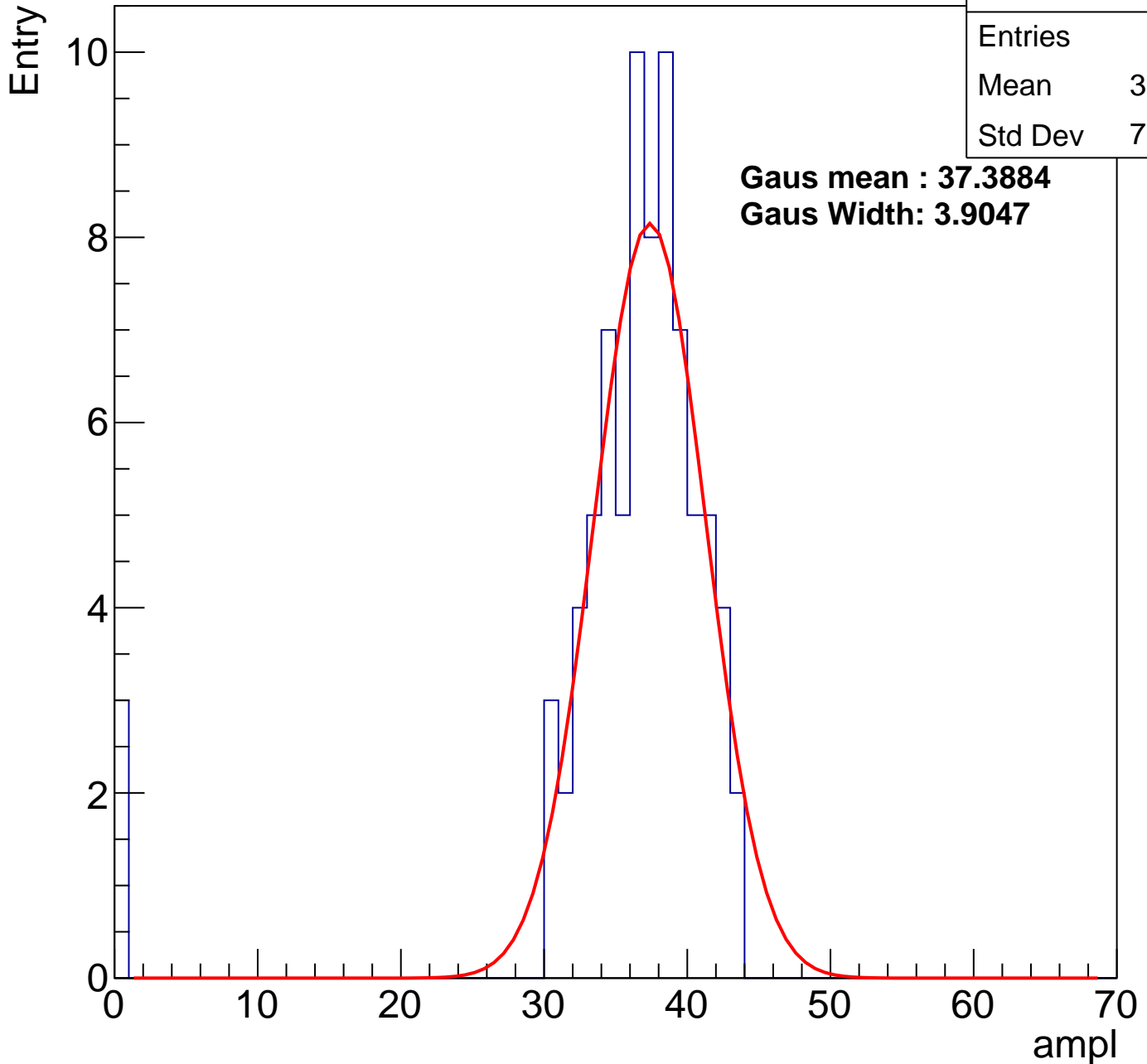
# B1L103S, U22-ch27, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	35.33
Std Dev	7.673

**Gaus mean : 37.3884**

**Gaus Width: 3.9047**



# B1L103S, U22-ch27, adc2

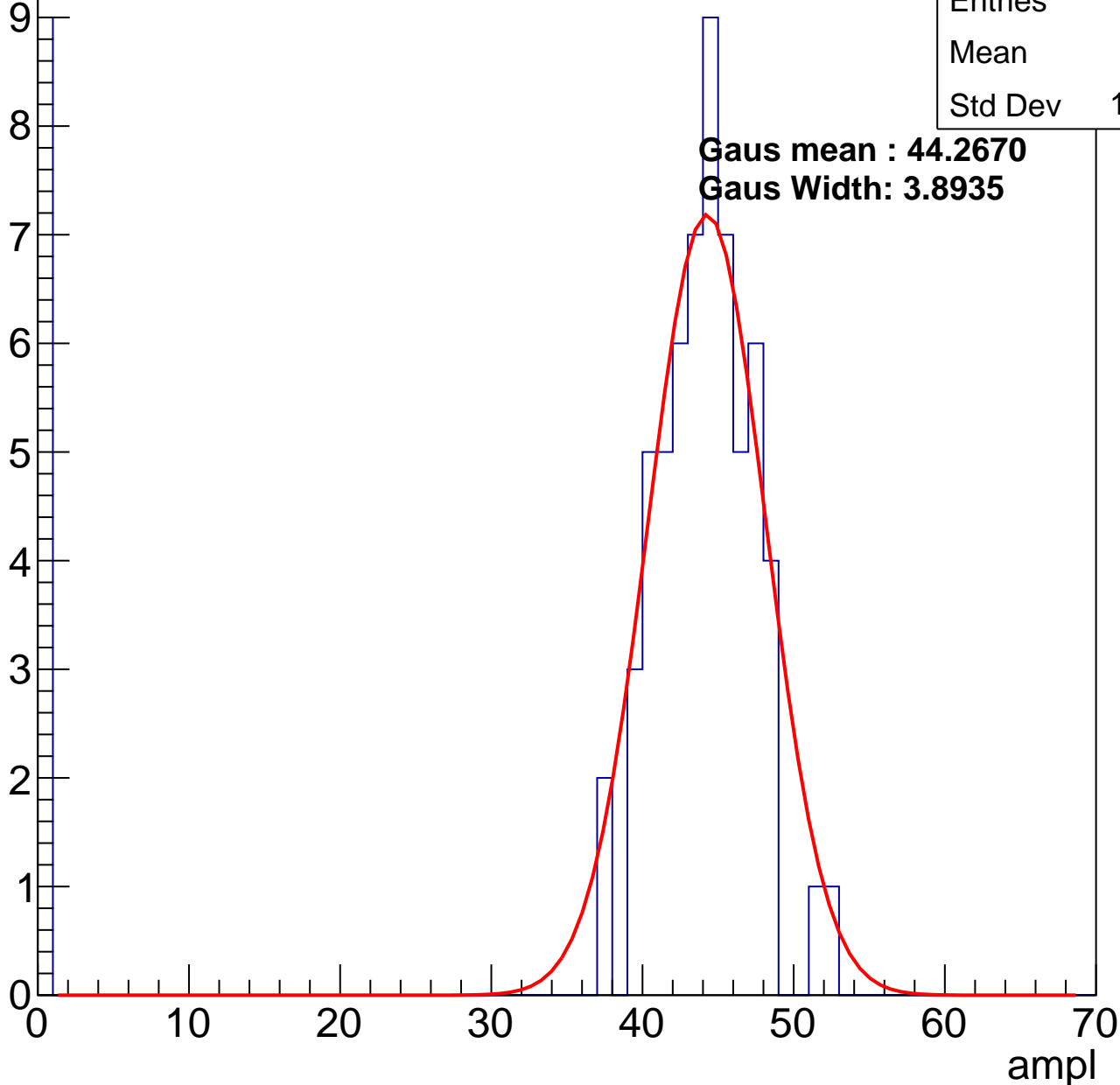
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	38.1
Std Dev	14.92

**Gaus mean : 44.2670**

**Gaus Width: 3.8935**

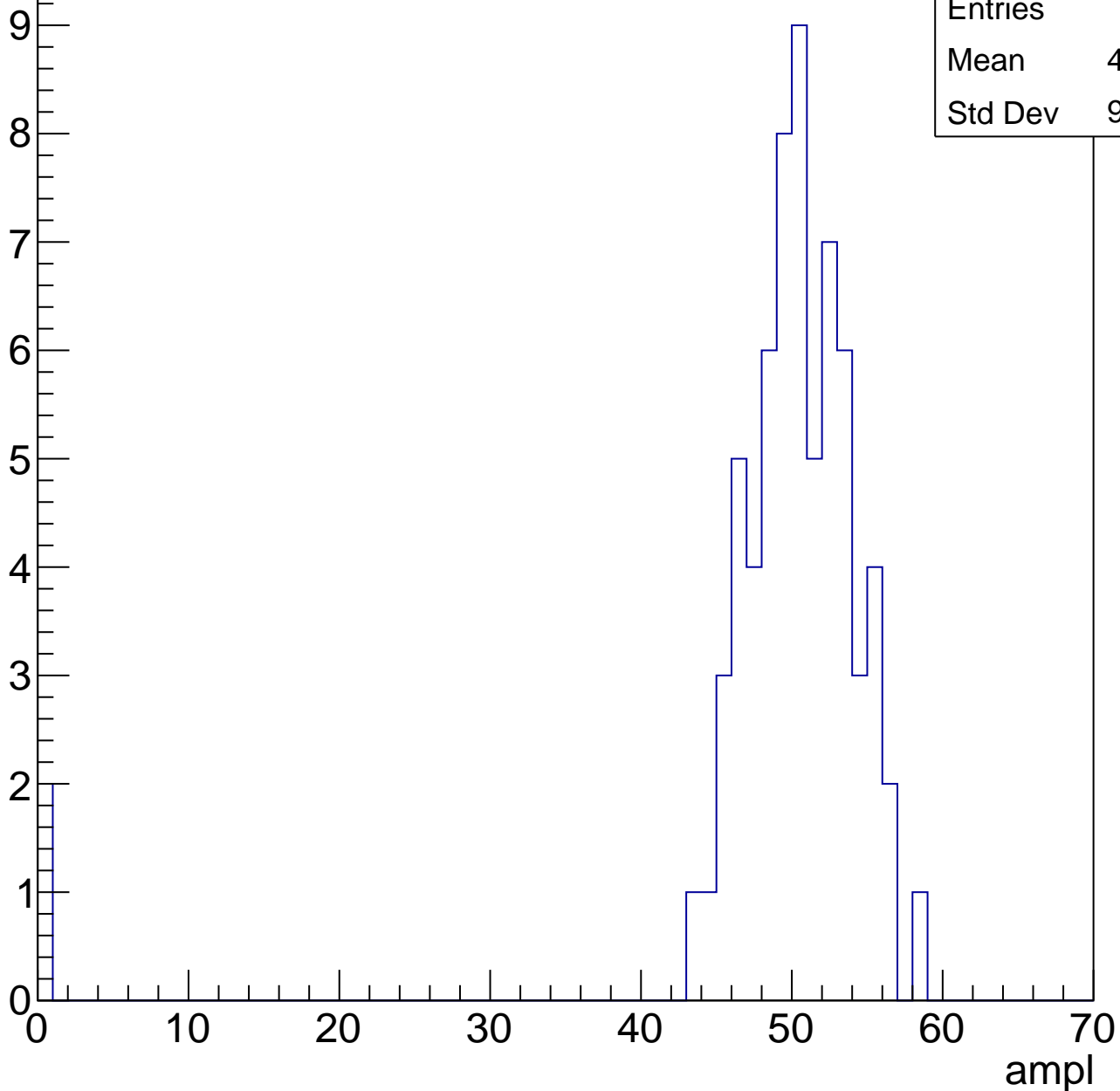


# B1L103S, U22-ch27, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

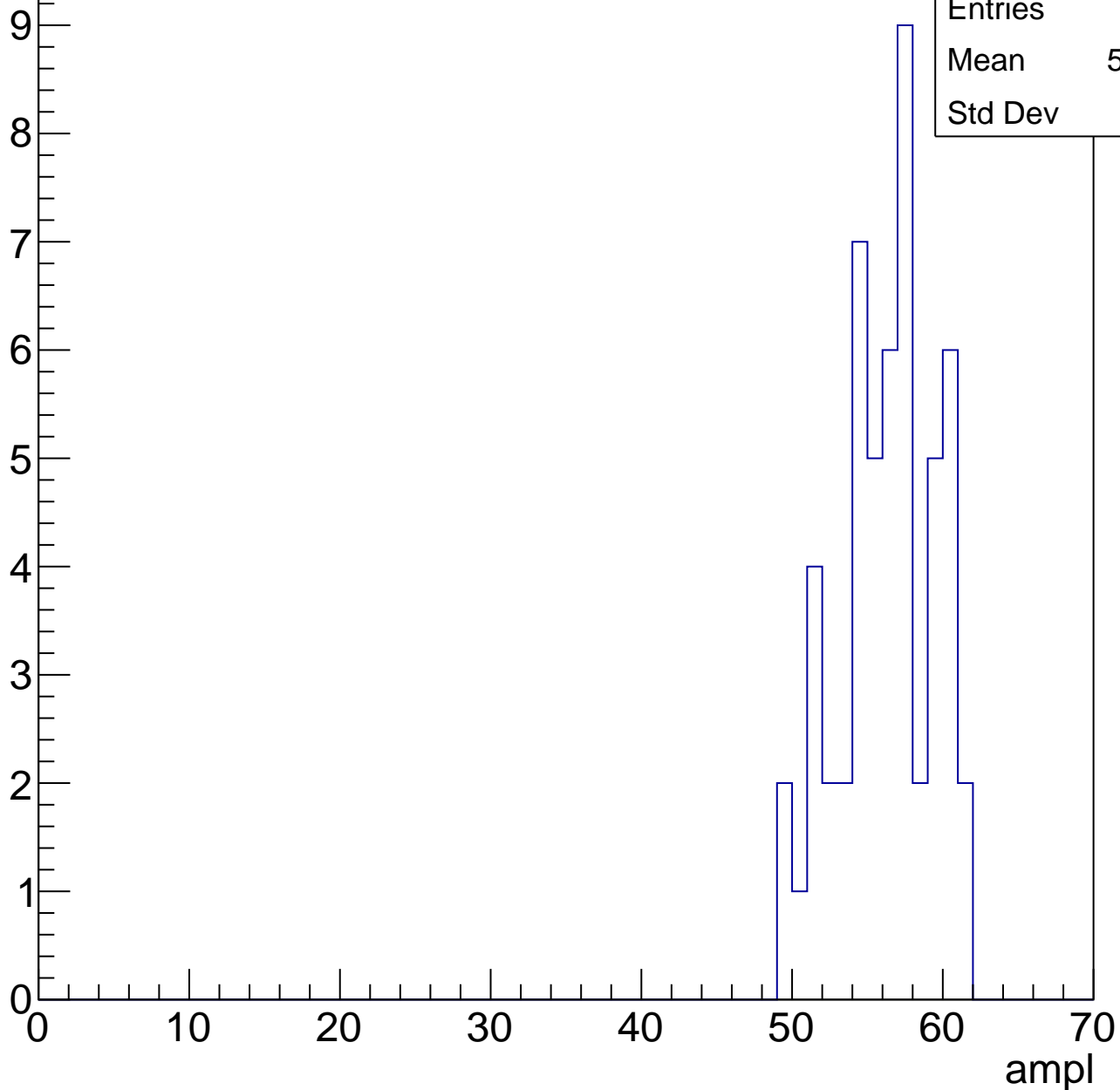
Entries	67
Mean	48.64
Std Dev	9.106



# B1L103S, U22-ch27, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

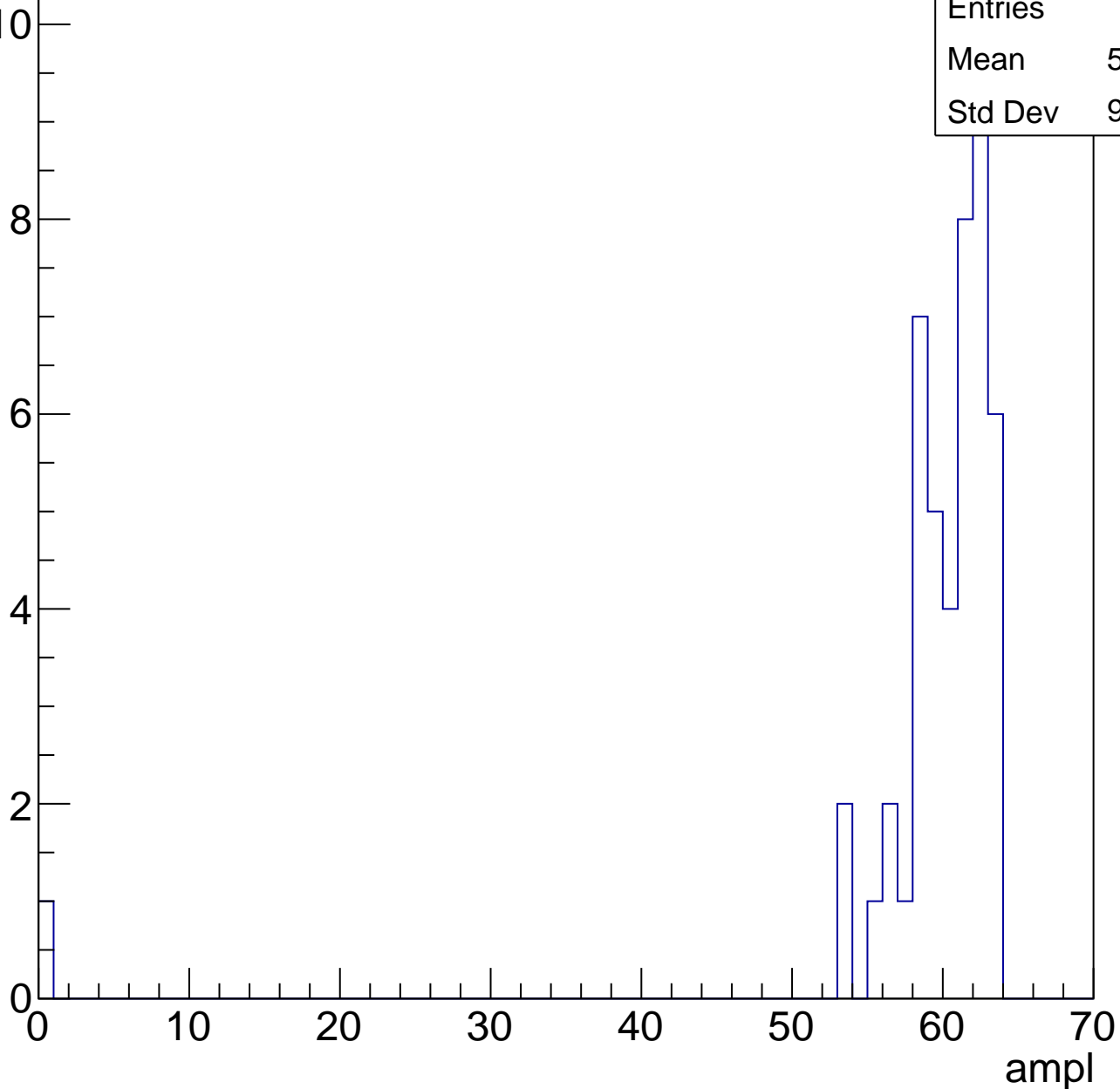


# B1L103S, U22-ch27, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

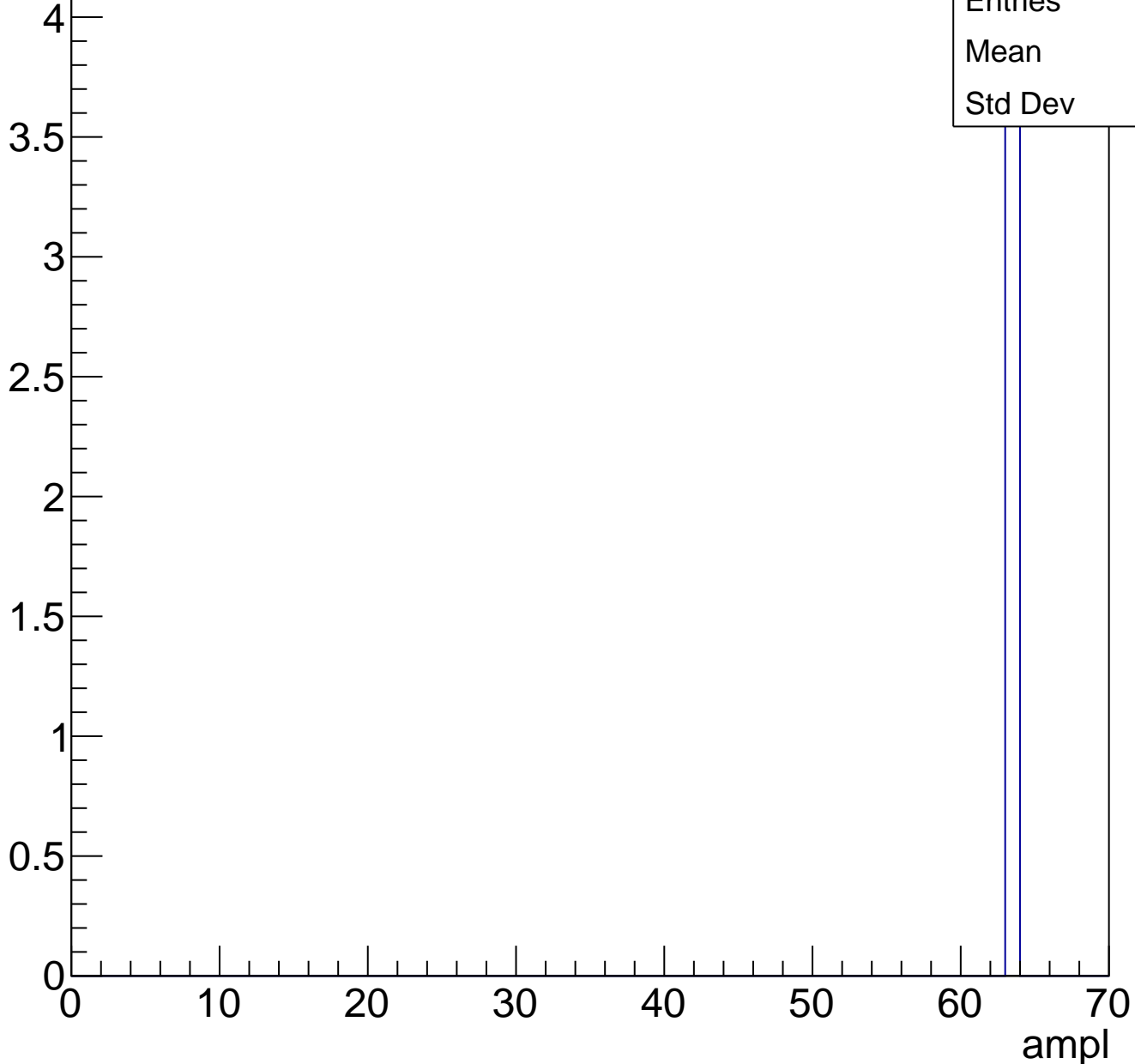
Entries	47
Mean	58.66
Std Dev	9.009



# B1L103S, U22-ch27, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch27, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



# B1L103S, U22-ch28, adc0

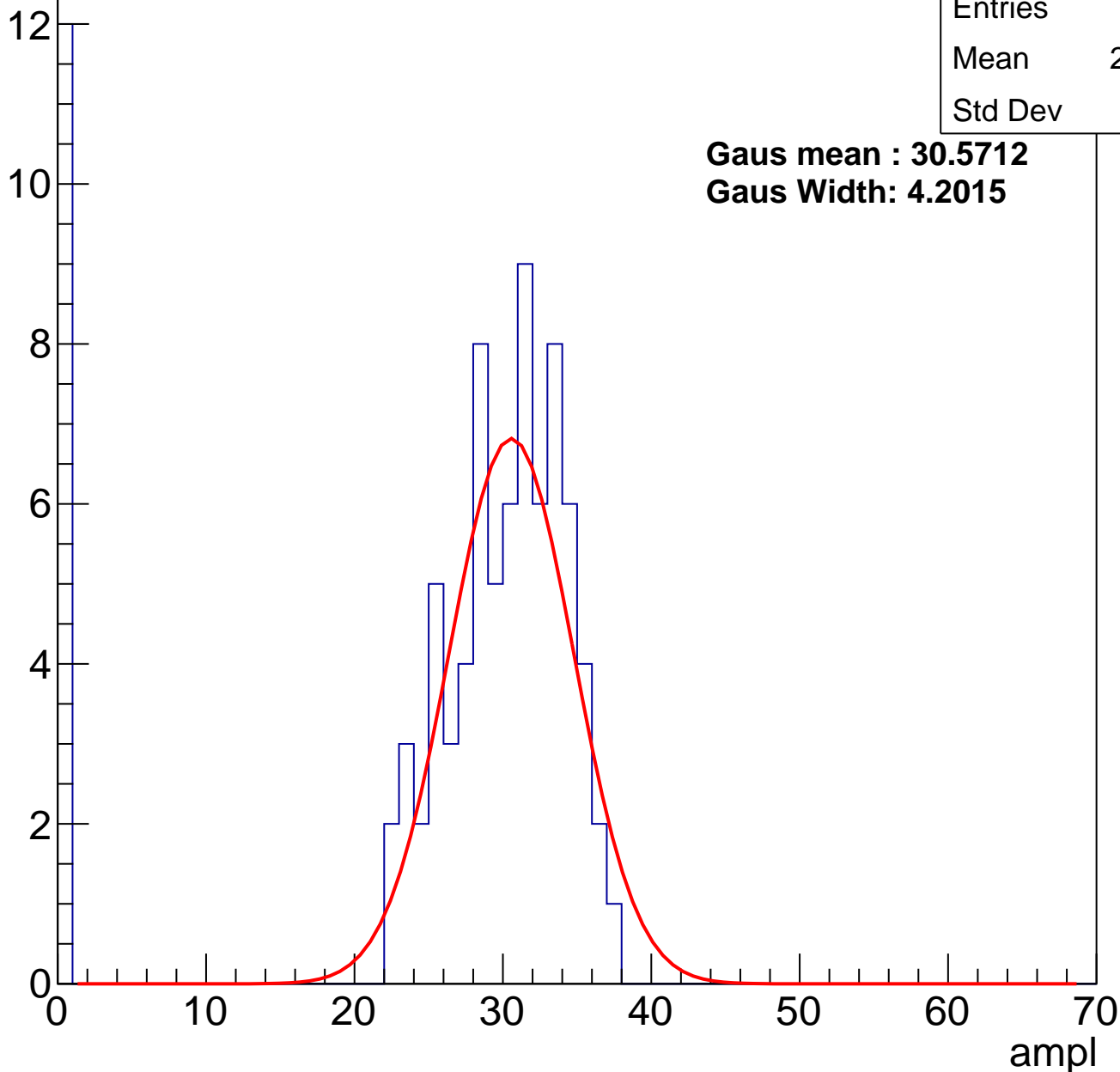
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	25.69
Std Dev	10.9

**Gaus mean : 30.5712**

**Gaus Width: 4.2015**

Entry



# B1L103S, U22-ch28, adc1

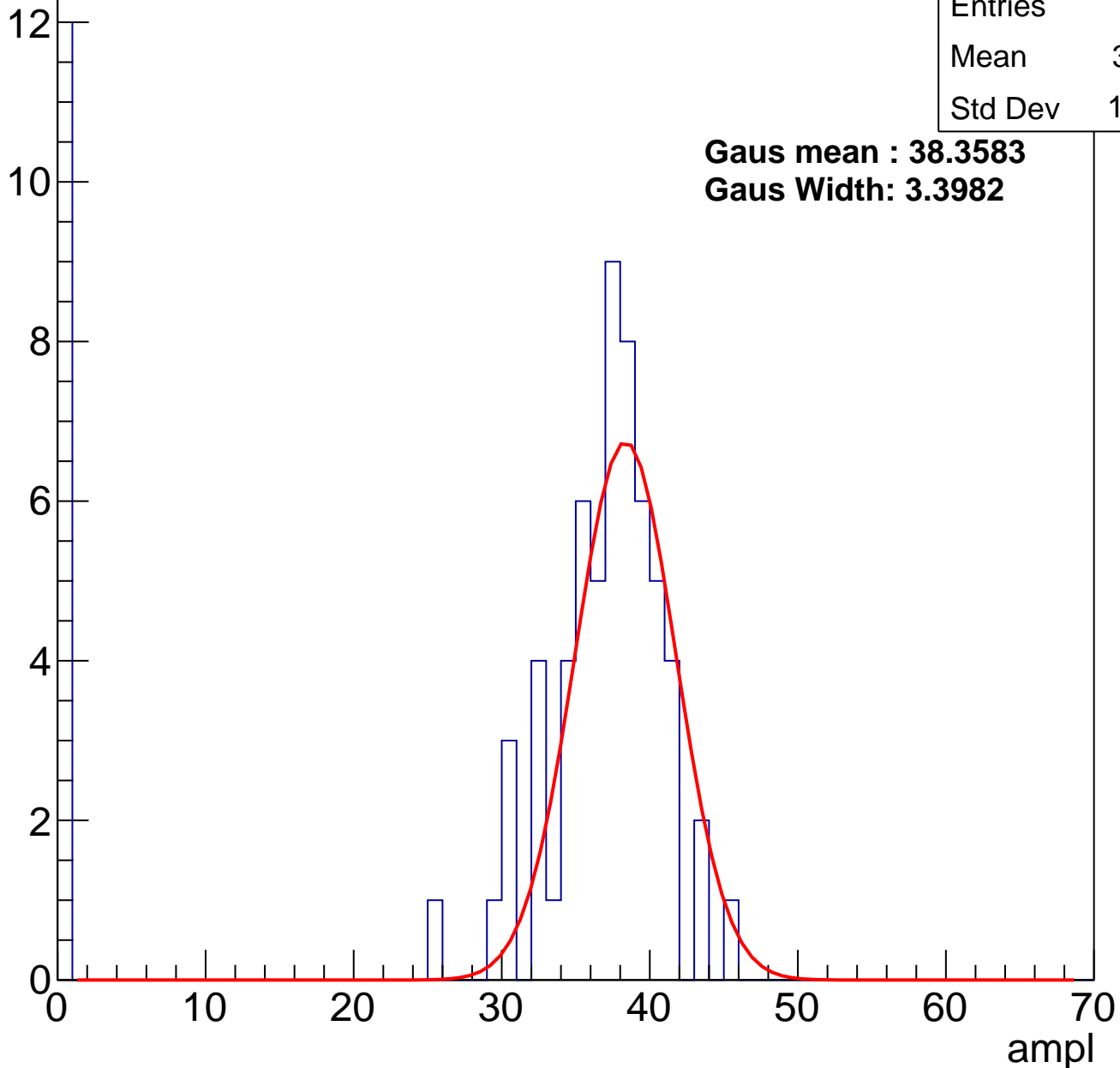
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	72
Mean	30.51
Std Dev	14.05

**Gaus mean : 38.3583**

**Gaus Width: 3.3982**

Entry



# B1L103S, U22-ch28, adc2

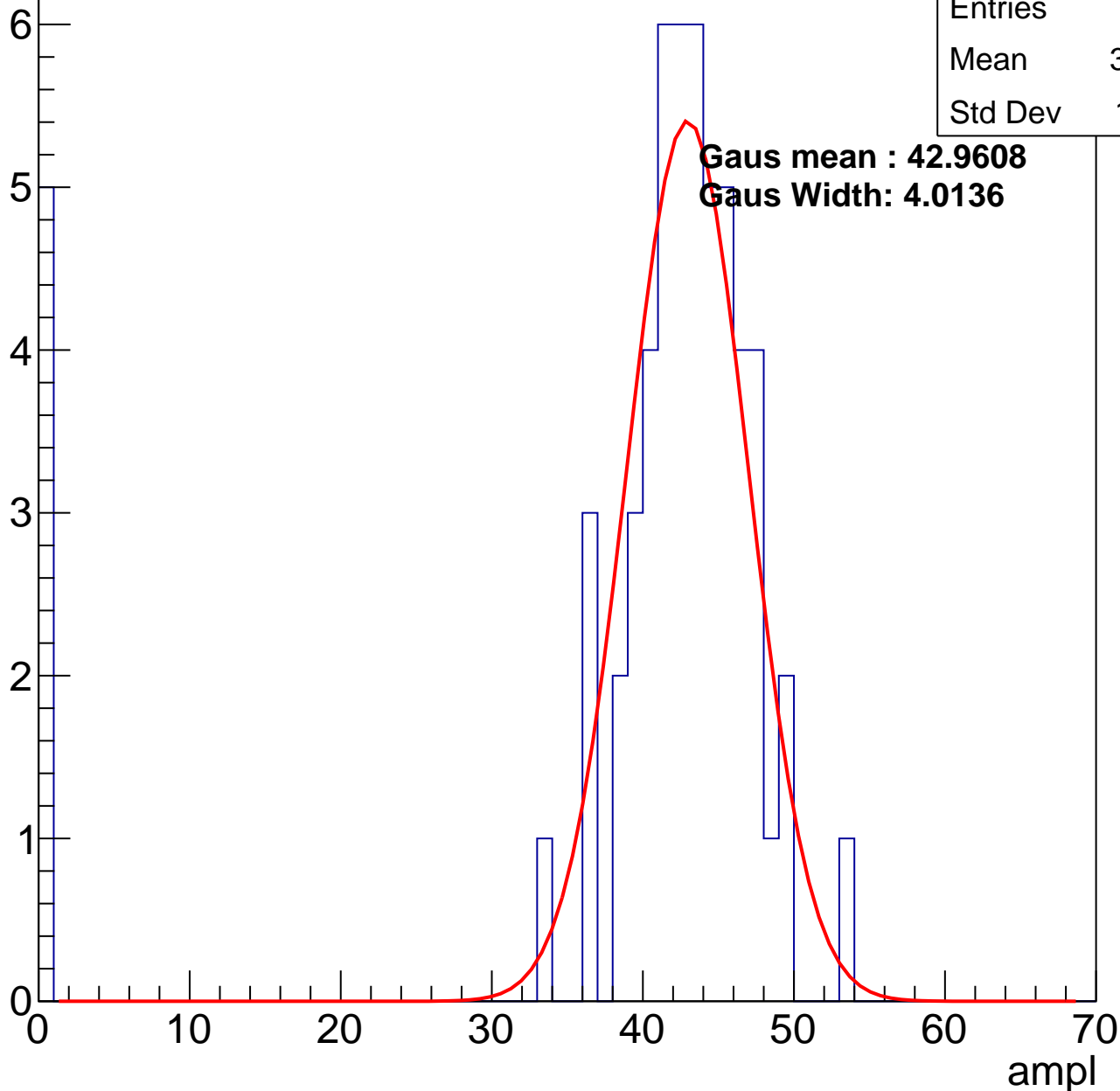
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	39.07
Std Dev	12.51

**Gaus mean : 42.9608**

**Gaus Width: 4.0136**

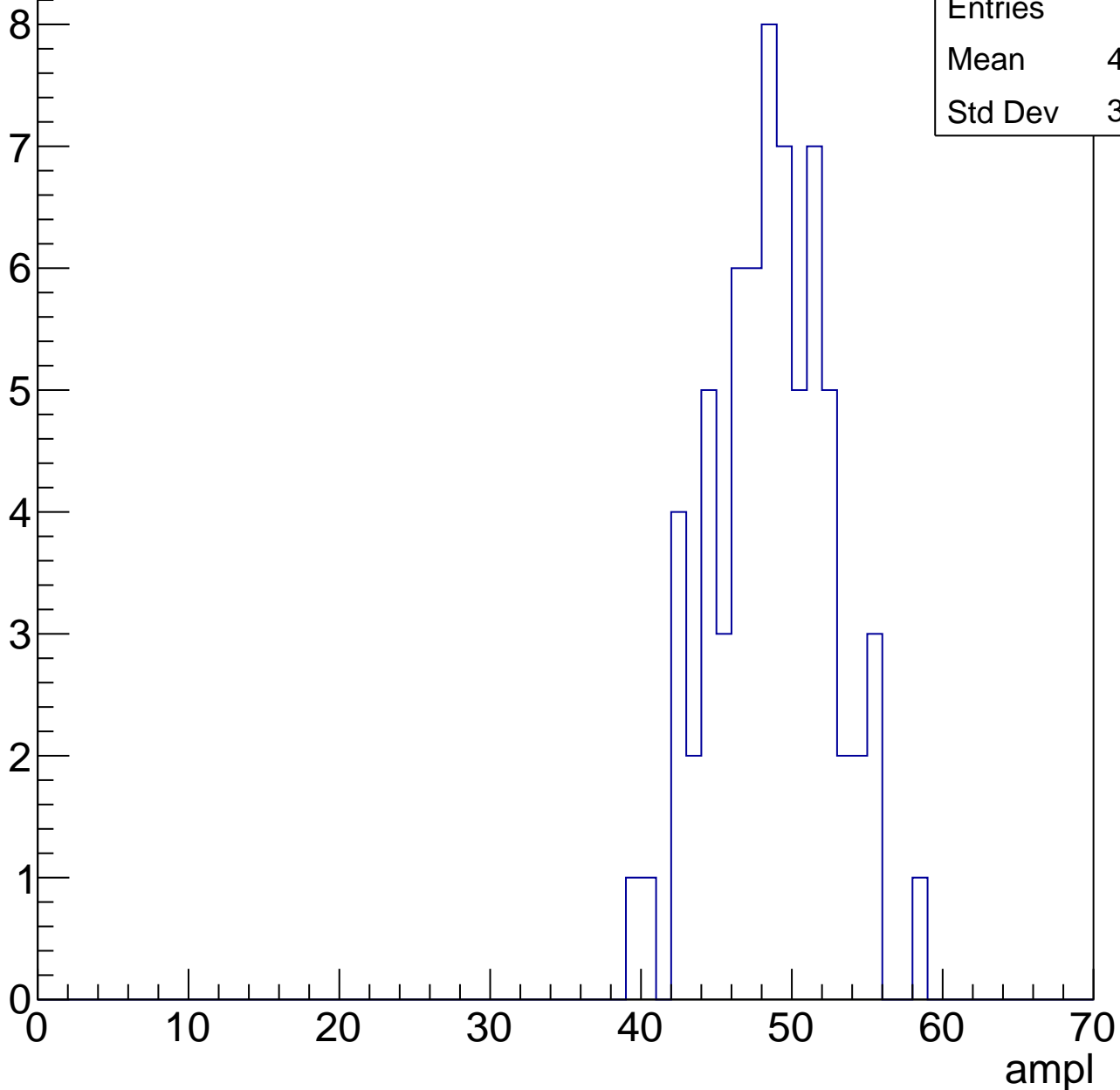


# B1L103S, U22-ch28, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	48.19
Std Dev	3.859

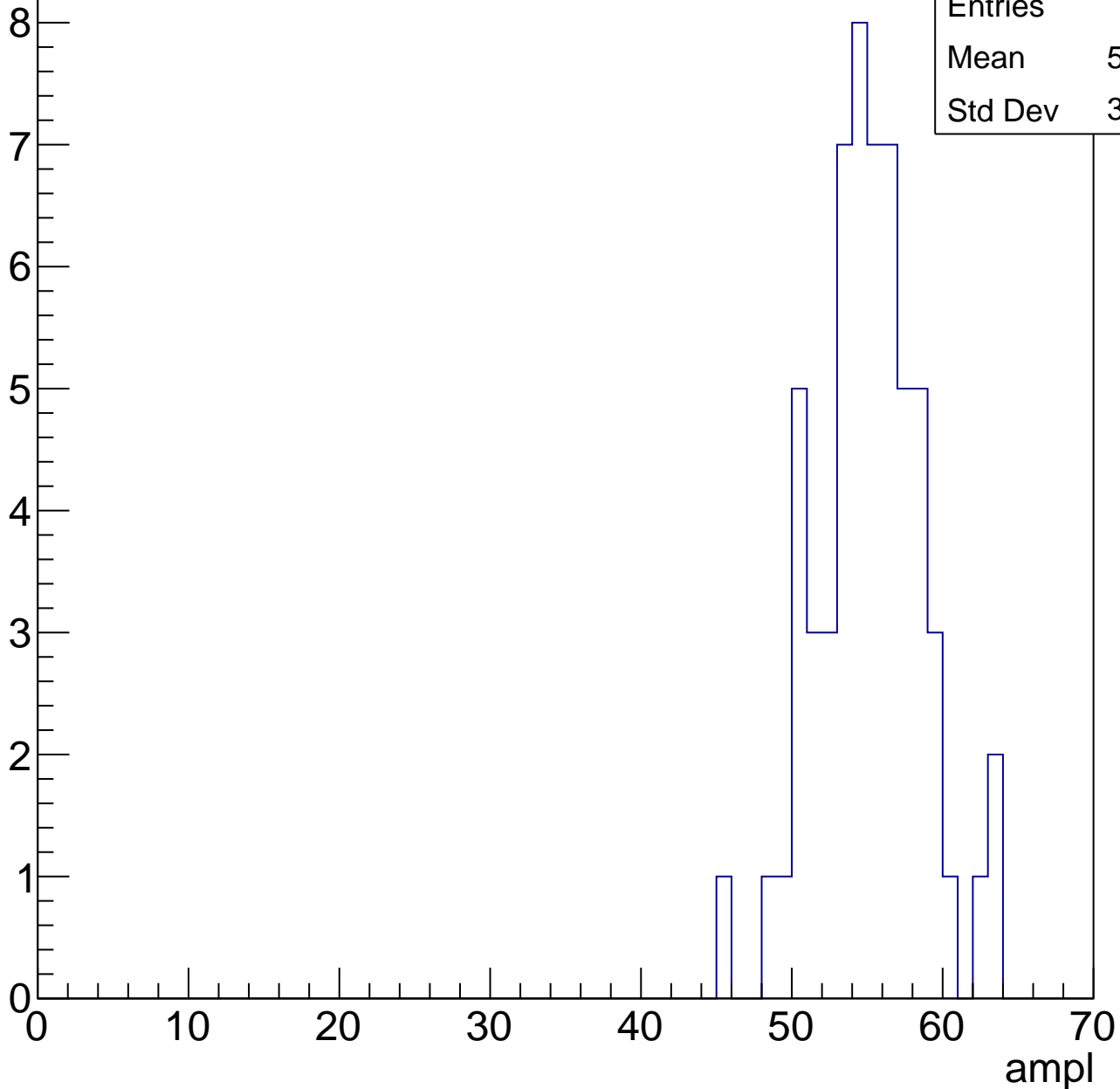


# B1L103S, U22-ch28, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.68
Std Dev	3.505

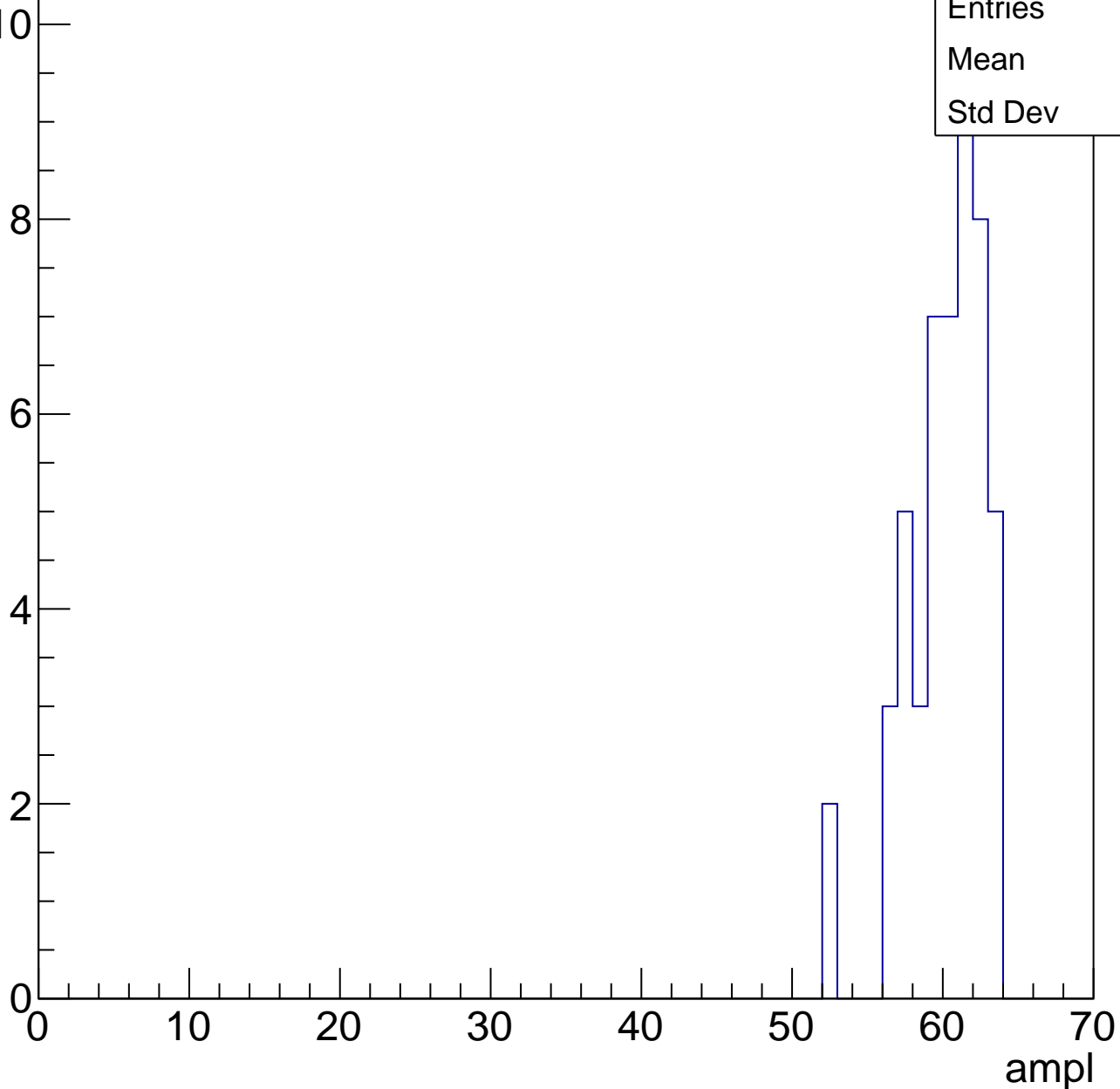


# B1L103S, U22-ch28, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

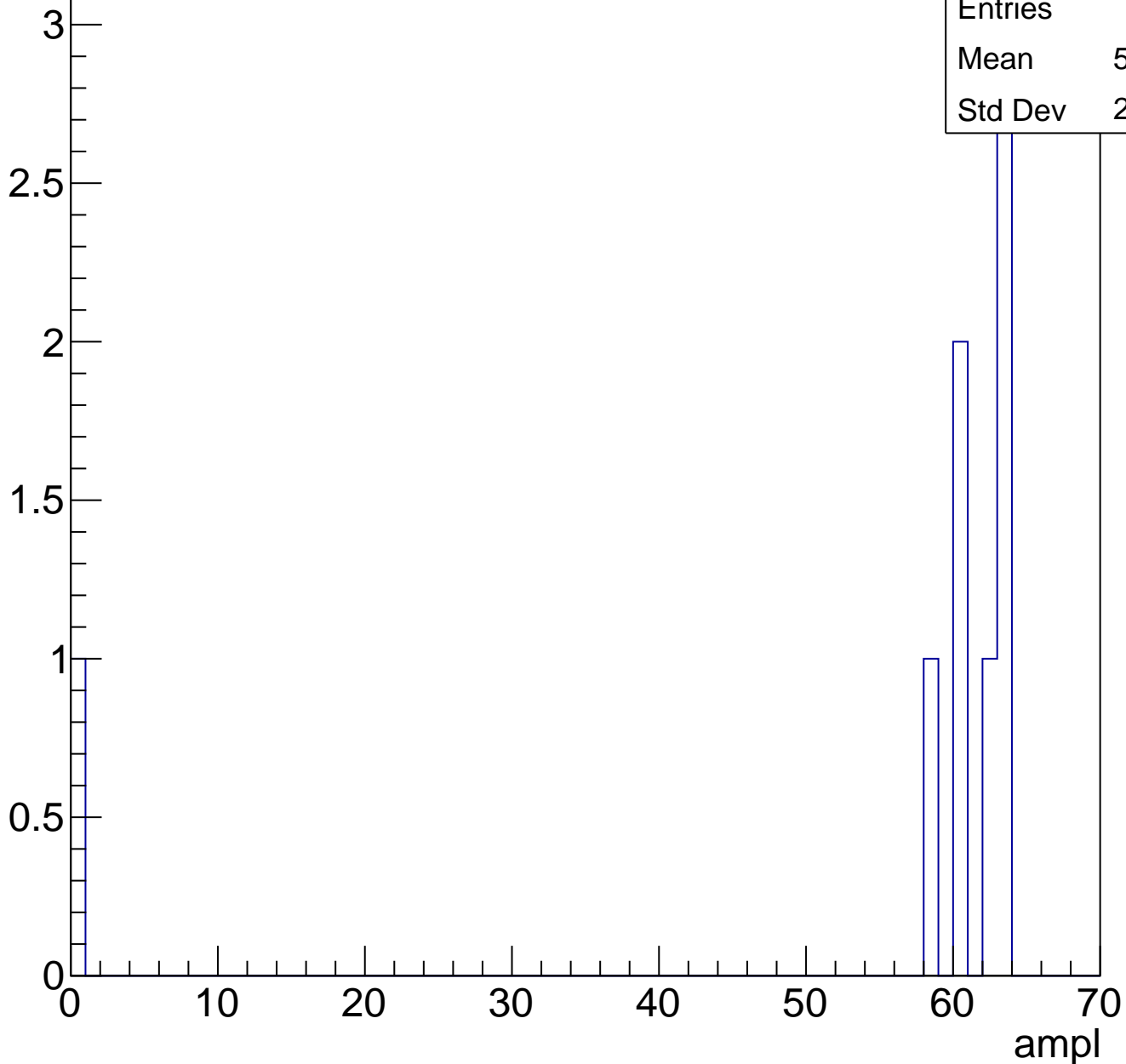
Entries	50
Mean	59.7
Std Dev	2.54



# B1L103S, U22-ch28, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch28, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch29, adc0

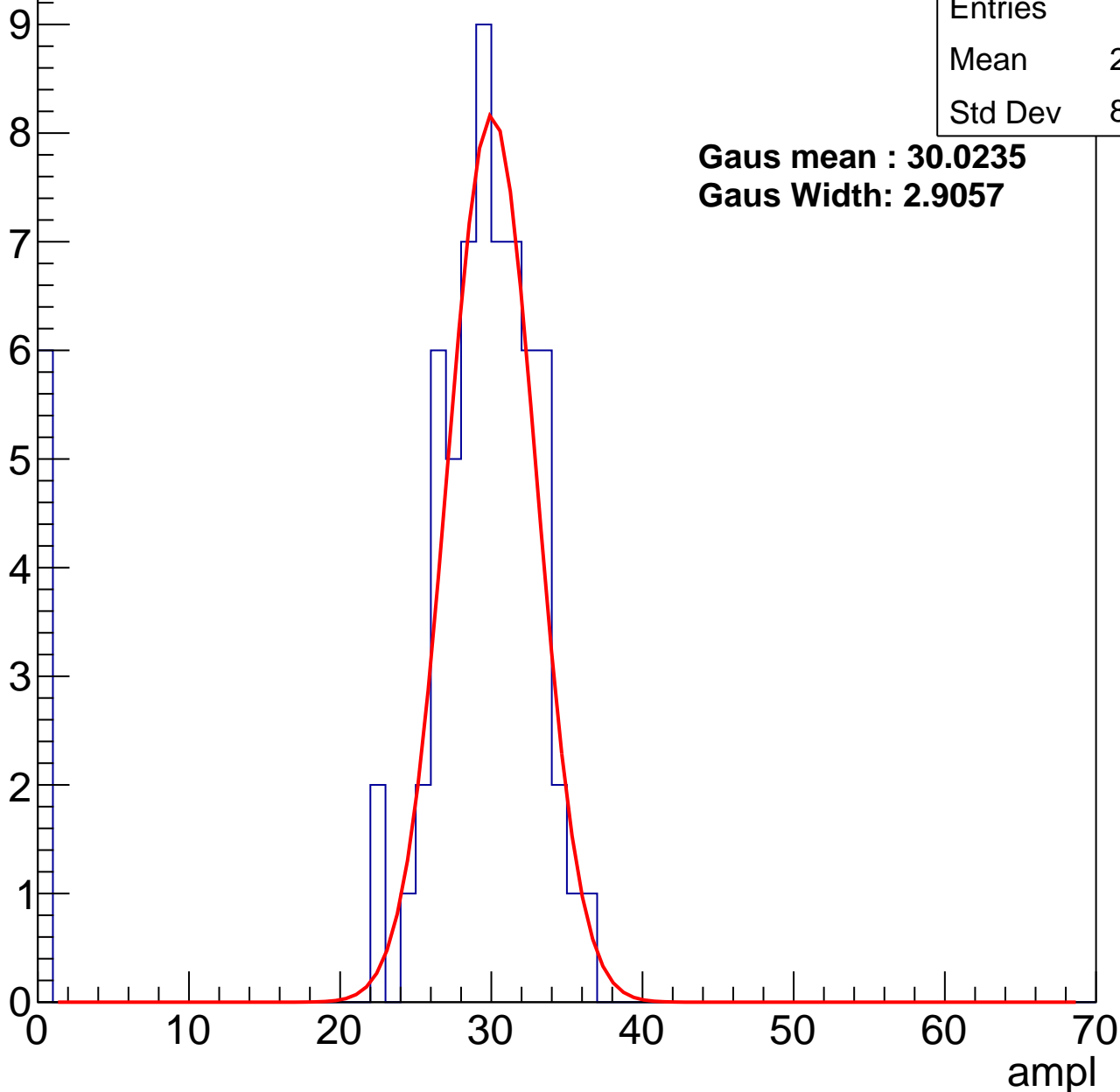
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	26.79
Std Dev	8.804

**Gaus mean : 30.0235**

**Gaus Width: 2.9057**



# B1L103S, U22-ch29, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	88
Mean	33.08
Std Dev	11.13

**Gaus mean : 36.7916**

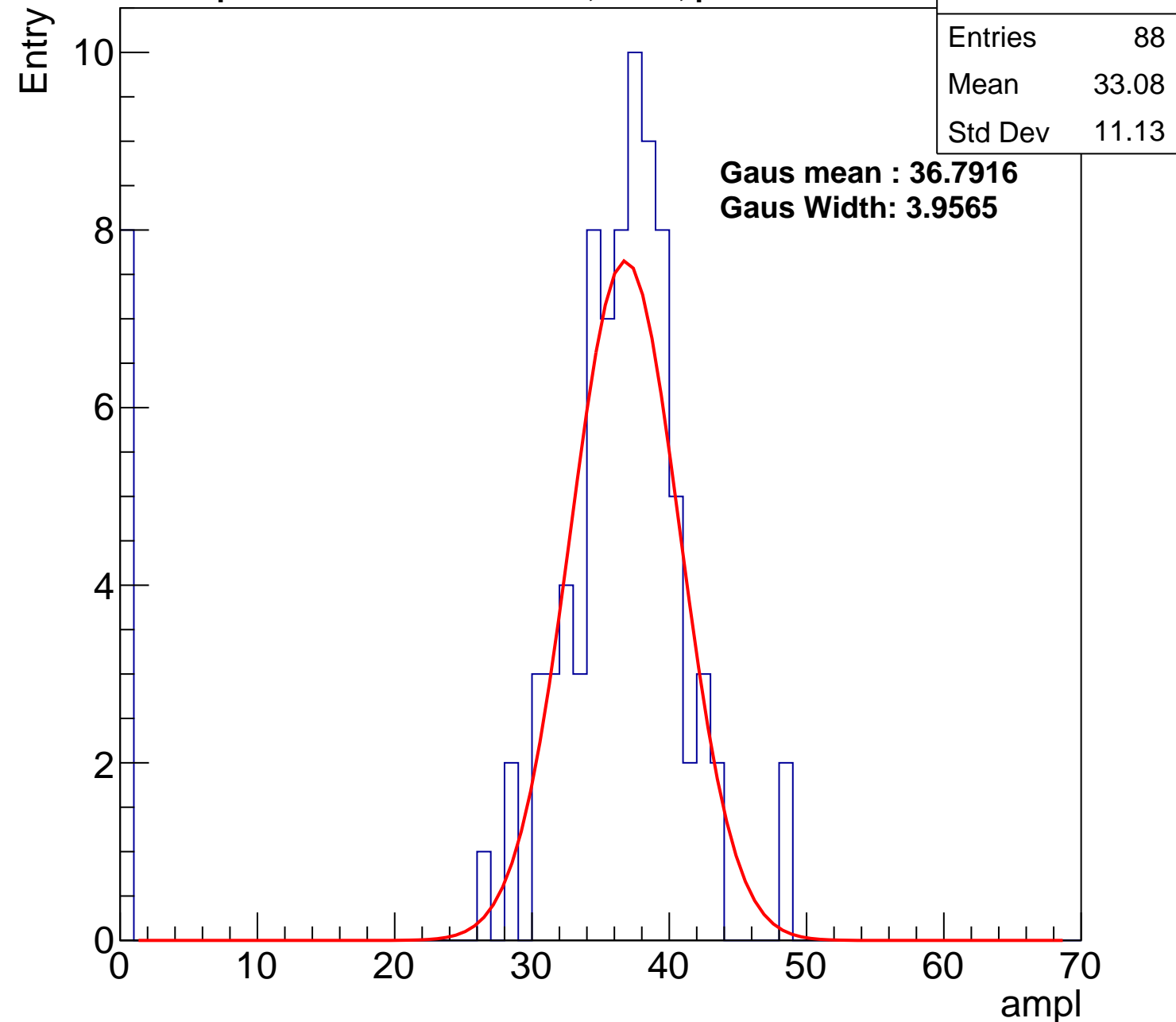
**Gaus Width: 3.9565**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch29, adc2

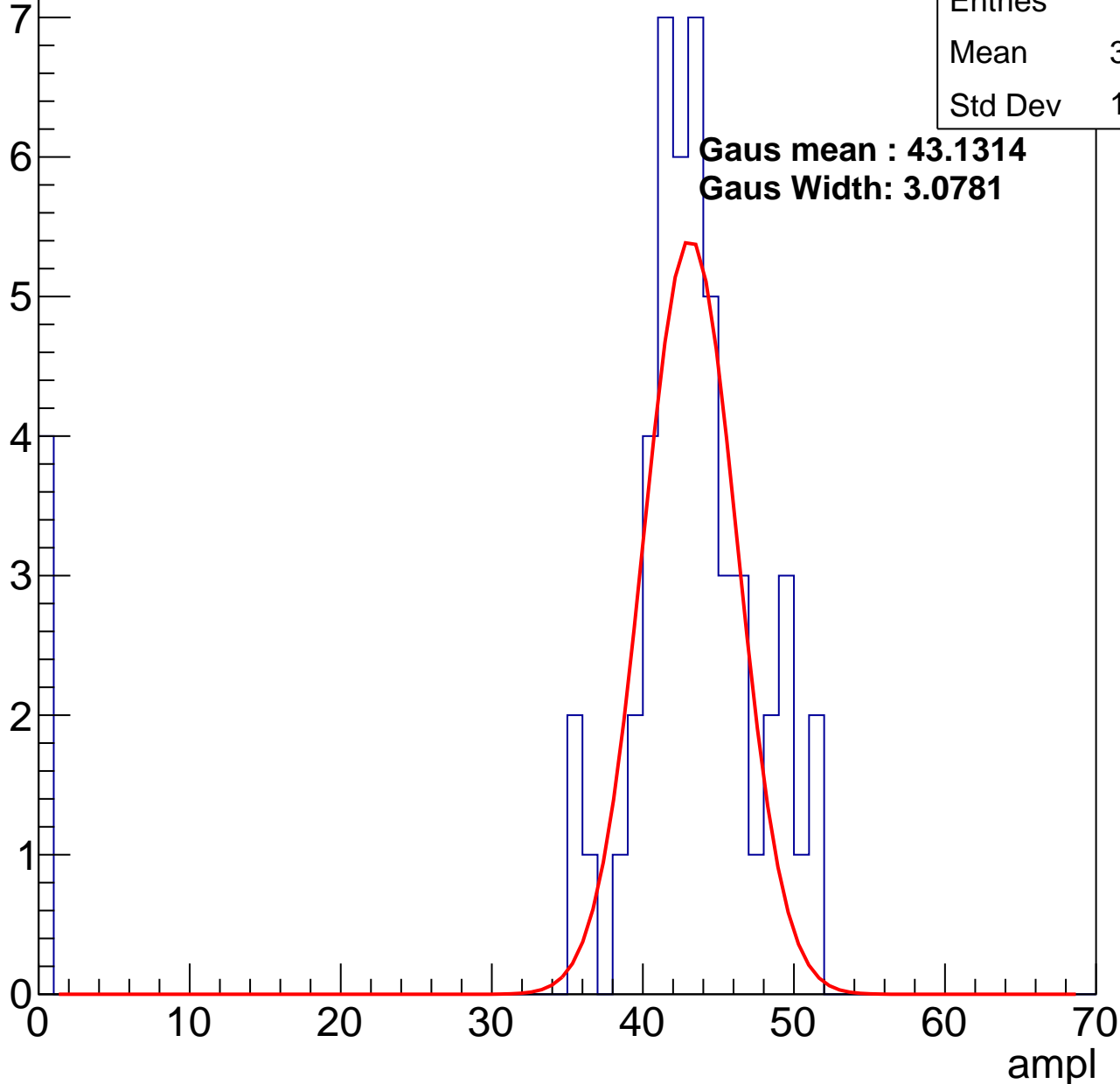
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	39.94
Std Dev	11.86

**Gaus mean : 43.1314**

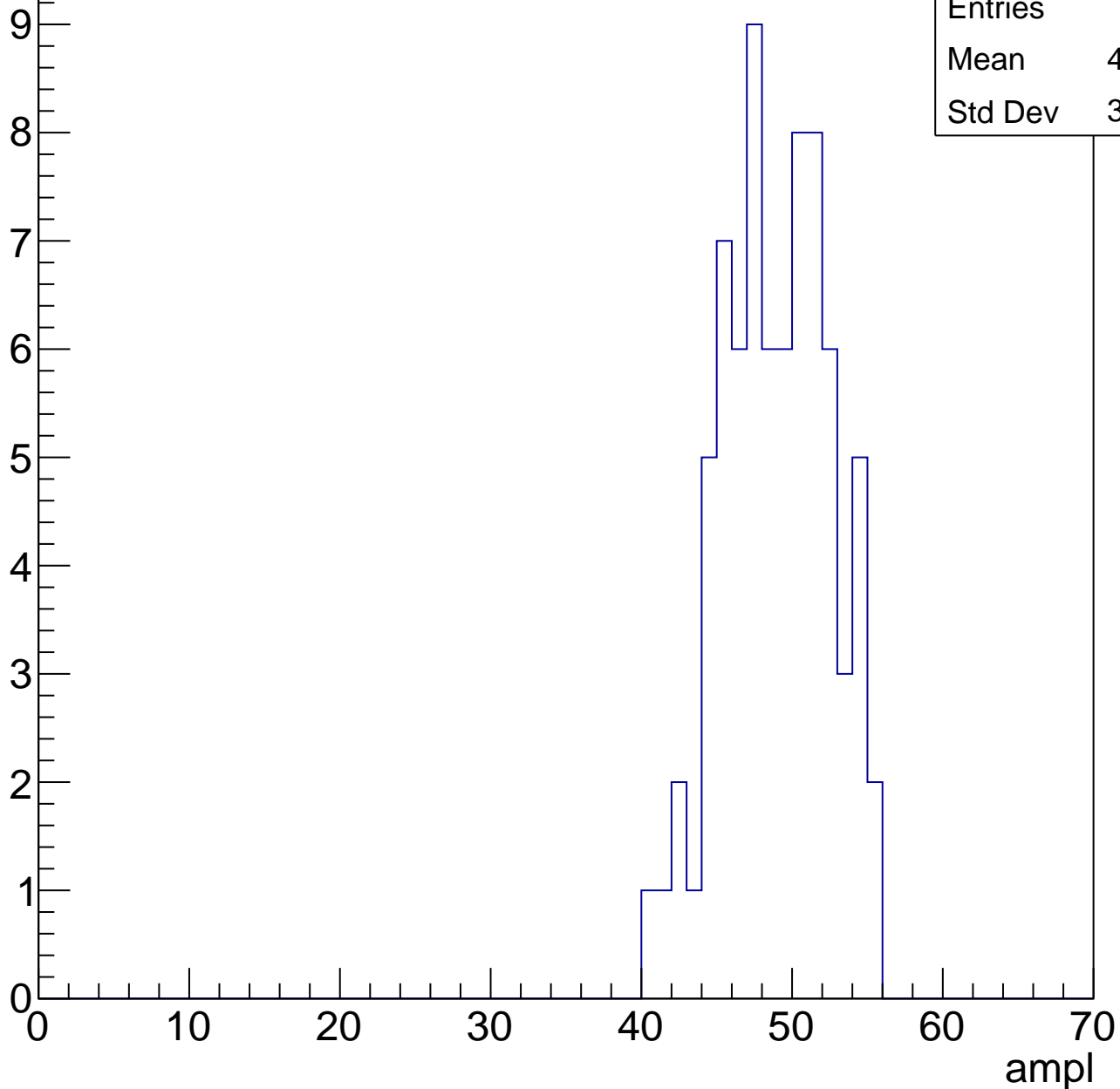
**Gaus Width: 3.0781**



# B1L103S, U22-ch29, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



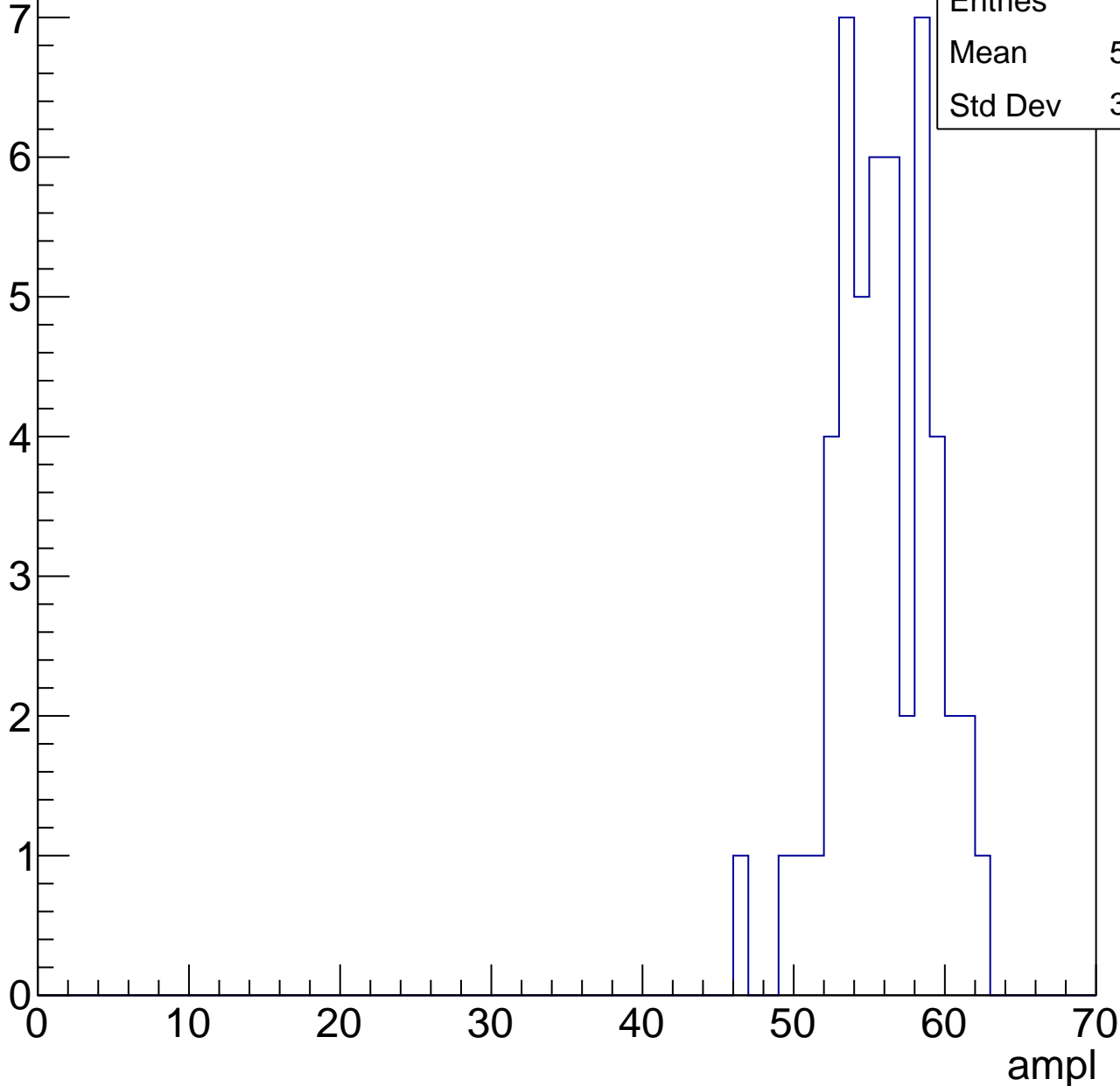
Entries	76
Mean	48.46
Std Dev	3.496

# B1L103S, U22-ch29, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

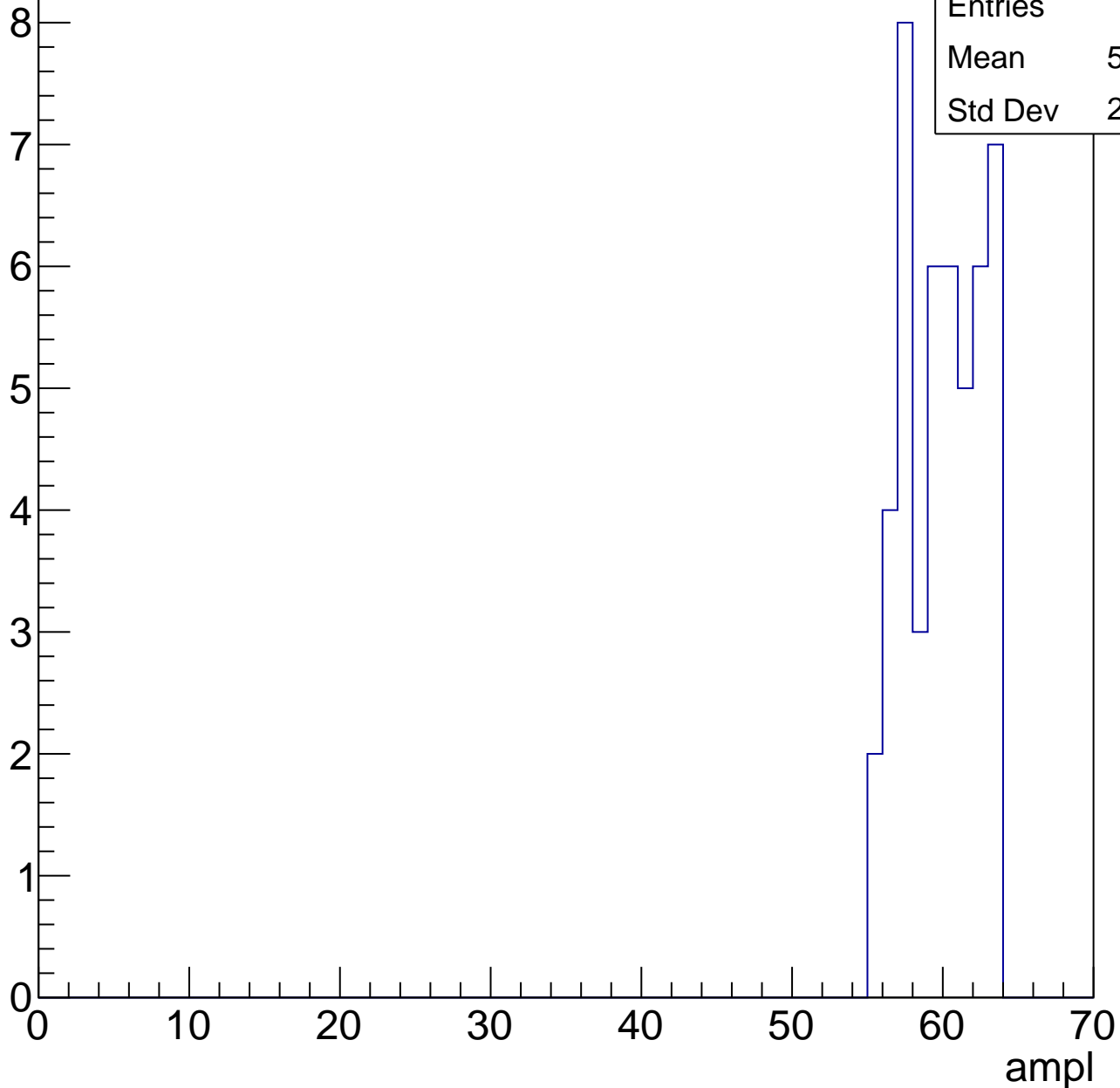
Entries	50
Mean	55.42
Std Dev	3.256



# B1L103S, U22-ch29, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

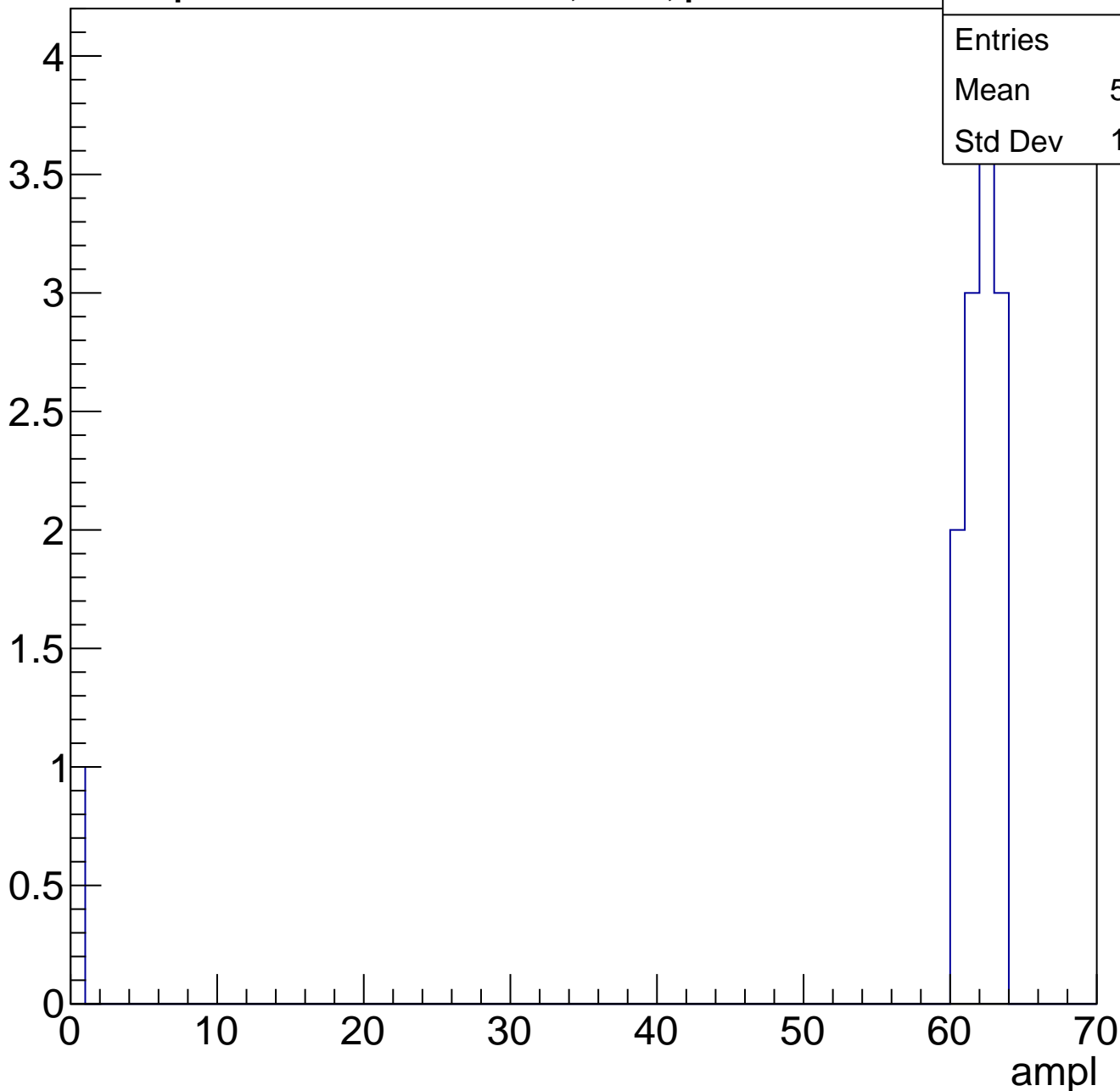


Entries	47
Mean	59.49
Std Dev	2.457

# B1L103S, U22-ch29, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch29, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch30, adc0

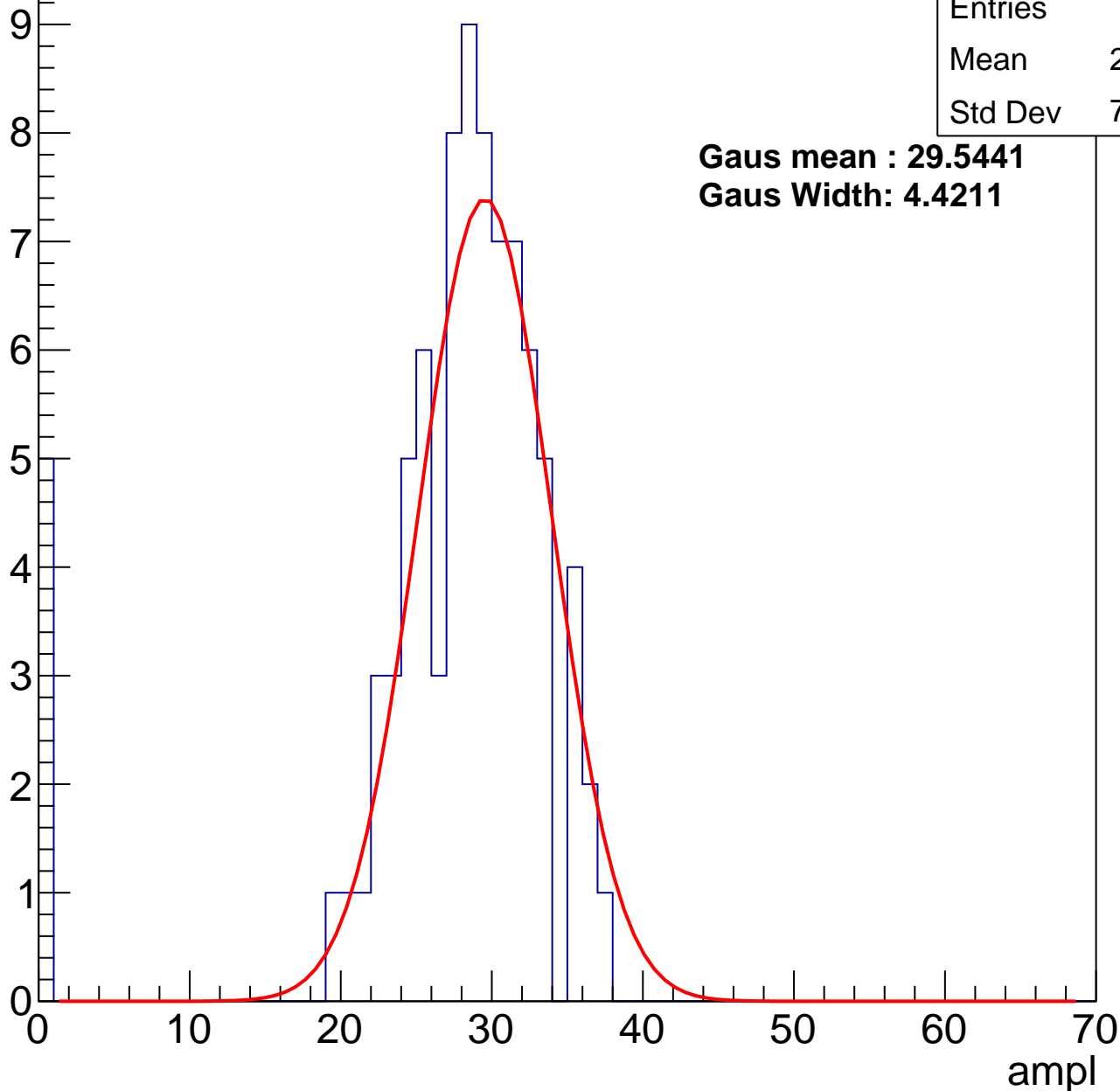
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	85
Mean	26.78
Std Dev	7.707

**Gaus mean : 29.5441**

**Gaus Width: 4.4211**



# B1L103S, U22-ch30, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	62
Mean	34.66
Std Dev	5.657

**Gaus mean : 35.8384**

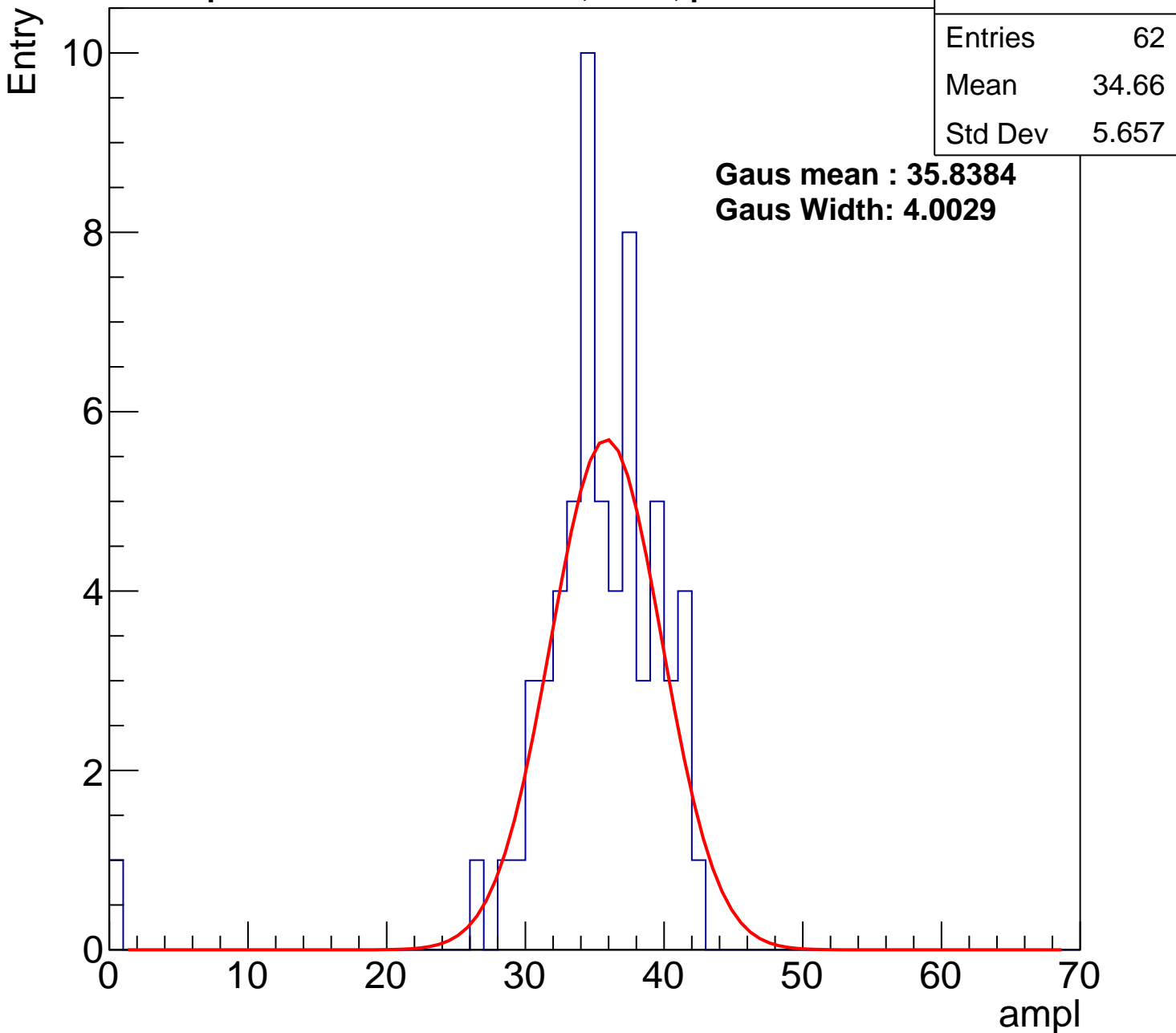
**Gaus Width: 4.0029**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch30, adc2

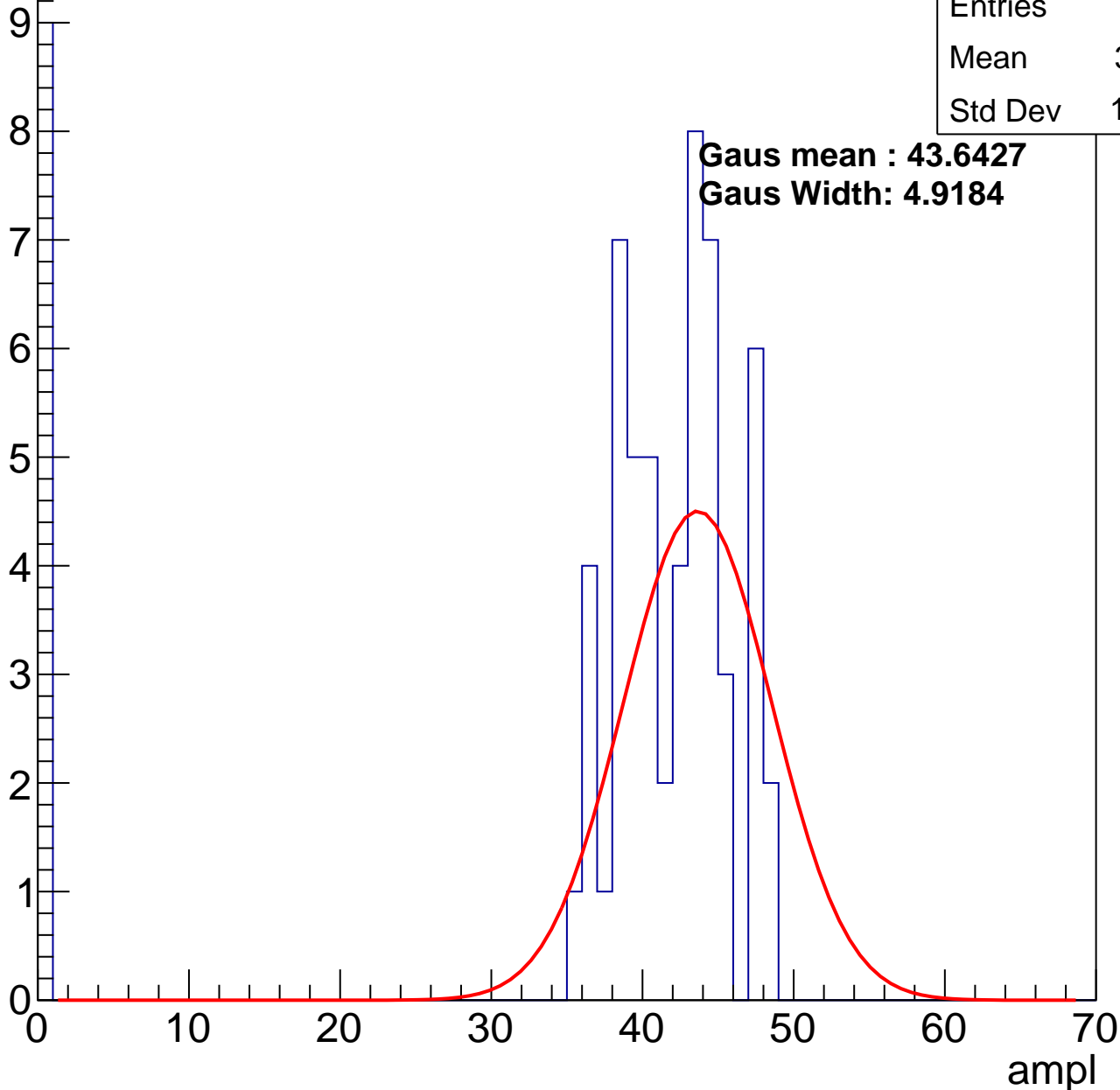
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.81
Std Dev	14.85

**Gaus mean : 43.6427**

**Gaus Width: 4.9184**

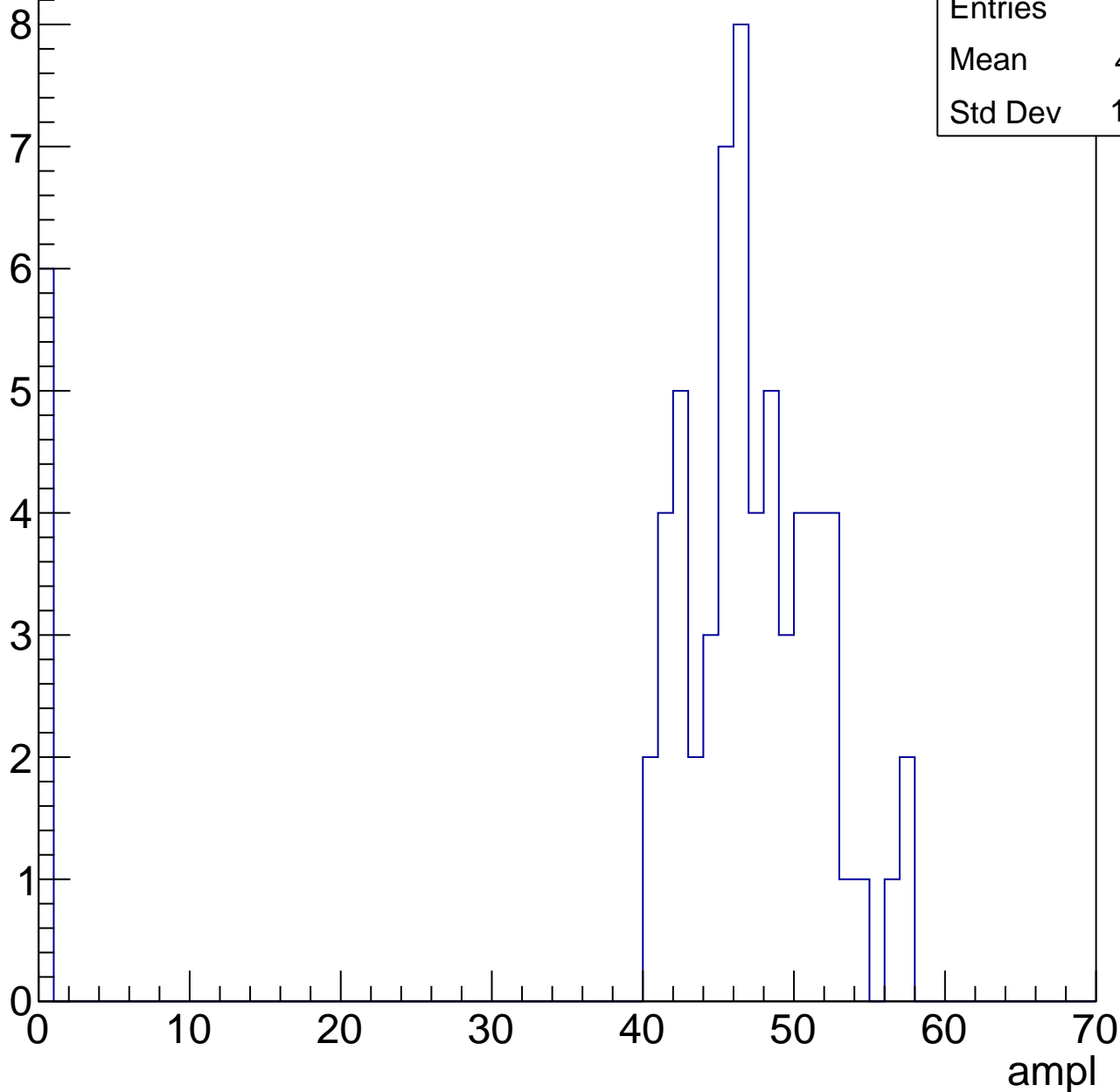


# B1L103S, U22-ch30, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	42.71
Std Dev	14.08

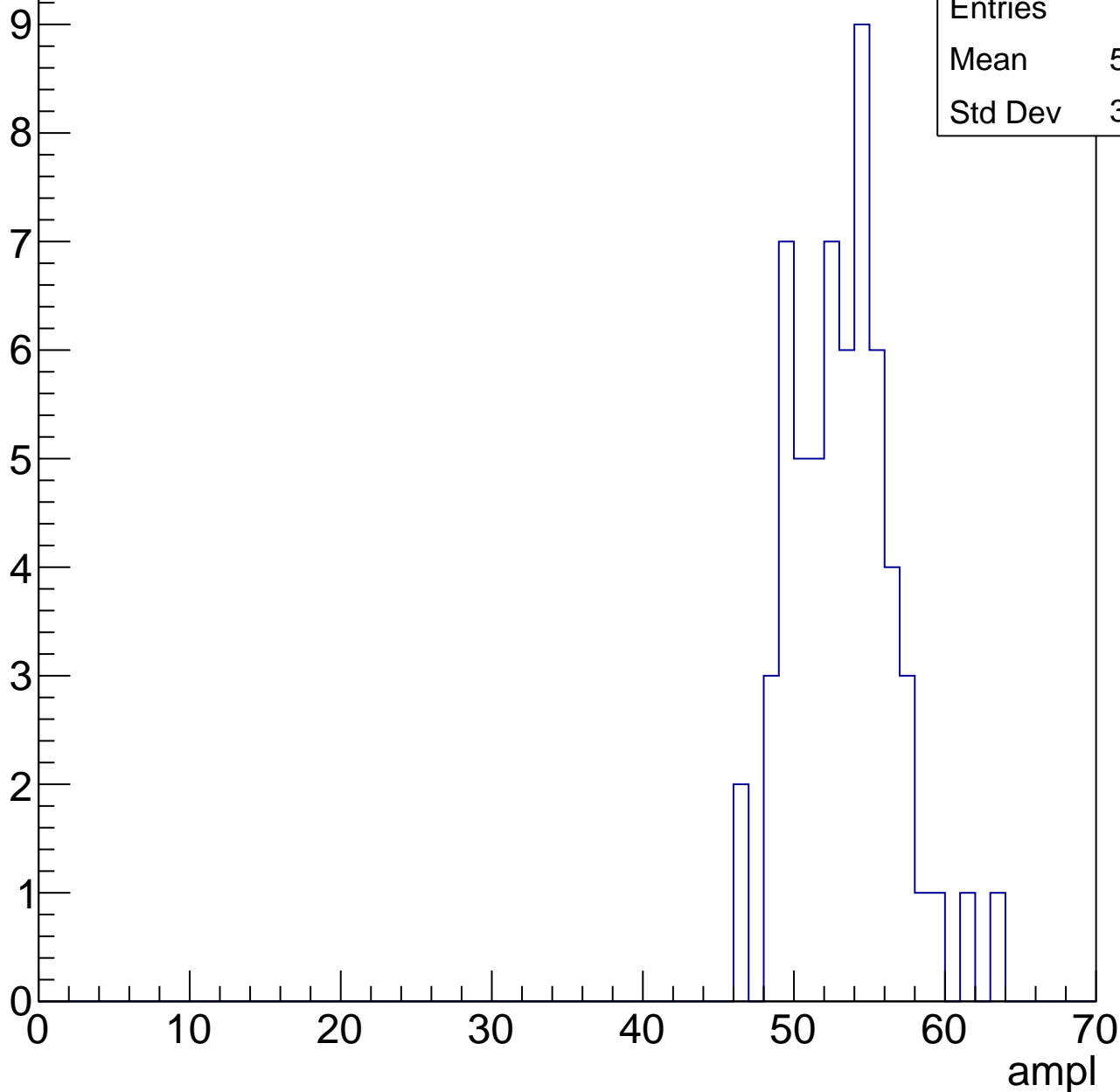


# B1L103S, U22-ch30, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

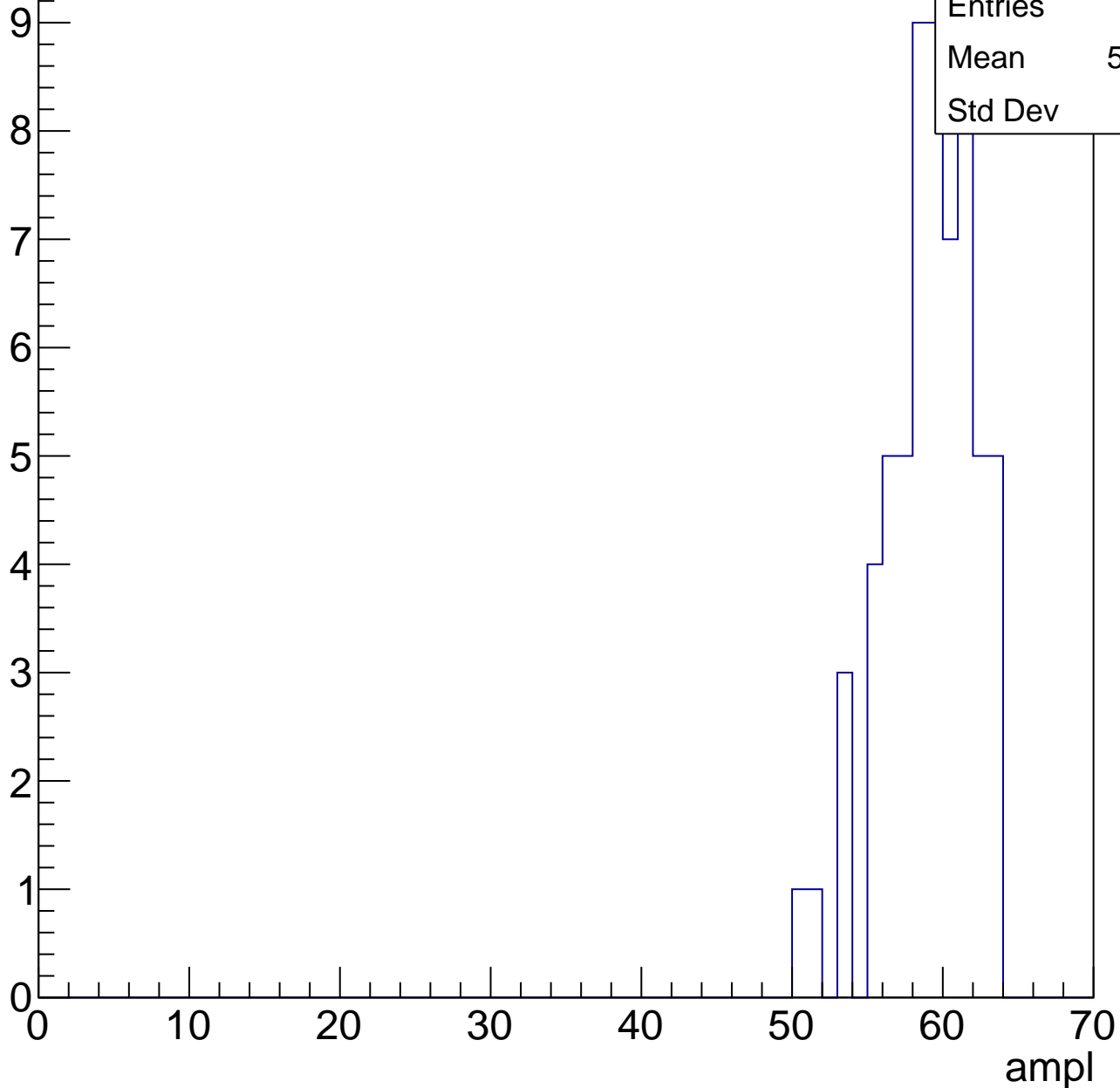
Entries	61
Mean	52.75
Std Dev	3.376



# B1L103S, U22-ch30, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

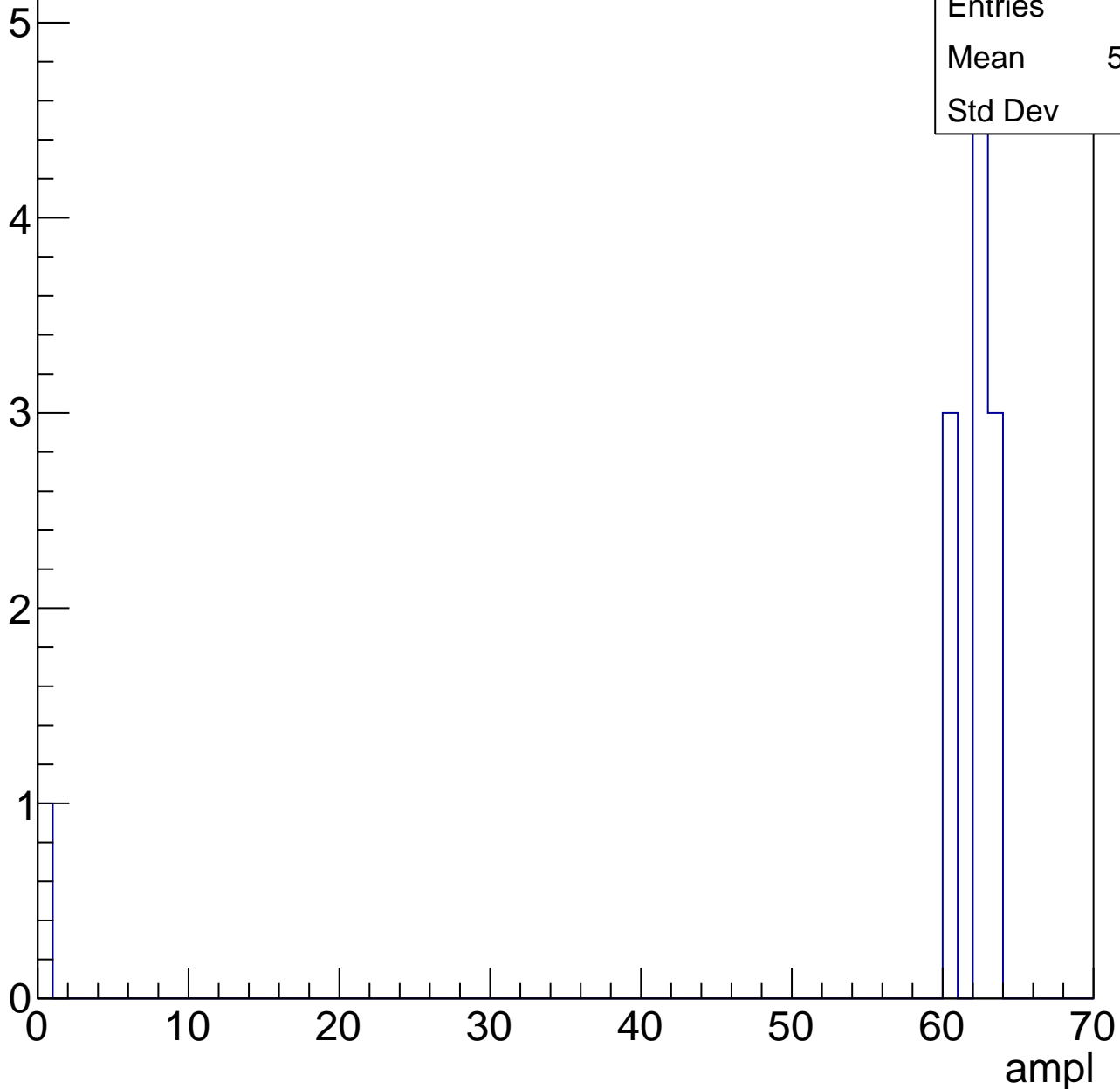


# B1L103S, U22-ch30, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.58
Std Dev	17.1



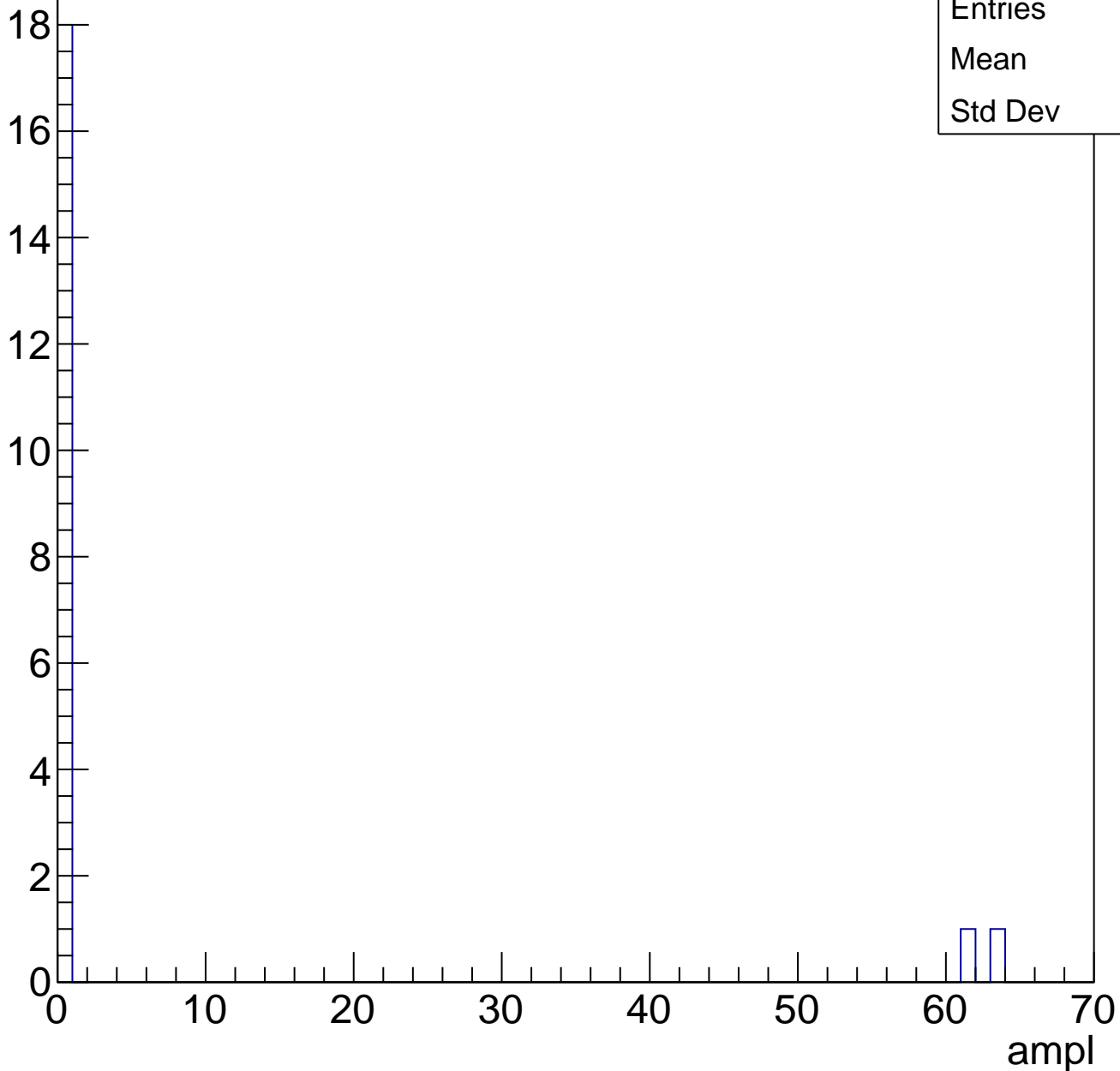


# B1L103S, U22-ch30, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	6.2
Std Dev	18.6

Entry



# B1L103S, U22-ch31, adc0

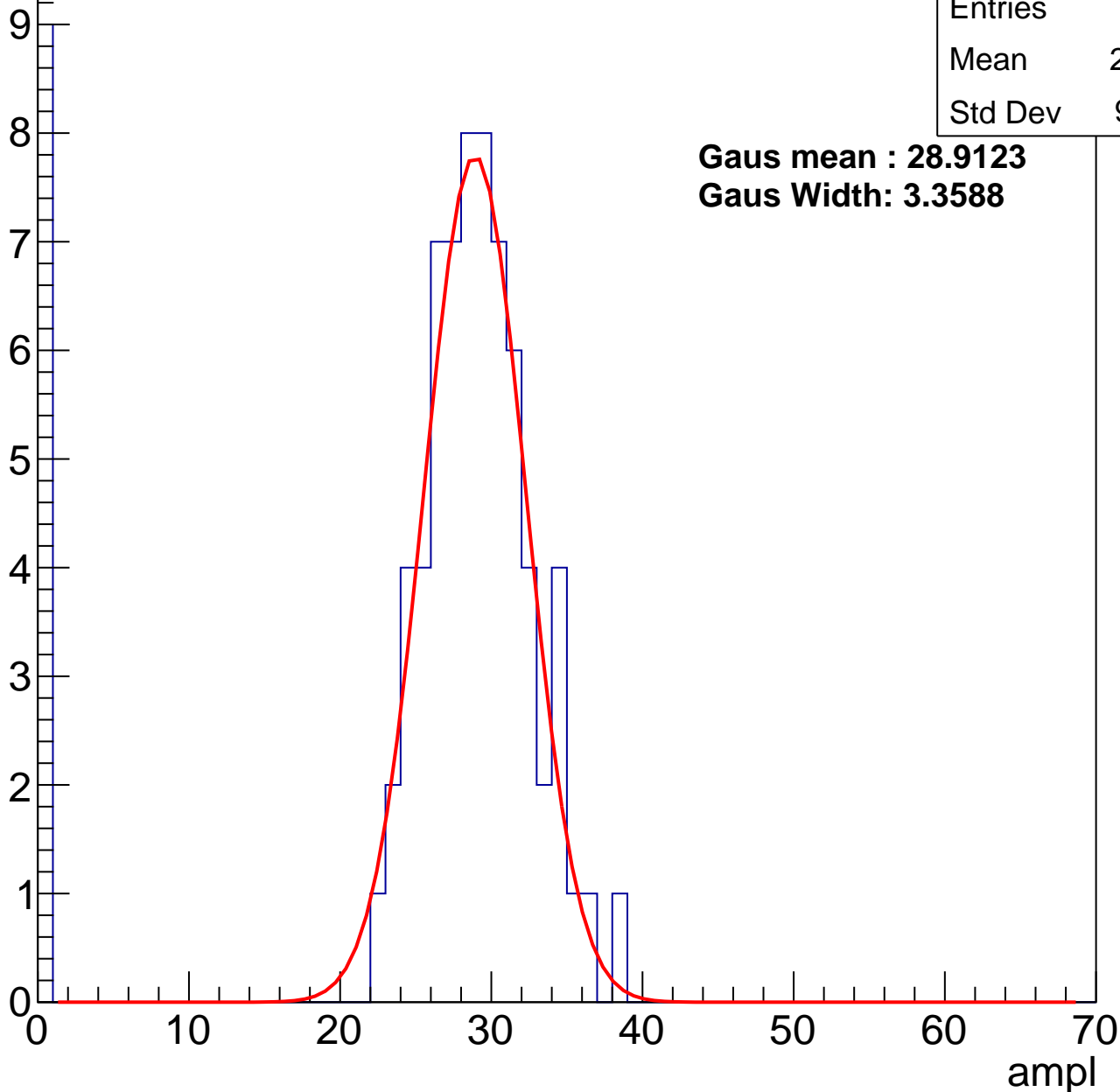
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	25.34
Std Dev	9.801

**Gaus mean : 28.9123**

**Gaus Width: 3.3588**



# B1L103S, U22-ch31, adc1

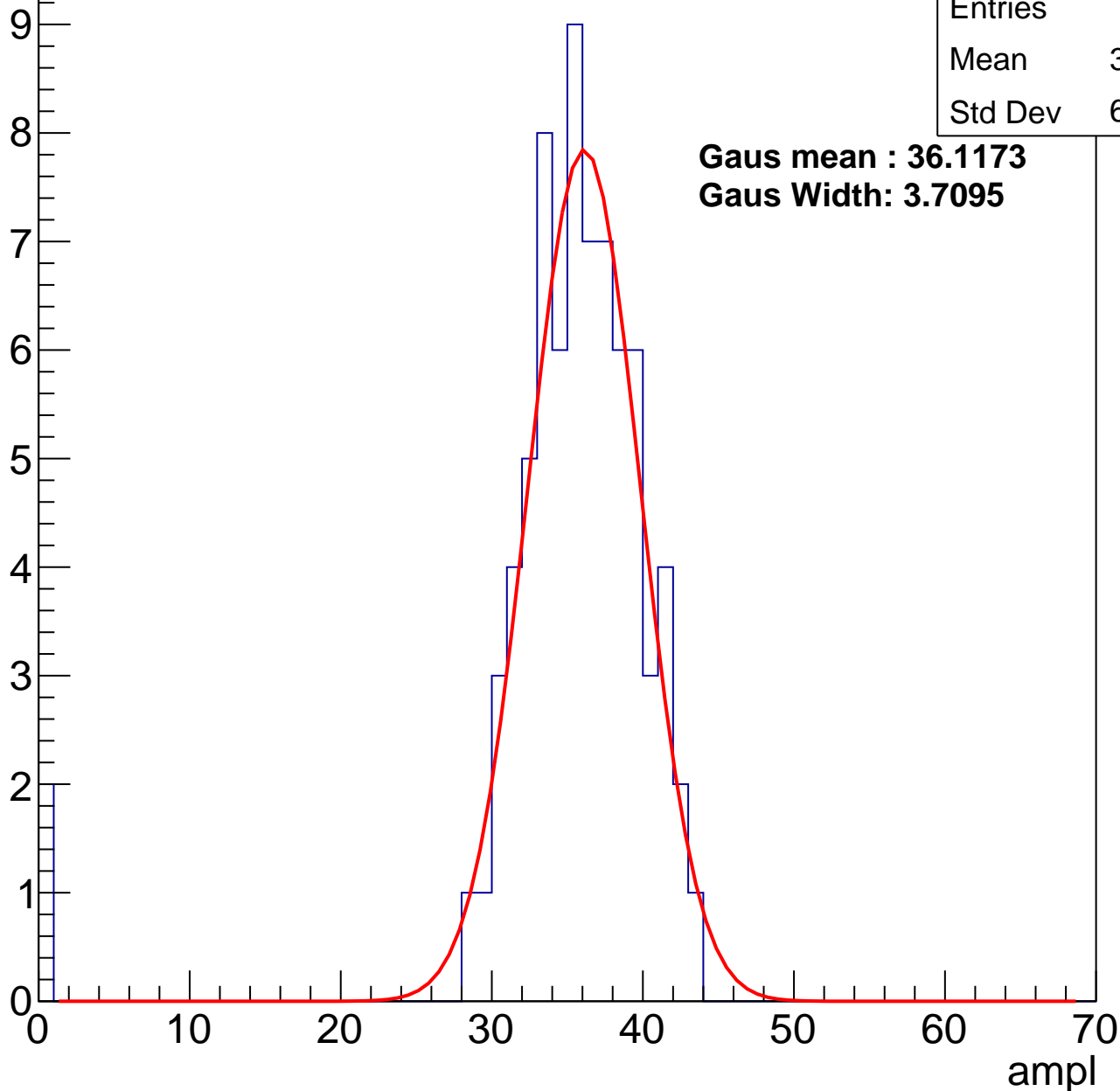
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	34.64
Std Dev	6.647

**Gaus mean : 36.1173**

**Gaus Width: 3.7095**



# B1L103S, U22-ch31, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	83
Mean	37.27
Std Dev	14.9

**Gaus mean : 43.3597**

**Gaus Width: 3.7866**

Entry

10

8

6

4

2

0

0

10

20

30

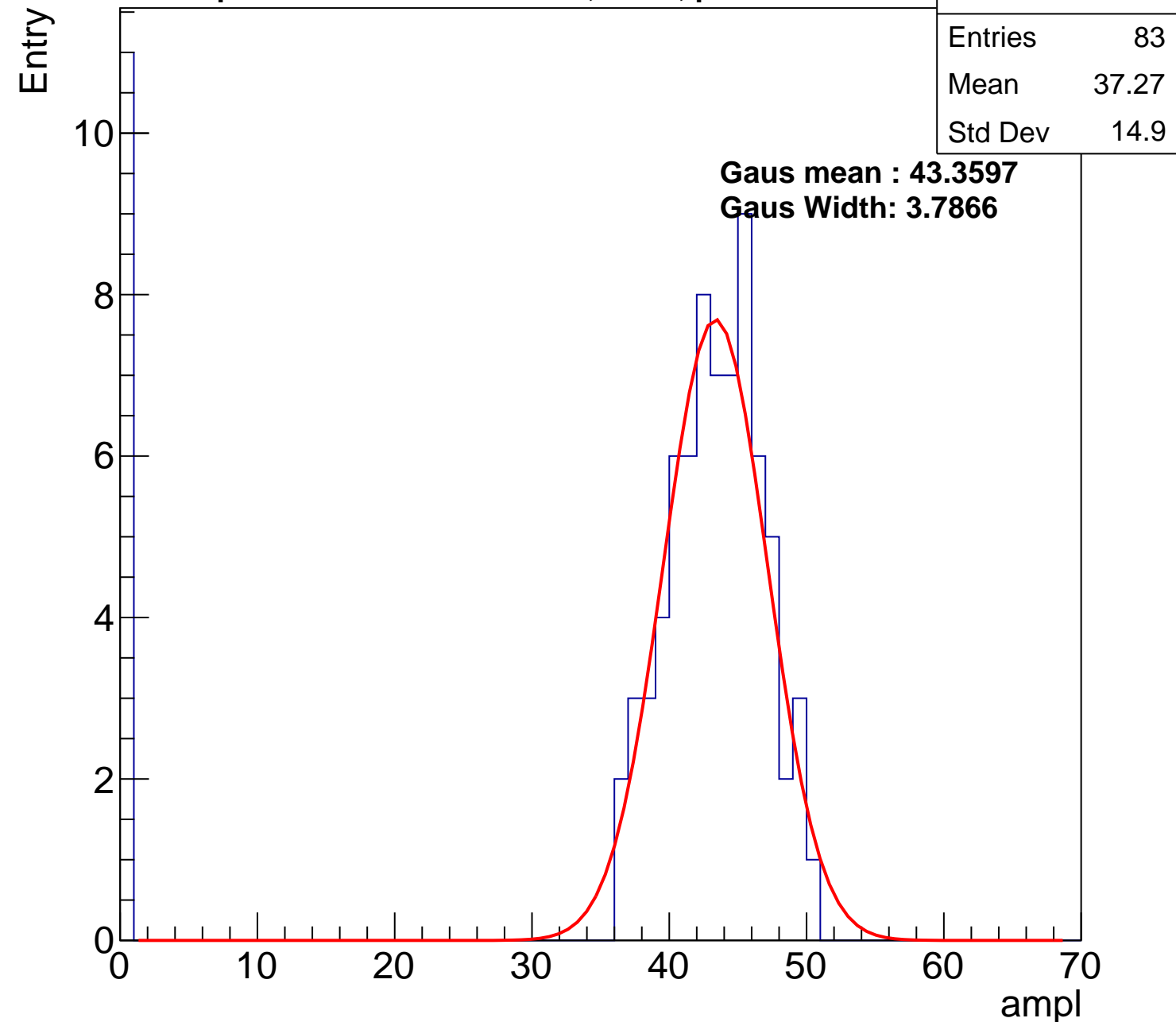
40

50

60

70

ampl

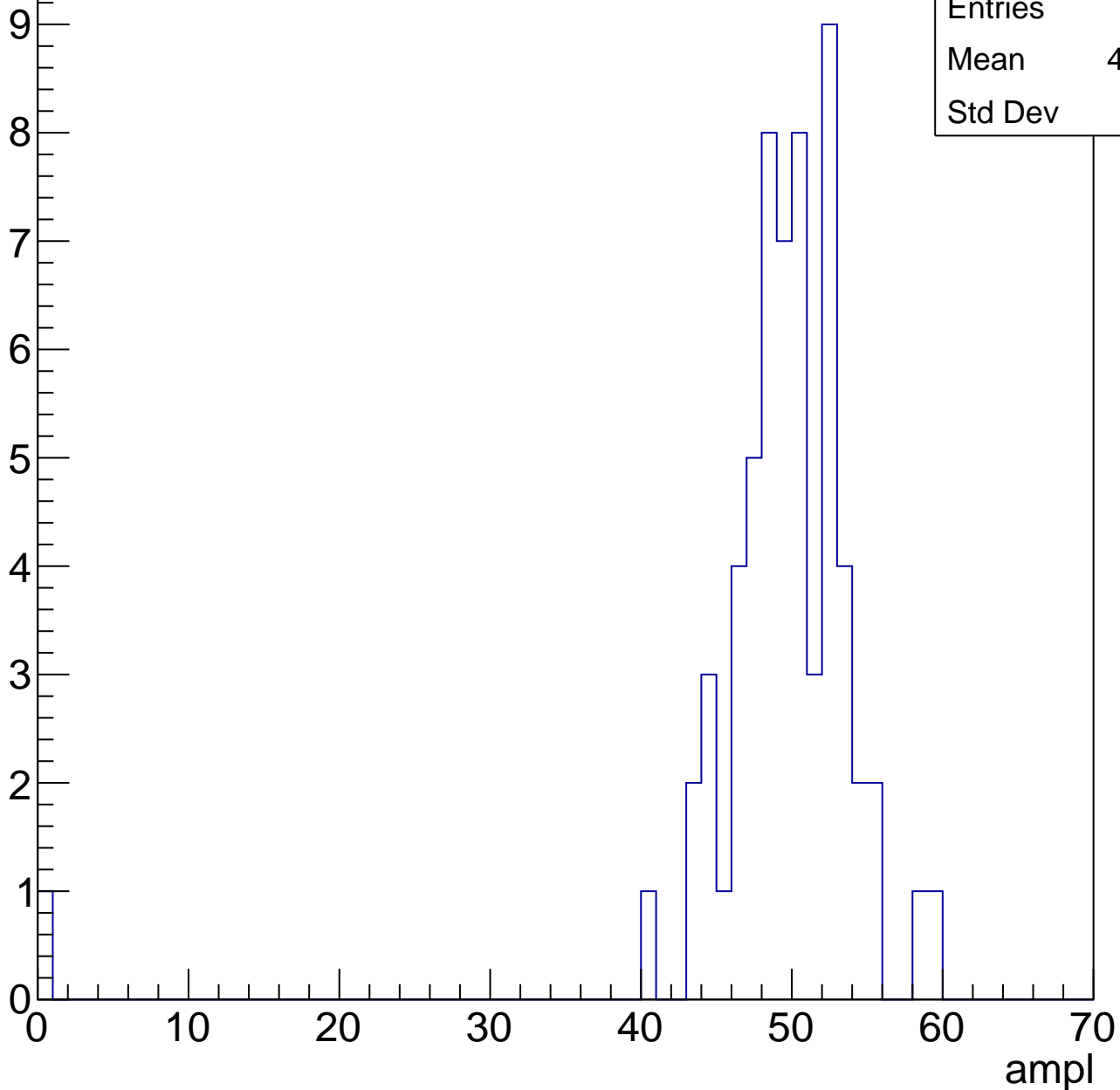


# B1L103S, U22-ch31, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	48.66
Std Dev	7.15

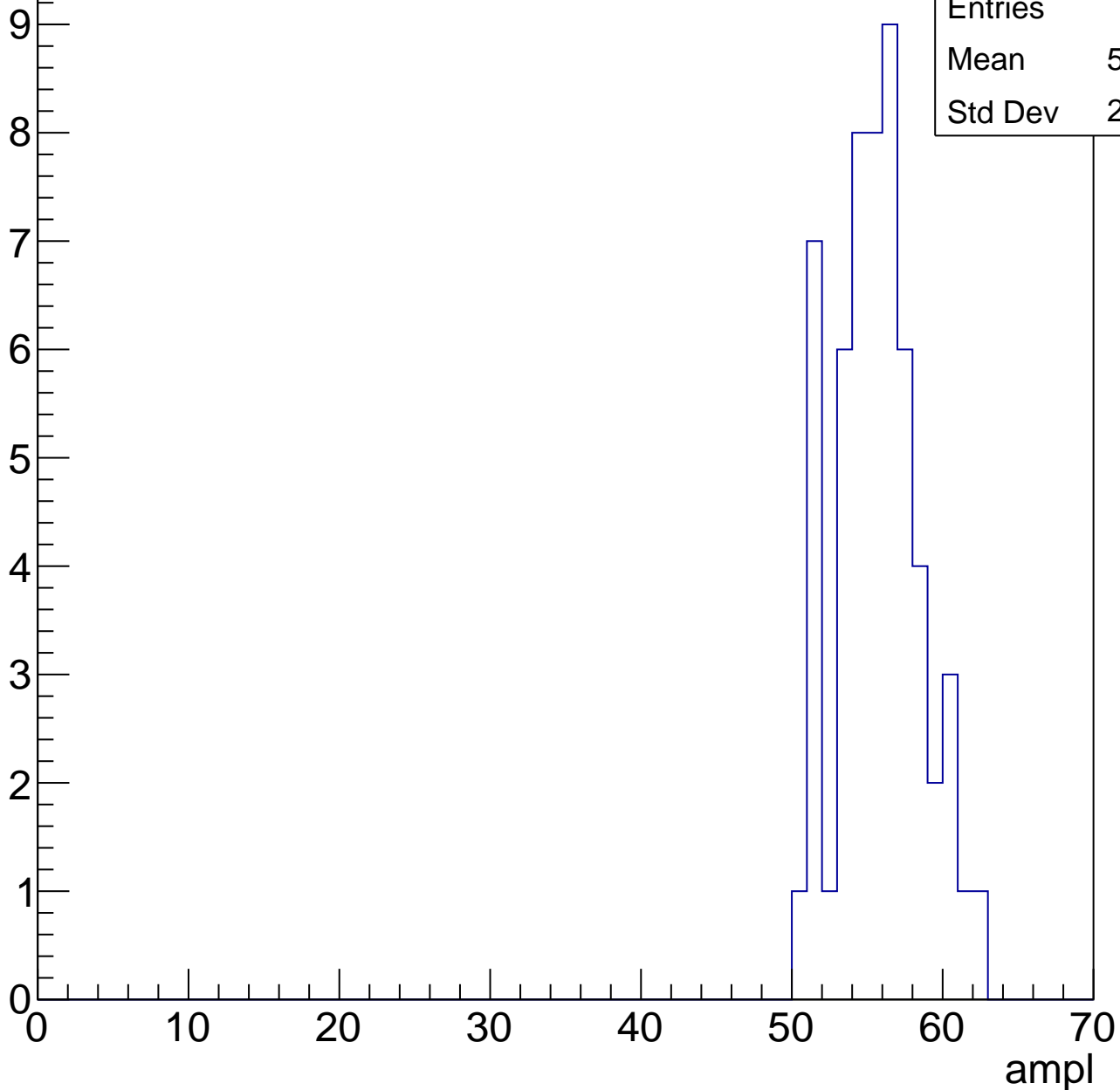


# B1L103S, U22-ch31, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.23
Std Dev	2.766

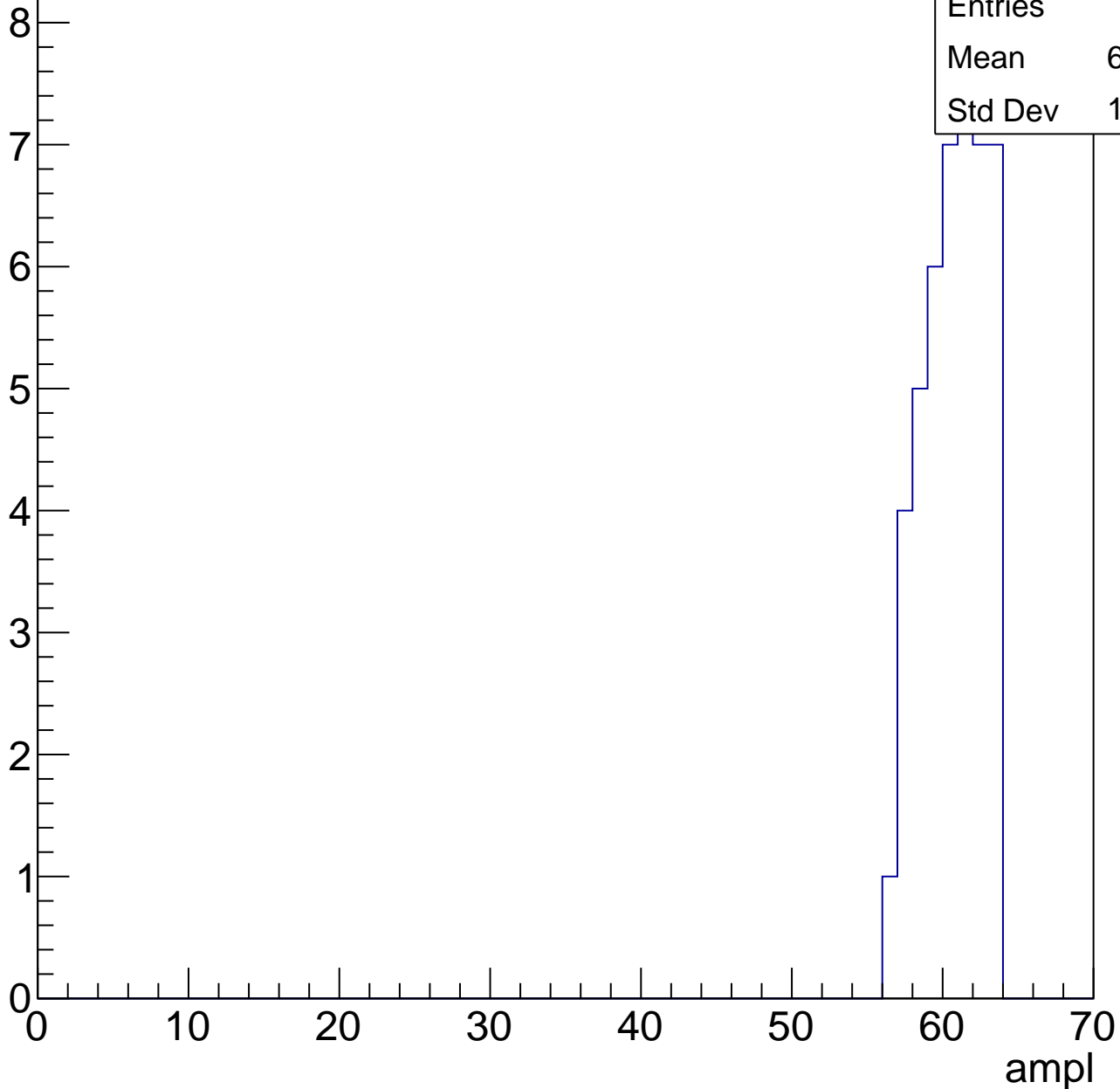


# B1L103S, U22-ch31, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

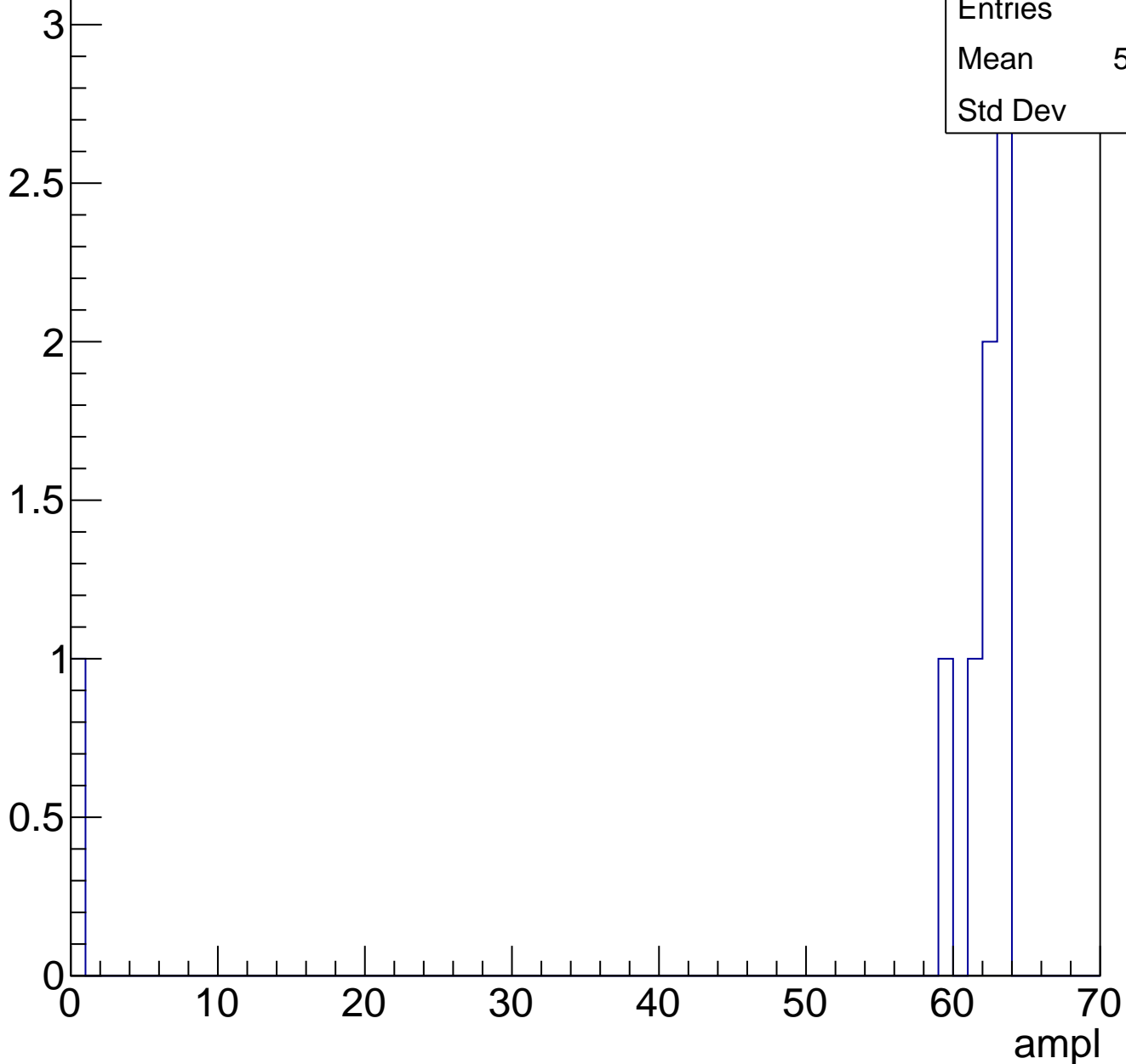
Entries	45
Mean	60.24
Std Dev	1.968



# B1L103S, U22-ch31, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



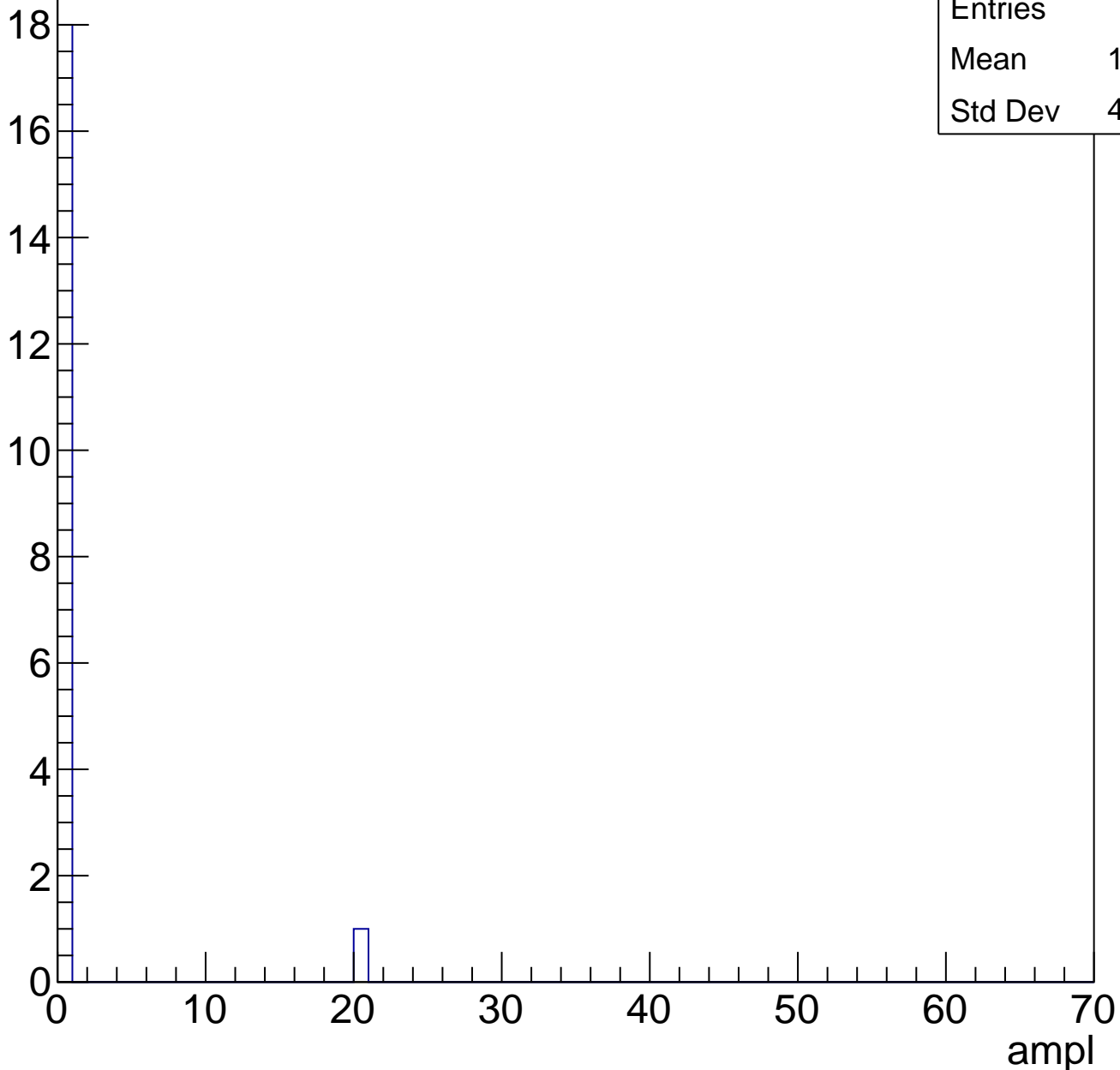


# B1L103S, U22-ch31, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



# B1L103S, U22-ch32, adc0

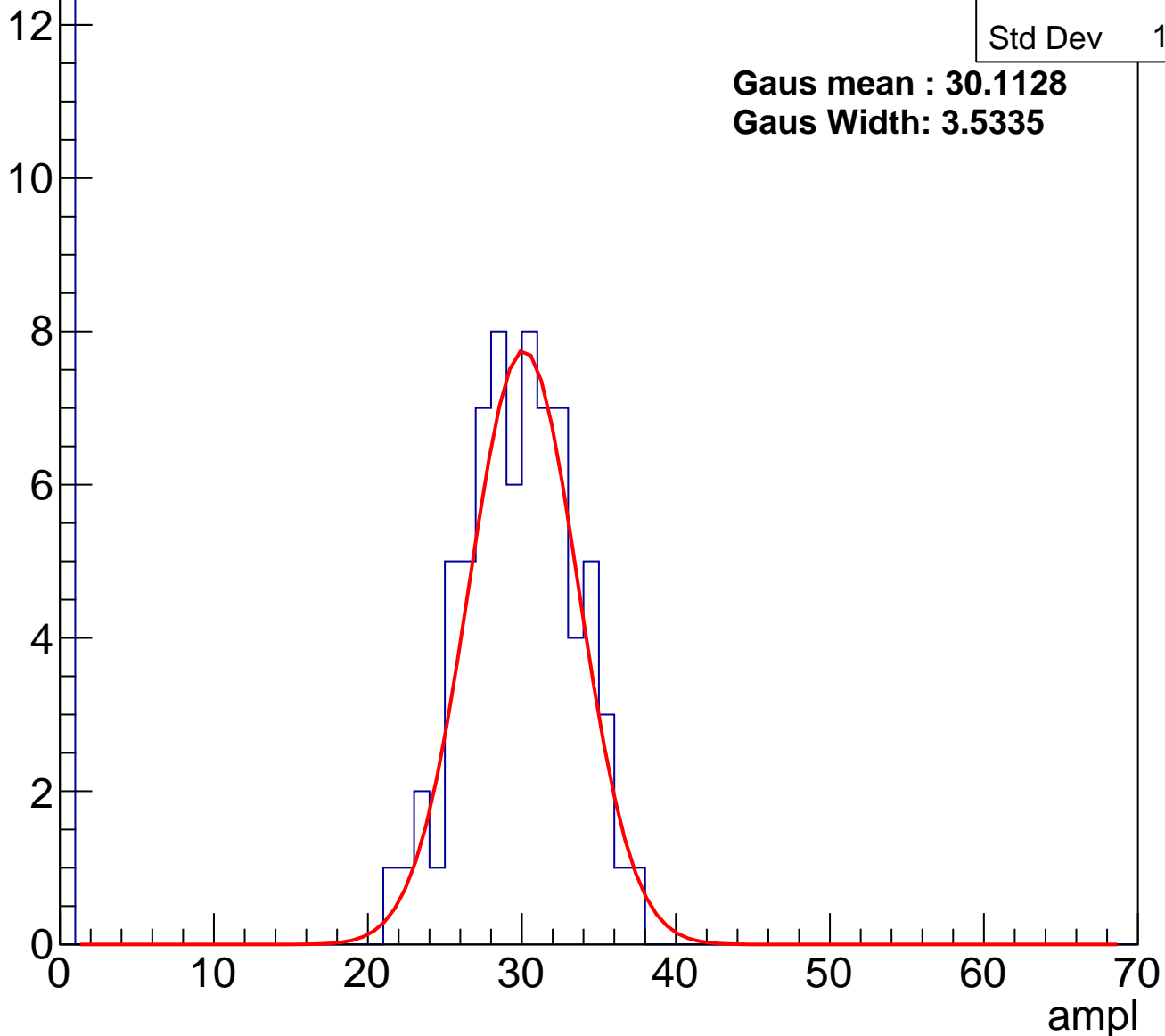
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	85
Mean	24.89
Std Dev	11.05

**Gaus mean : 30.1128**

**Gaus Width: 3.5335**



# B1L103S, U22-ch32, adc1

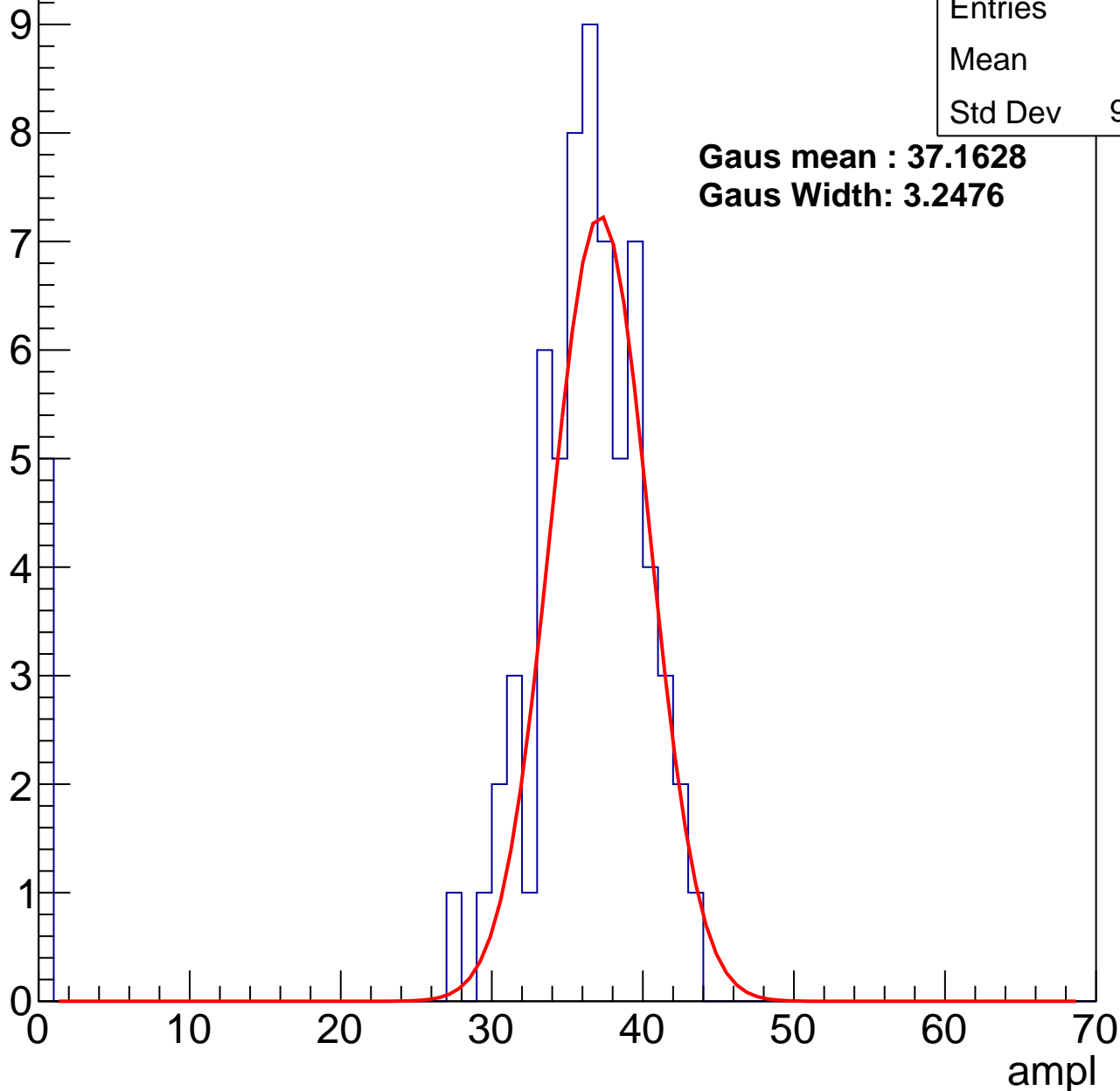
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	33.5
Std Dev	9.833

**Gaus mean : 37.1628**

**Gaus Width: 3.2476**



# B1L103S, U22-ch32, adc2

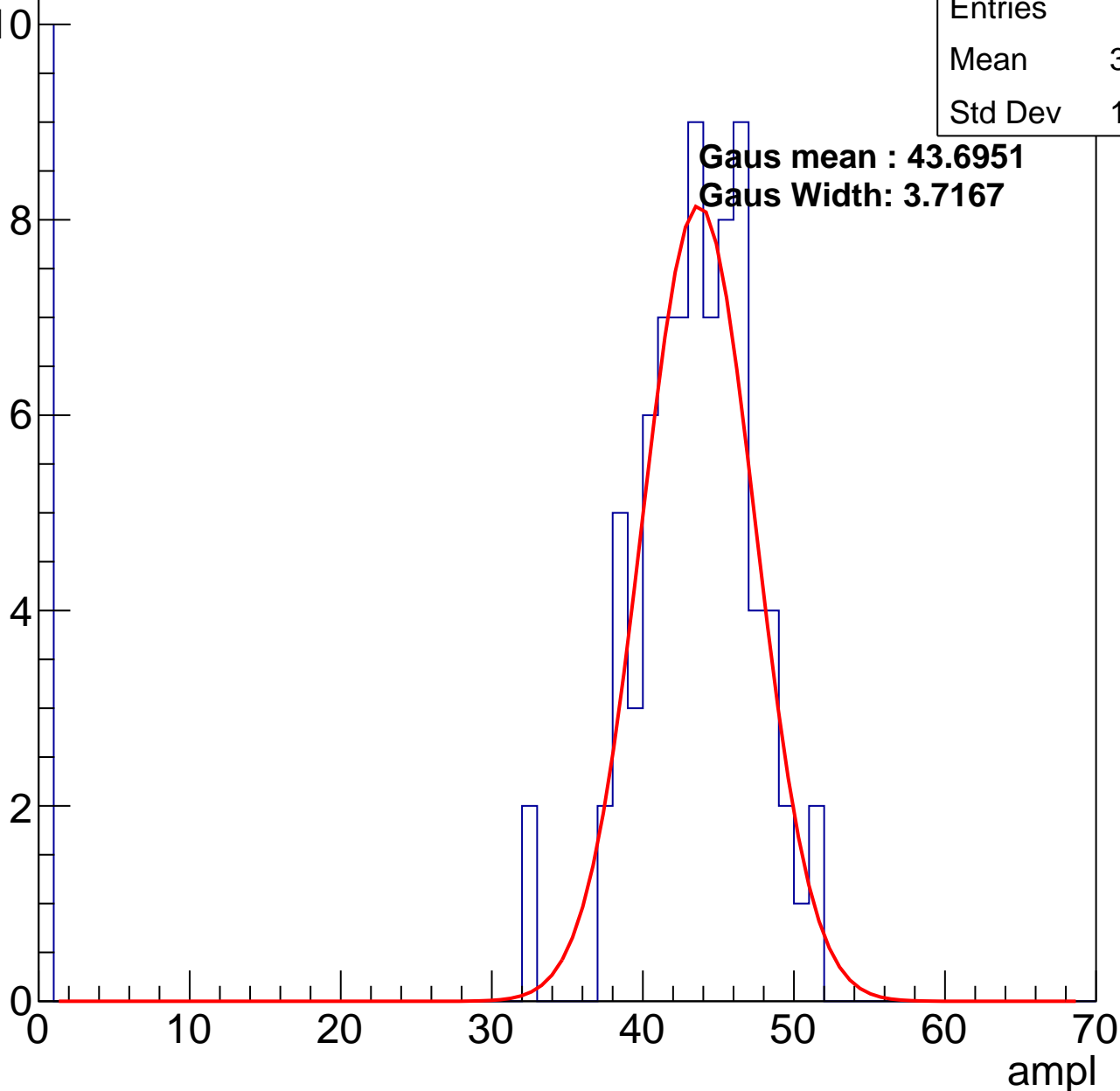
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	88
Mean	38.24
Std Dev	14.14

**Gaus mean : 43.6951**

**Gaus Width: 3.7167**

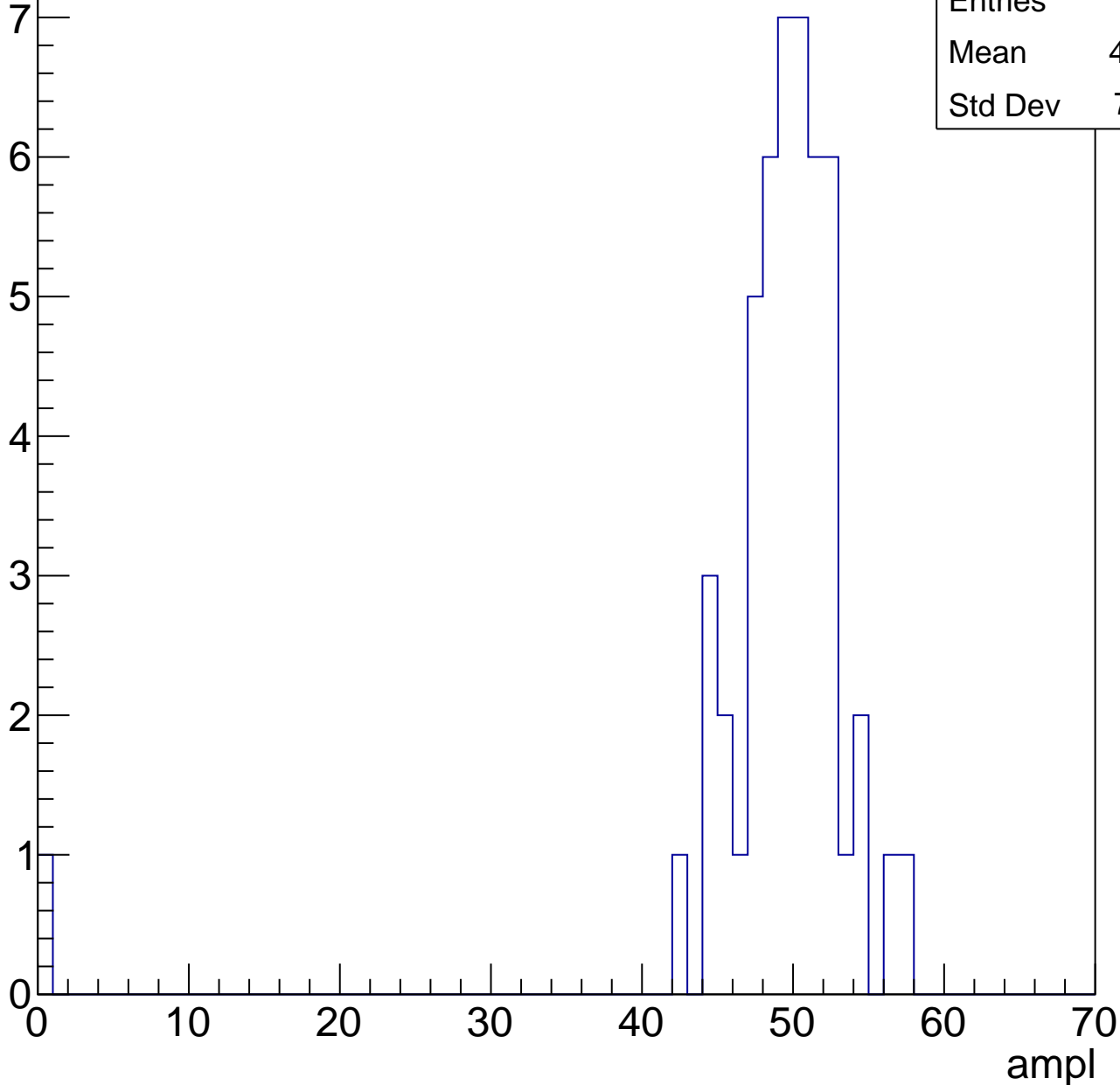


# B1L103S, U22-ch32, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	48.36
Std Dev	7.531

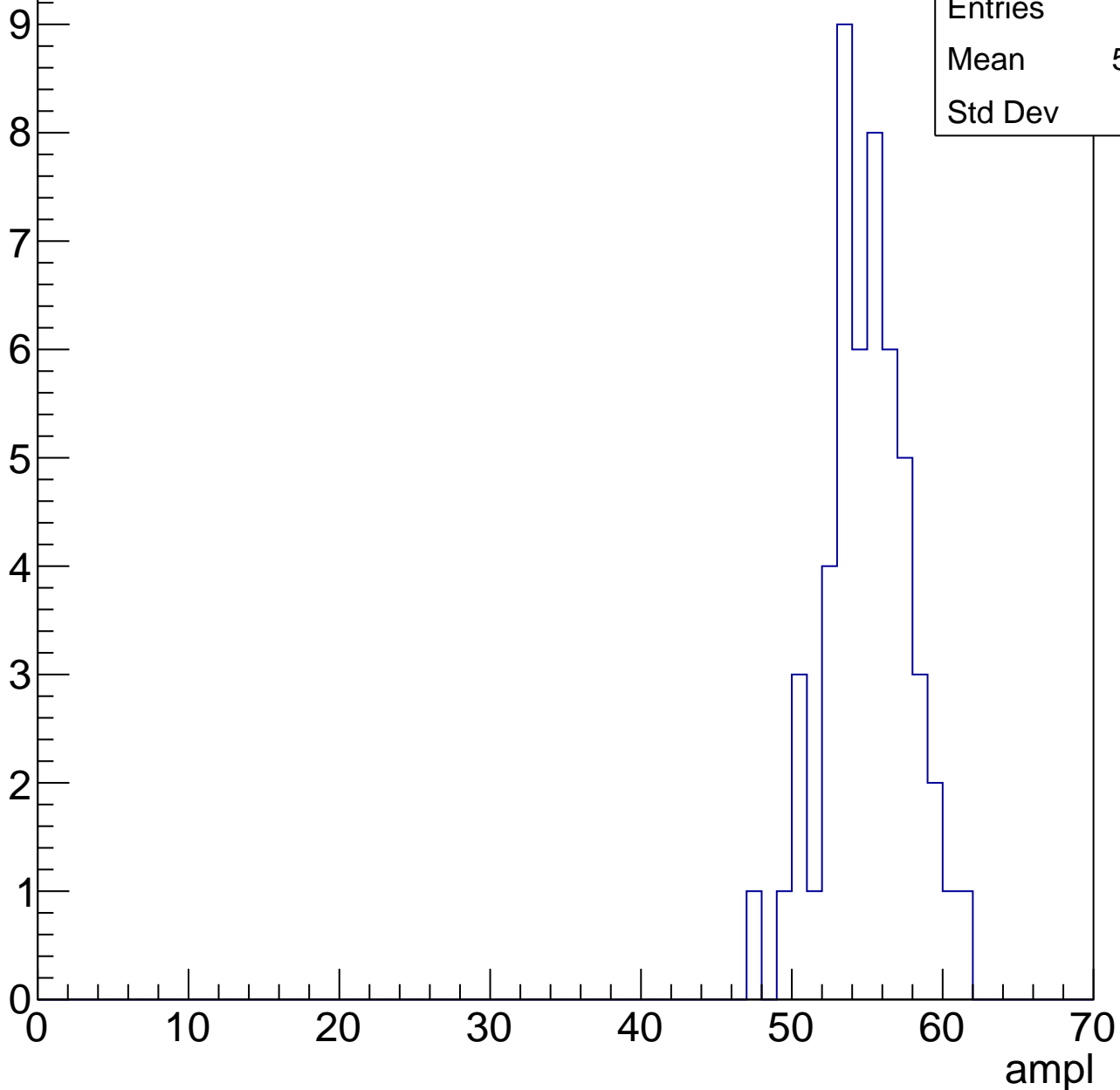


# B1L103S, U22-ch32, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

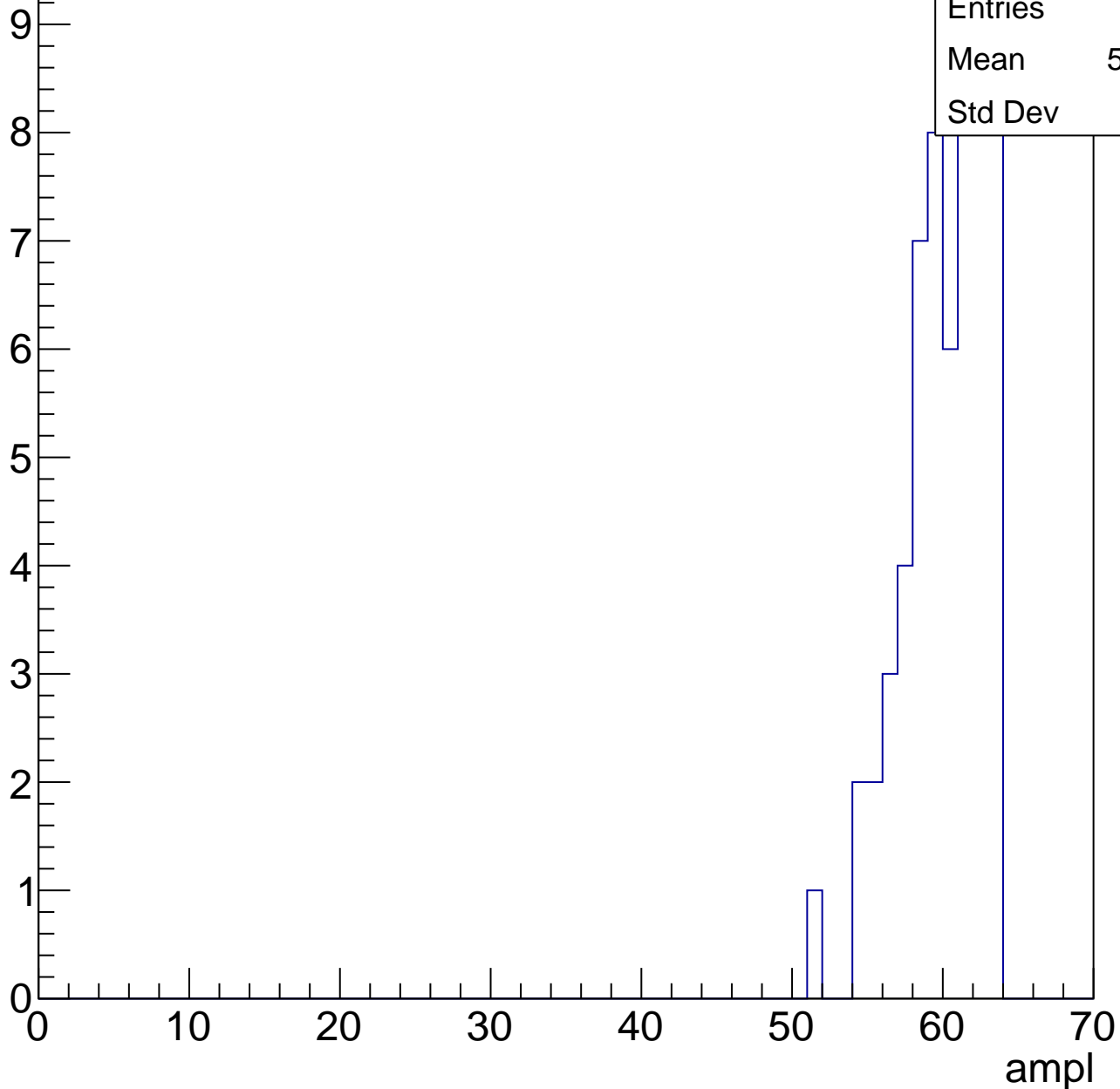
Entries	51
Mean	54.51
Std Dev	2.81



# B1L103S, U22-ch32, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

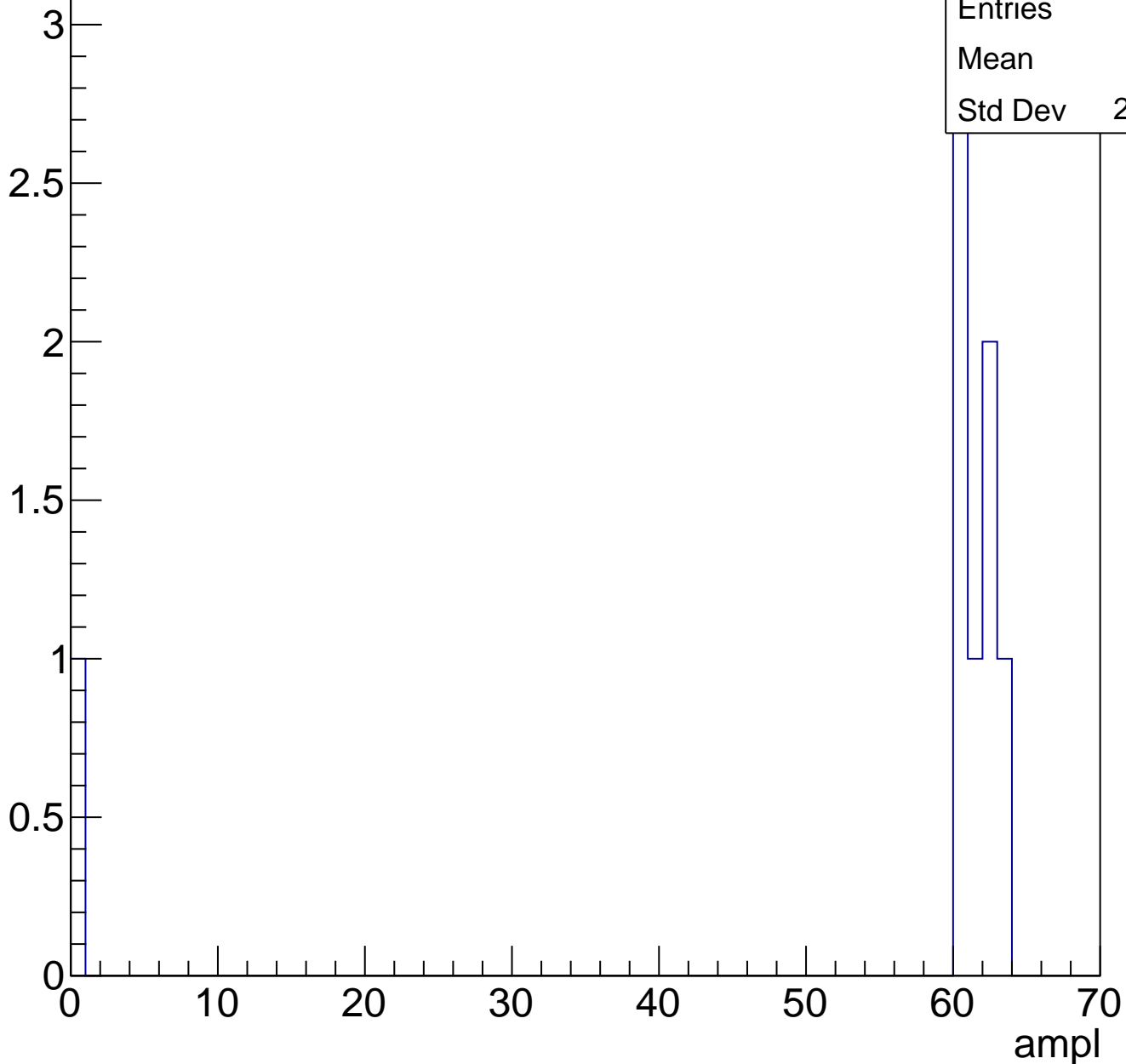
Entry



# B1L103S, U22-ch32, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	8
Mean	53.5
Std Dev	20.25



# B1L103S, U22-ch32, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch33, adc0

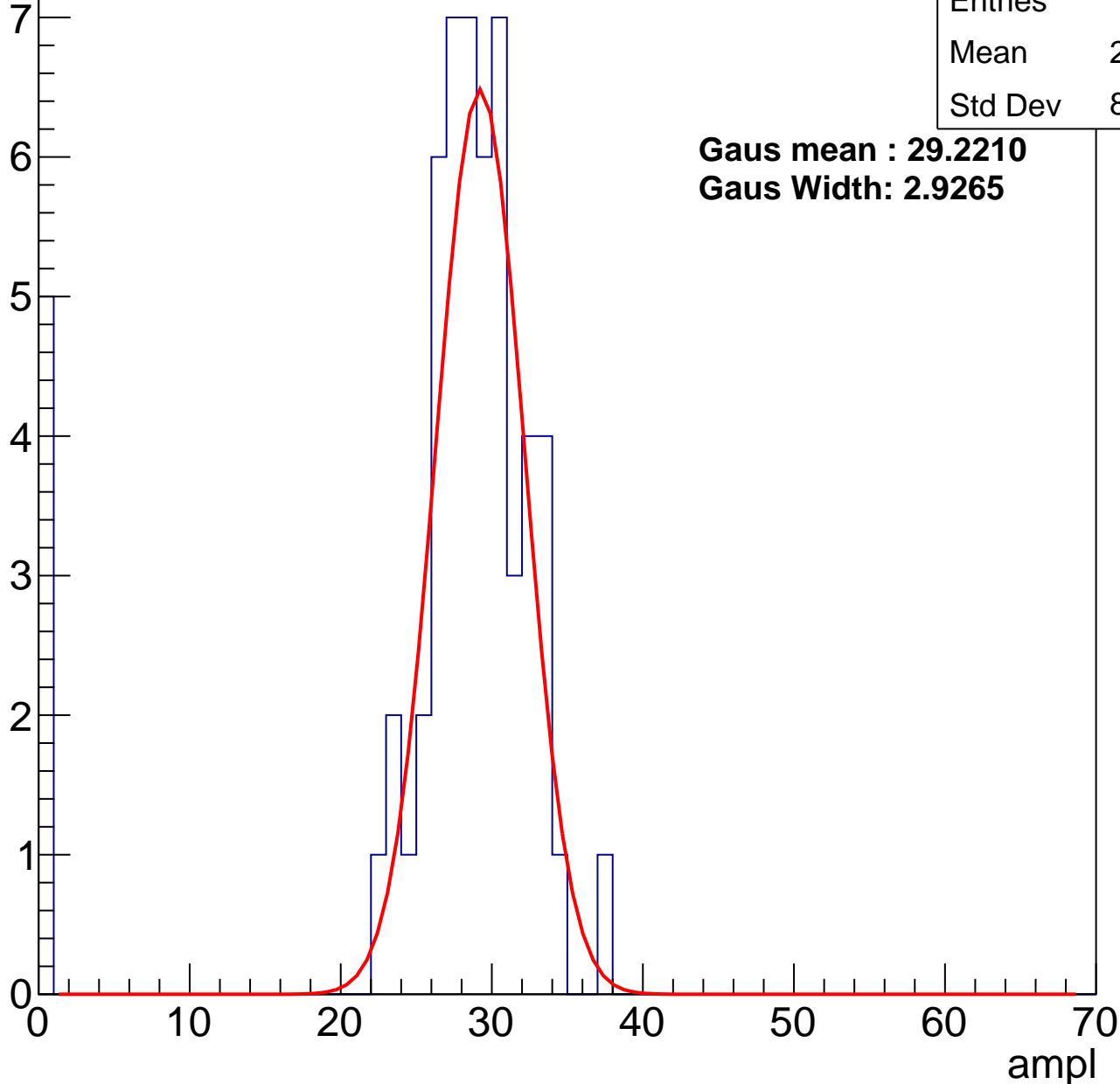
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	26.16
Std Dev	8.604

**Gaus mean : 29.2210**

**Gaus Width: 2.9265**



# B1L103S, U22-ch33, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	35.44
Std Dev	5.304

**Gaus mean : 36.5570**

**Gaus Width: 3.8310**

Entry

10

8

6

4

2

0

0

10

20

30

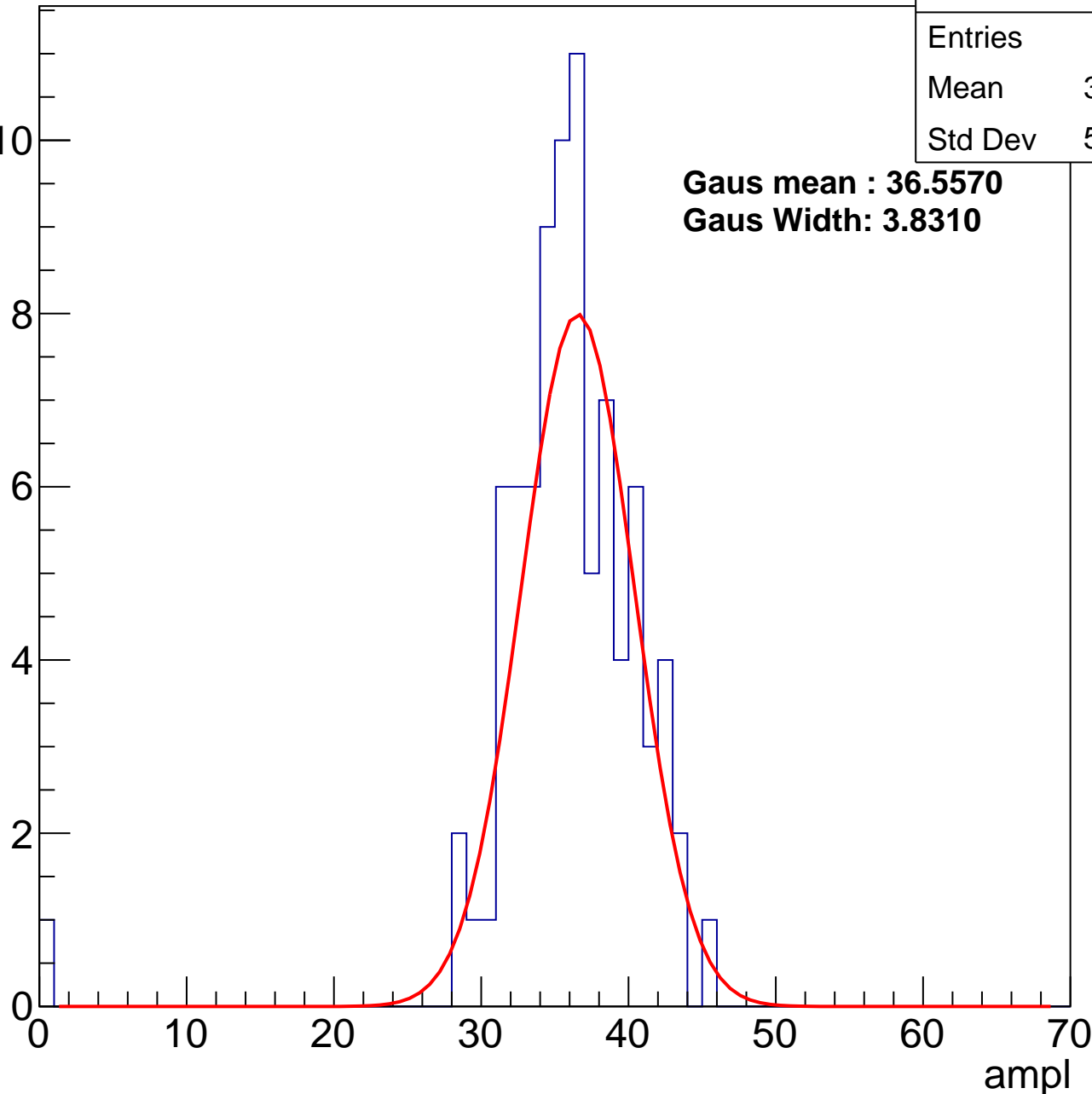
40

50

60

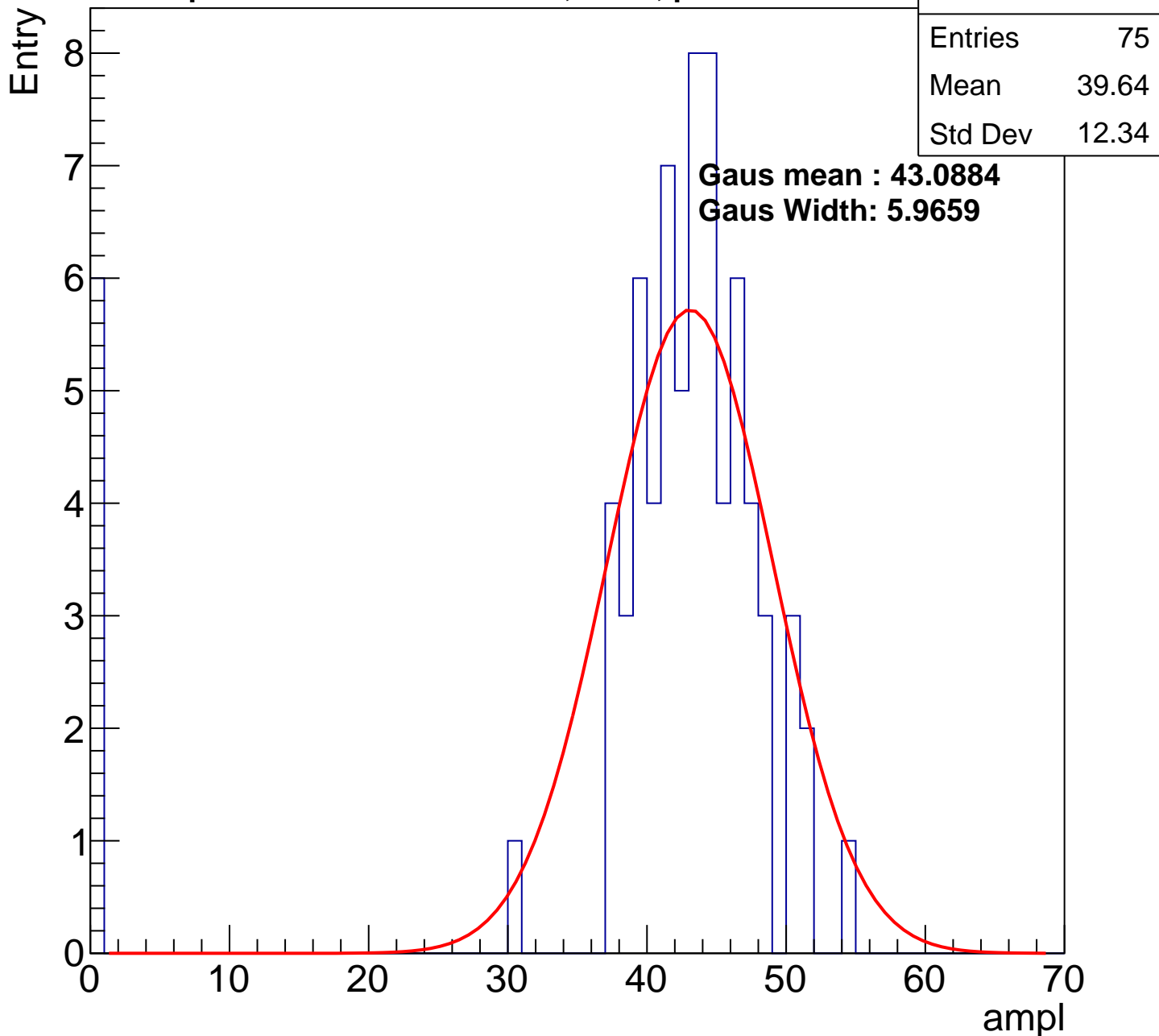
70

ampl



# B1L103S, U22-ch33, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

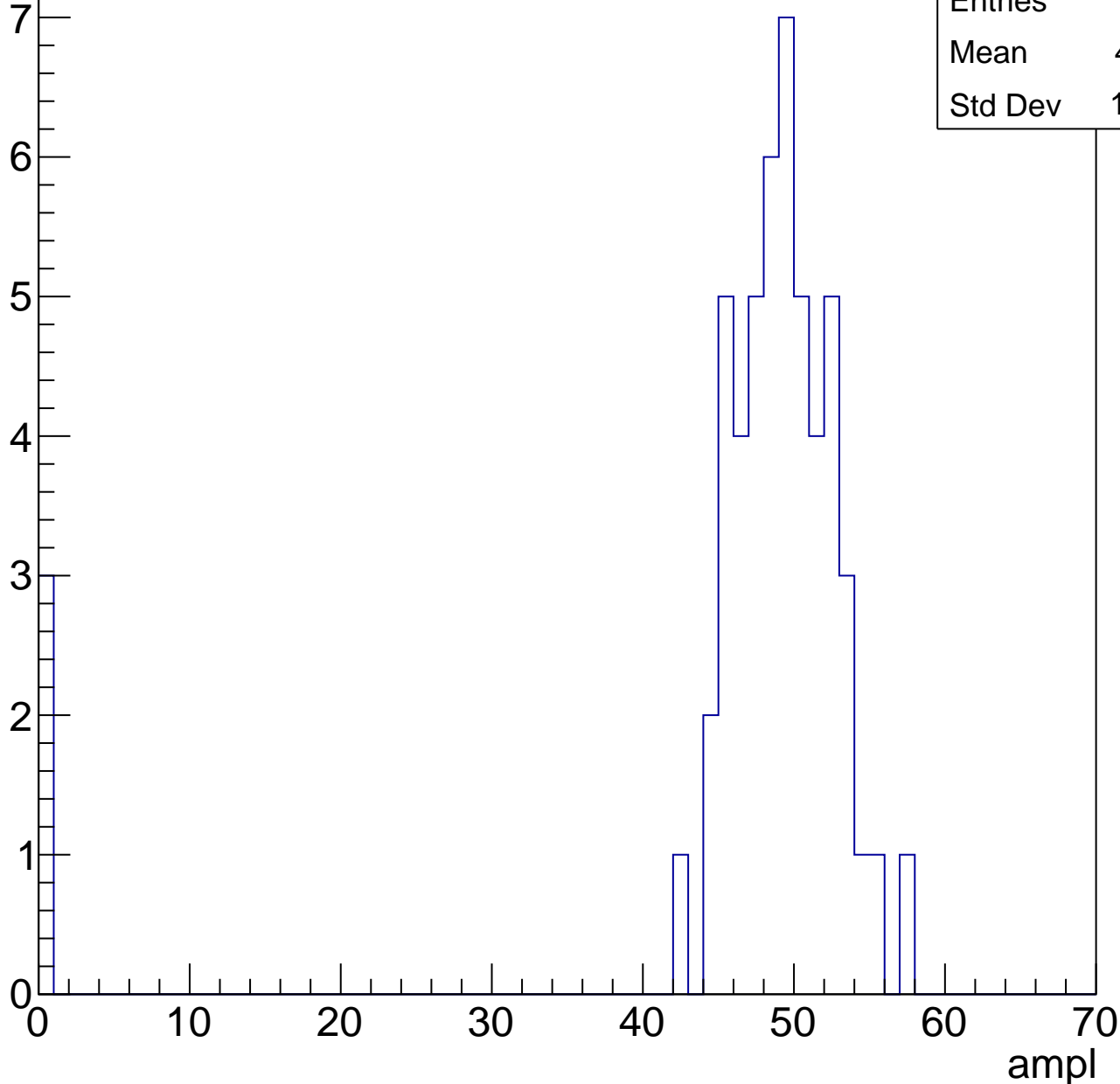


# B1L103S, U22-ch33, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	46.11
Std Dev	11.69

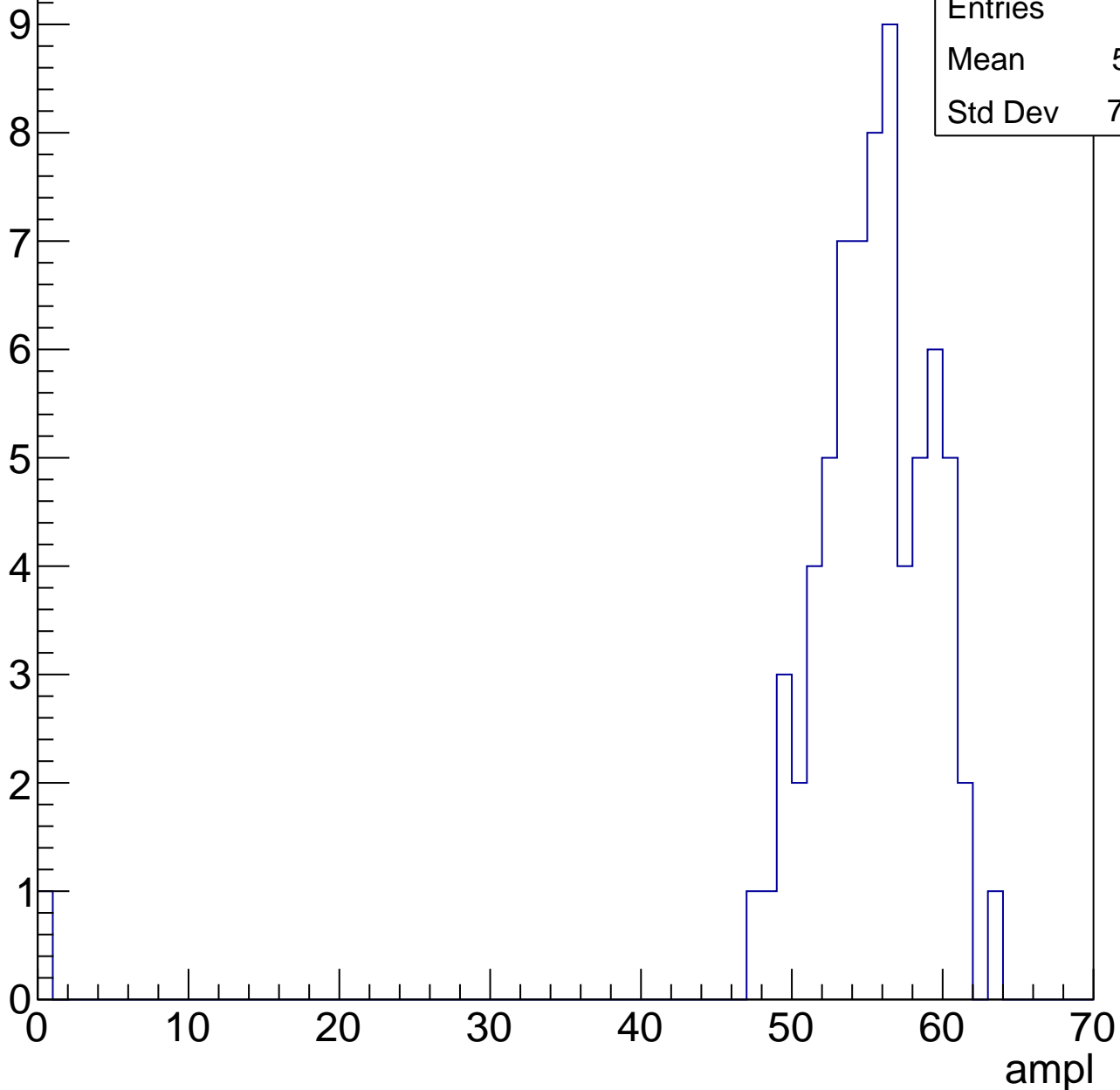


# B1L103S, U22-ch33, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	54.31
Std Dev	7.348

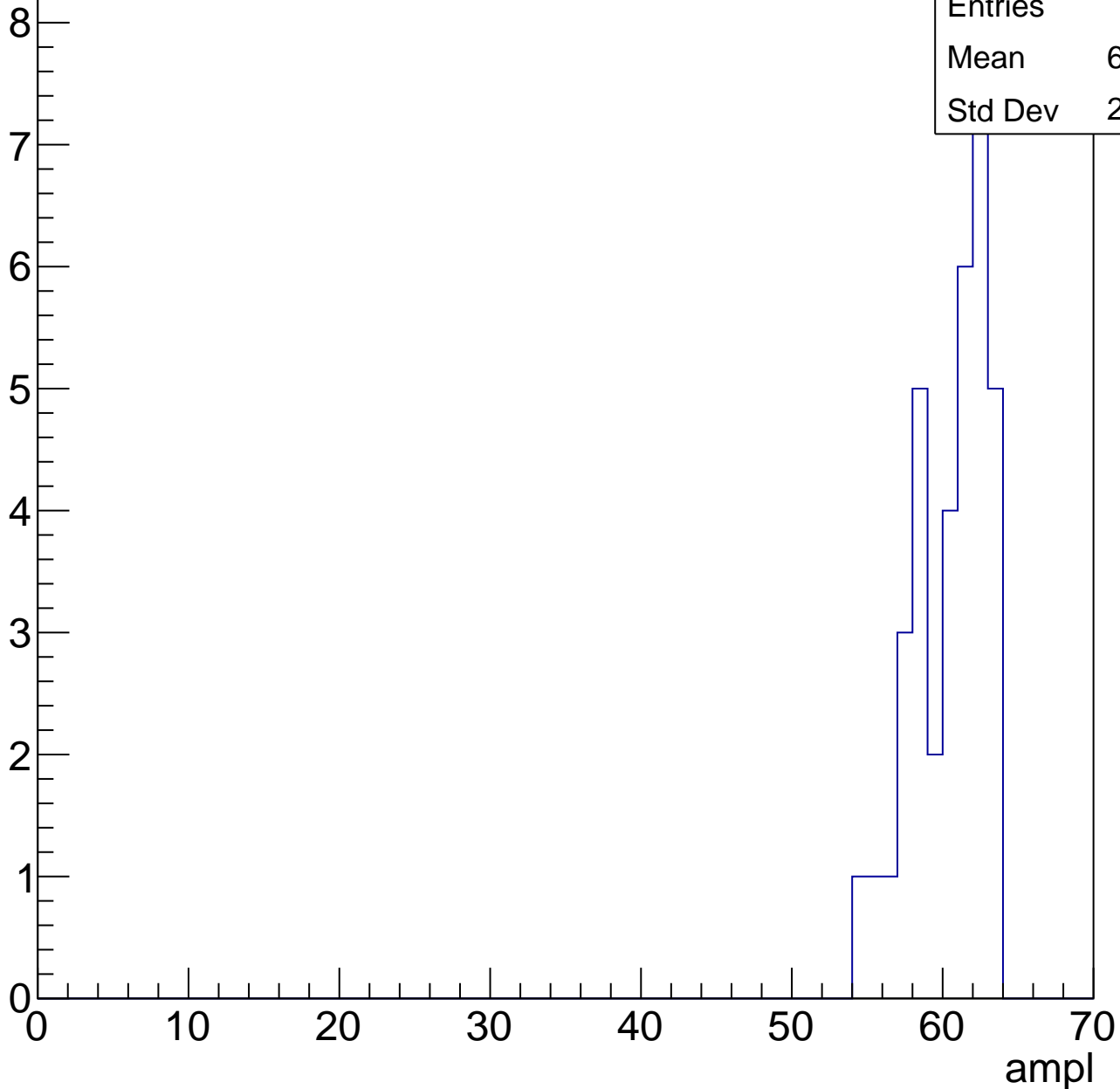


# B1L103S, U22-ch33, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

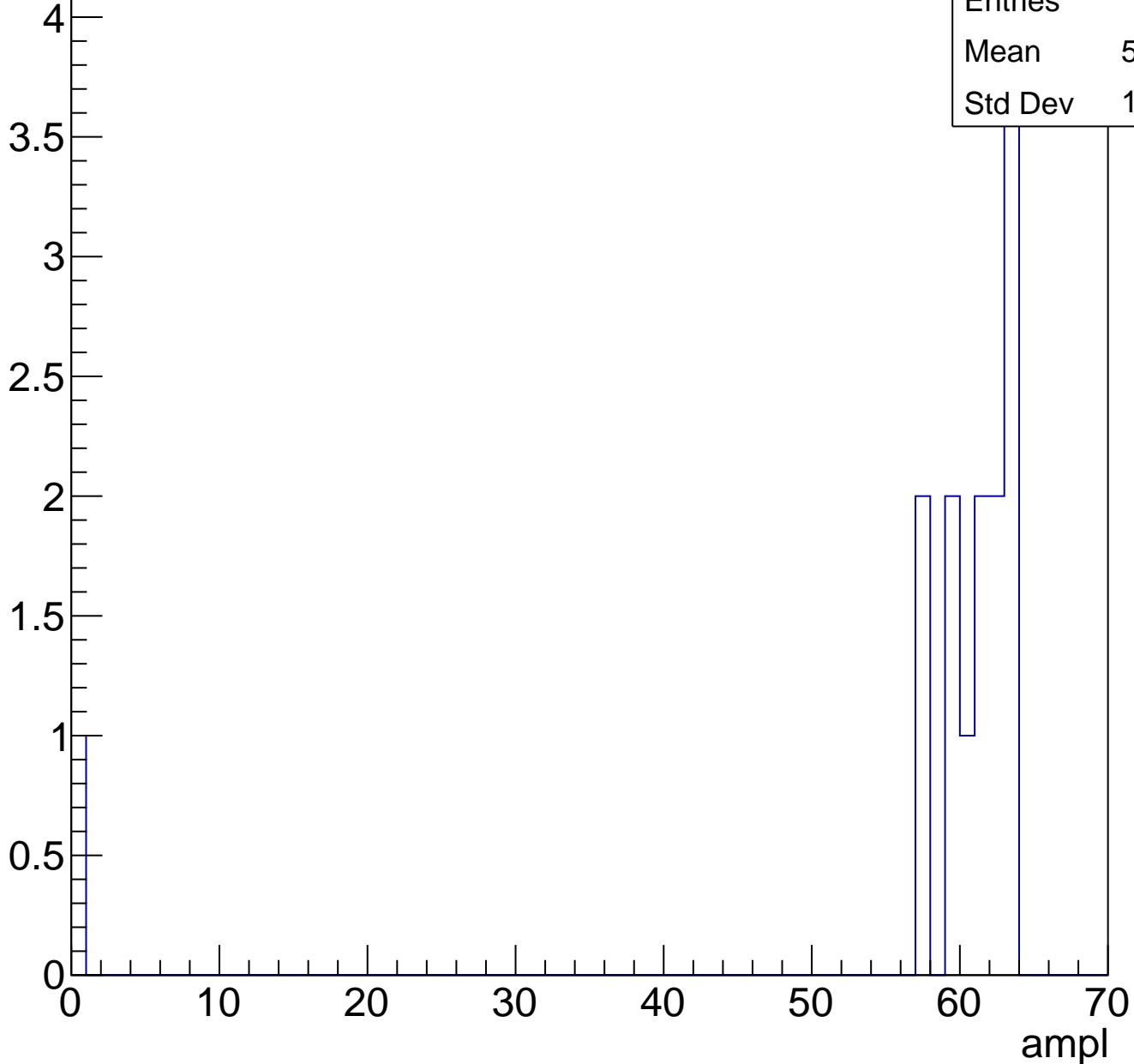
Entries	36
Mean	60.03
Std Dev	2.409



# B1L103S, U22-ch33, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



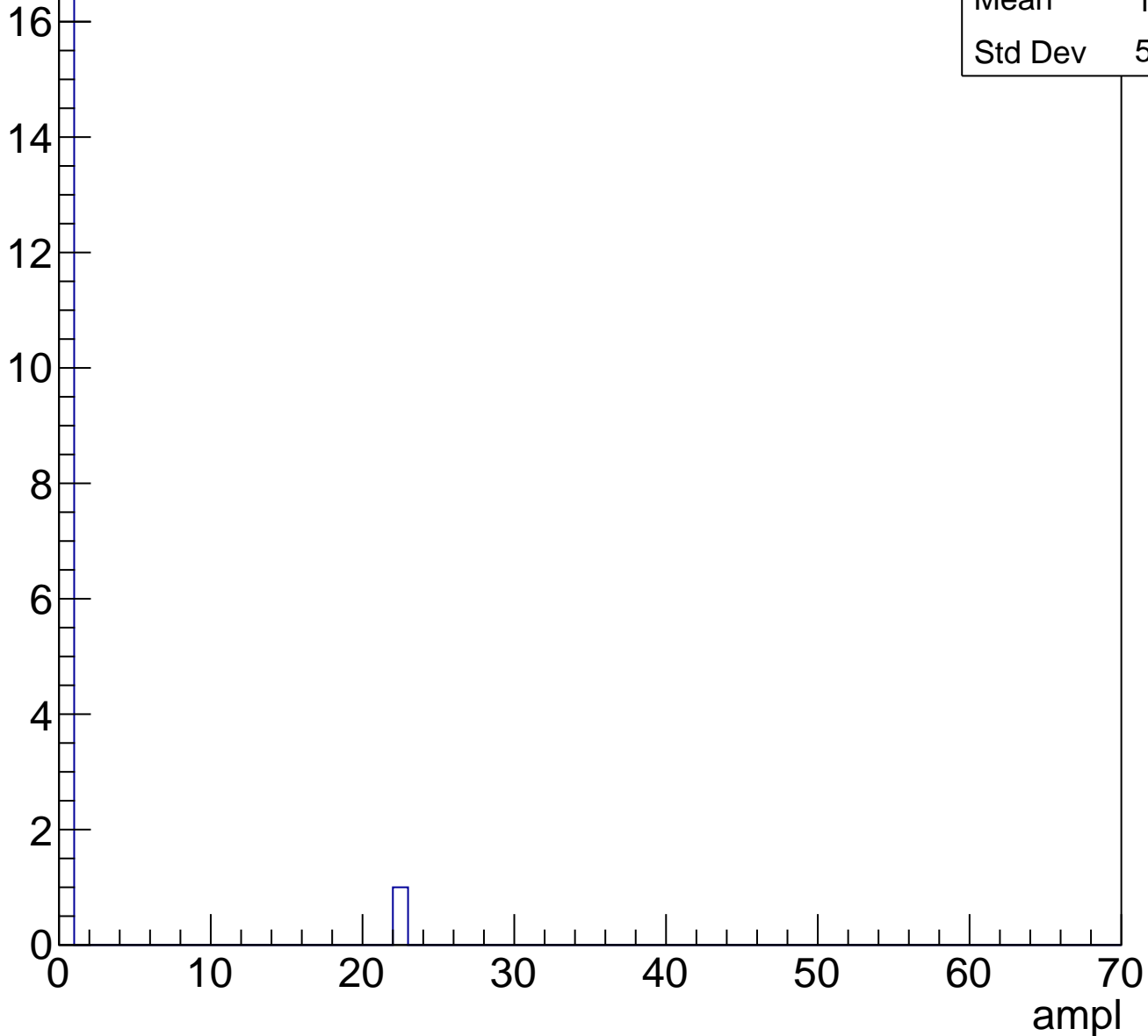


# B1L103S, U22-ch33, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	18
Mean	1.222
Std Dev	5.039

Entry



# B1L103S, U22-ch34, adc0

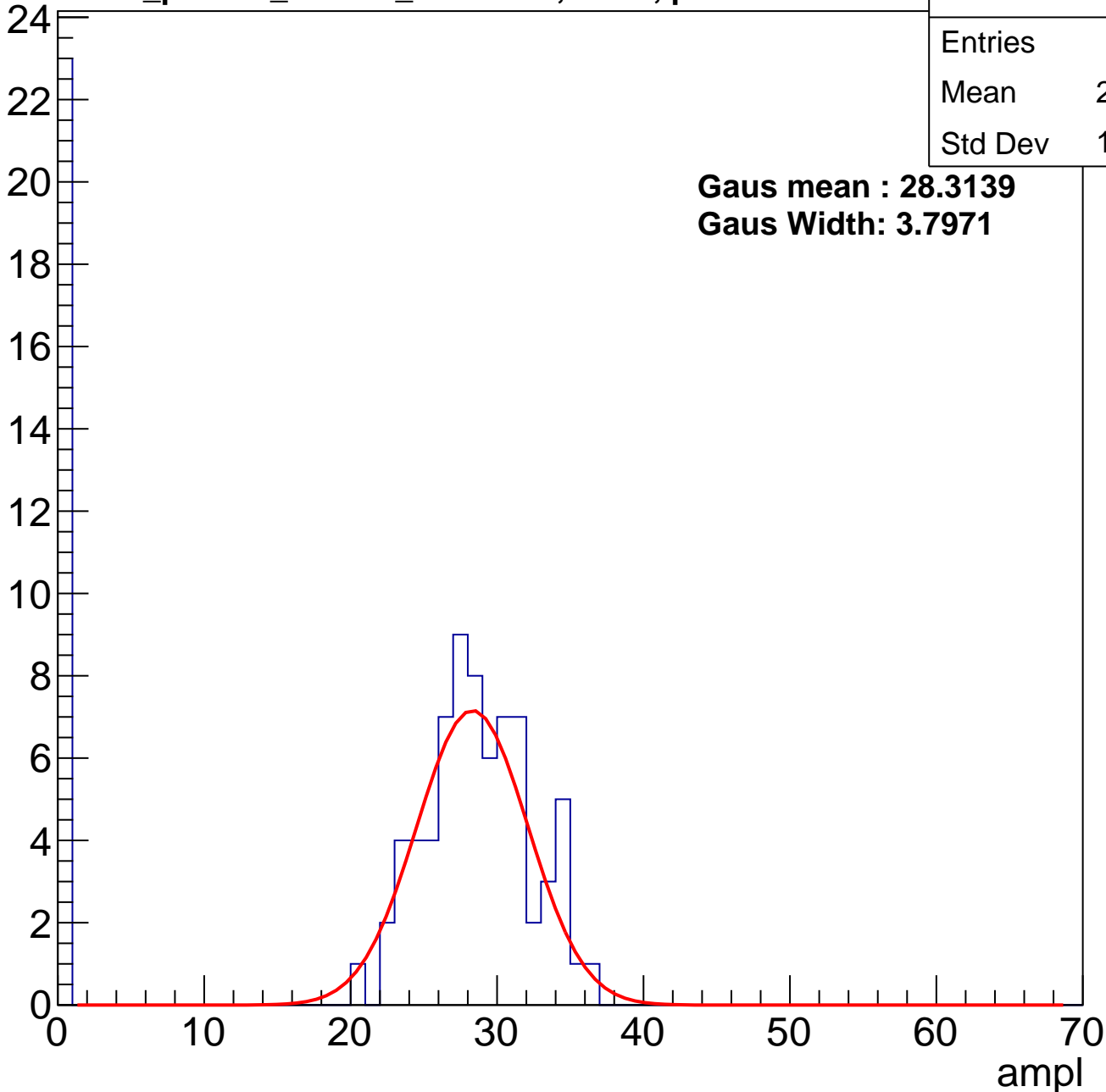
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	94
Mean	21.34
Std Dev	12.52

**Gaus mean : 28.3139**

**Gaus Width: 3.7971**

Entry



# B1L103S, U22-ch34, adc1

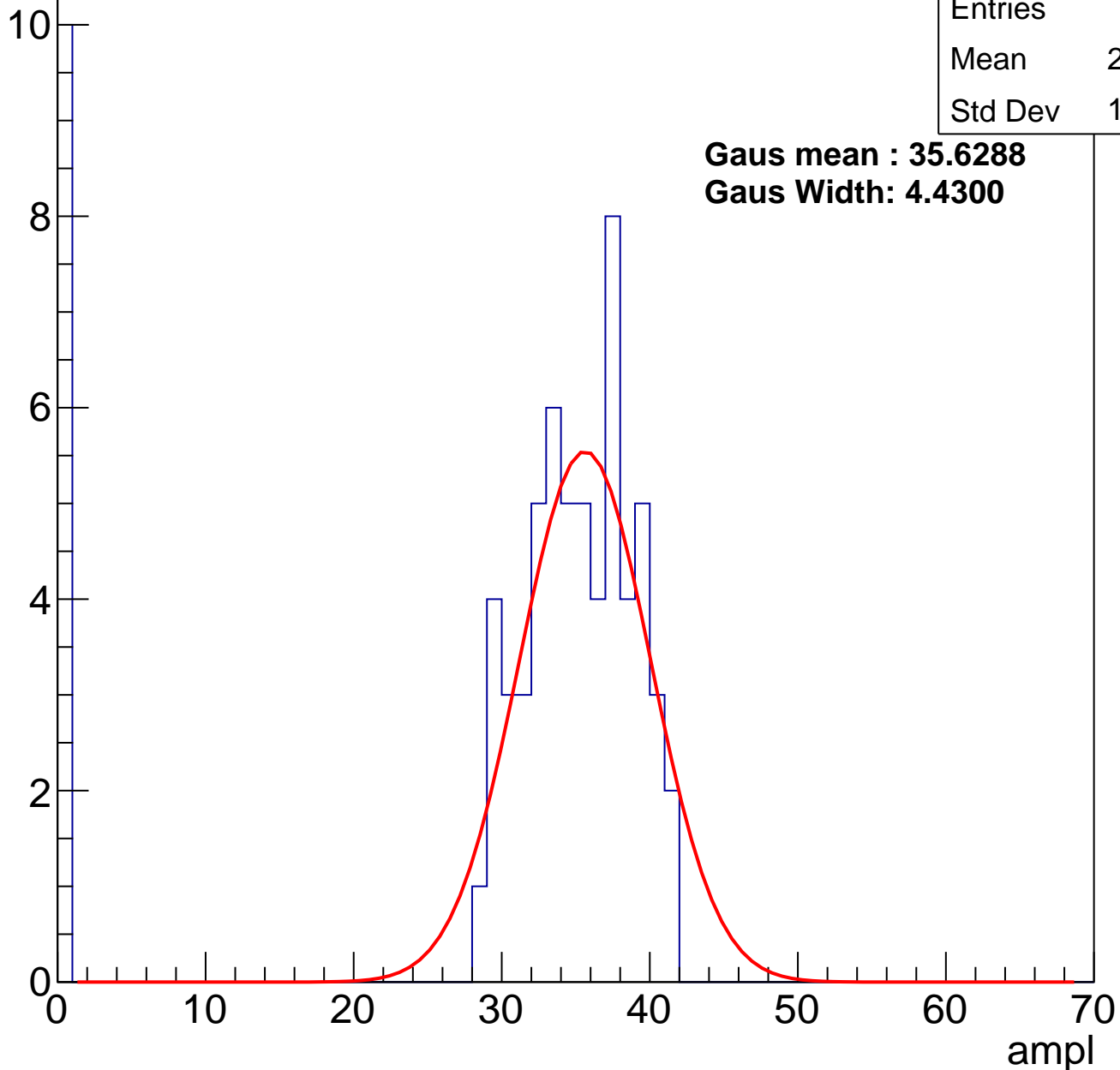
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	68
Mean	29.69
Std Dev	12.73

**Gaus mean : 35.6288**

**Gaus Width: 4.4300**

Entry



# B1L103S, U22-ch34, adc2

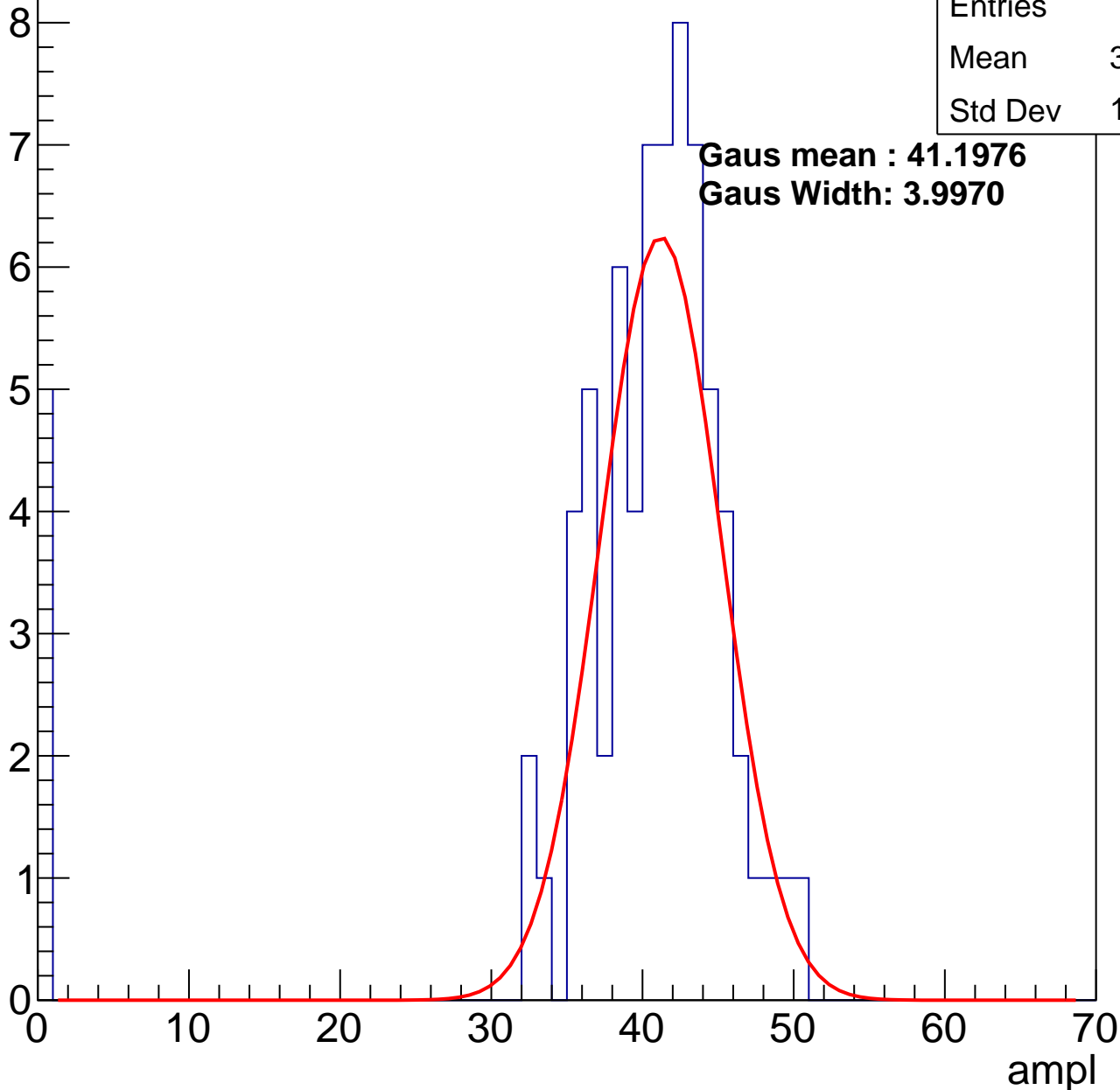
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	37.88
Std Dev	10.93

**Gaus mean : 41.1976**

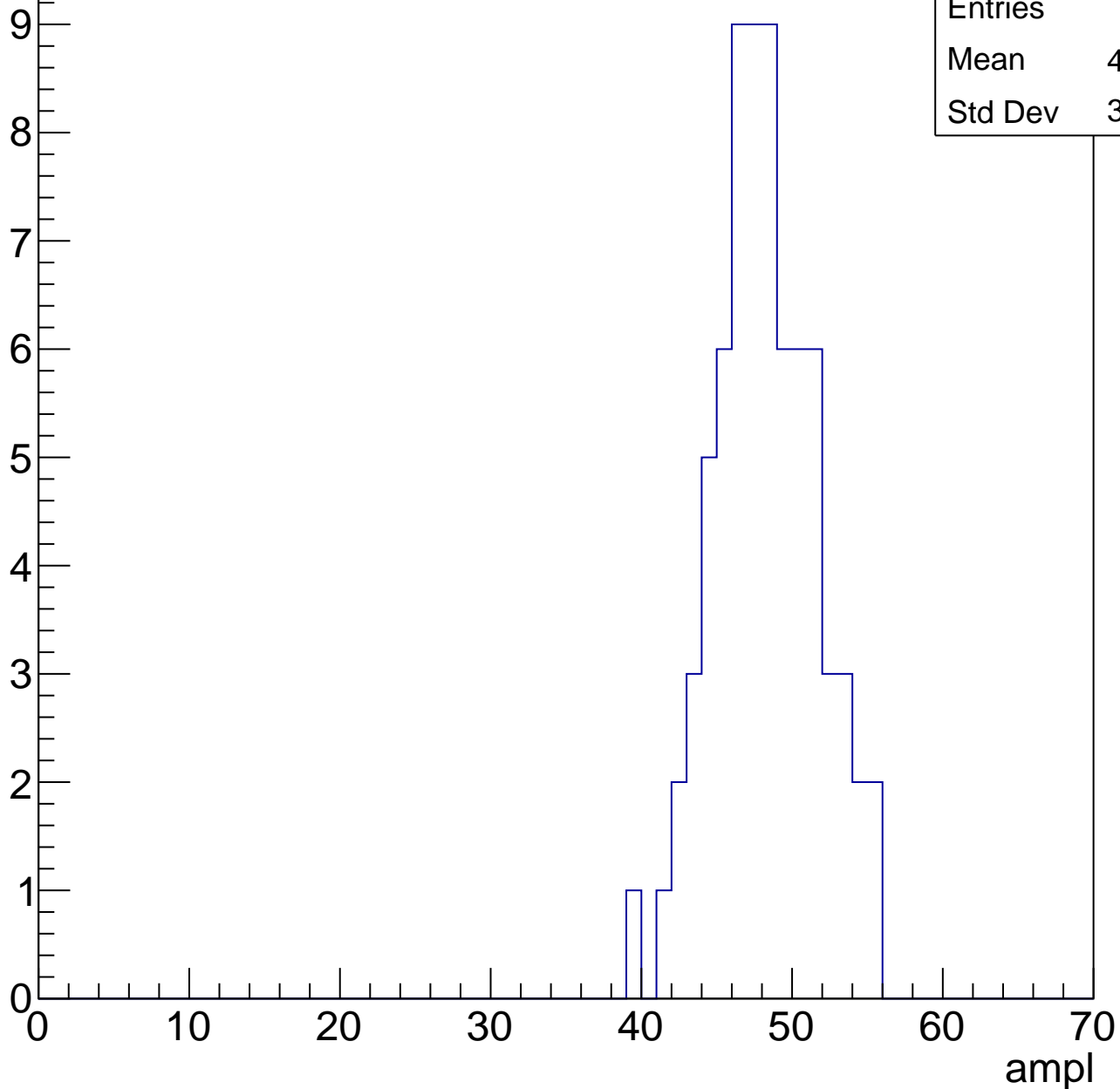
**Gaus Width: 3.9970**



# B1L103S, U22-ch34, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



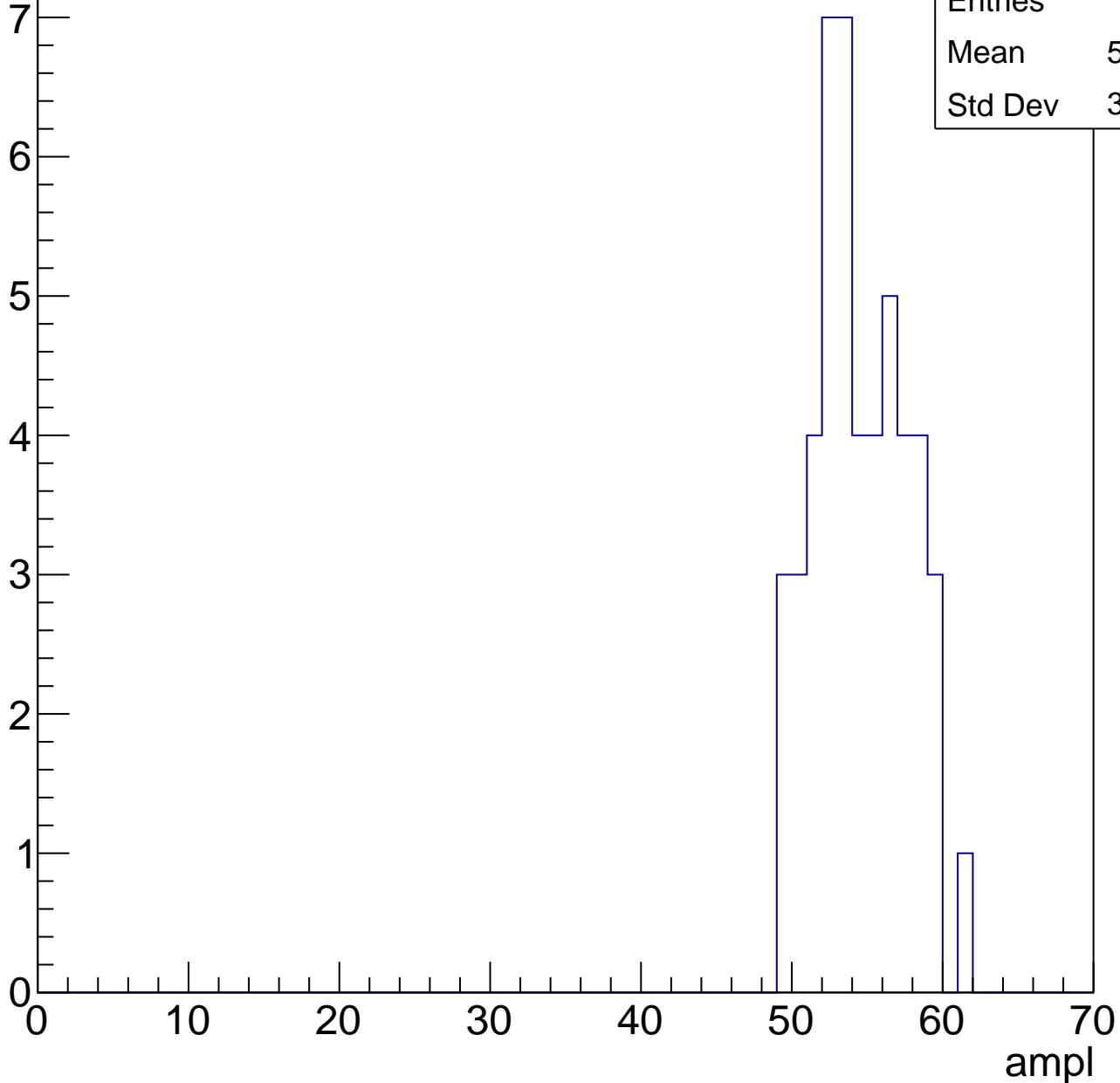
Entries	73
Mean	47.74
Std Dev	3.368

# B1L103S, U22-ch34, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	54.08
Std Dev	3.002

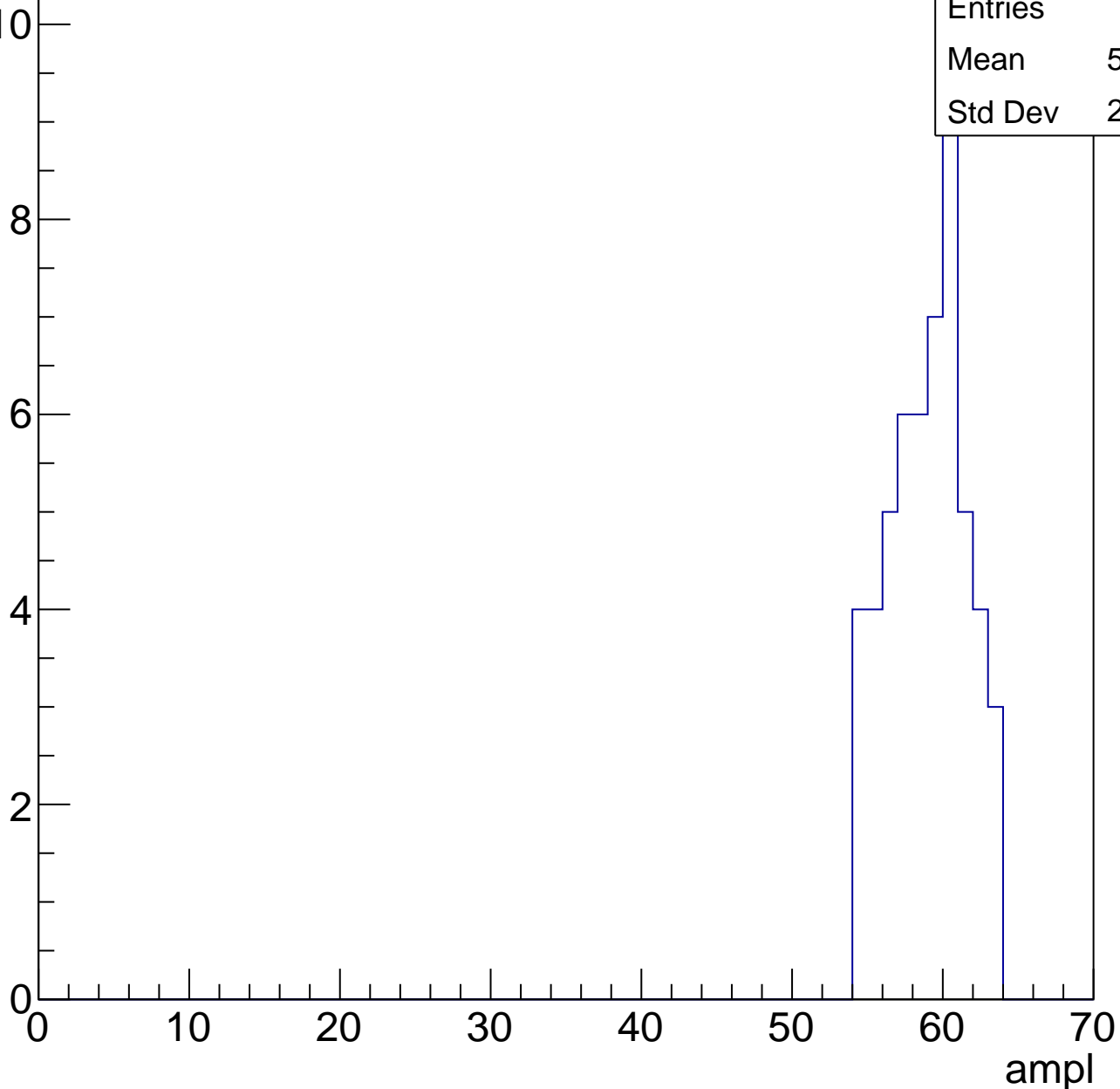


# B1L103S, U22-ch34, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	58.54
Std Dev	2.514

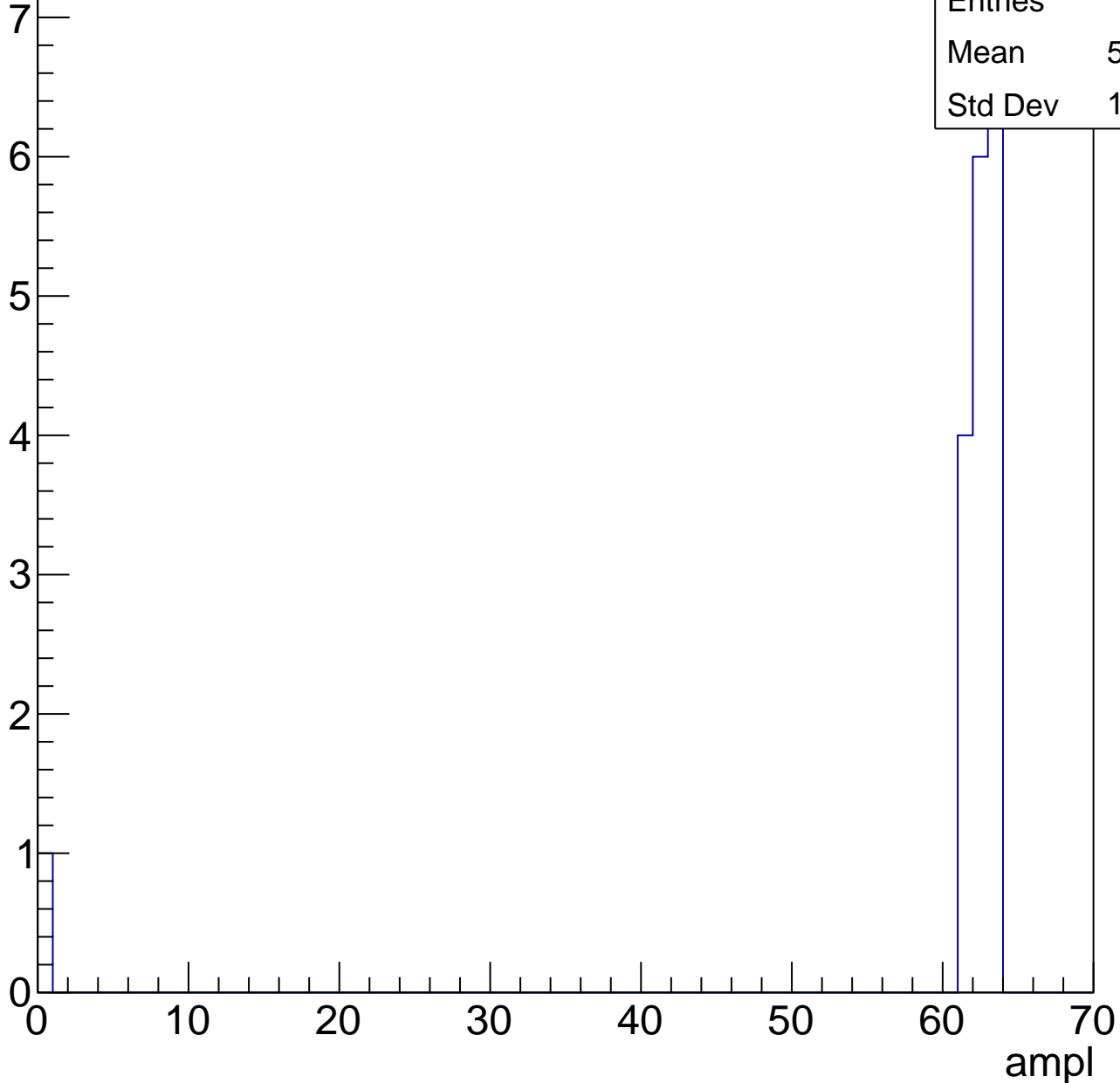


# B1L103S, U22-ch34, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	18
Mean	58.72
Std Dev	14.26





# B1L103S, U22-ch34, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	18
Mean	1.056
Std Dev	4.352

Entry

16

14

12

10

8

6

4

2

0

0

10

20

30

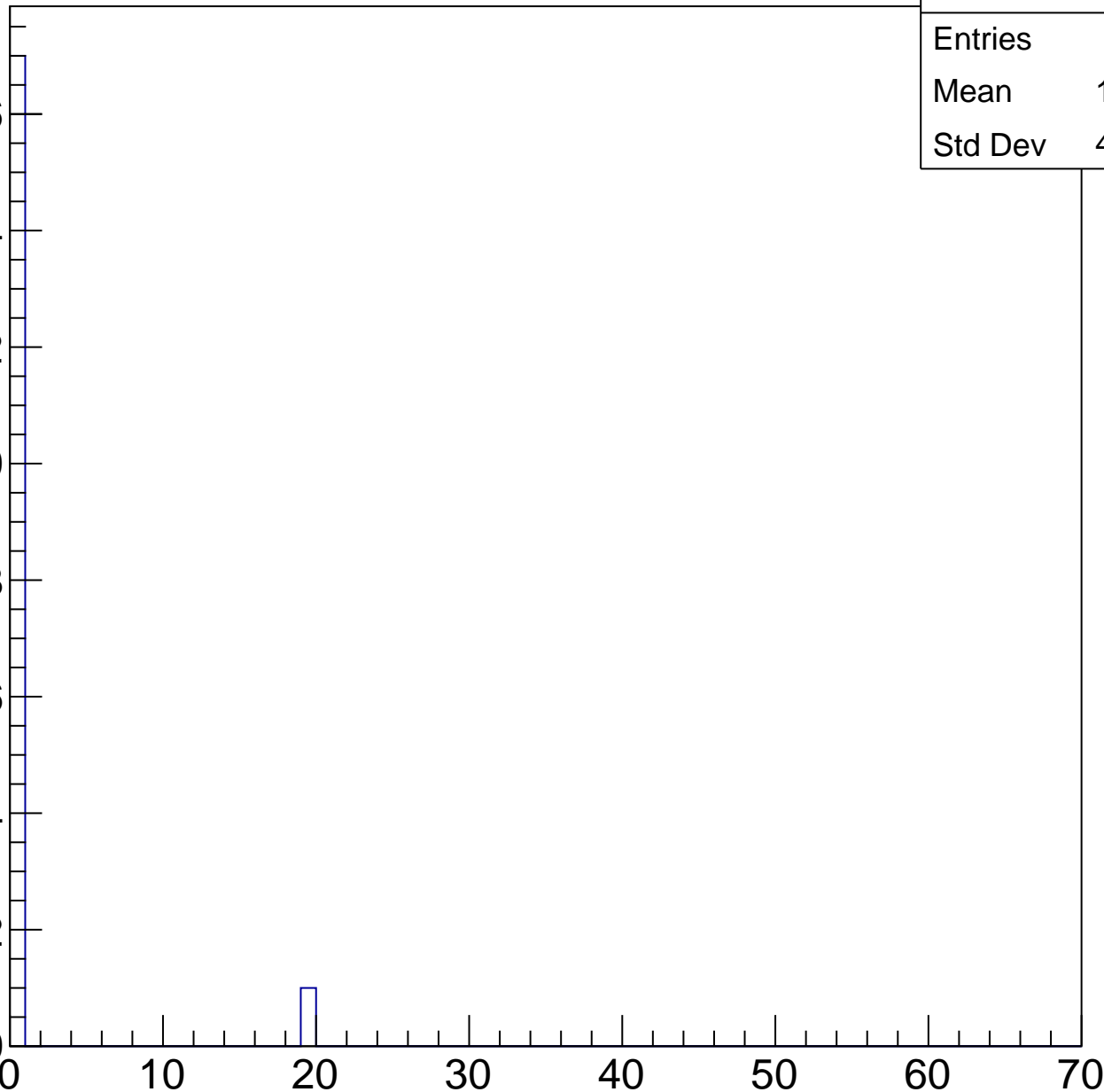
40

50

60

70

ampl



# B1L103S, U22-ch35, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	21.15
Std Dev	13.59

**Gaus mean : 30.1255**

**Gaus Width: 2.9006**

Entry

25

20

15

10

5

0

0

10

20

30

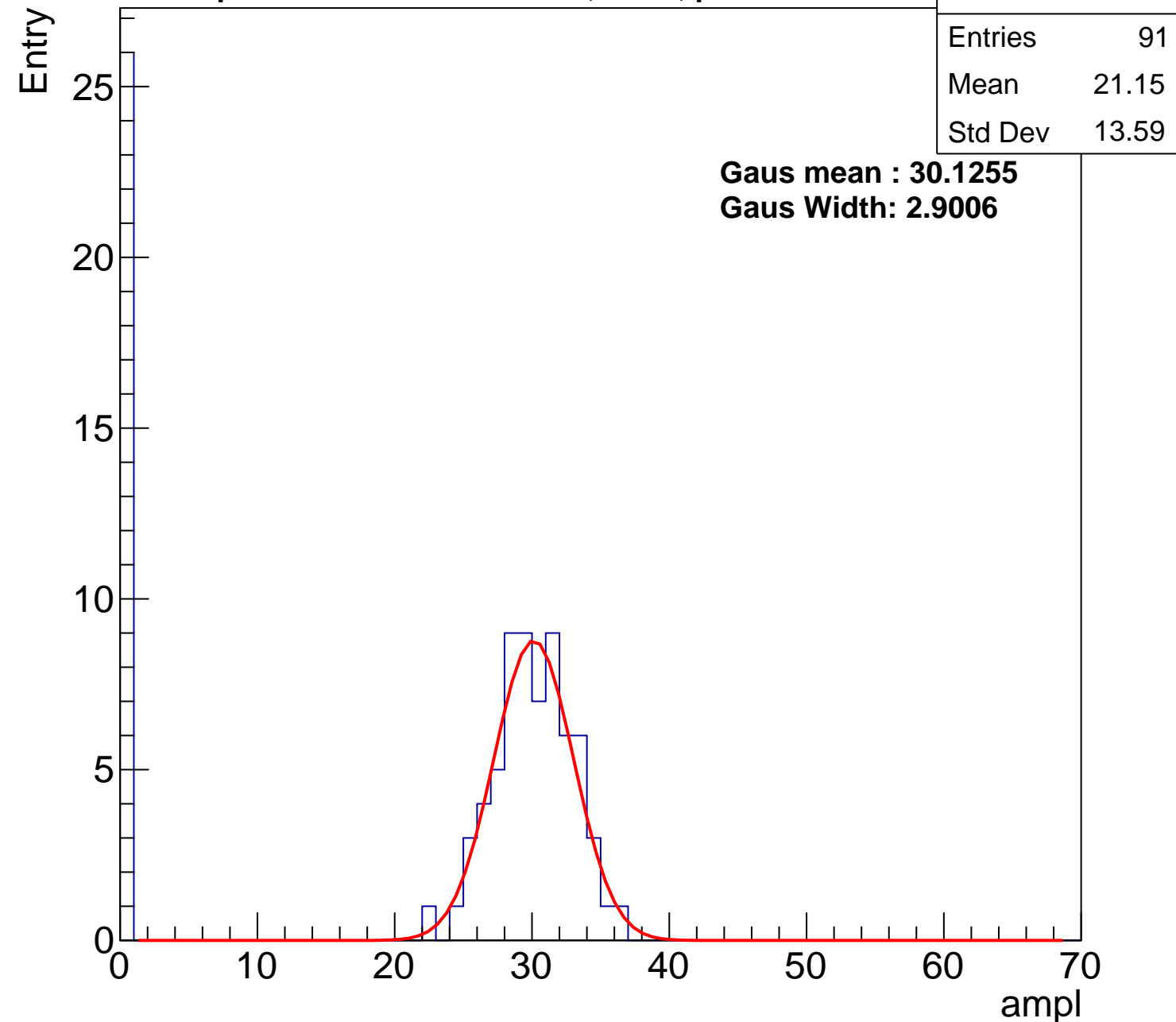
40

50

60

70

ampl



# B1L103S, U22-ch35, adc1

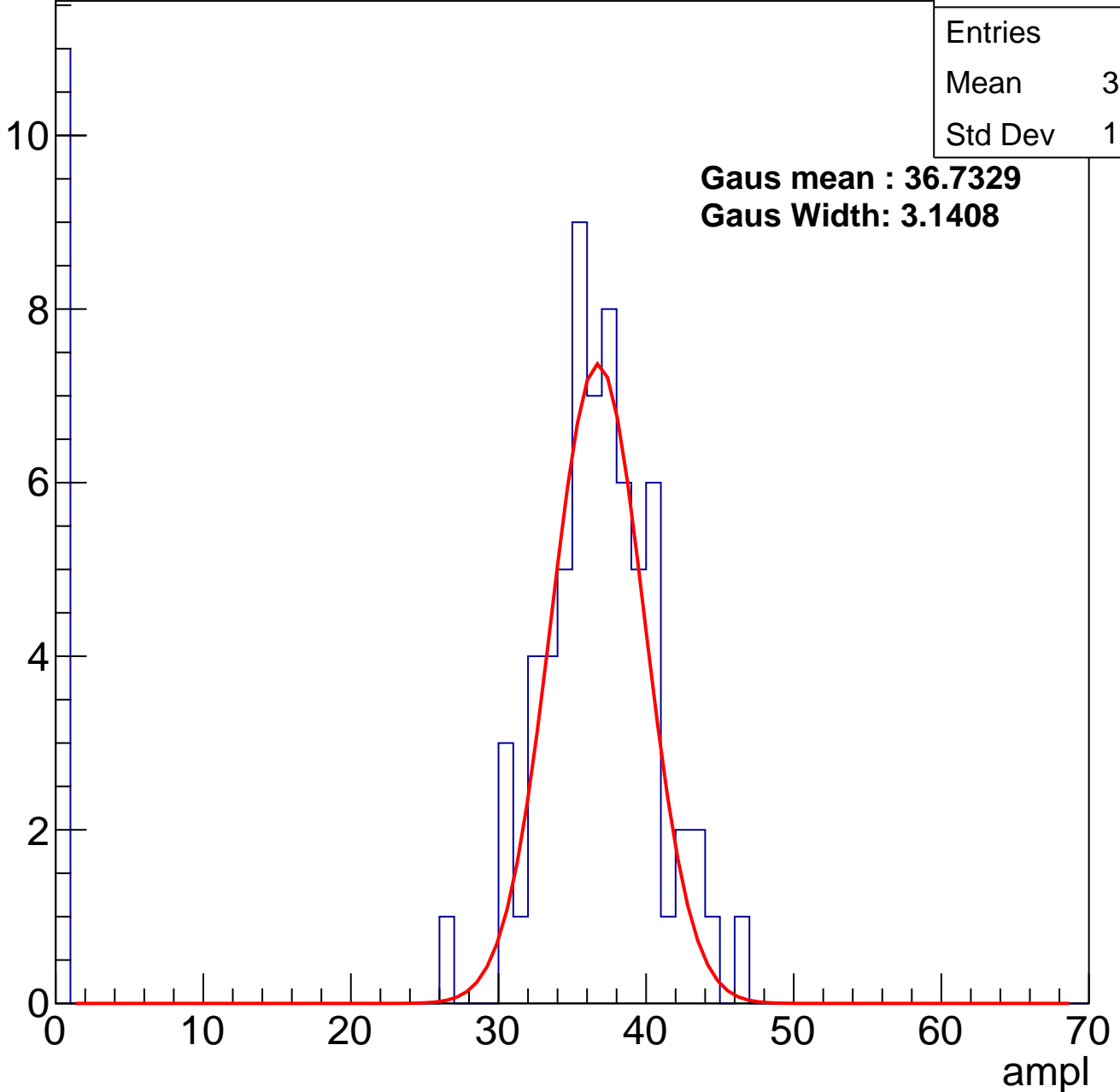
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	77
Mean	31.22
Std Dev	13.19

**Gaus mean : 36.7329**

**Gaus Width: 3.1408**



# B1L103S, U22-ch35, adc2

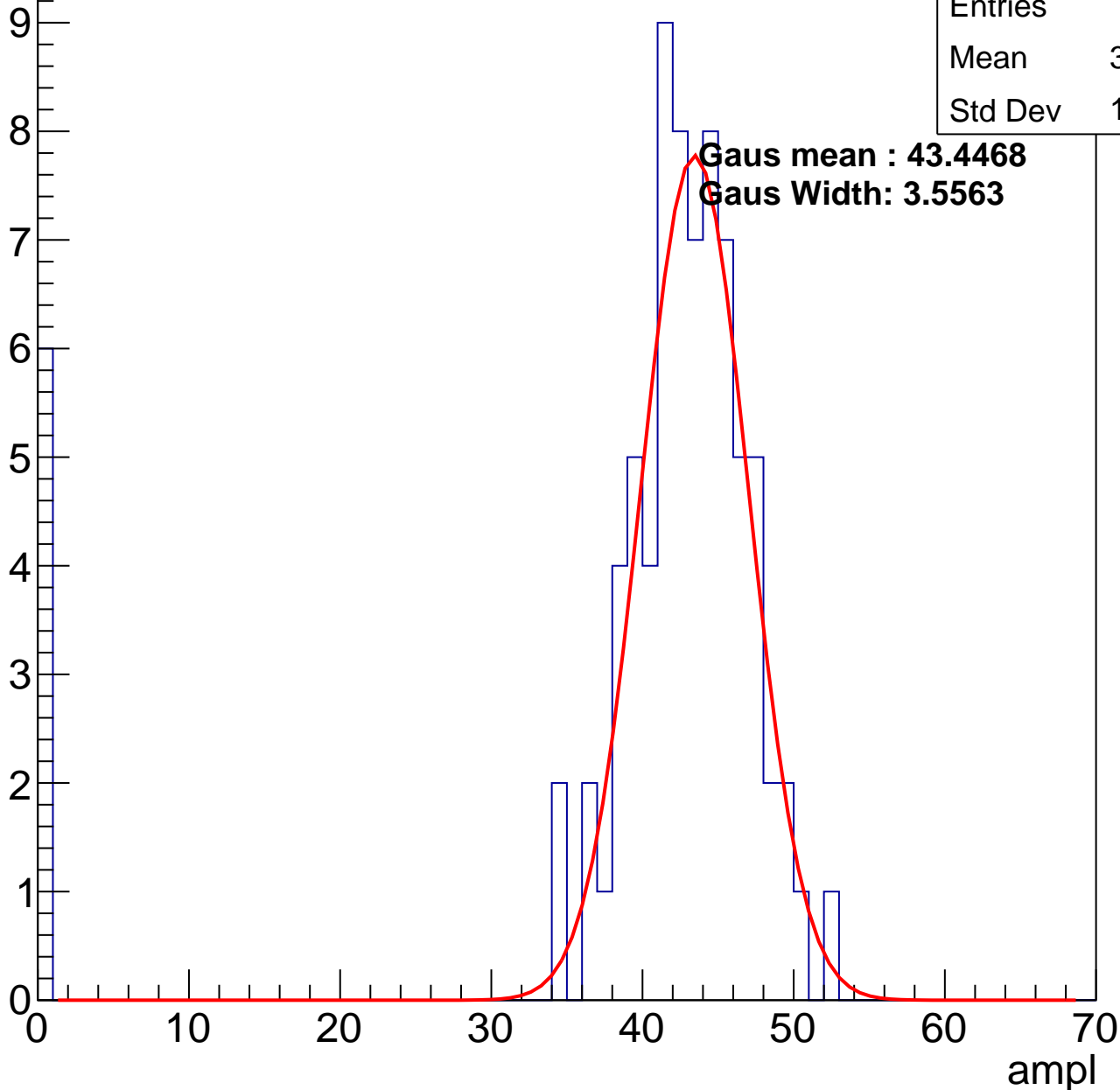
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	39.47
Std Dev	11.84

**Gaus mean : 43.4468**

**Gaus Width: 3.5563**

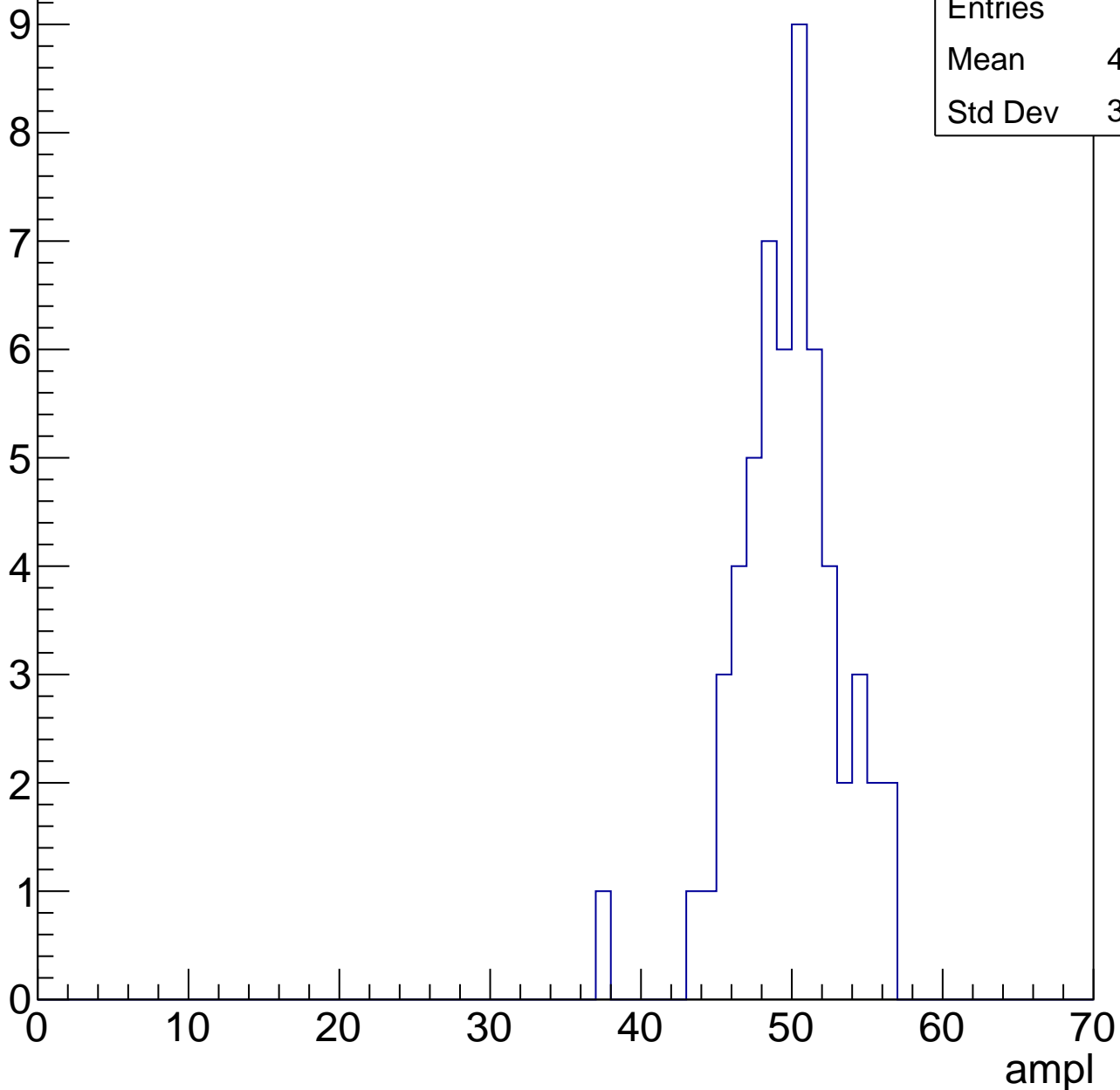


# B1L103S, U22-ch35, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	49.32
Std Dev	3.423

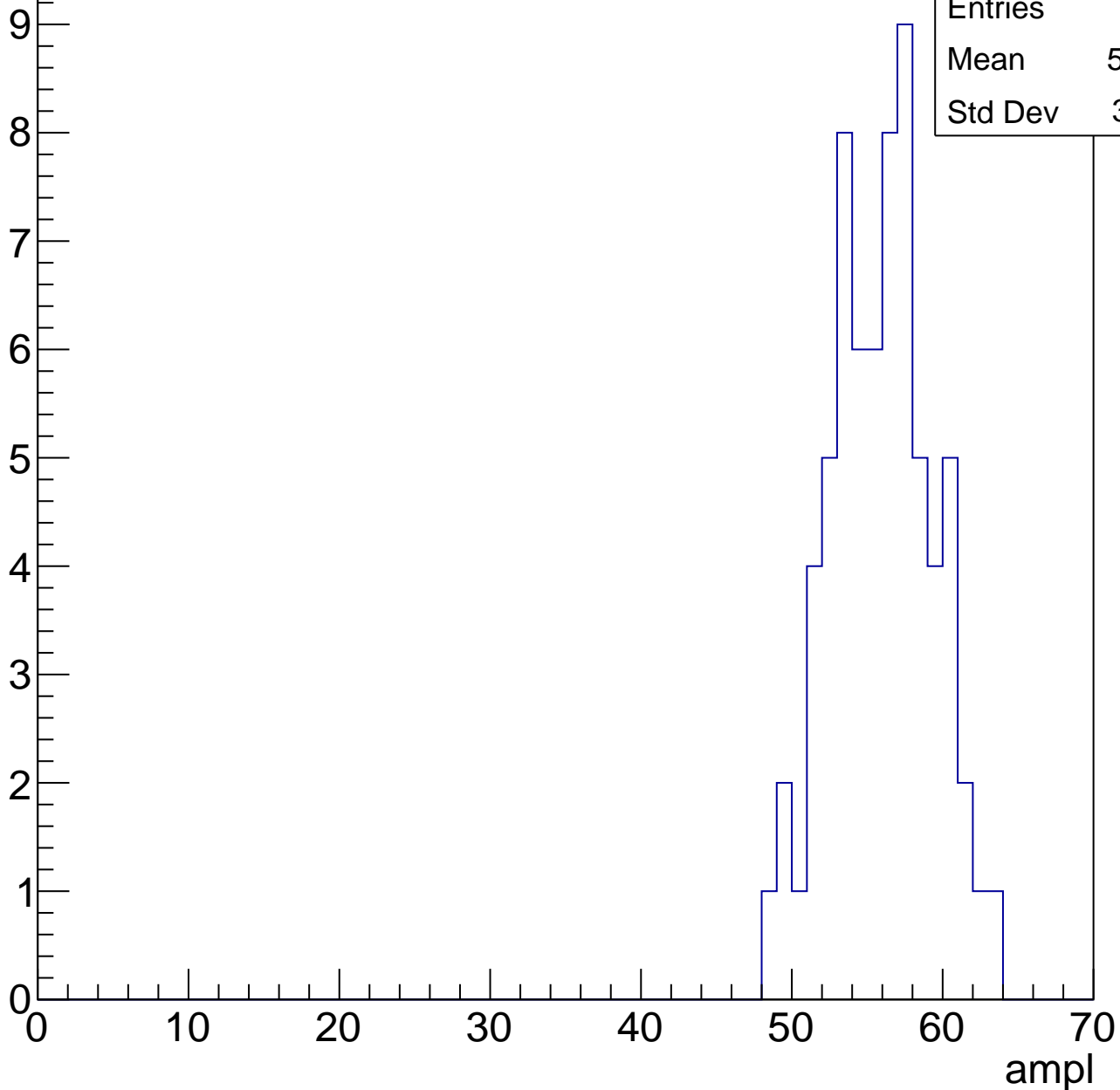


# B1L103S, U22-ch35, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	55.47
Std Dev	3.301

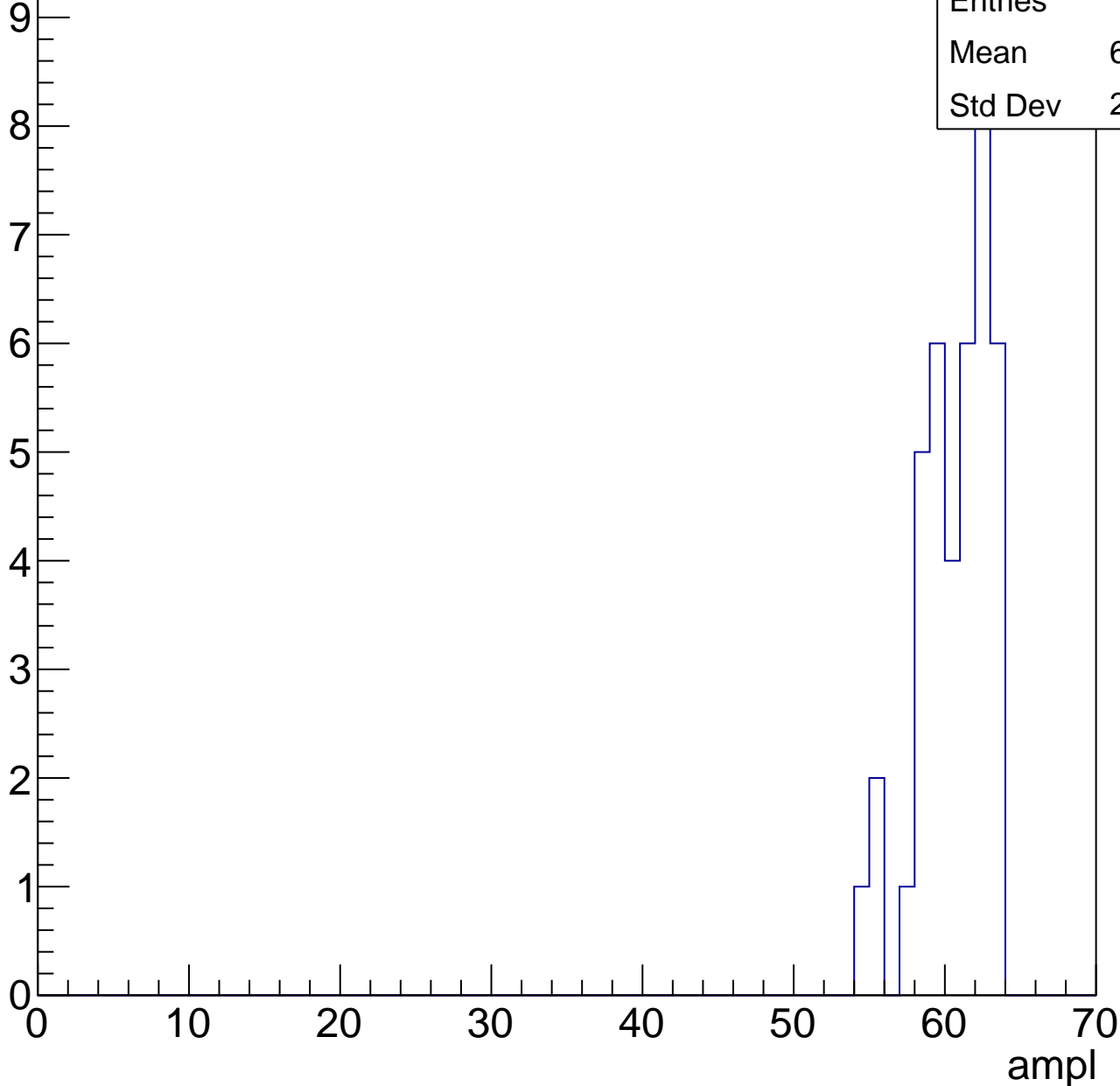


# B1L103S, U22-ch35, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

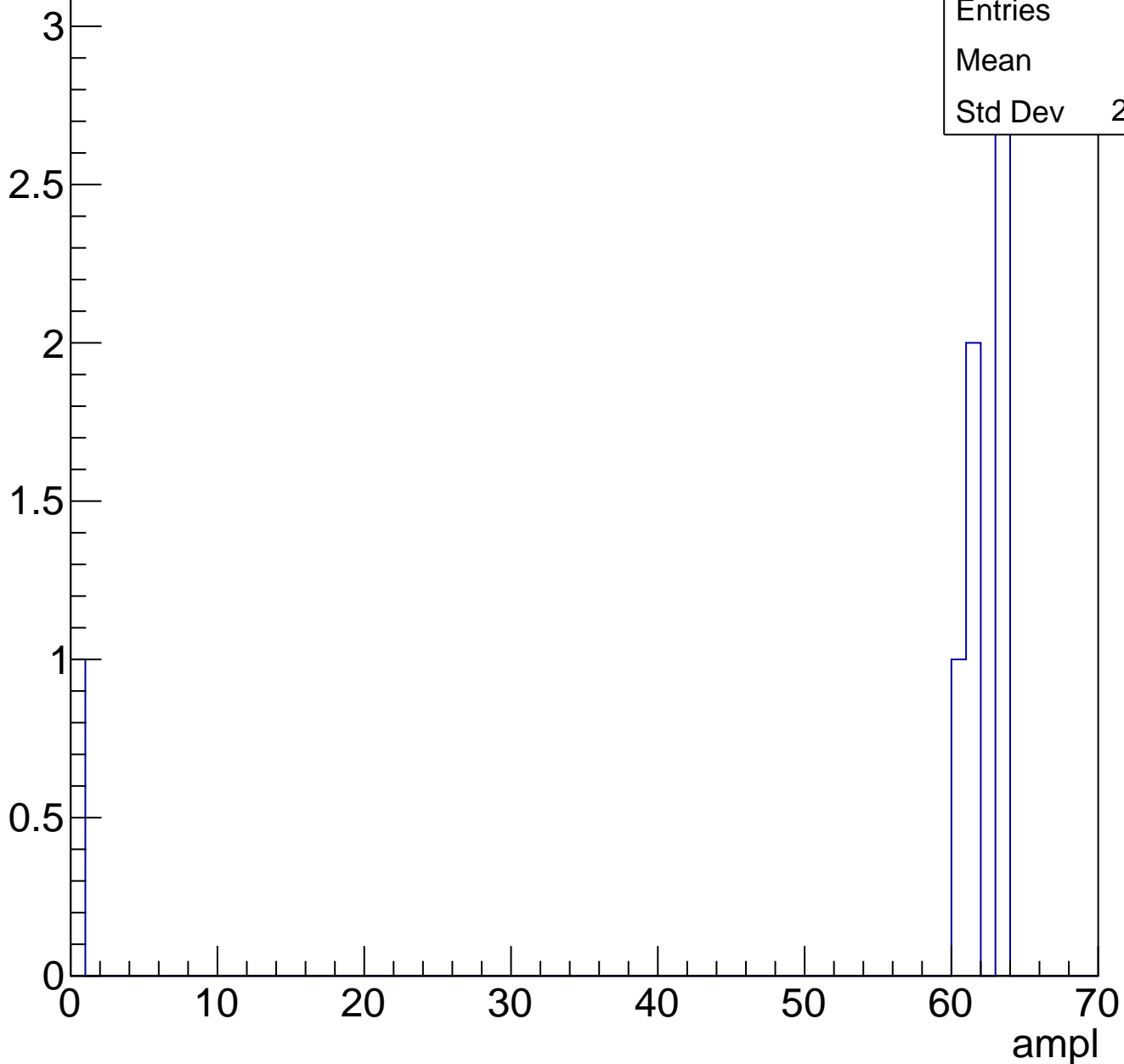
Entries	40
Mean	60.17
Std Dev	2.323



# B1L103S, U22-ch35, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch35, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch36, adc0

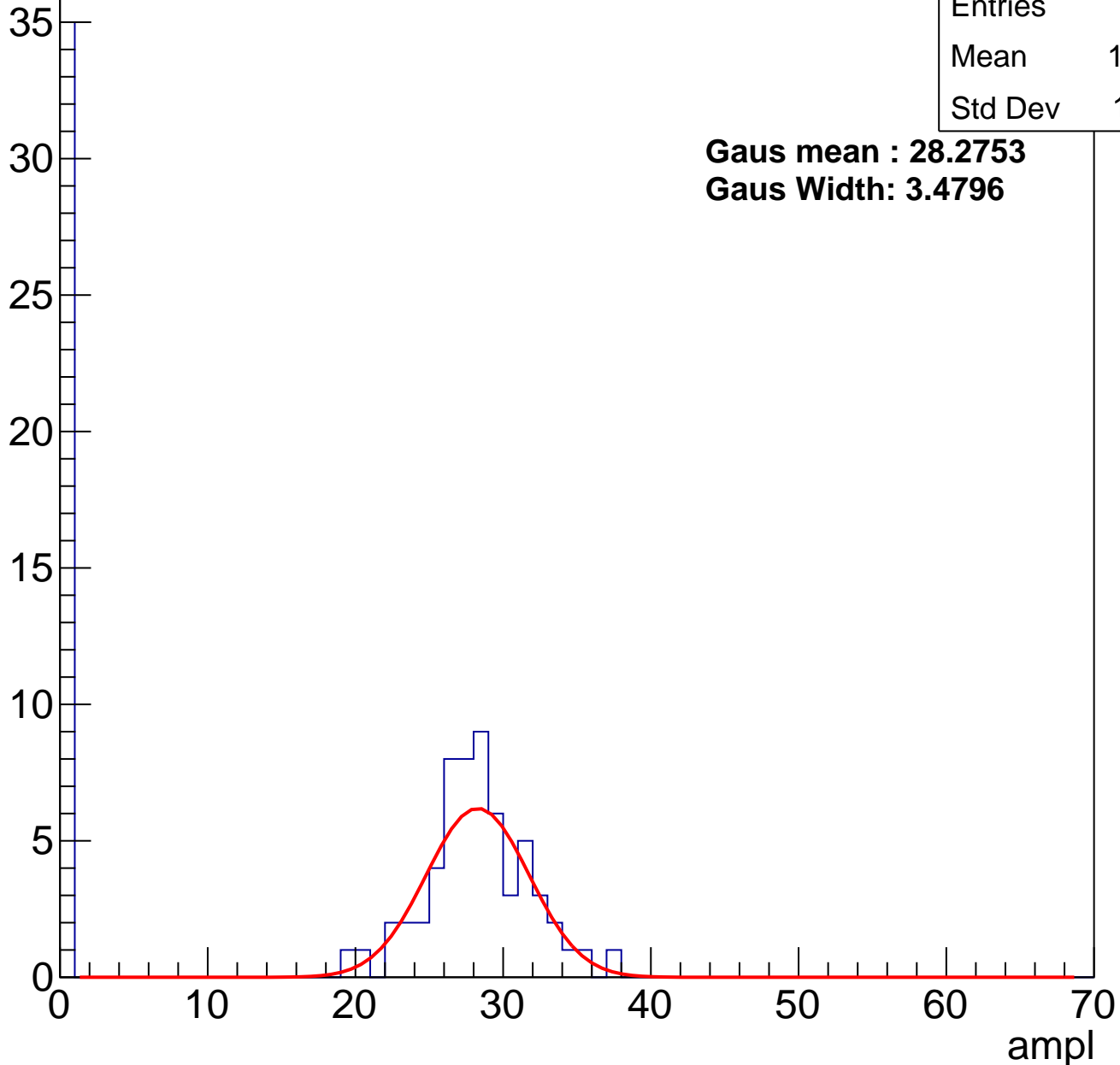
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	94
Mean	17.45
Std Dev	13.71

**Gaus mean : 28.2753**

**Gaus Width: 3.4796**

Entry



# B1L103S, U22-ch36, adc1

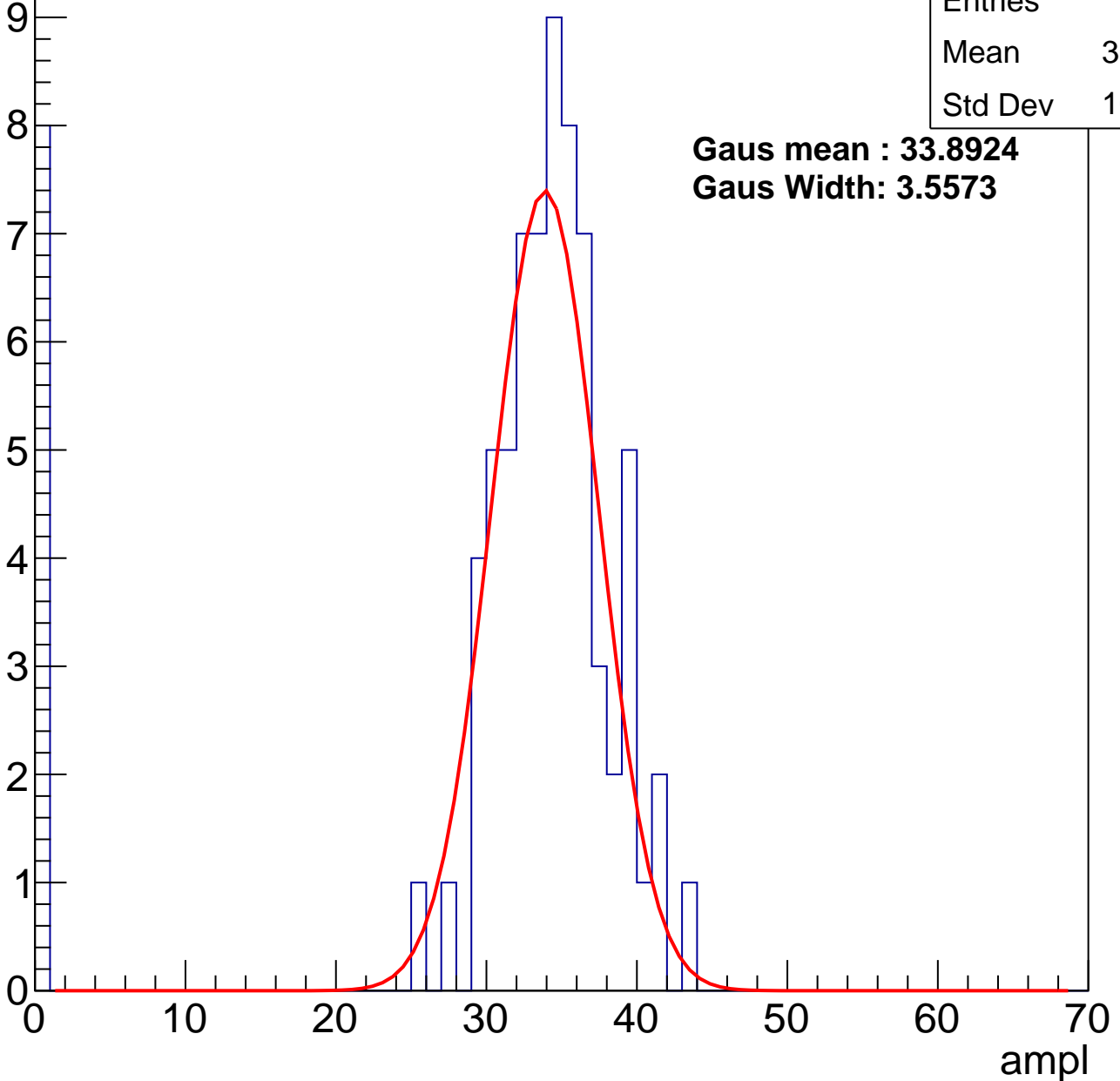
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	30.43
Std Dev	10.95

**Gaus mean : 33.8924**

**Gaus Width: 3.5573**



# B1L103S, U22-ch36, adc2

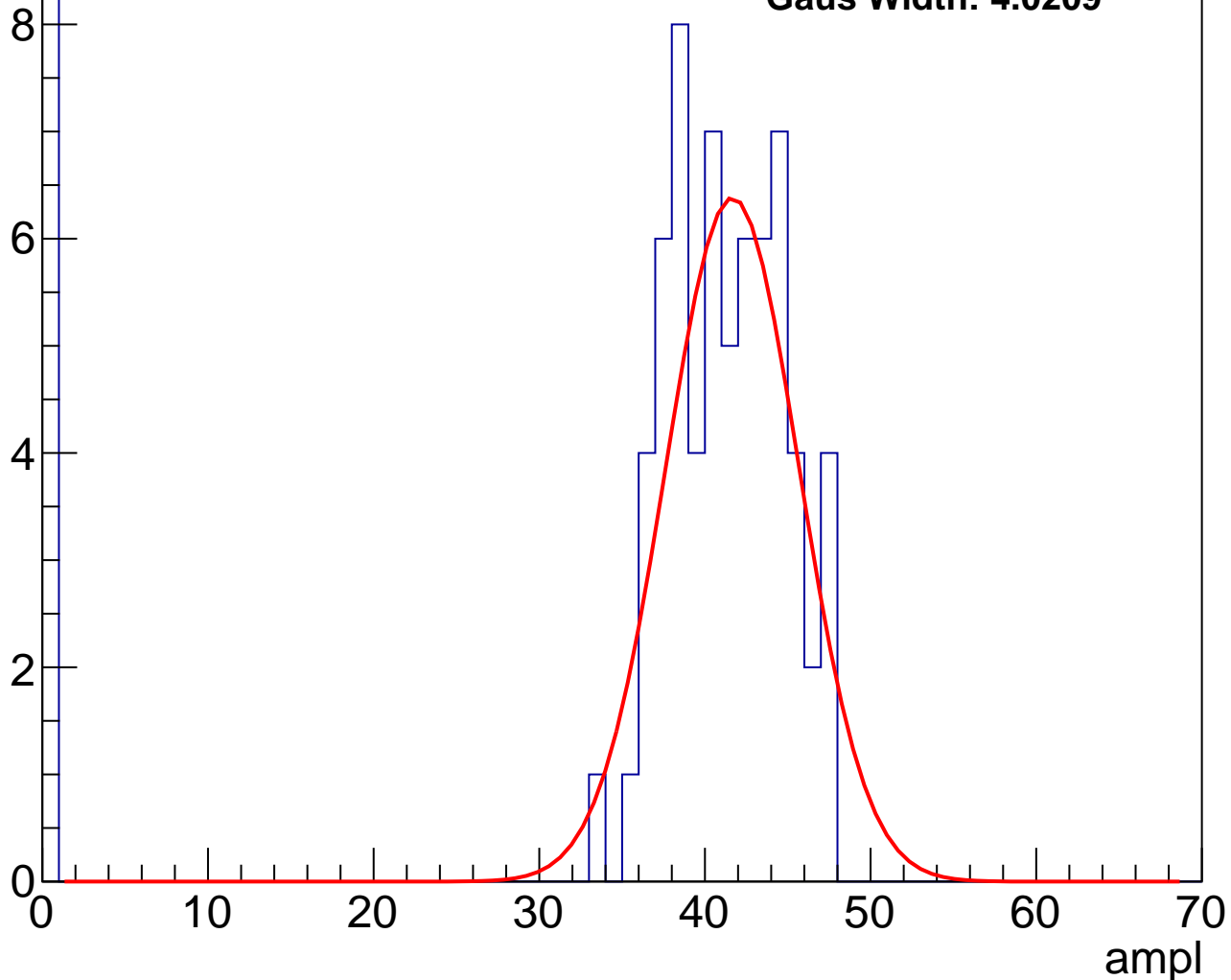
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	35.43
Std Dev	14.25

**Gaus mean : 41.6588**

**Gaus Width: 4.0209**

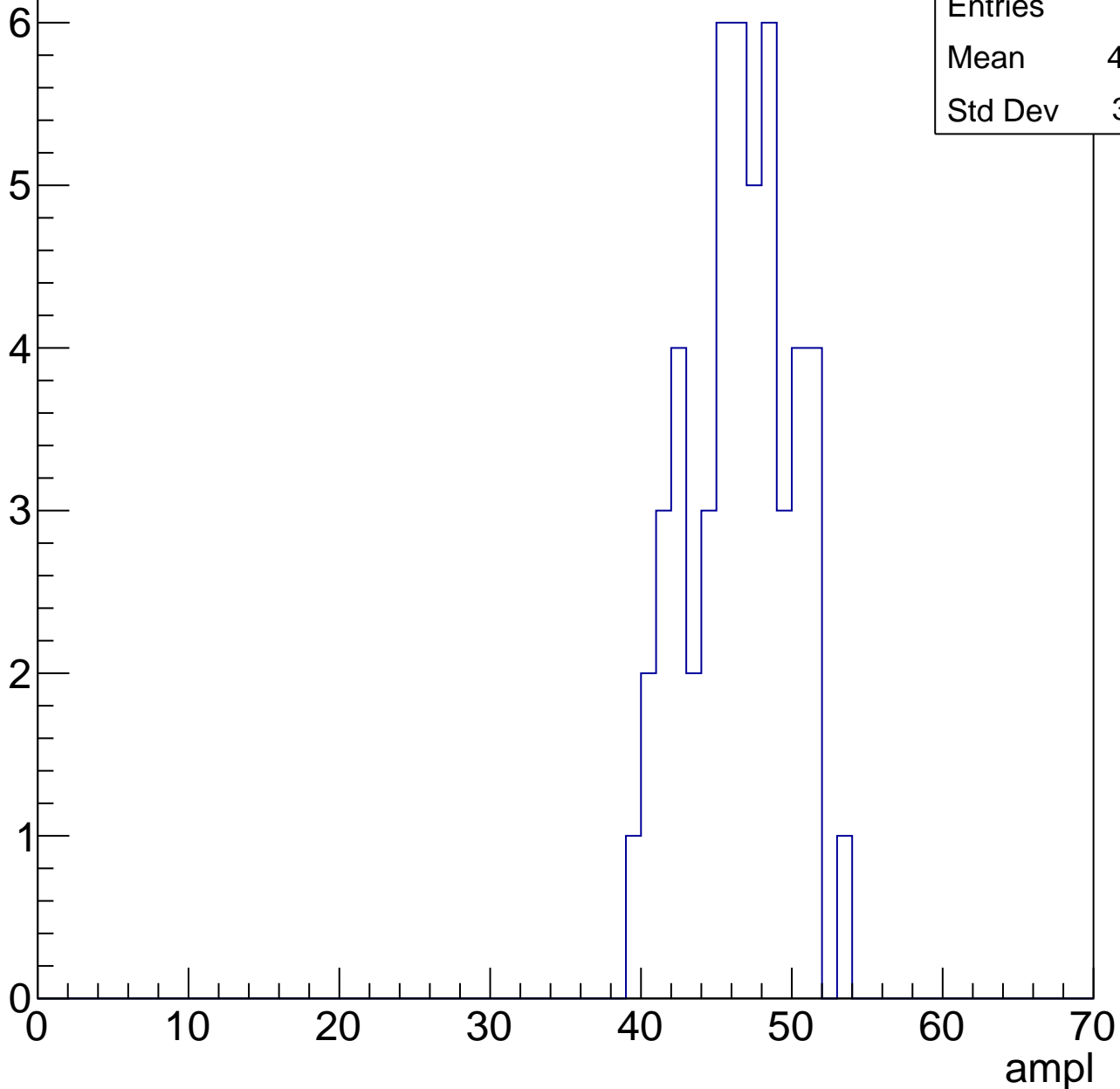


# B1L103S, U22-ch36, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

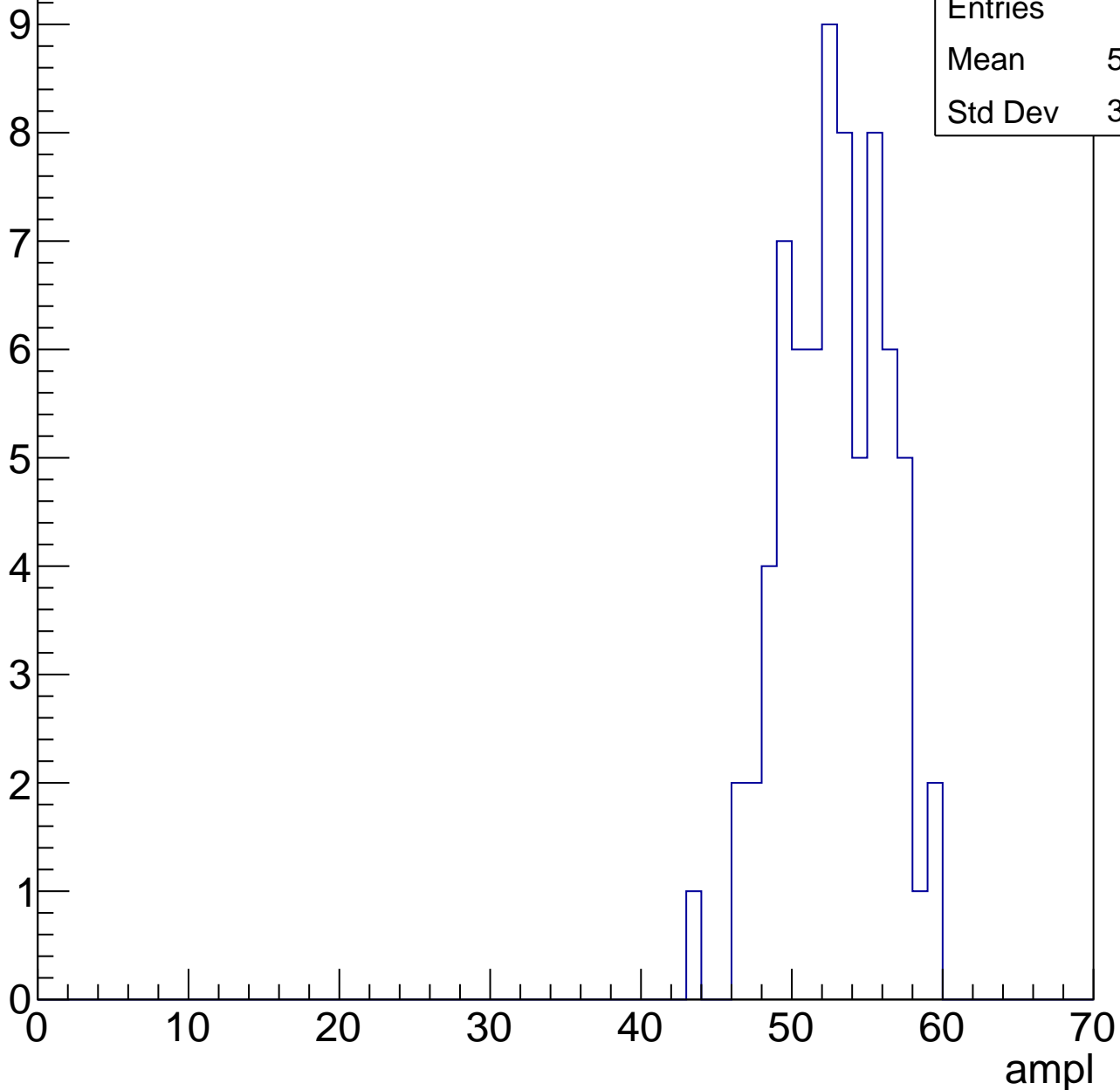
Entries	50
Mean	46.02
Std Dev	3.361



# B1L103S, U22-ch36, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

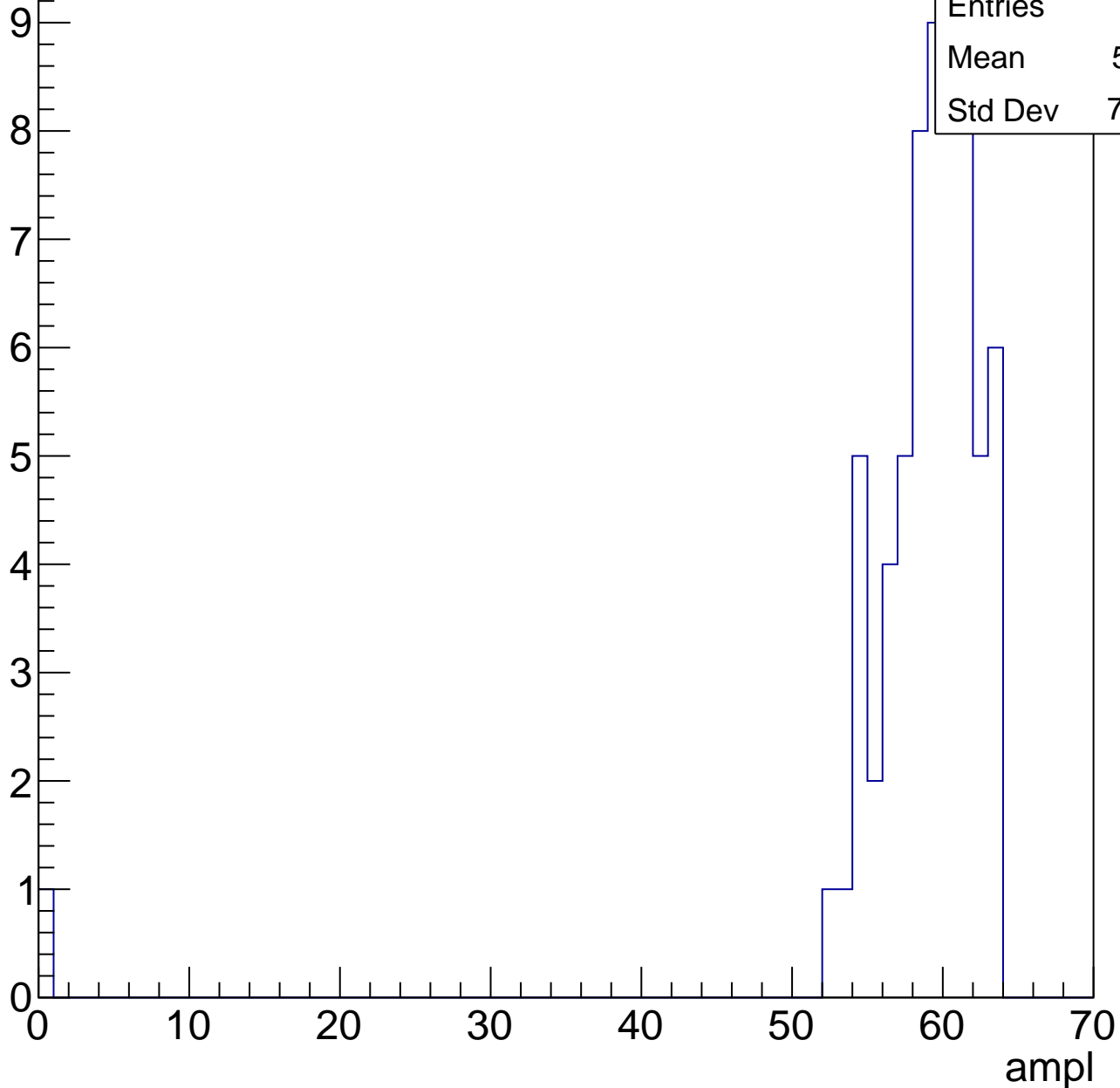
Entry



# B1L103S, U22-ch36, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

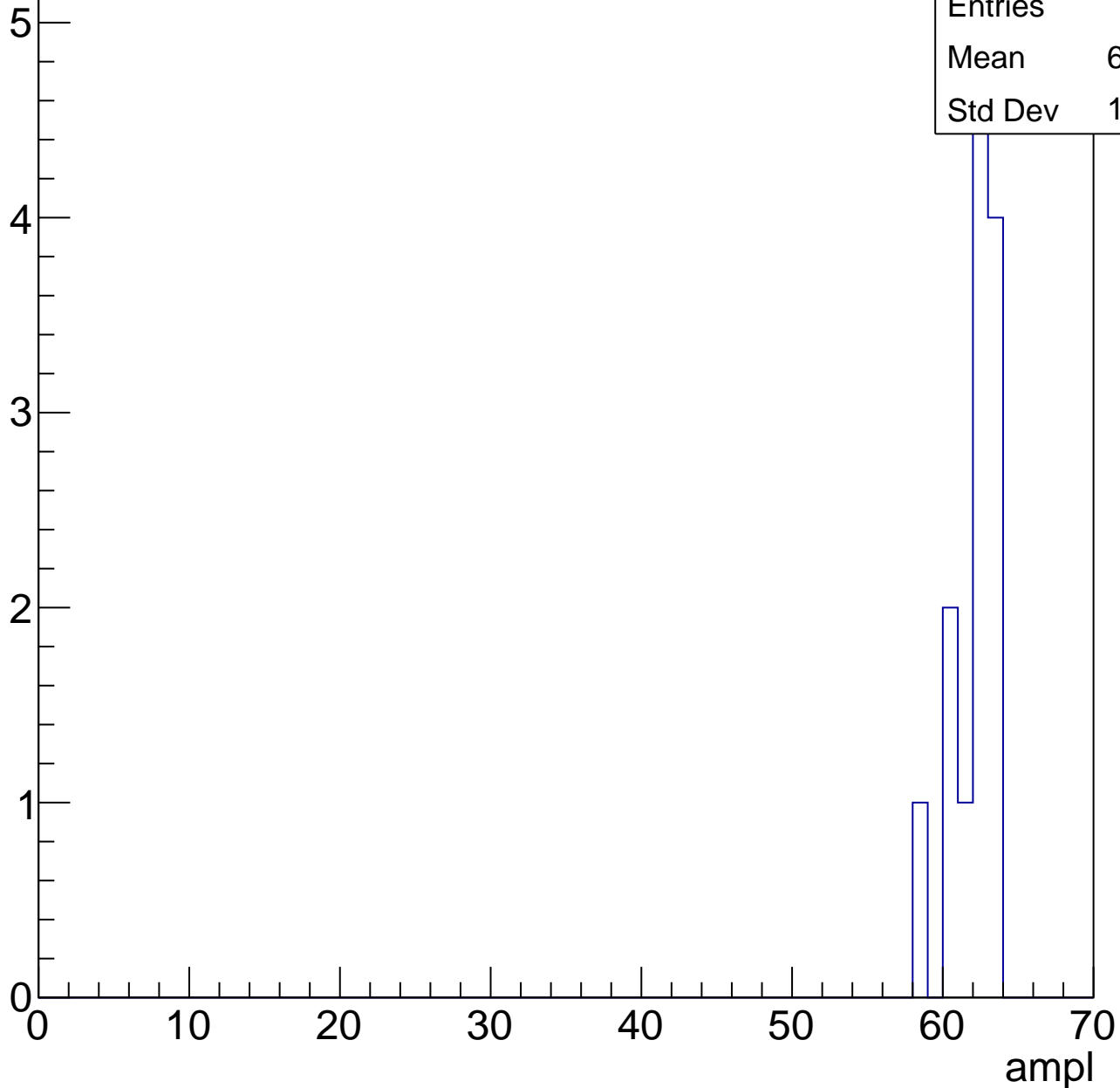


# B1L103S, U22-ch36, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	13
Mean	61.62
Std Dev	1.443





# B1L103S, U22-ch36, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch37, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	96
Mean	27.83
Std Dev	8.079

**Gaus mean : 29.9590**

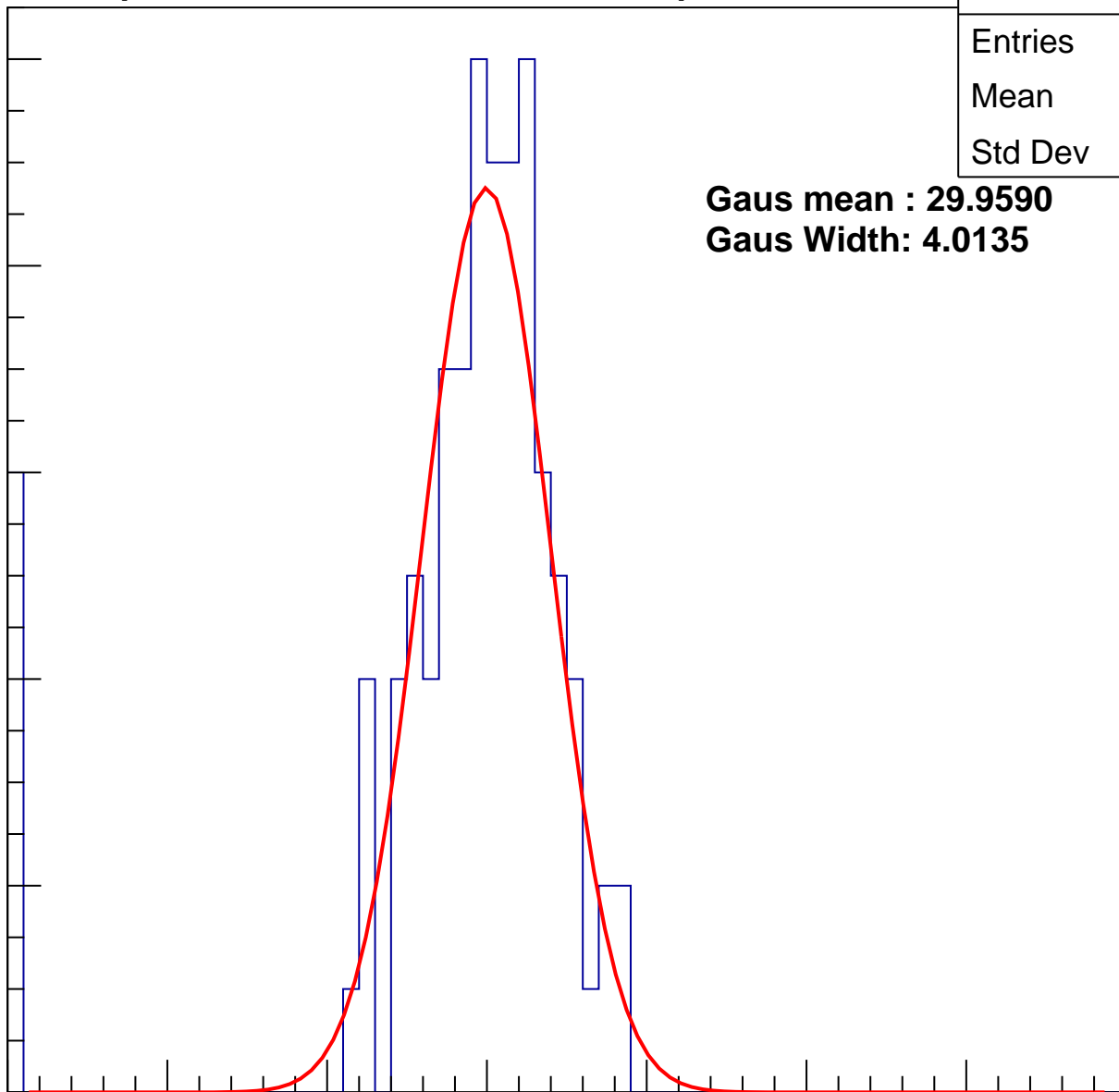
**Gaus Width: 4.0135**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch37, adc1

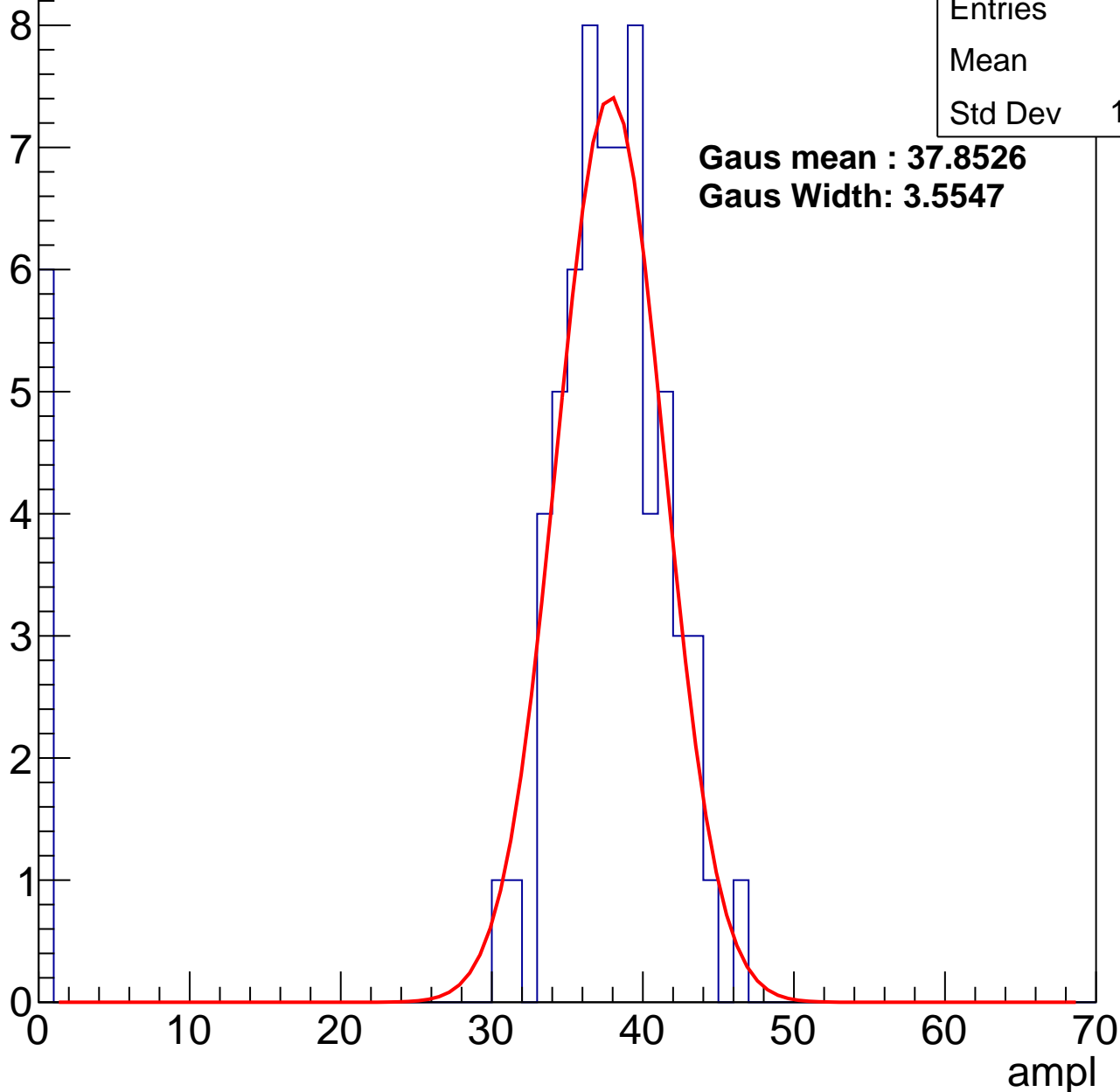
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.4
Std Dev	10.98

**Gaus mean : 37.8526**

**Gaus Width: 3.5547**



# B1L103S, U22-ch37, adc2

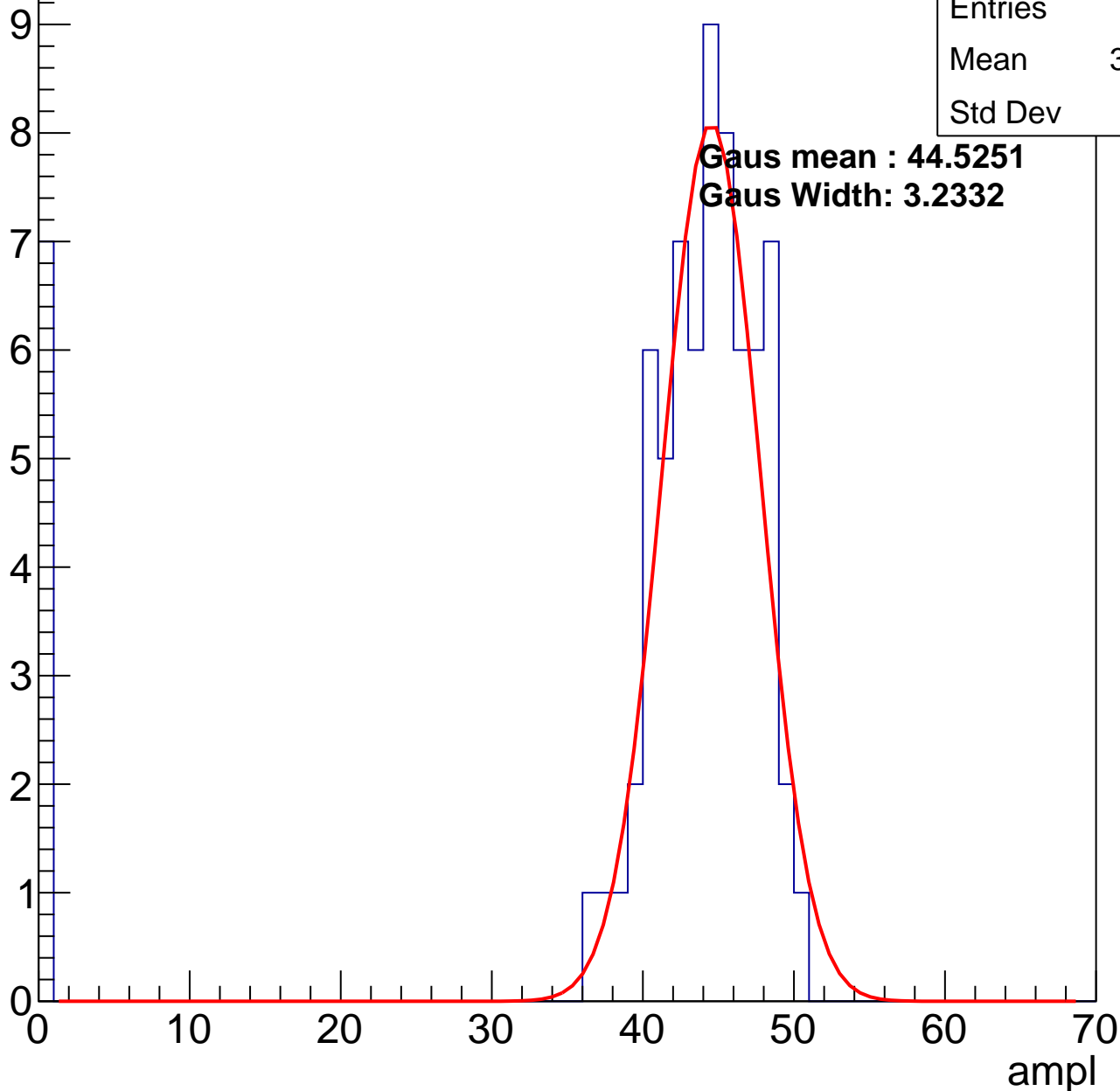
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	39.79
Std Dev	13.1

**Gaus mean : 44.5251**

**Gaus Width: 3.2332**

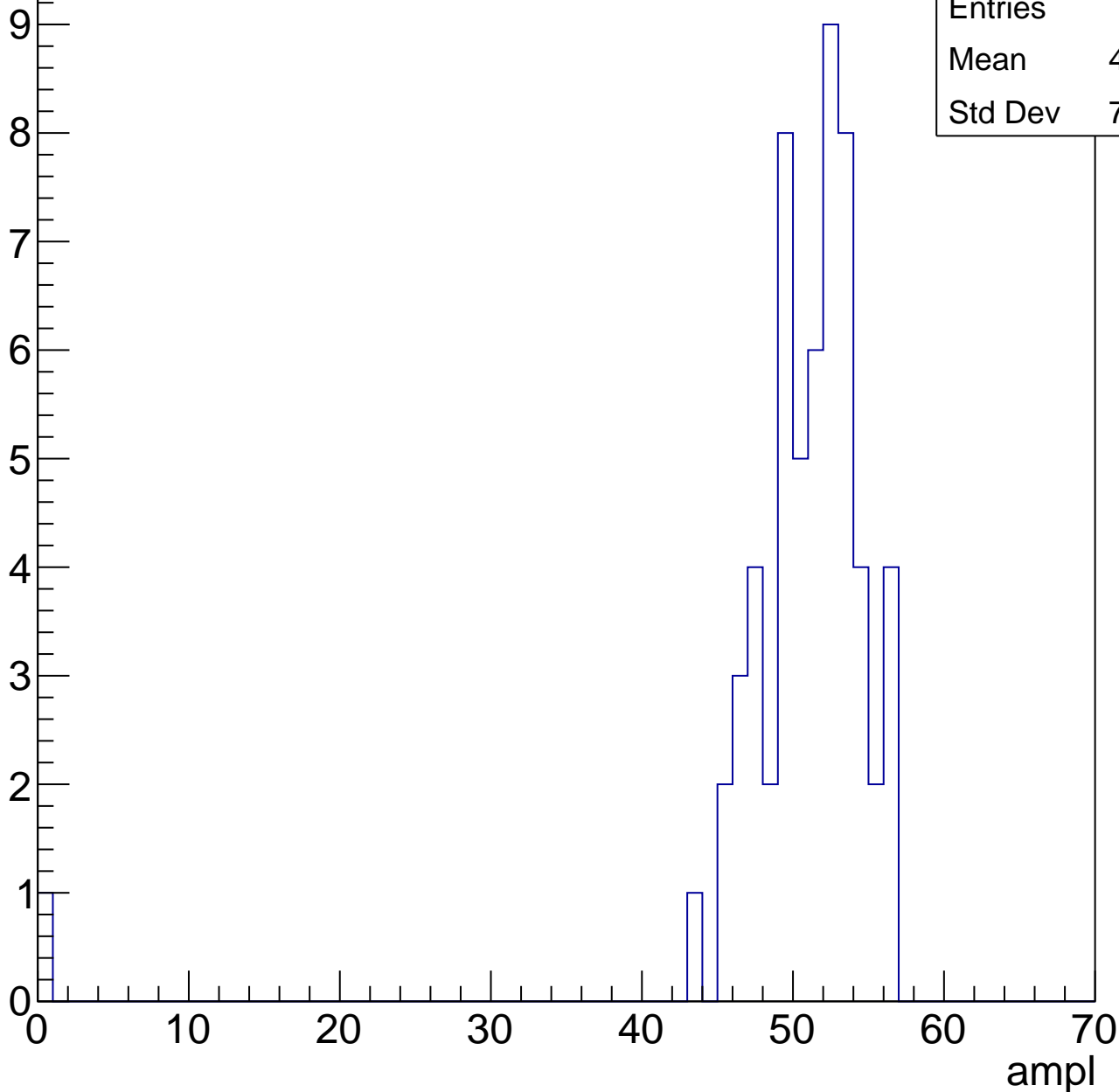


# B1L103S, U22-ch37, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	49.92
Std Dev	7.214

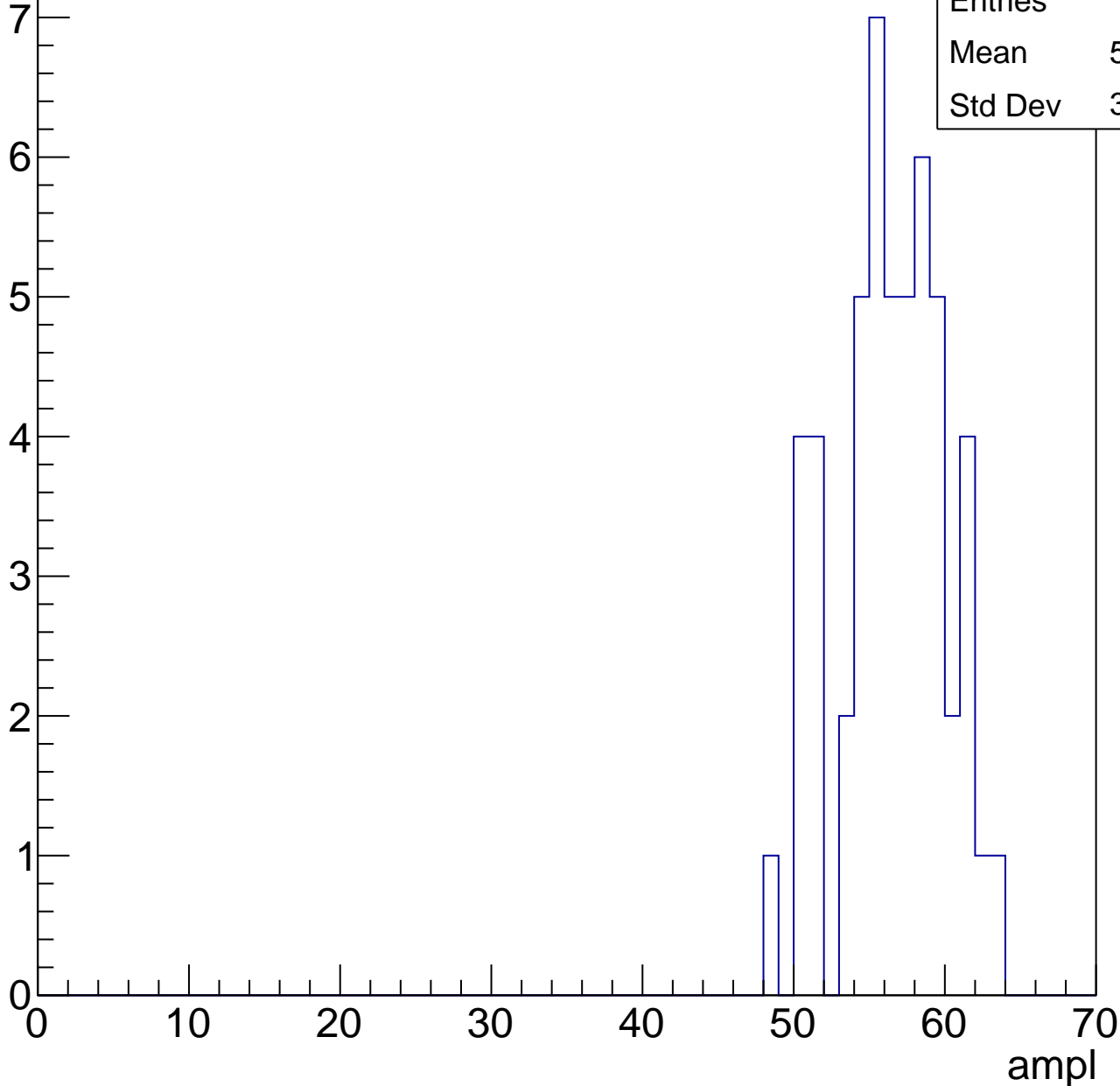


# B1L103S, U22-ch37, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

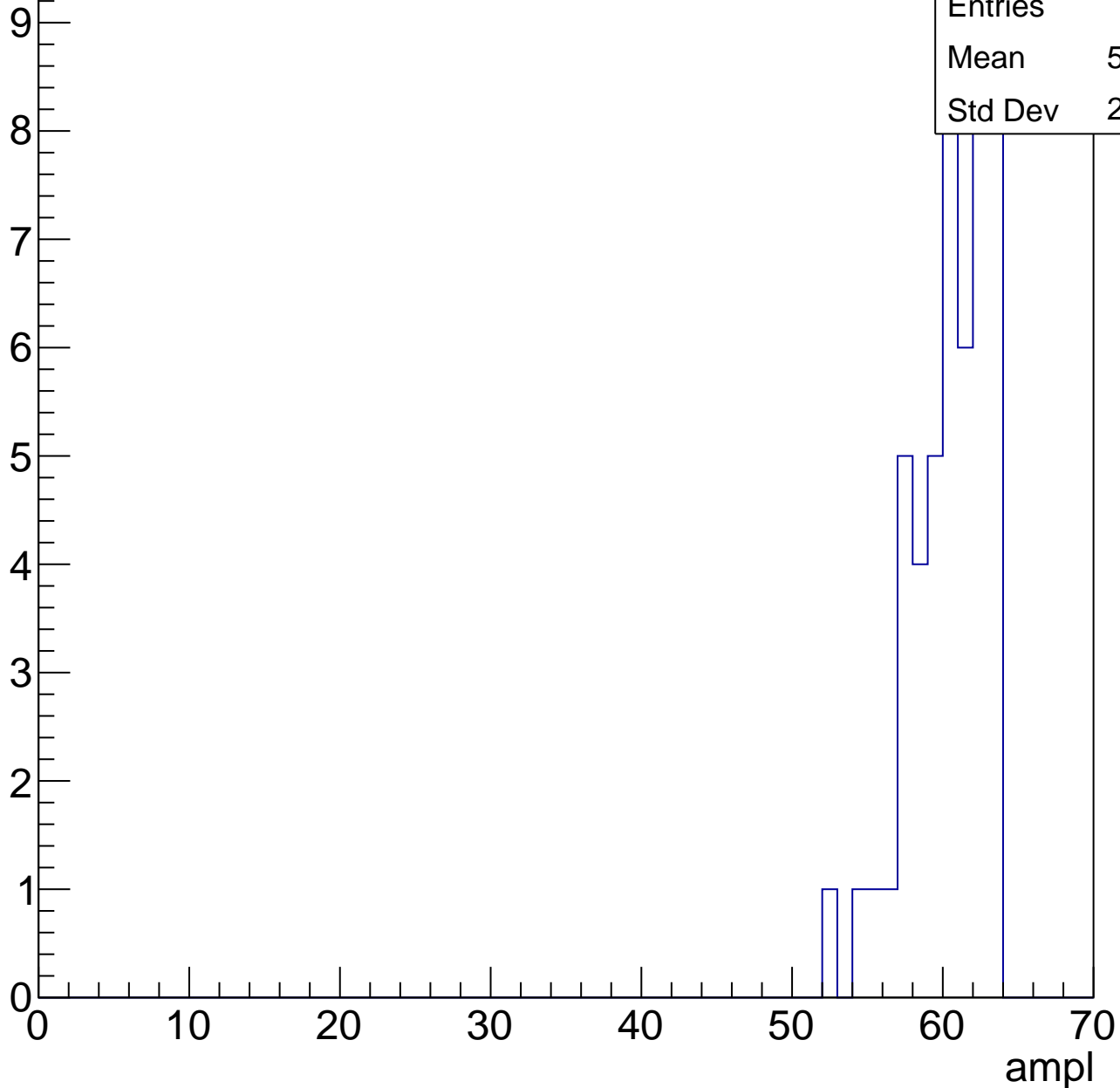
Entries	52
Mean	55.96
Std Dev	3.519



# B1L103S, U22-ch37, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

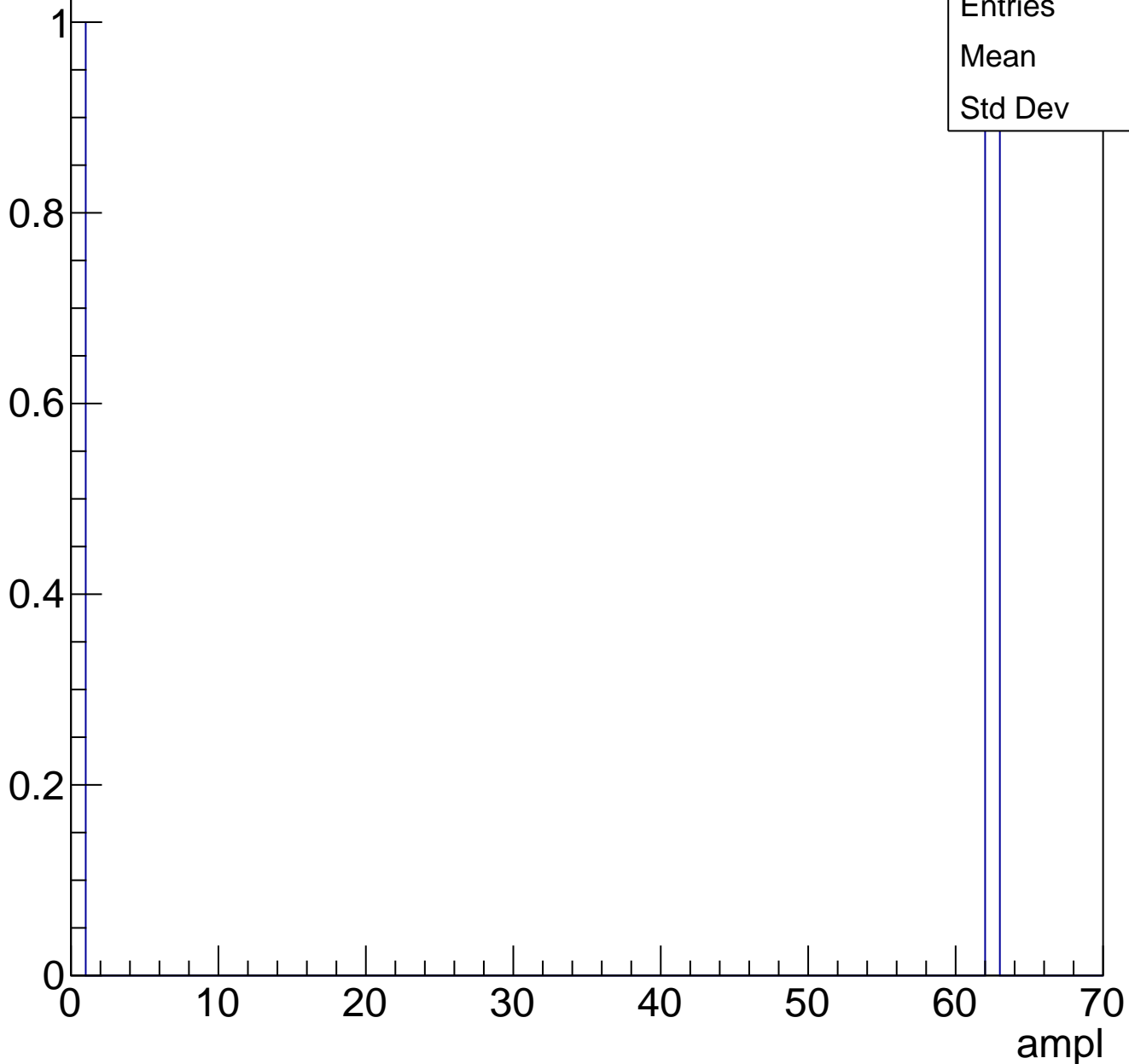
Entry



# B1L103S, U22-ch37, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch37, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch38, adc0

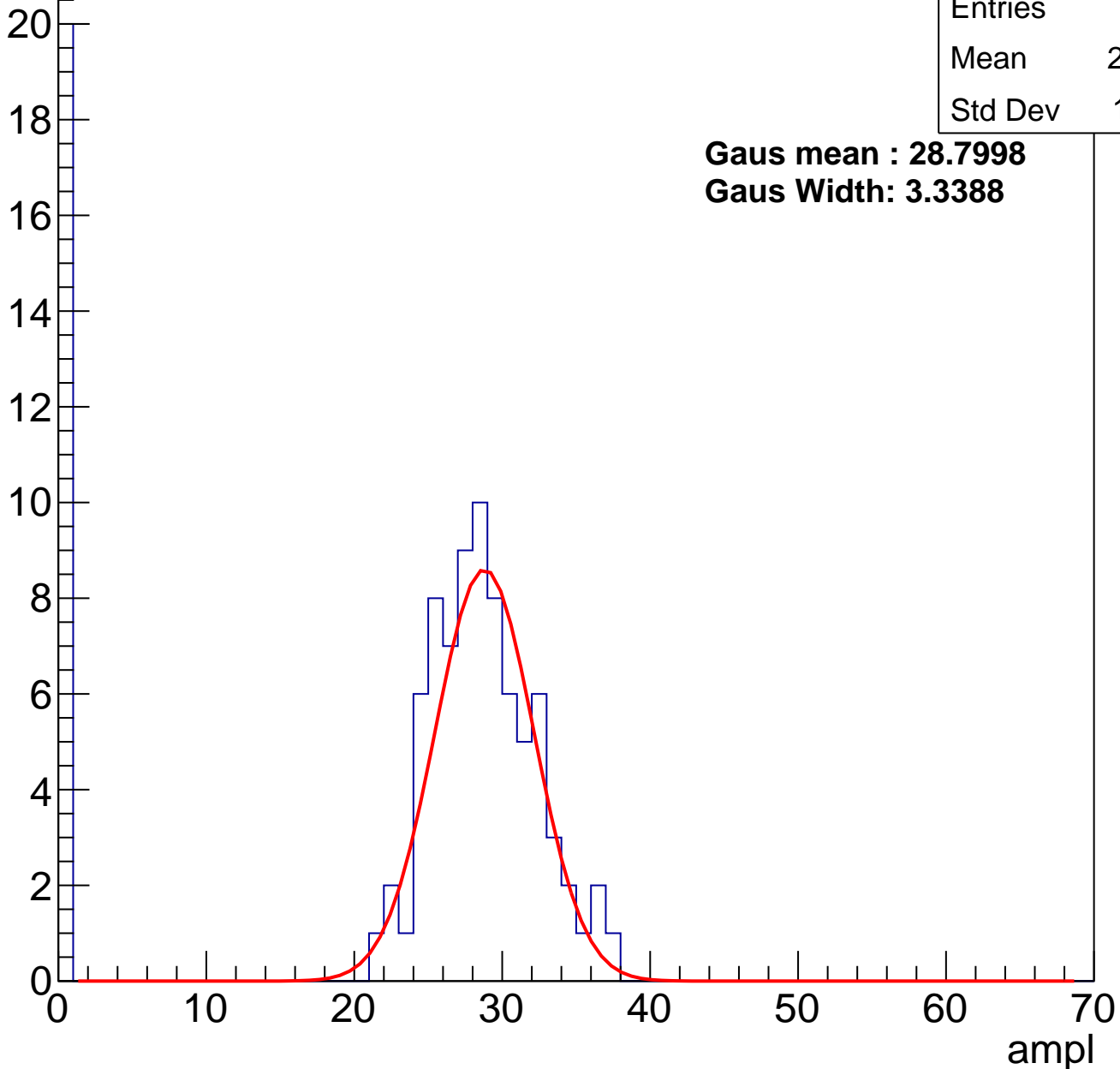
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	98
Mean	22.52
Std Dev	11.81

**Gaus mean : 28.7998**

**Gaus Width: 3.3388**

Entry



# B1L103S, U22-ch38, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	30.05
Std Dev	12.63

**Gaus mean : 35.6884**

**Gaus Width: 3.8263**

Entry

10

8

6

4

2

0

0

10

20

30

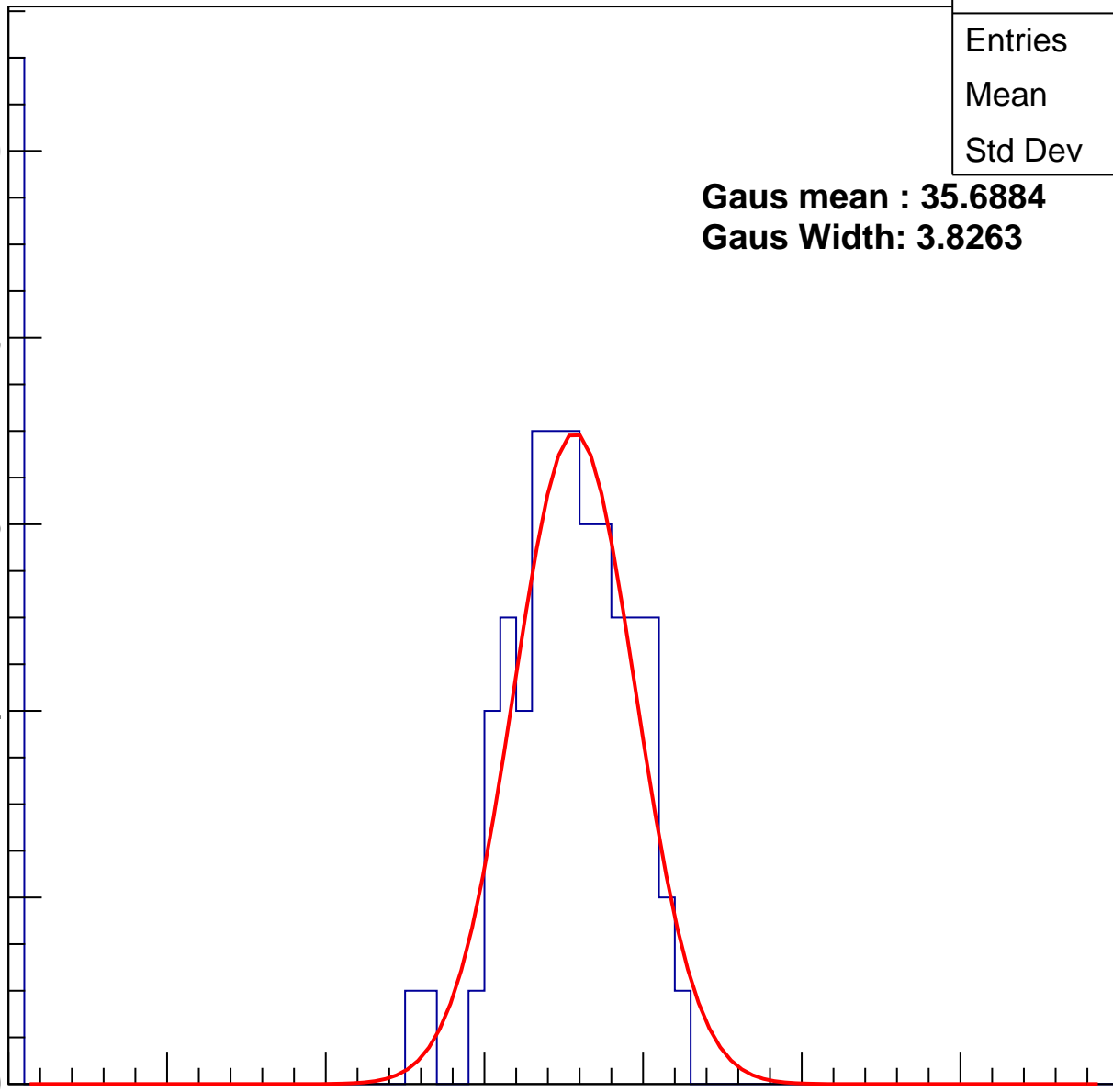
40

50

60

70

ampl



# B1L103S, U22-ch38, adc2

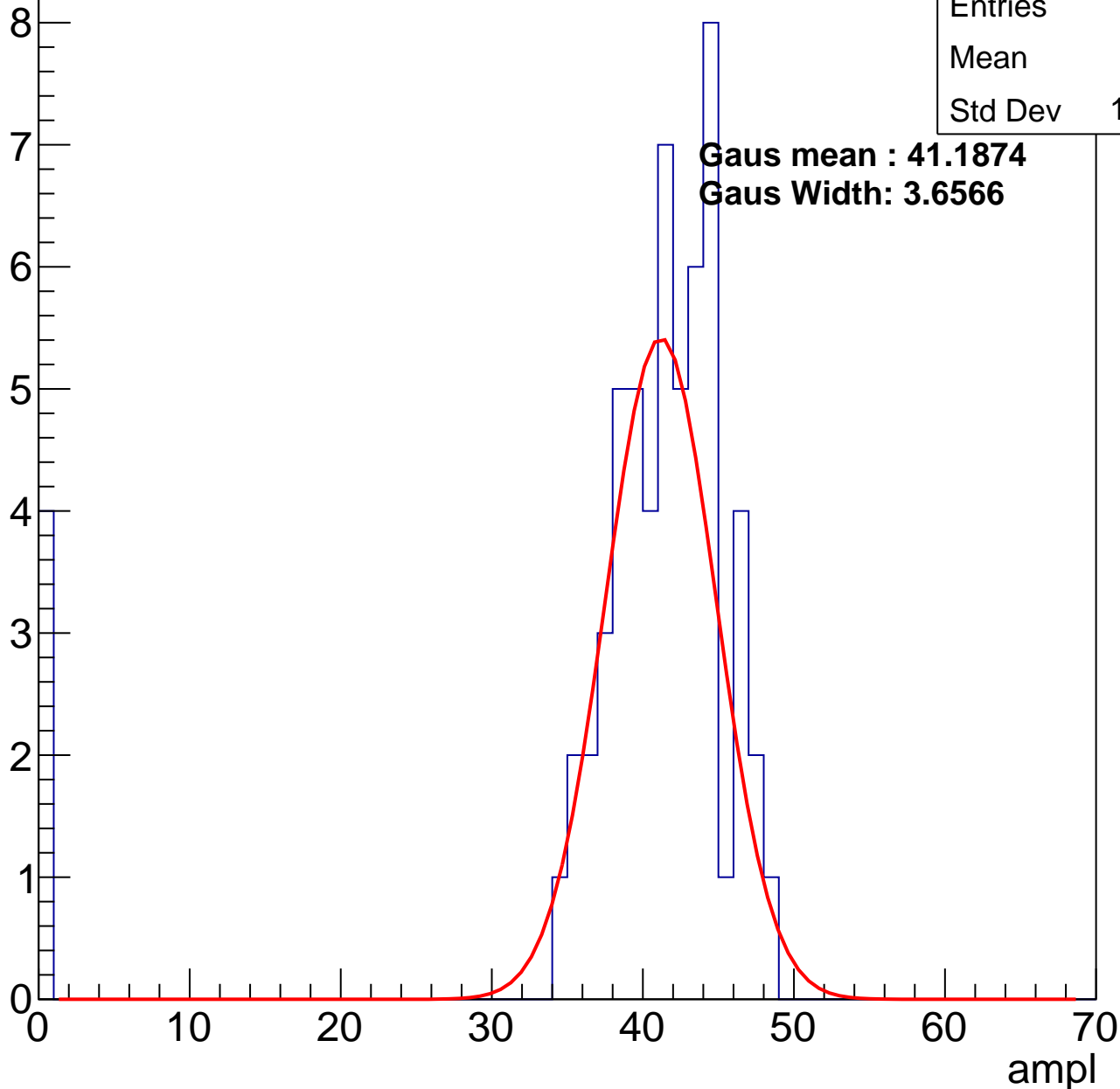
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.5
Std Dev	10.78

**Gaus mean : 41.1874**

**Gaus Width: 3.6566**

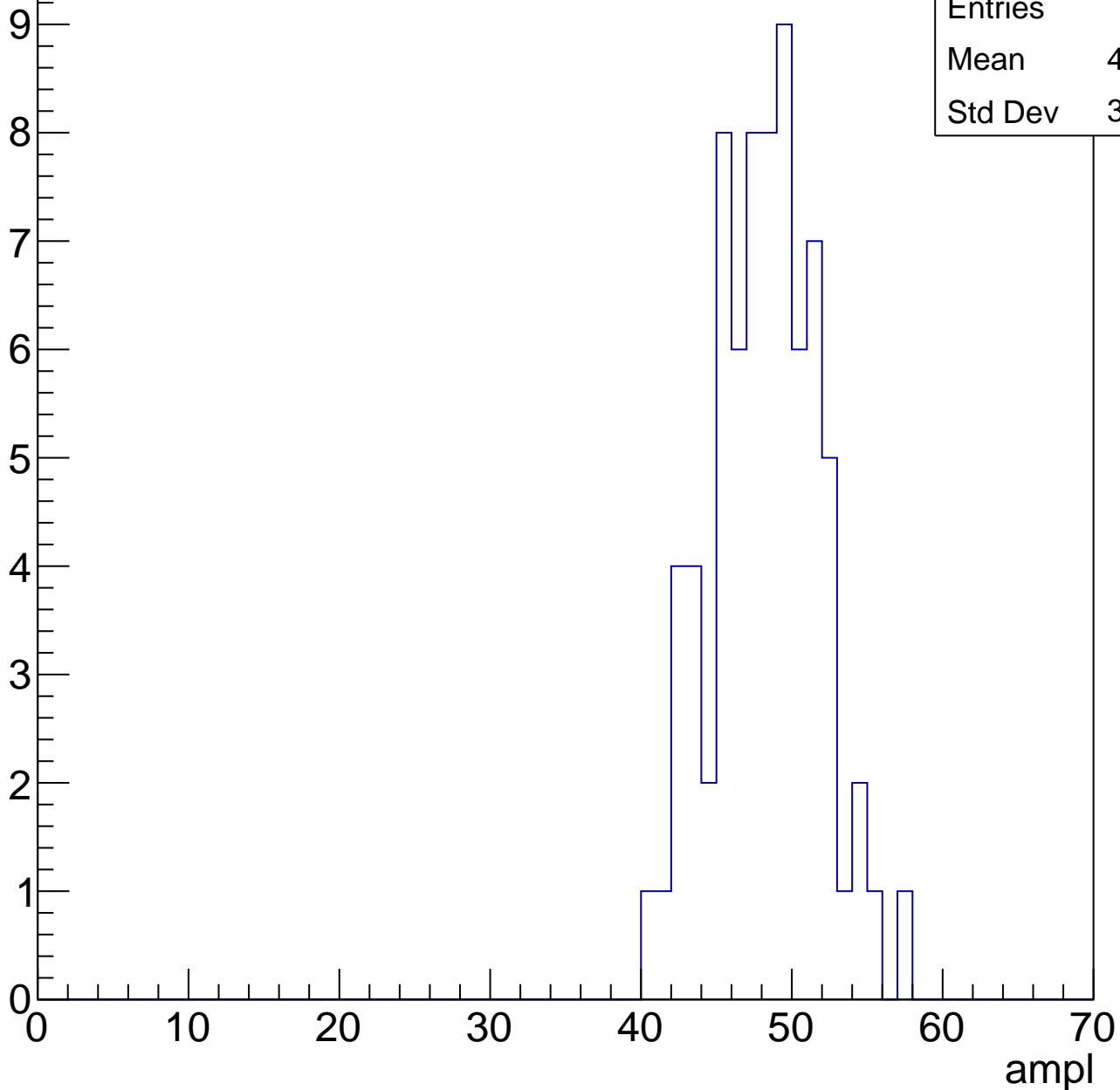


# B1L103S, U22-ch38, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	47.78
Std Dev	3.473



# B1L103S, U22-ch38, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

7

6

5

4

3

2

1

0

Entries 46

Mean 53.91

Std Dev 3.035

ampl

0

10

20

30

40

50

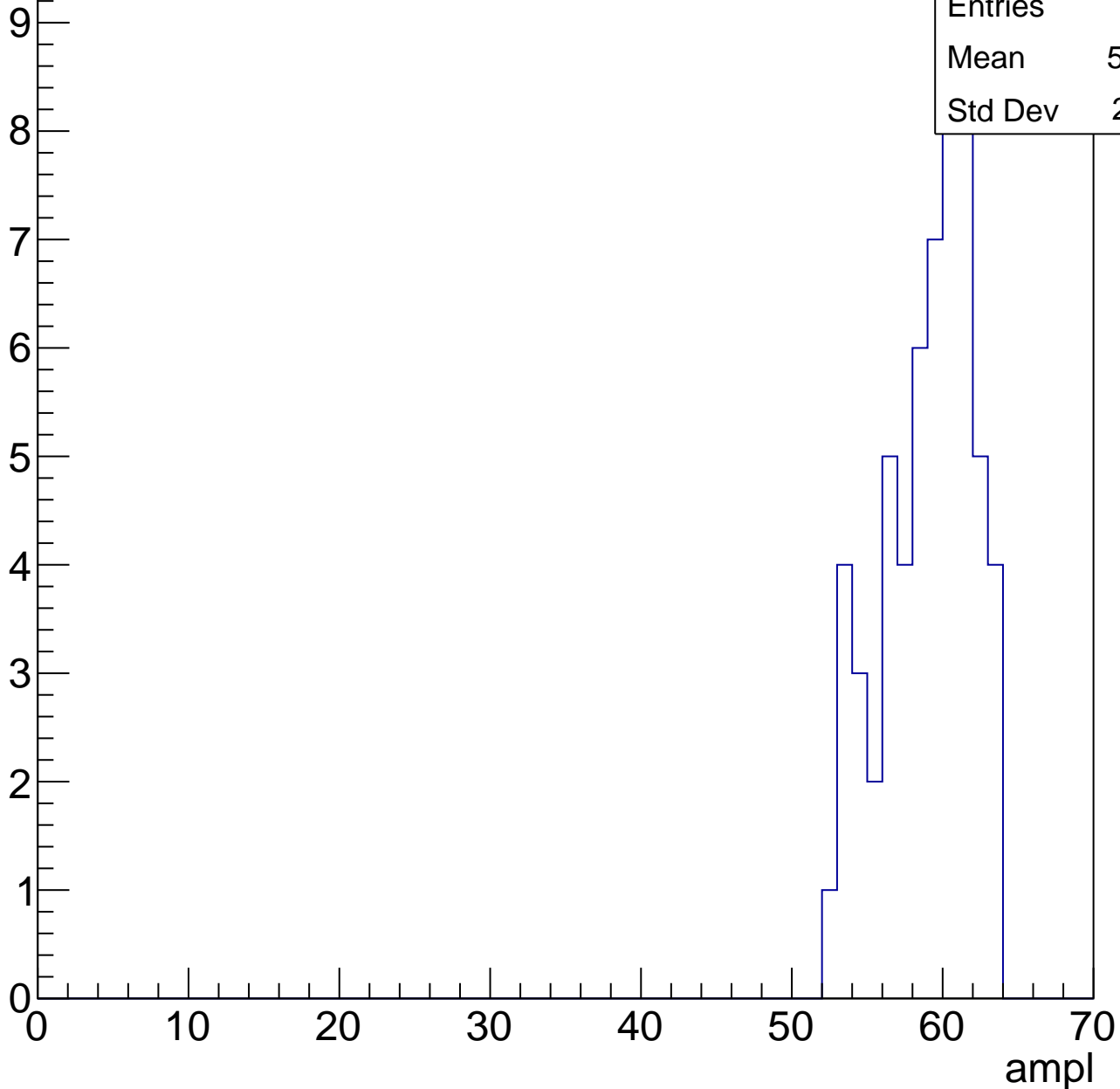
60

70

# B1L103S, U22-ch38, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



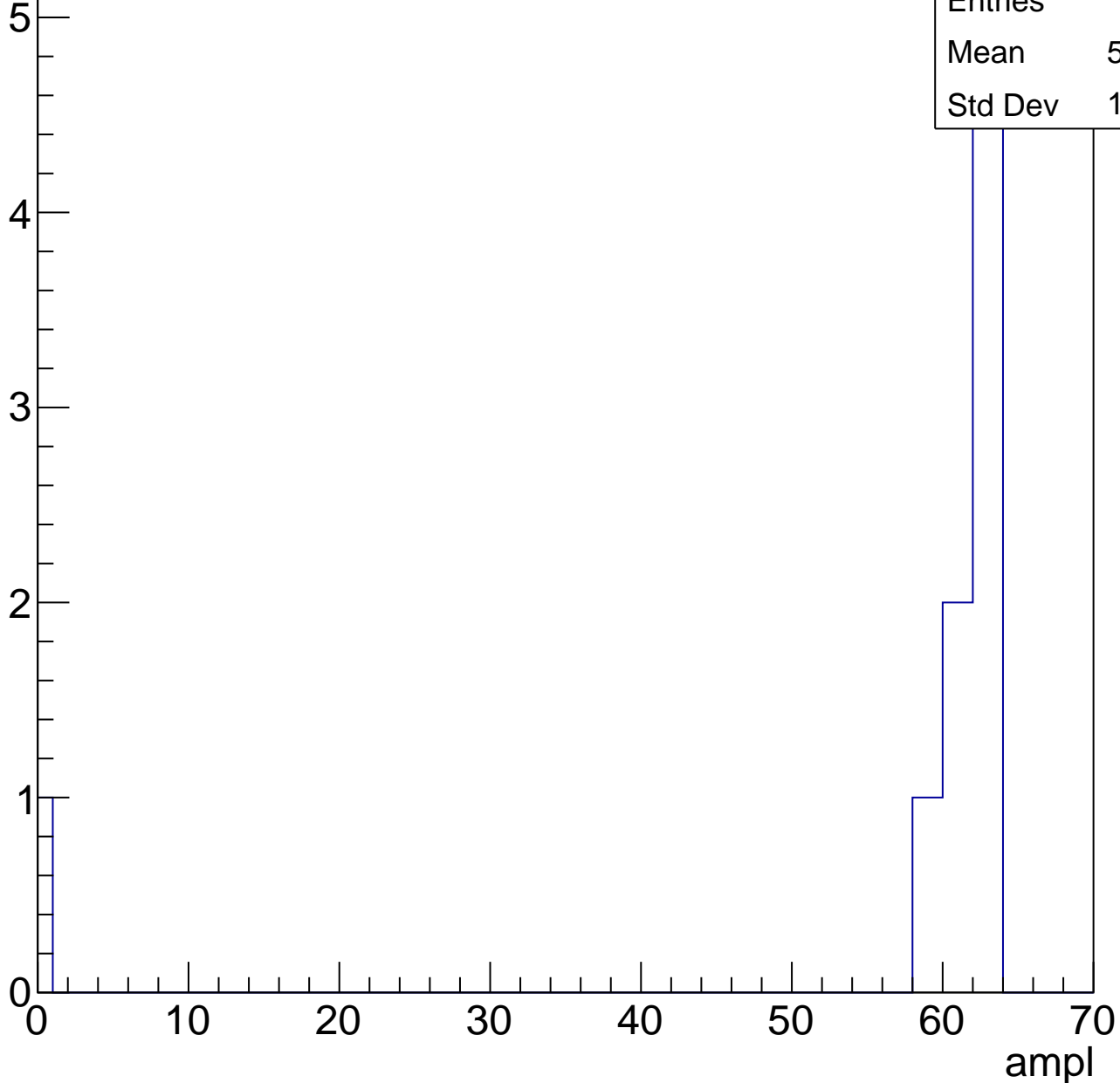
Entries	58
Mean	58.55
Std Dev	2.961

# B1L103S, U22-ch38, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	17
Mean	57.88
Std Dev	14.54





# B1L103S, U22-ch38, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



# B1L103S, U22-ch39, adc0

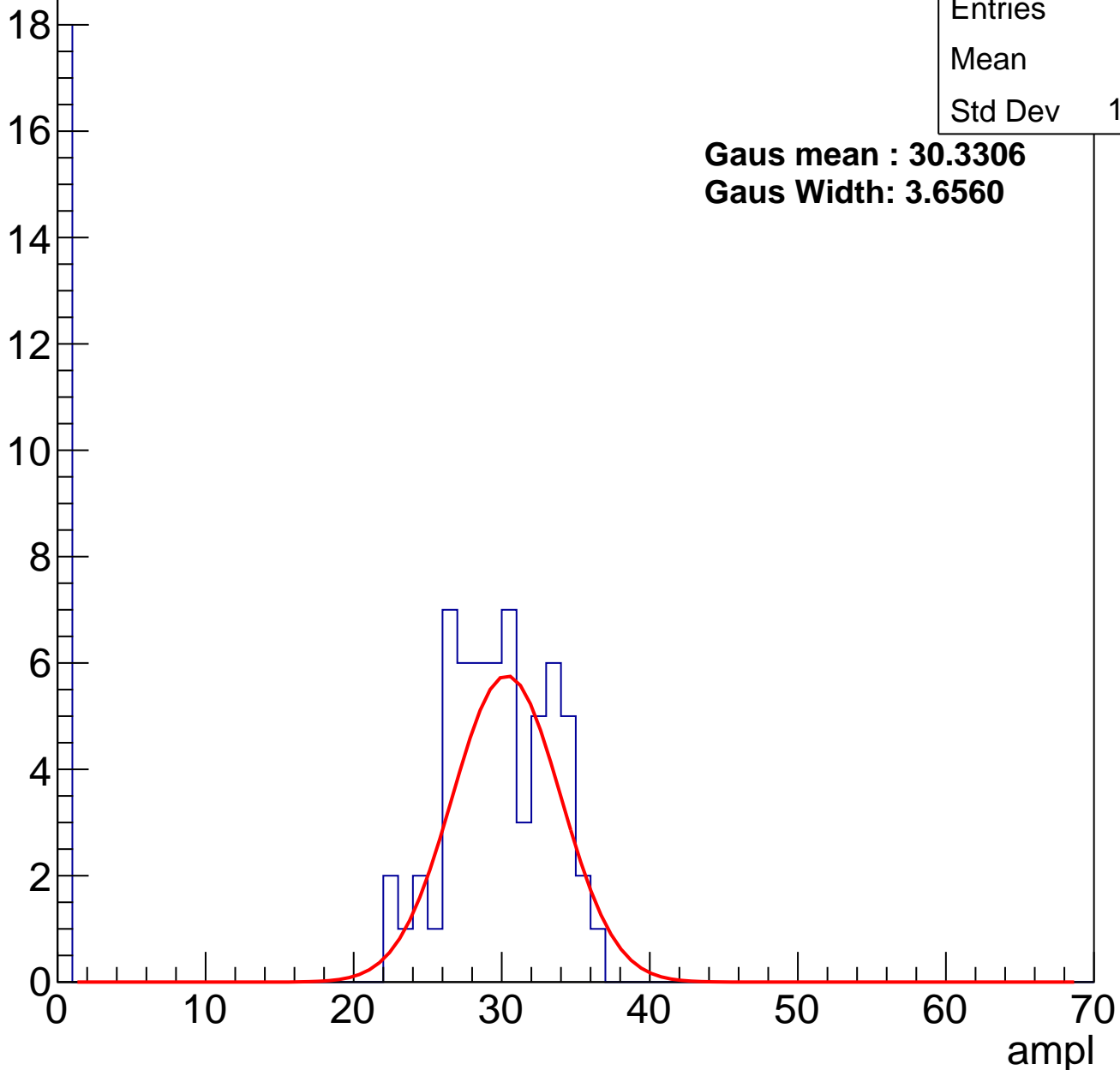
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	22.6
Std Dev	12.73

**Gaus mean : 30.3306**

**Gaus Width: 3.6560**

Entry



# B1L103S, U22-ch39, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	30.57
Std Dev	12.96

**Gaus mean : 36.0408**

**Gaus Width: 3.8890**

Entry

10

8

6

4

2

0

0

10

20

30

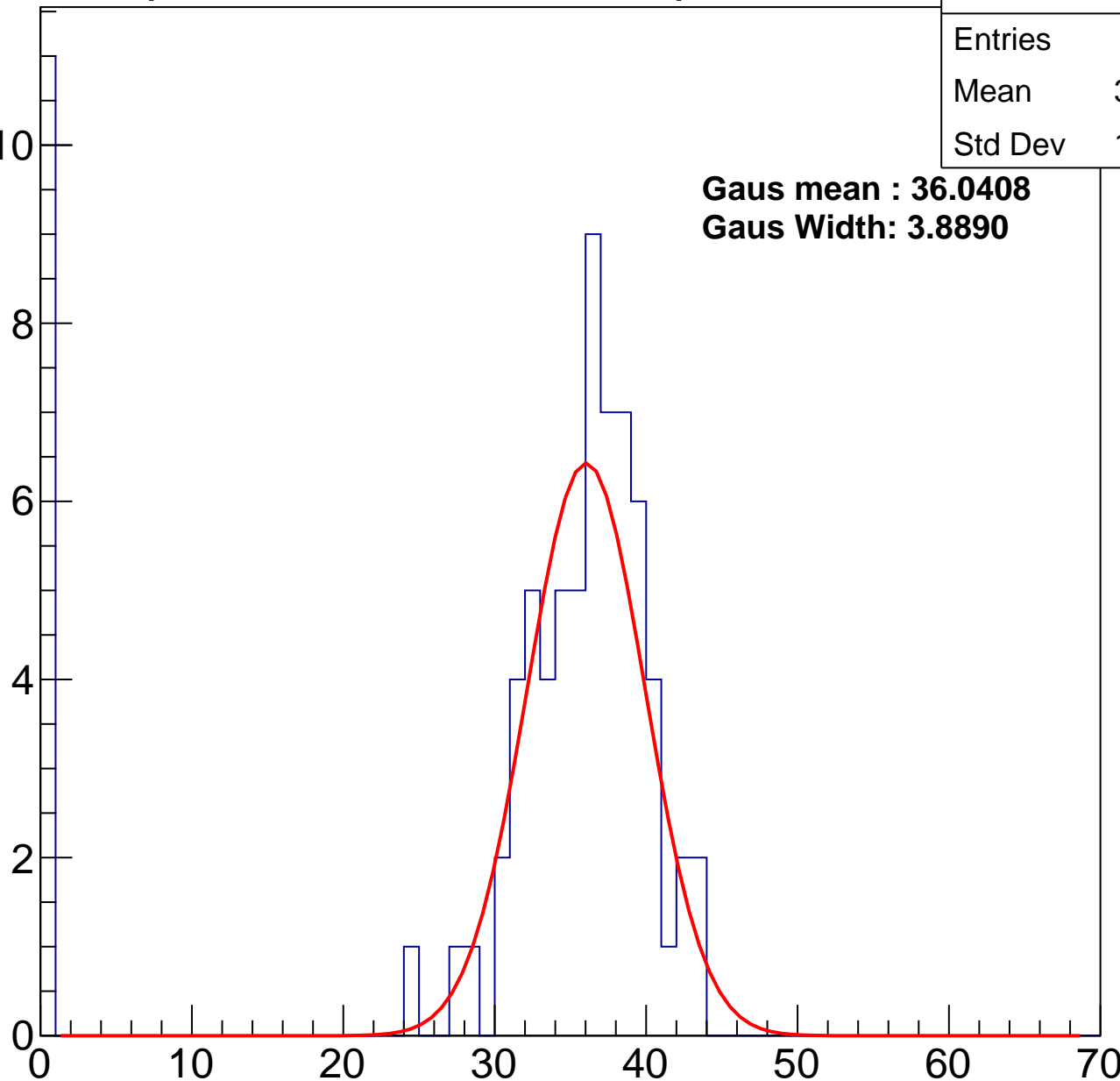
40

50

60

70

ampl



# B1L103S, U22-ch39, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	74
Mean	42.77
Std Dev	3.52

**Gaus mean : 43.0883**

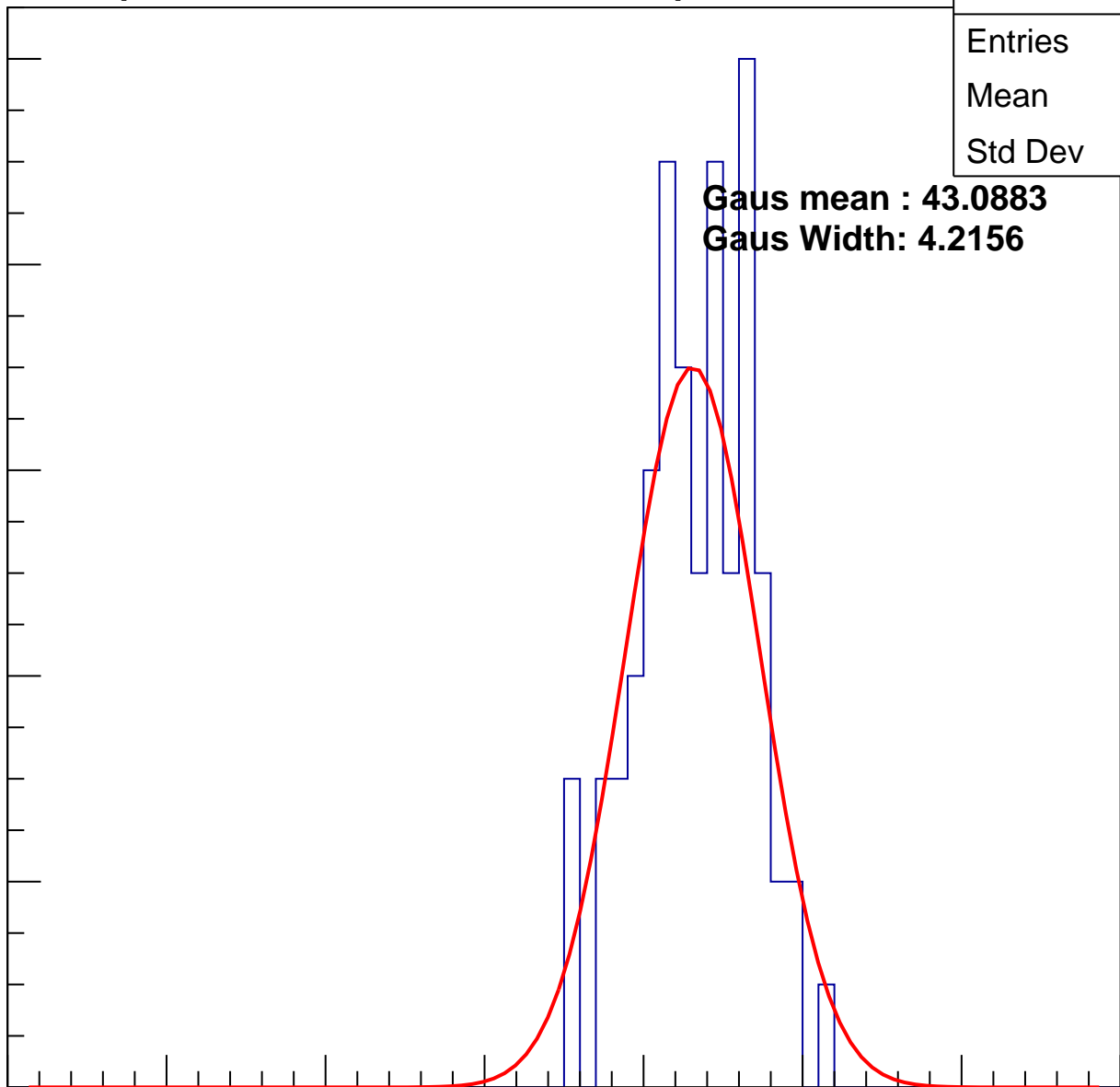
**Gaus Width: 4.2156**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch39, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

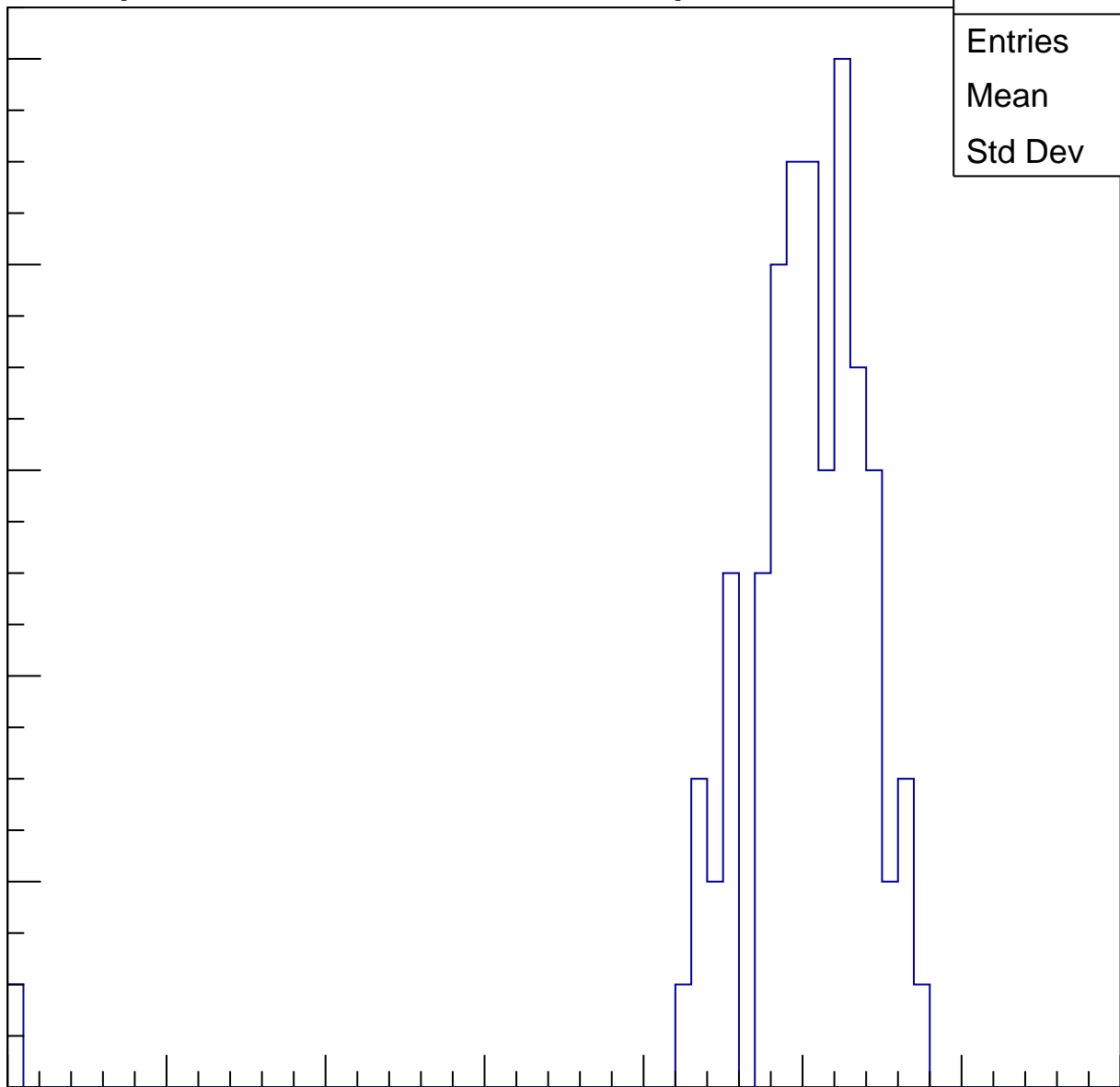
Entries	78
Mean	49.36
Std Dev	6.577

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

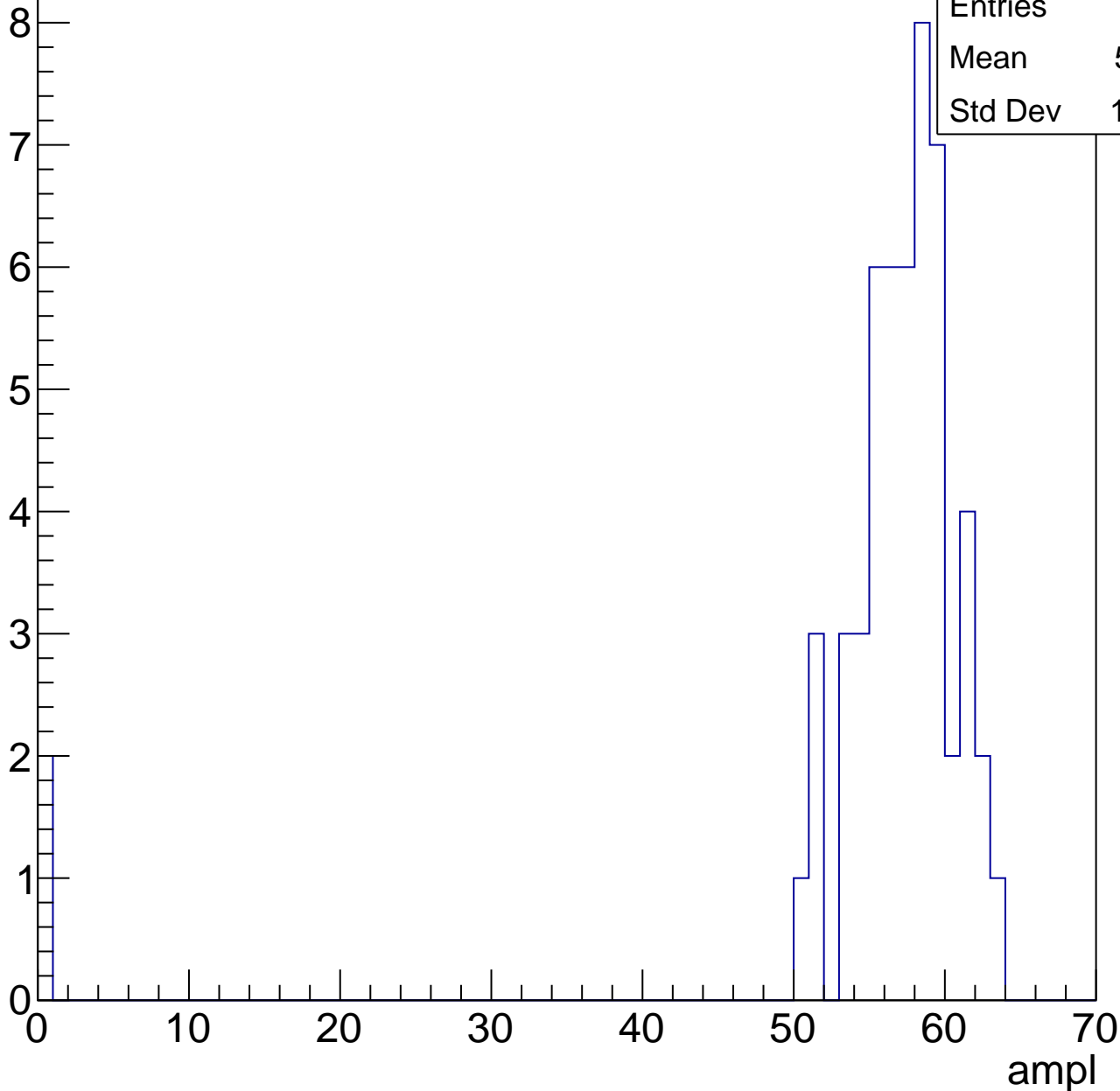


# B1L103S, U22-ch39, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	54.81
Std Dev	11.14

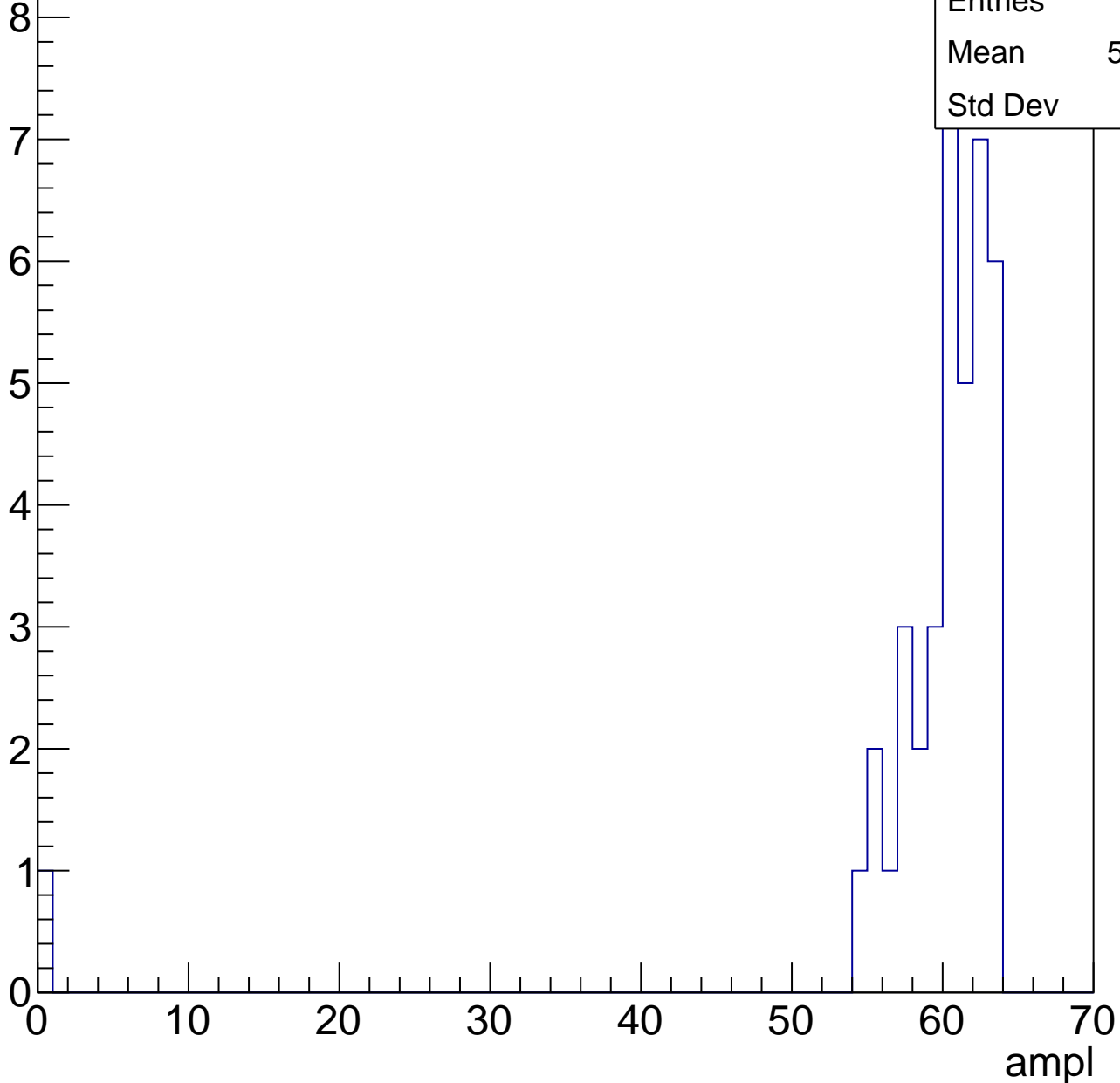


# B1L103S, U22-ch39, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	39
Mean	58.49
Std Dev	9.79



# B1L103S, U22-ch39, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch39, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch40, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	23.93
Std Dev	10.16

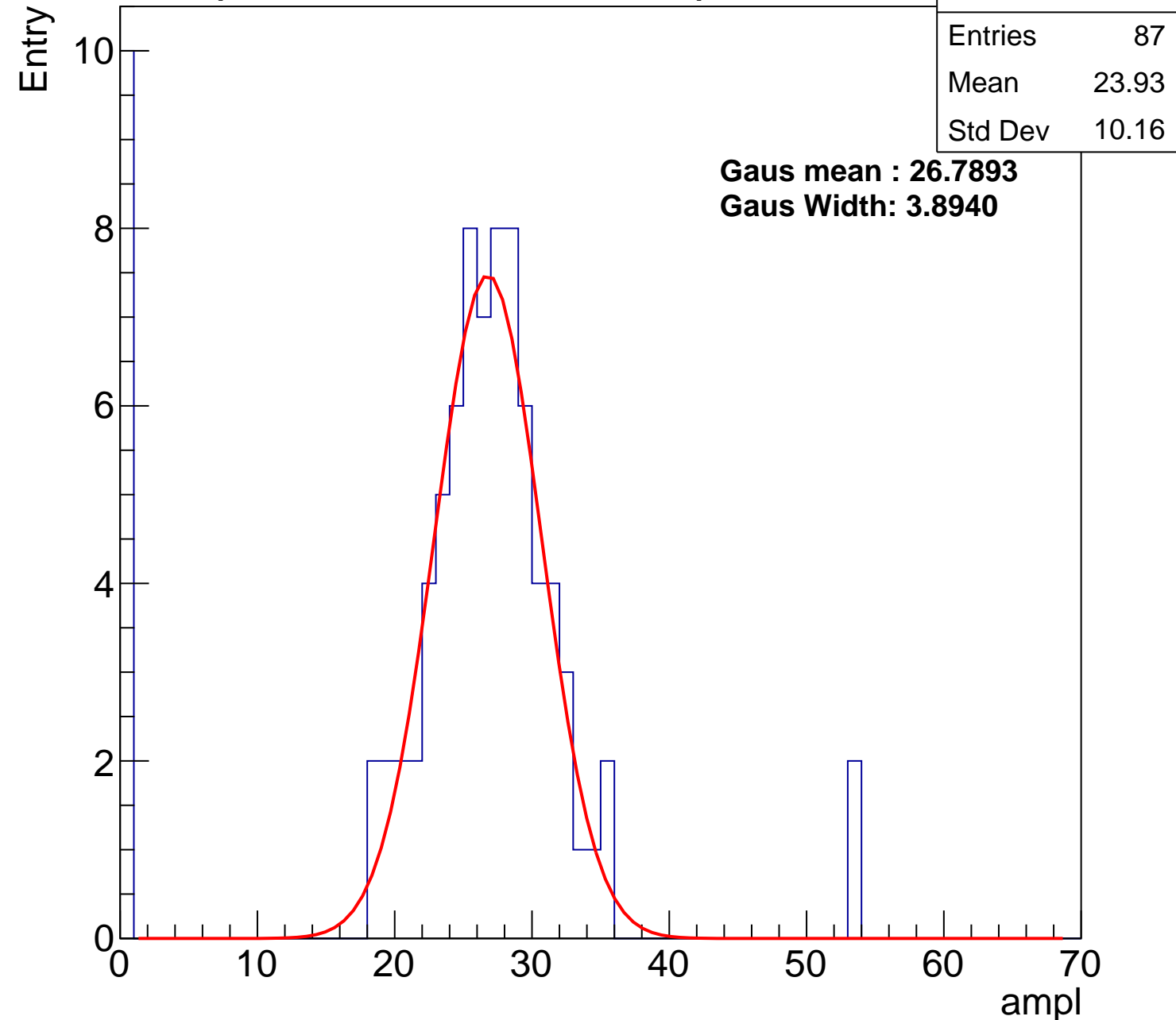
**Gaus mean : 26.7893**

**Gaus Width: 3.8940**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch40, adc1

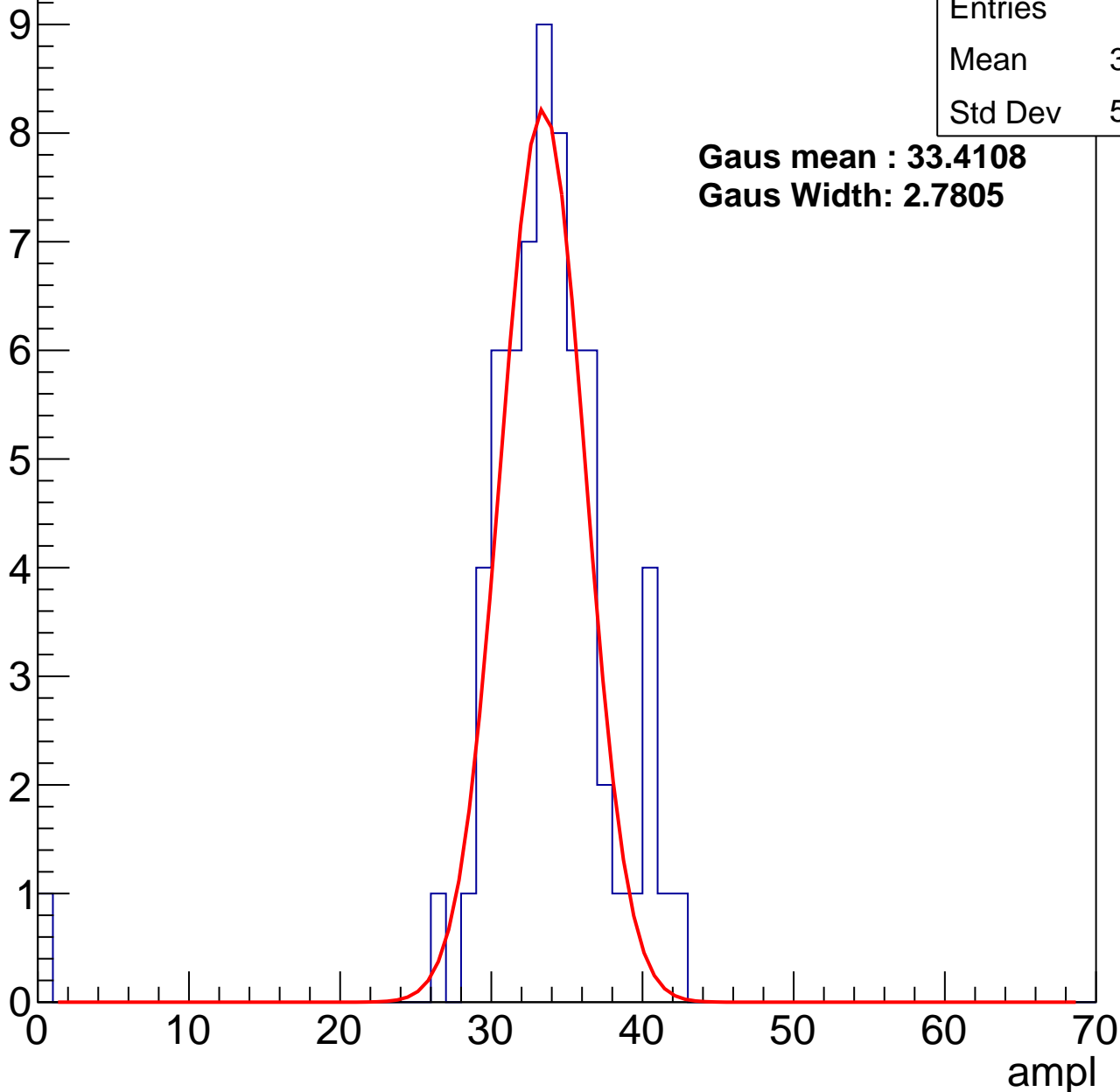
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	33.06
Std Dev	5.309

**Gaus mean : 33.4108**

**Gaus Width: 2.7805**



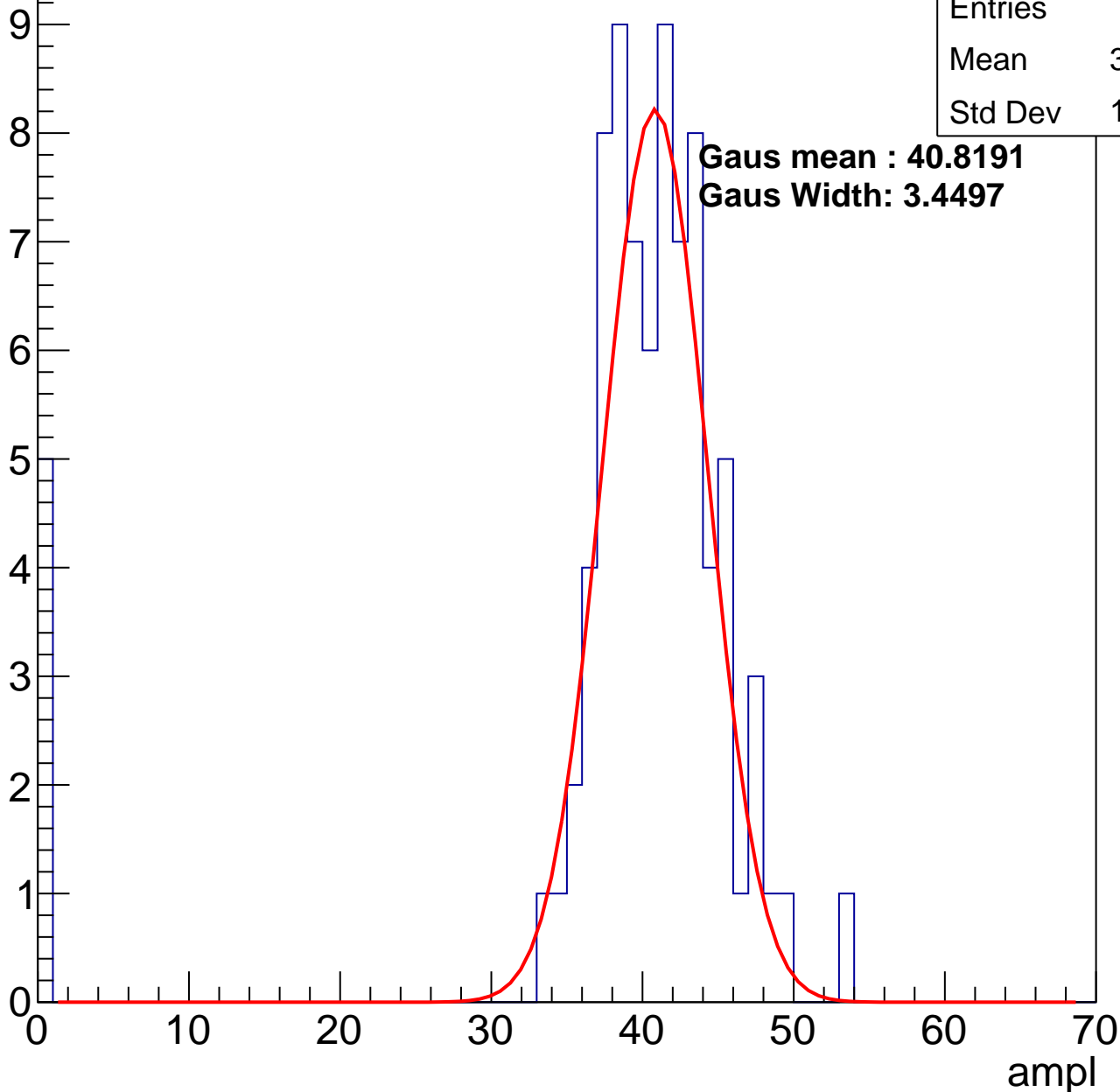
# B1L103S, U22-ch40, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	83
Mean	38.28
Std Dev	10.33

**Gaus mean : 40.8191**  
**Gaus Width: 3.4497**

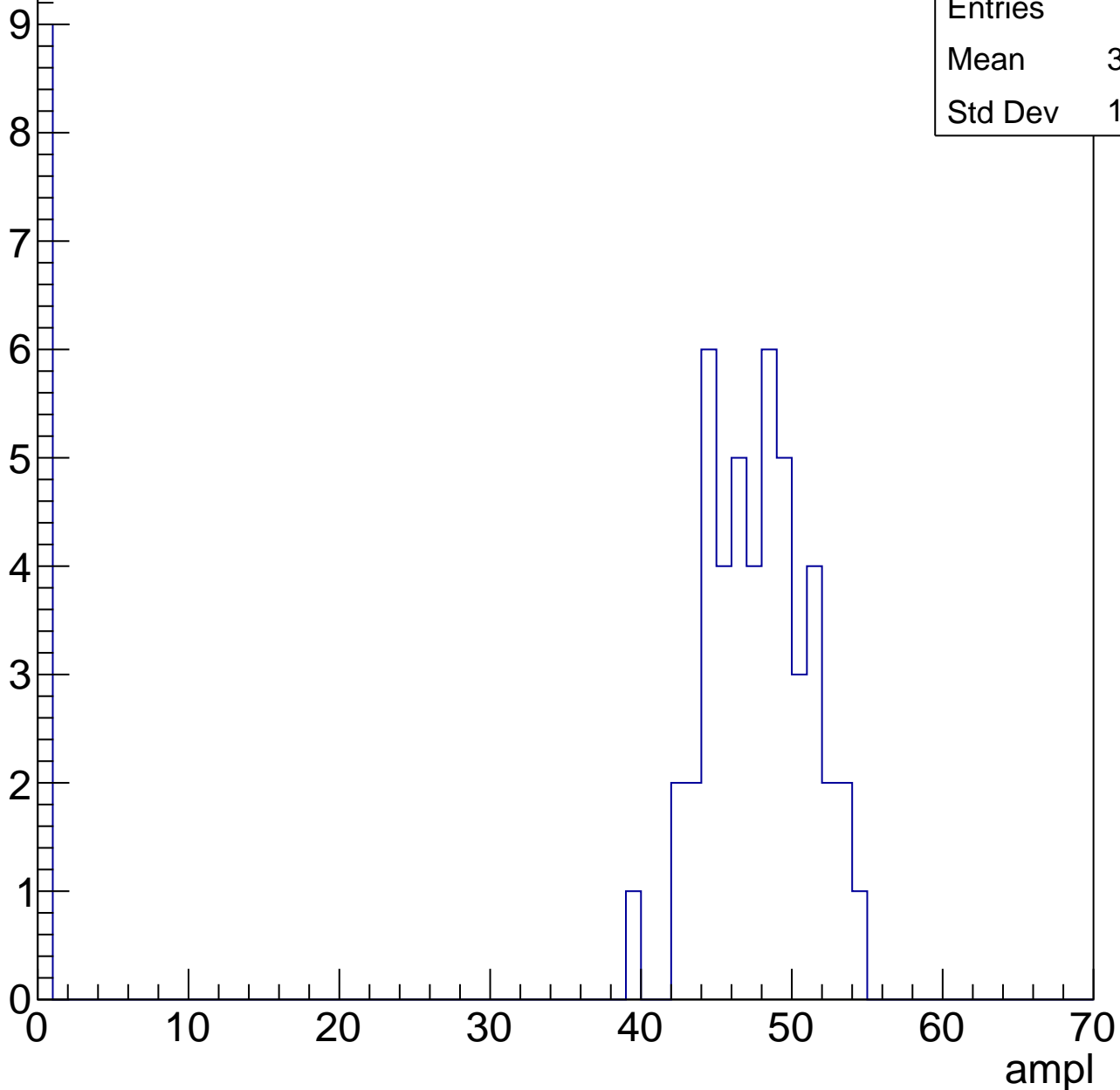


# B1L103S, U22-ch40, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	39.68
Std Dev	17.62

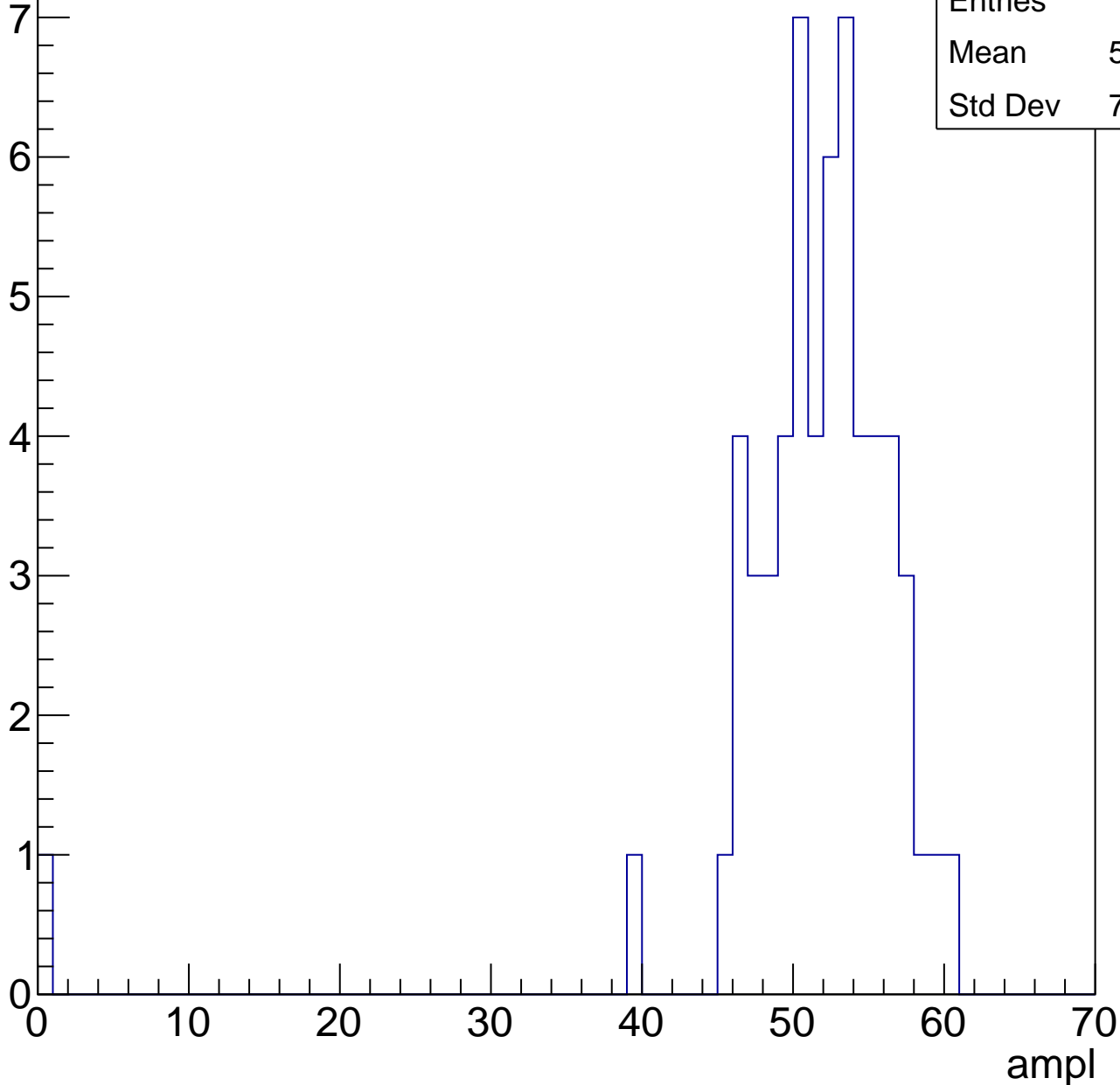


# B1L103S, U22-ch40, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

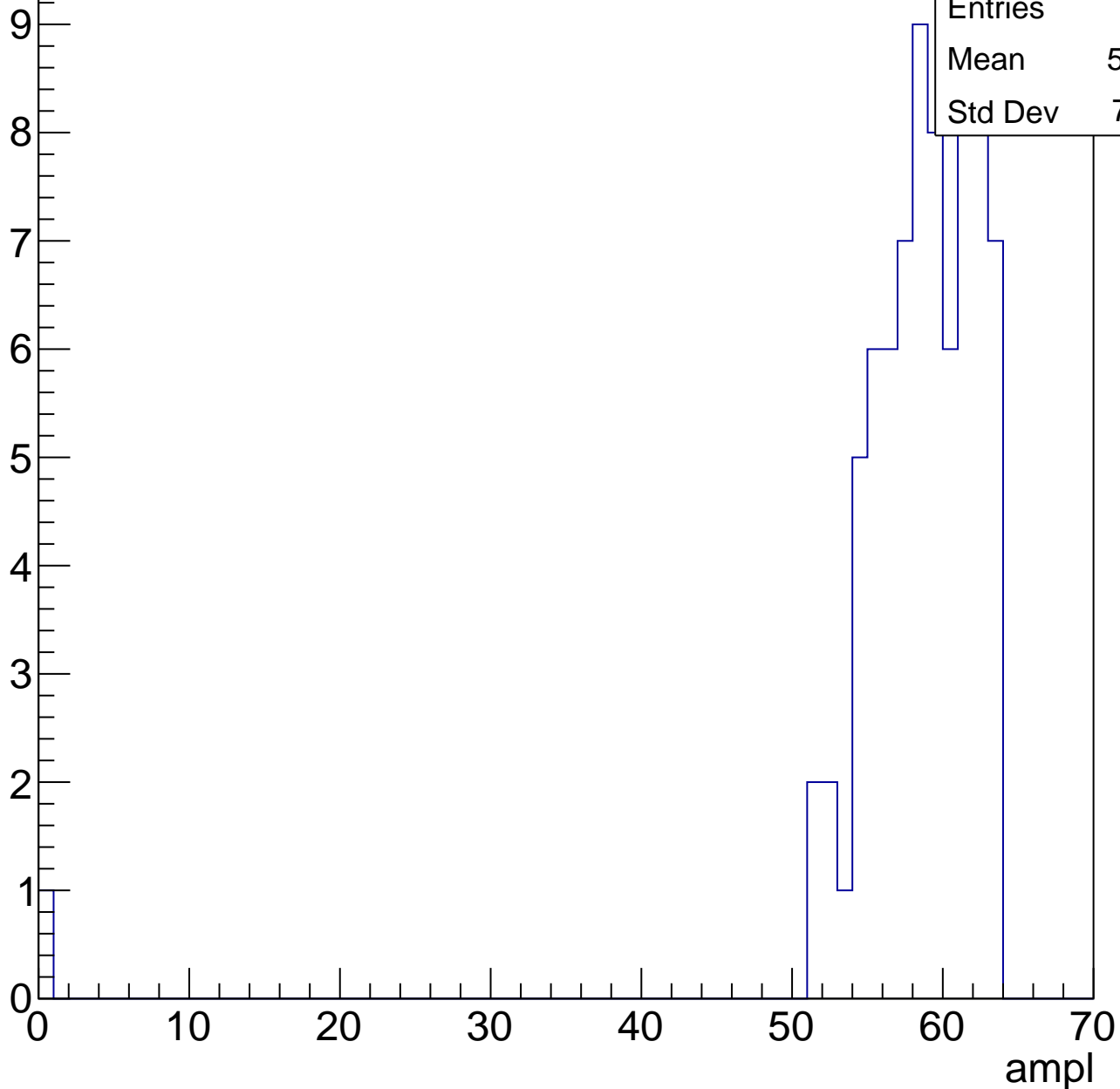
Entries	59
Mean	50.75
Std Dev	7.714



# B1L103S, U22-ch40, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch40, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

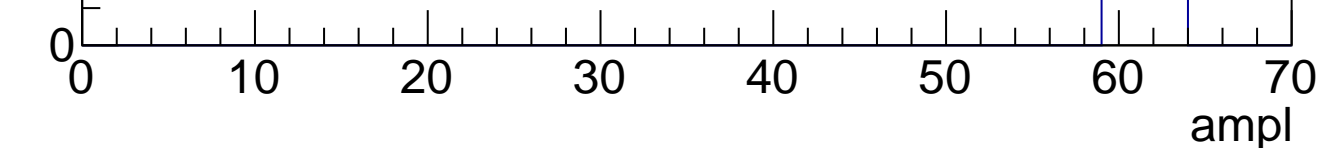
9

Mean

61.44

Std Dev

1.423



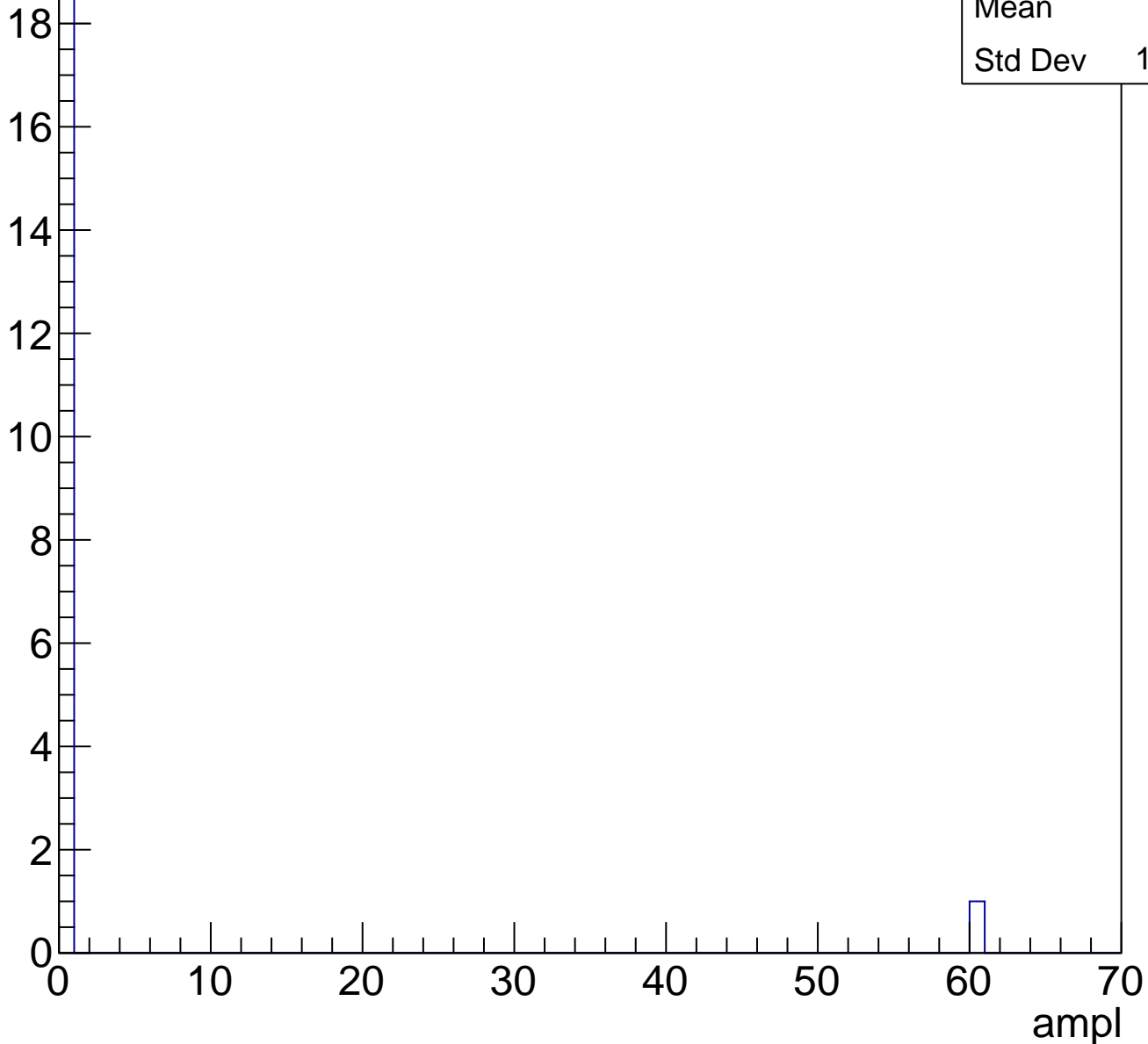


# B1L103S, U22-ch40, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3
Std Dev	13.08

Entry



# B1L103S, U22-ch41, adc0

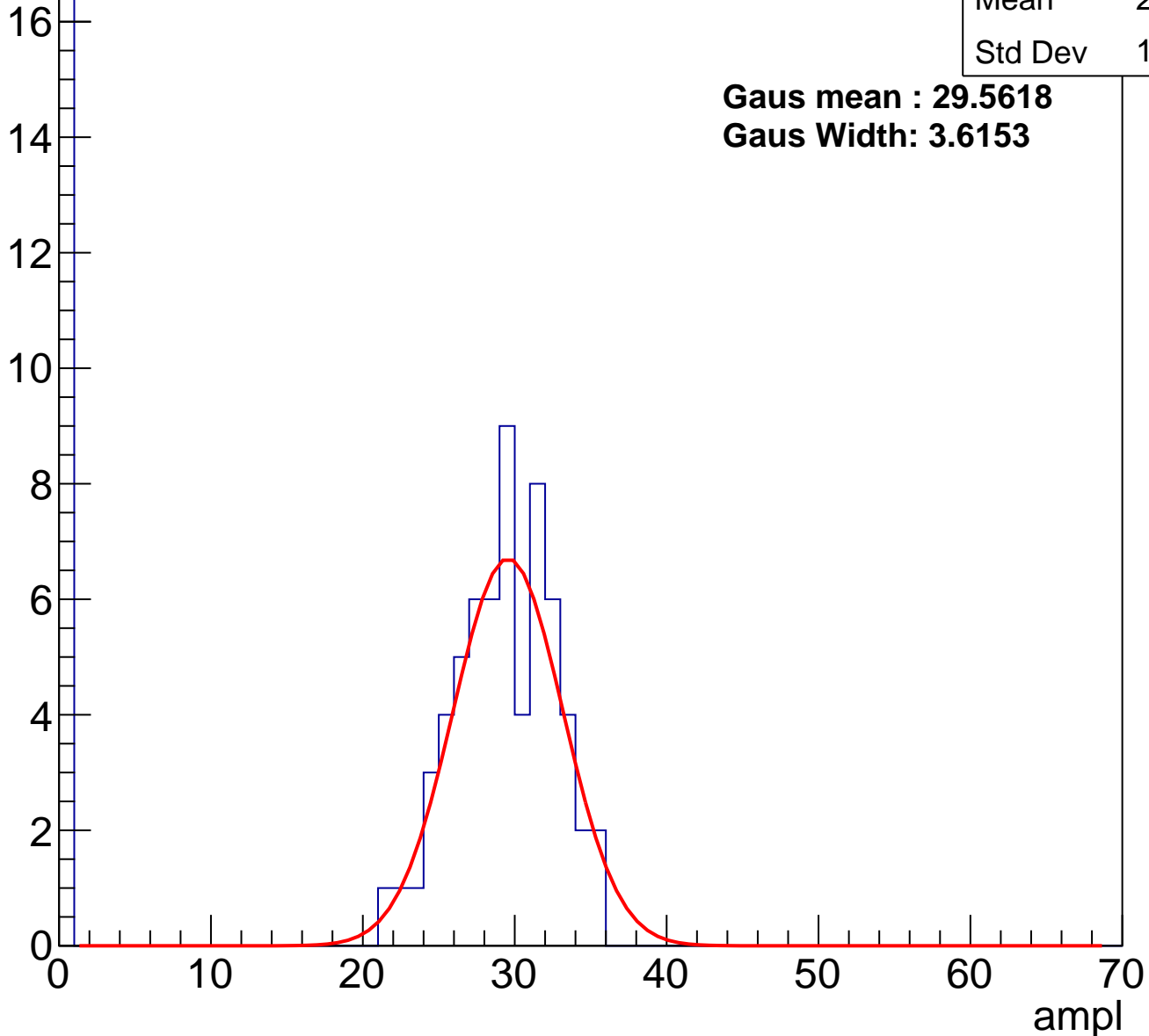
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	22.65
Std Dev	12.19

**Gaus mean : 29.5618**

**Gaus Width: 3.6153**

Entry



# B1L103S, U22-ch41, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	101
Mean	31.61
Std Dev	12.57

**Gaus mean : 37.3619**

**Gaus Width: 3.3733**

Entry

12

10

8

6

4

2

0

0

10

20

30

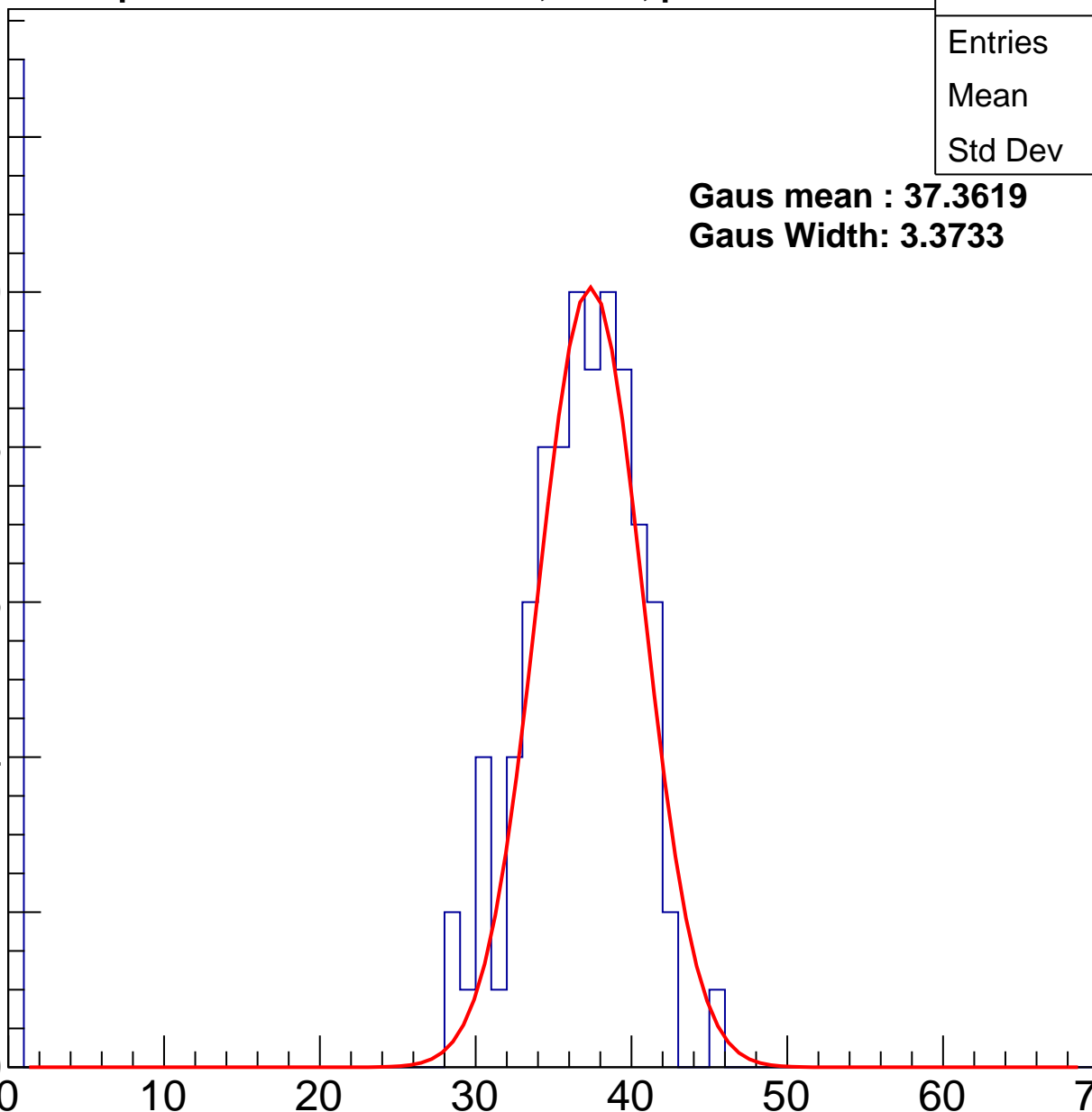
40

50

60

70

ampl



# B1L103S, U22-ch41, adc2

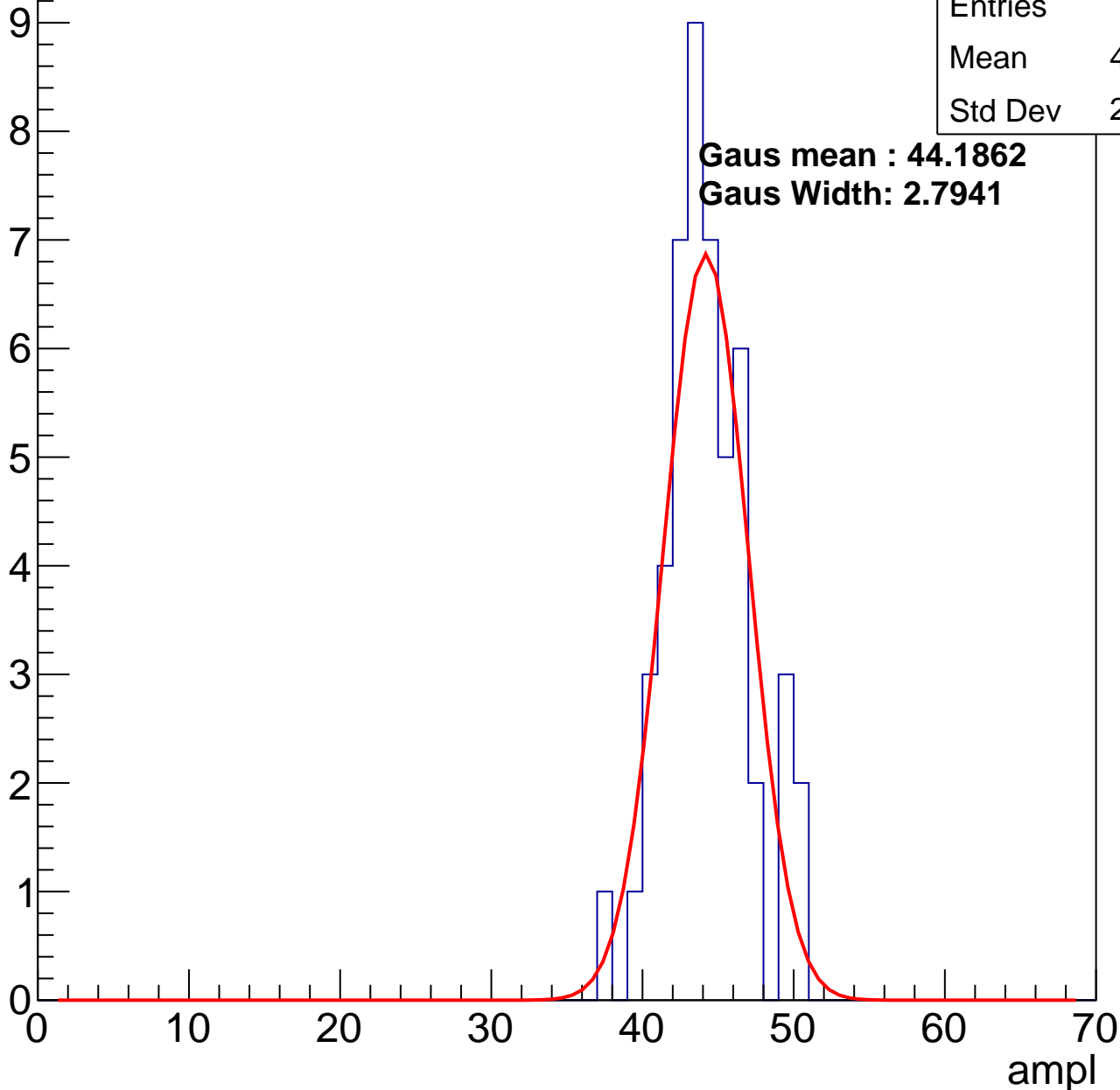
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	43.82
Std Dev	2.783

**Gaus mean : 44.1862**

**Gaus Width: 2.7941**



# B1L103S, U22-ch41, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

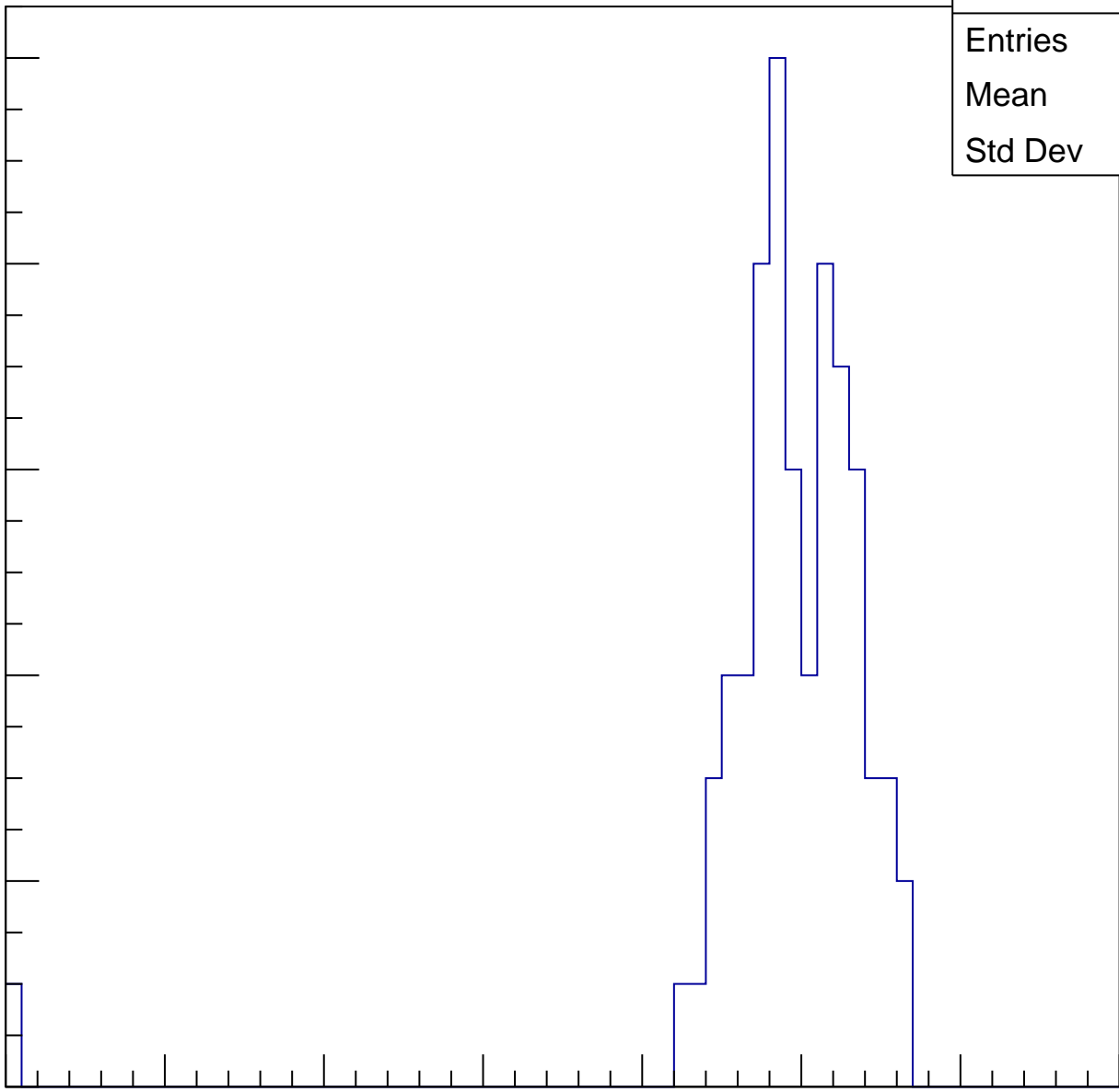
Entries	71
Mean	48.73
Std Dev	6.686

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

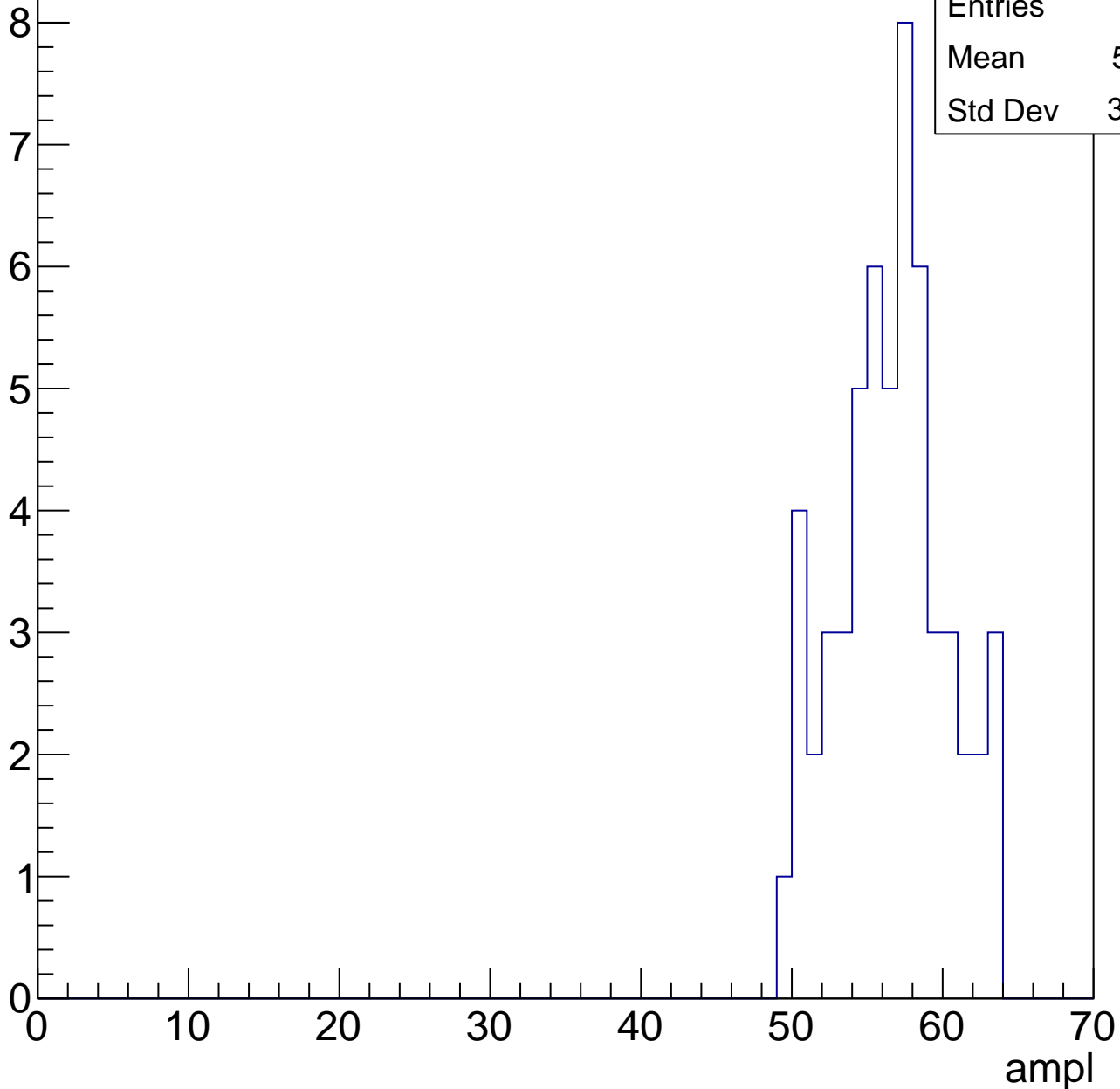


# B1L103S, U22-ch41, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

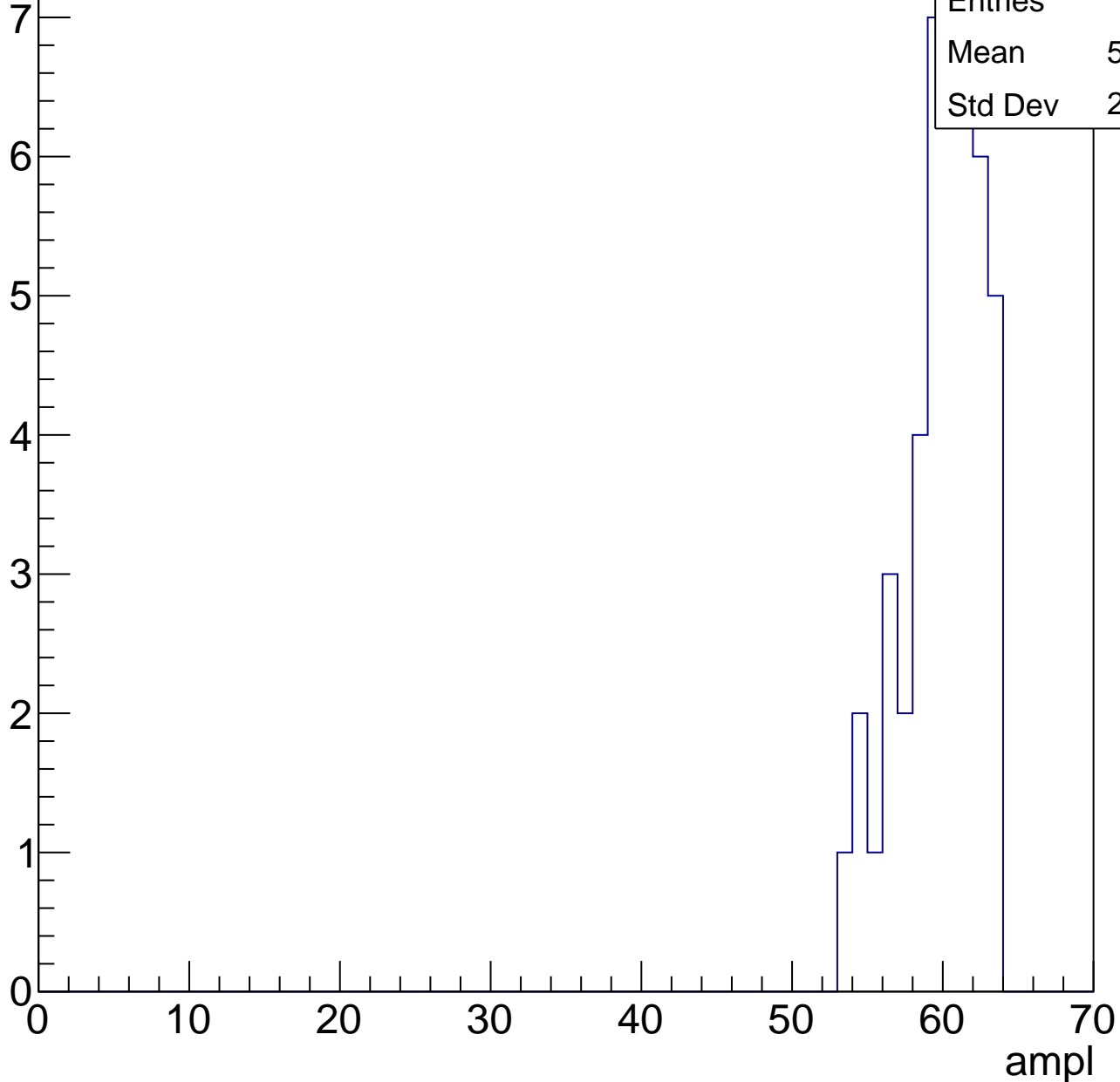
Entries	56
Mean	56.11
Std Dev	3.584



# B1L103S, U22-ch41, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch41, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

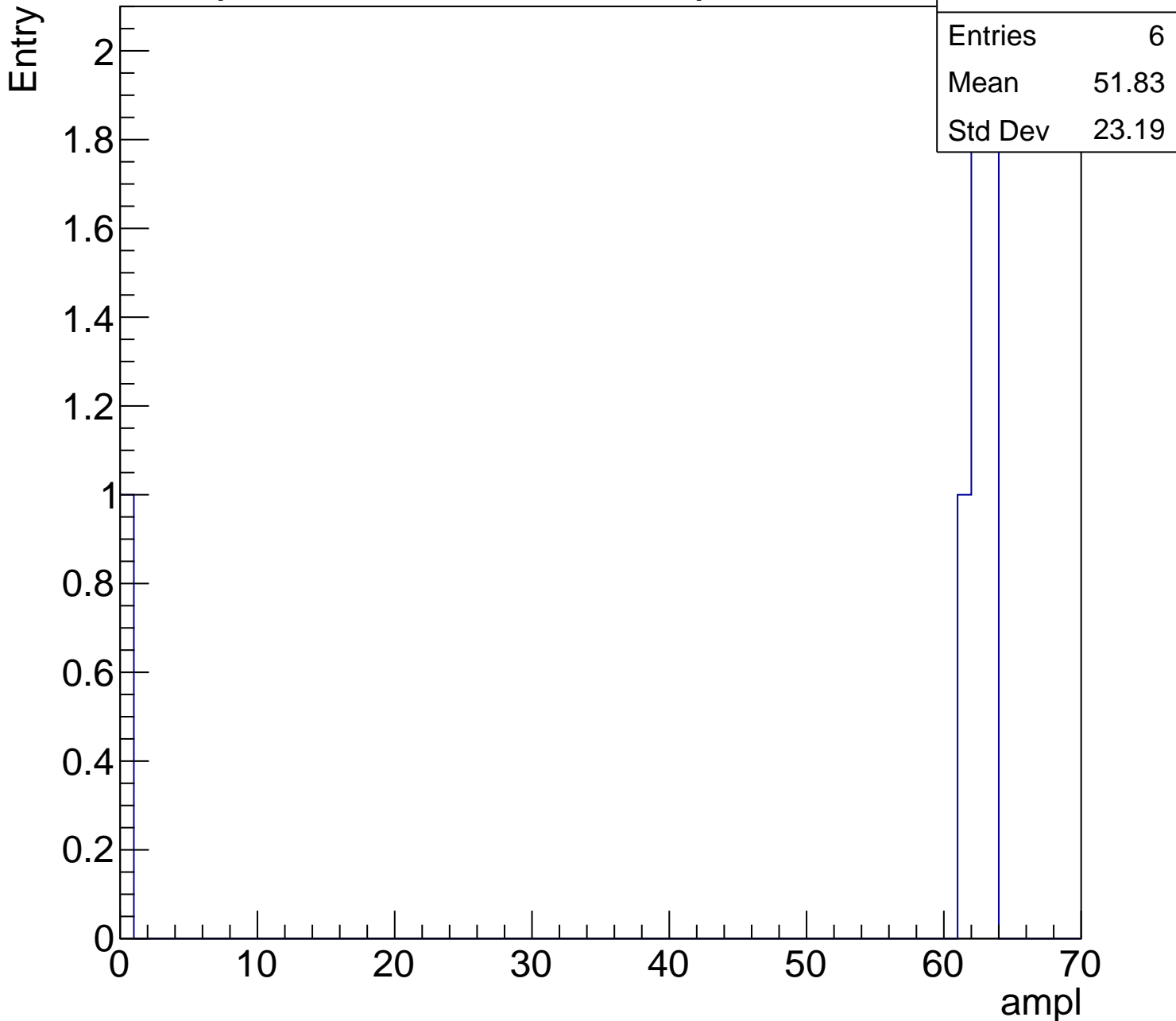
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

ampl





# B1L103S, U22-ch41, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch42, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	24.57
Std Dev	9.853

**Gaus mean : 28.2299**

**Gaus Width: 3.4269**

Entry

10

8

6

4

2

0

0

10

20

30

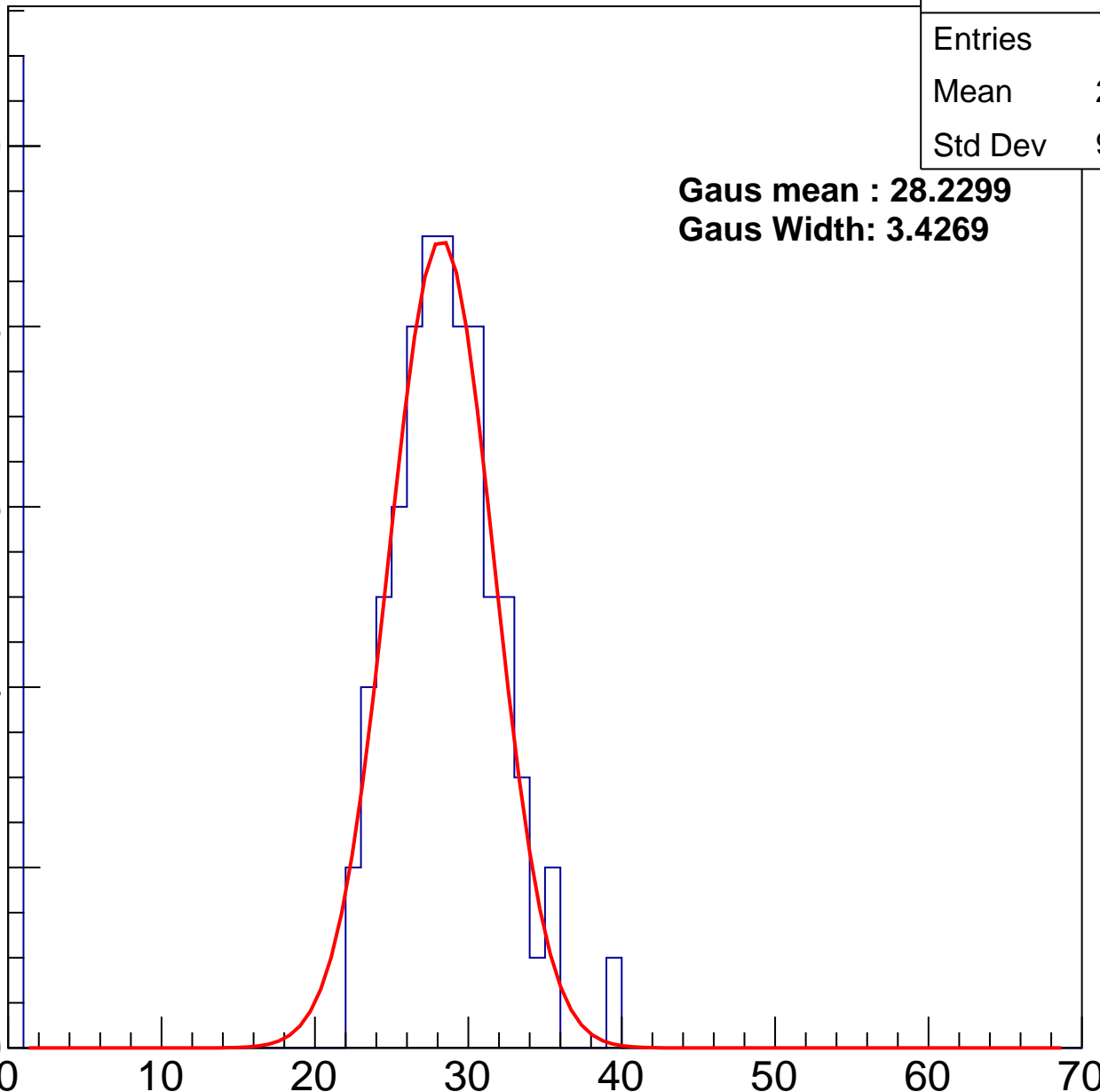
40

50

60

70

ampl



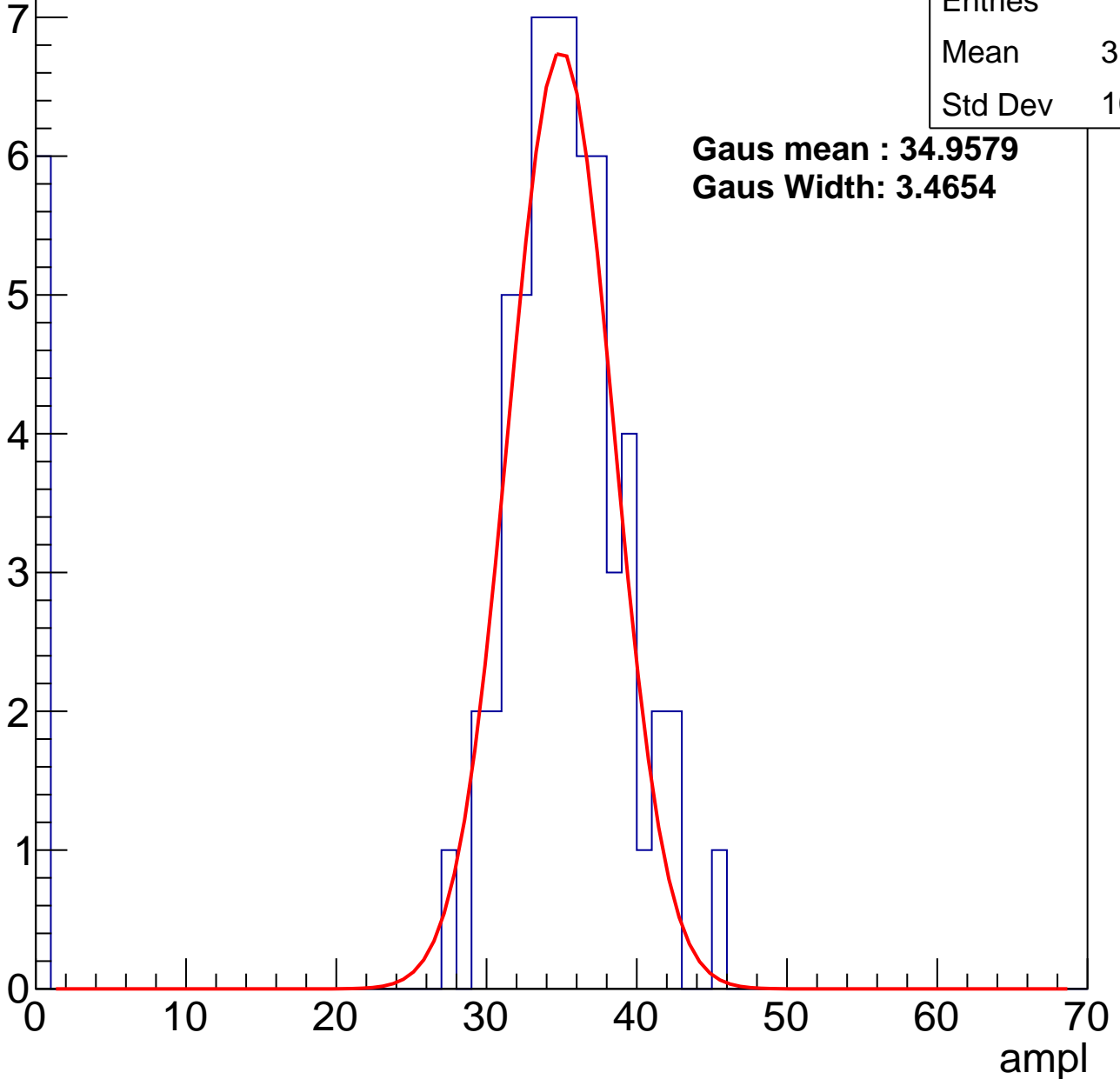
# B1L103S, U22-ch42, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	31.84
Std Dev	10.54

**Gaus mean : 34.9579**  
**Gaus Width: 3.4654**



# B1L103S, U22-ch42, adc2

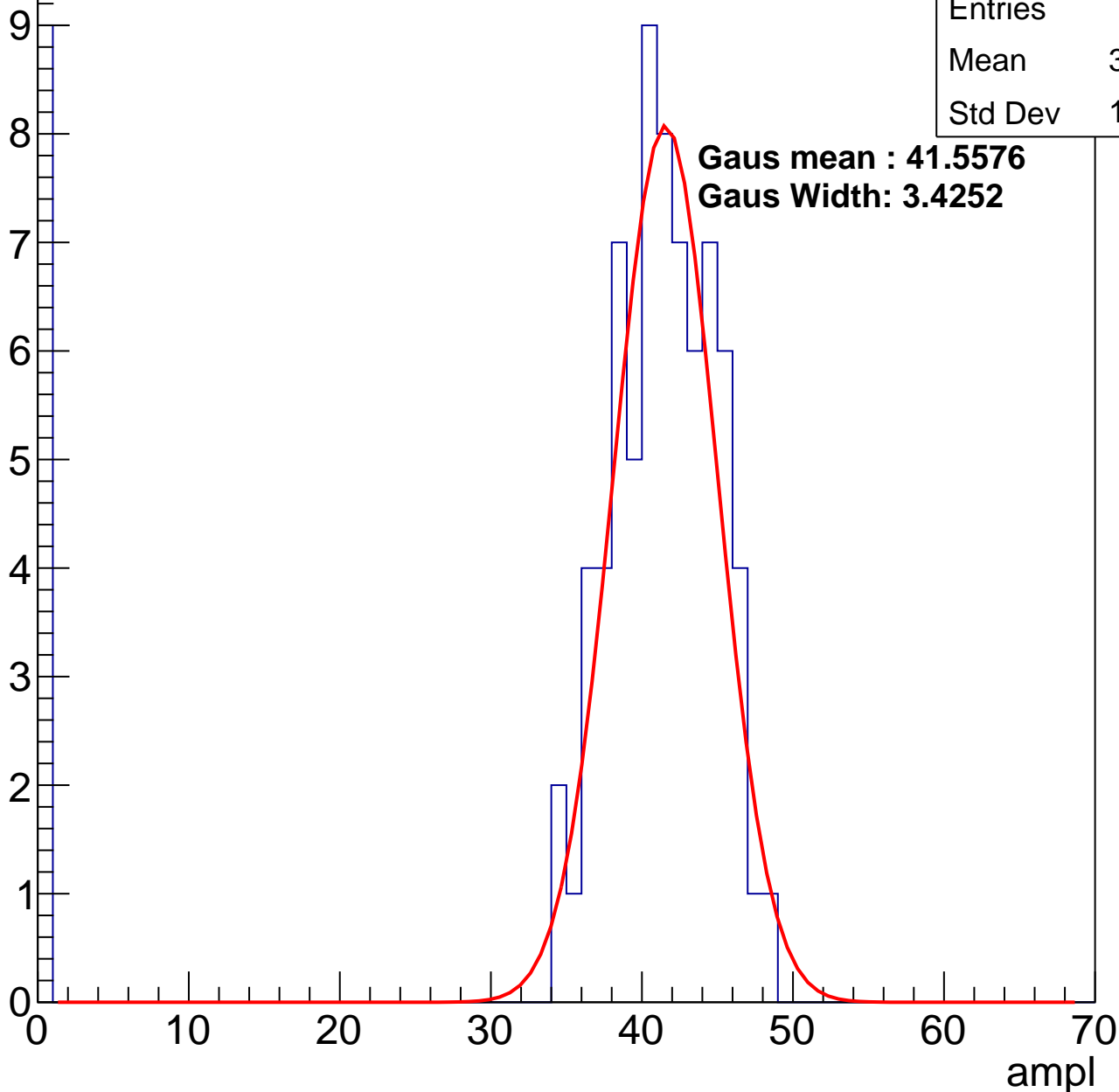
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	36.46
Std Dev	13.25

**Gaus mean : 41.5576**

**Gaus Width: 3.4252**

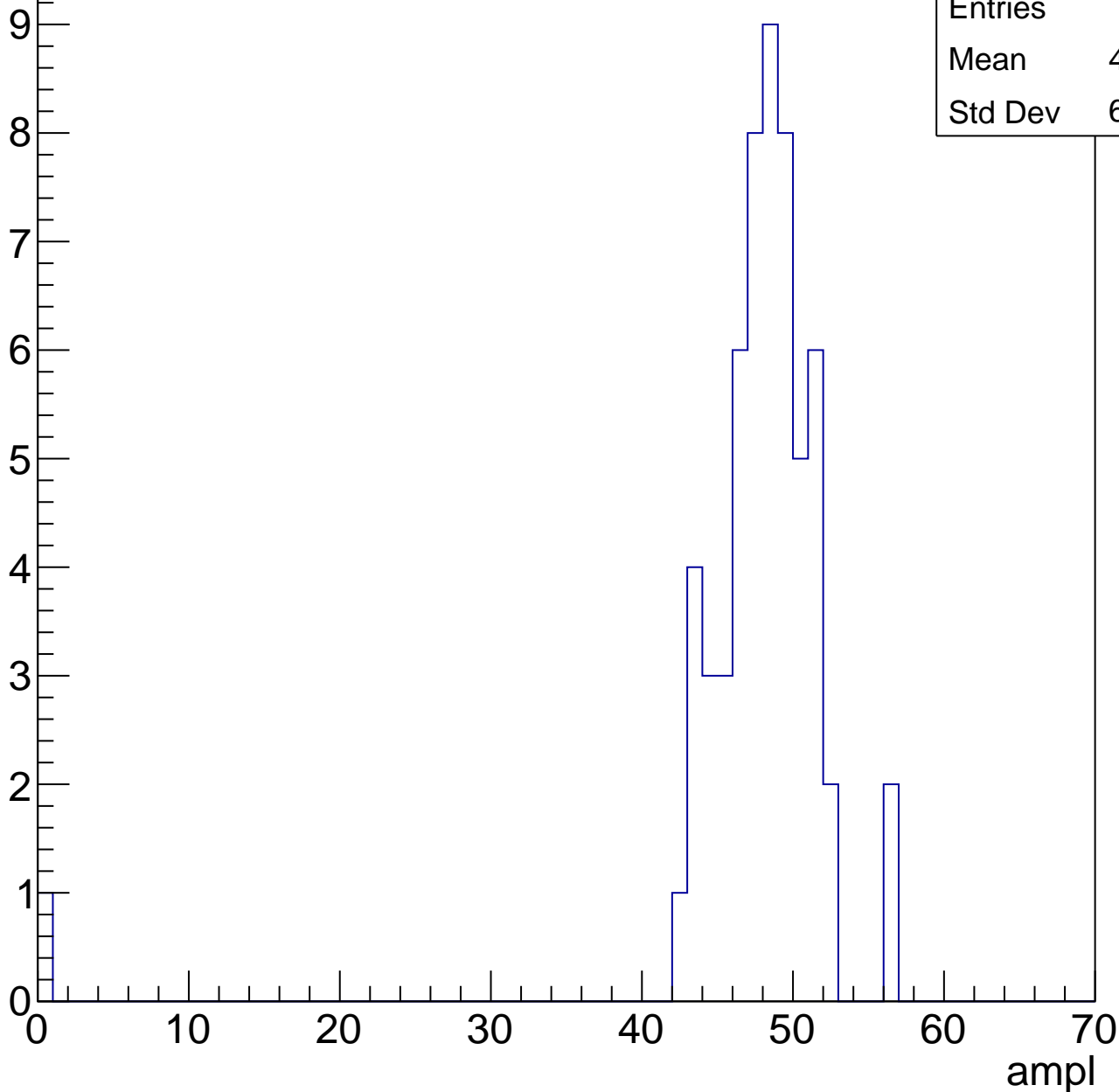


# B1L103S, U22-ch42, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	47.05
Std Dev	6.869

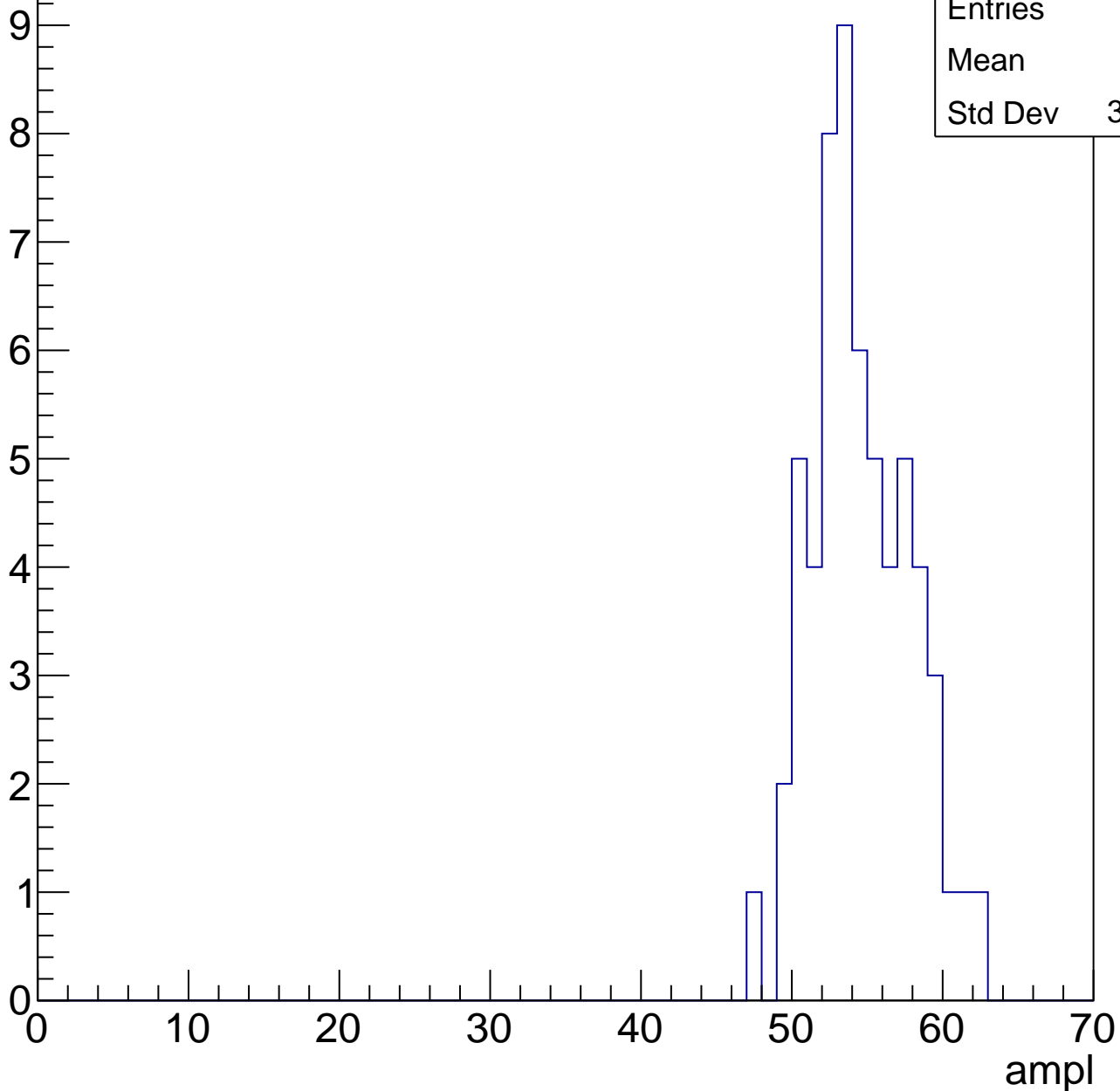


# B1L103S, U22-ch42, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.1
Std Dev	3.214

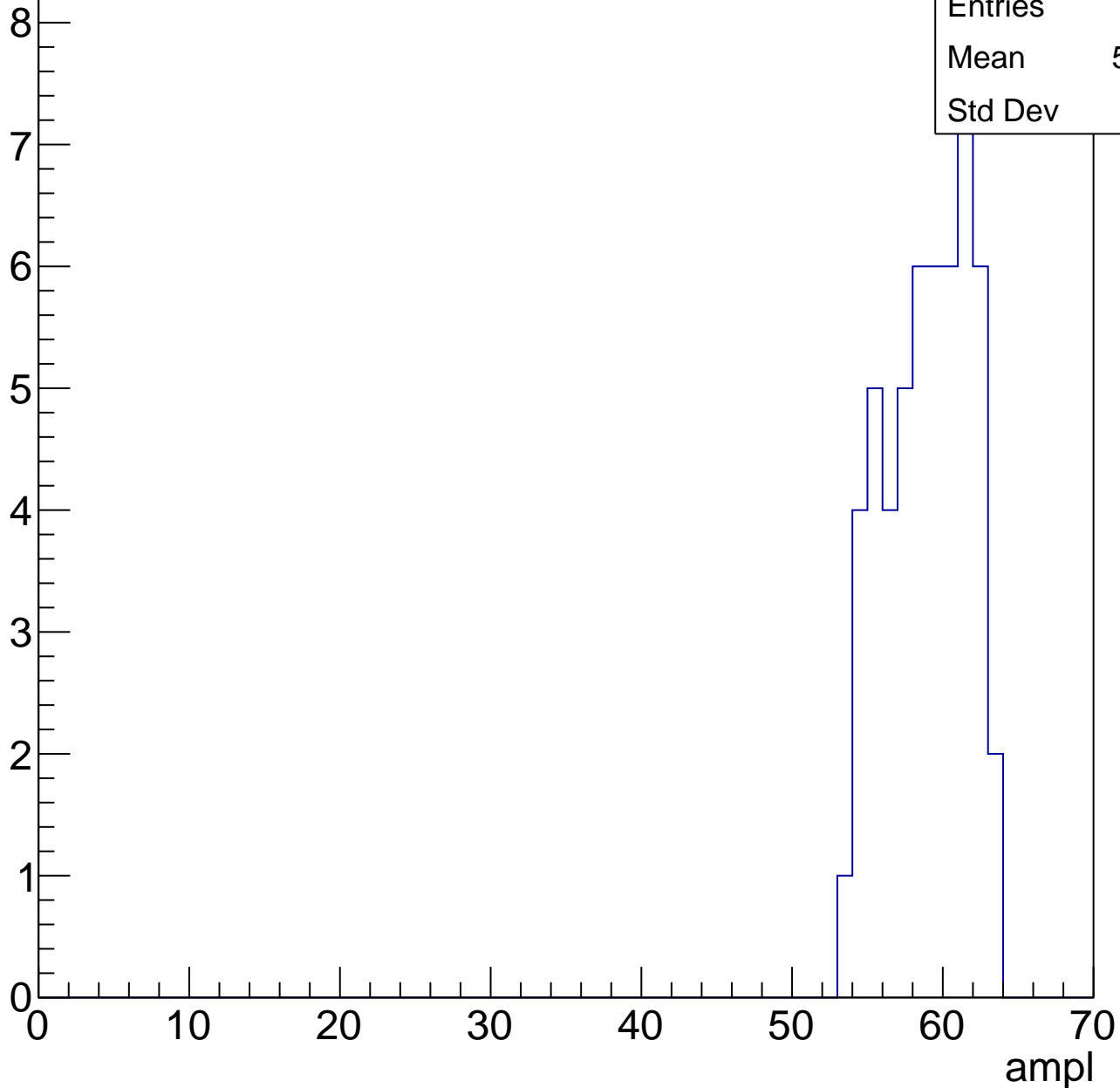


# B1L103S, U22-ch42, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	58.51
Std Dev	2.71

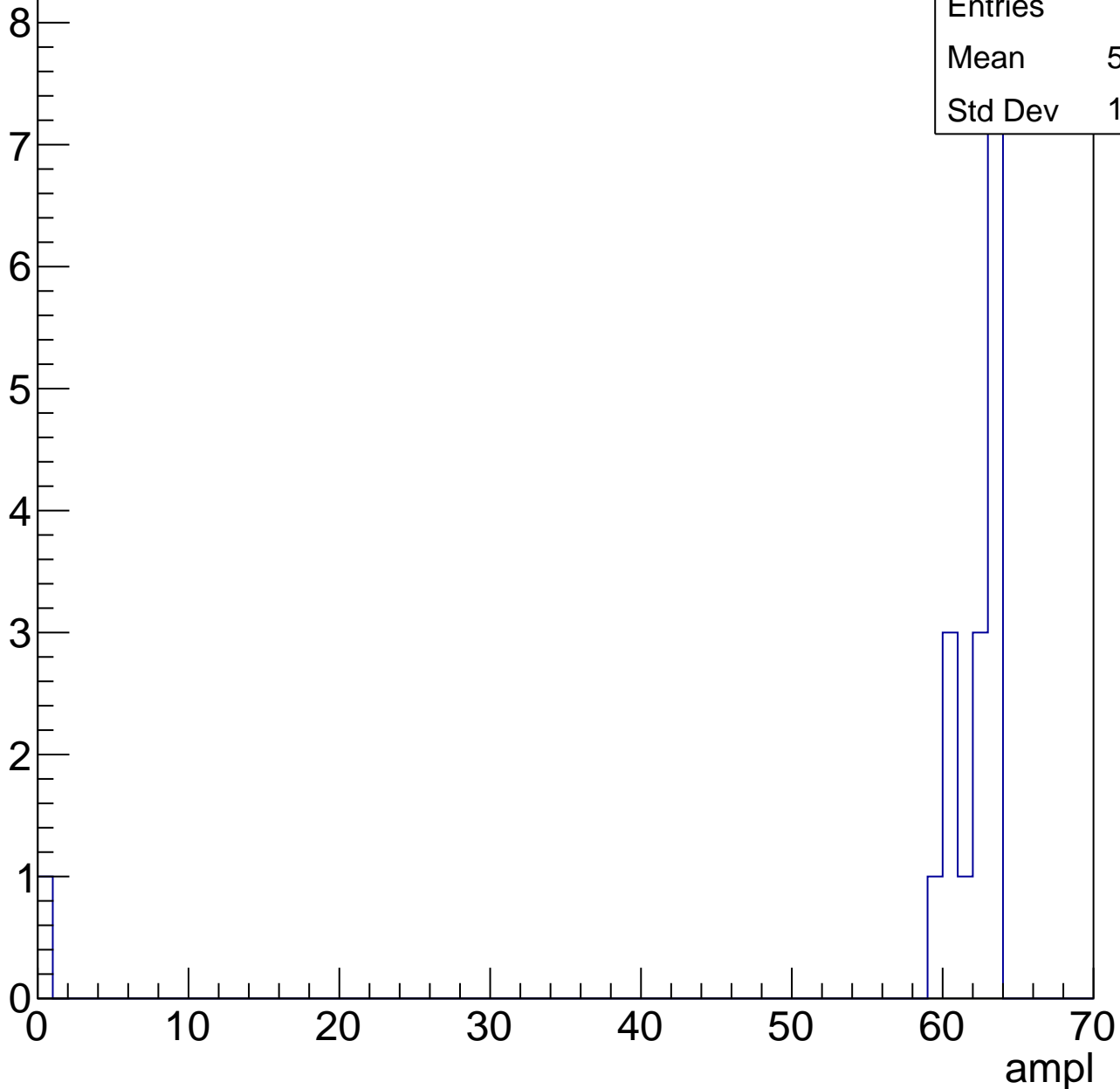


# B1L103S, U22-ch42, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	17
Mean	58.24
Std Dev	14.62





# B1L103S, U22-ch42, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch43, adc0

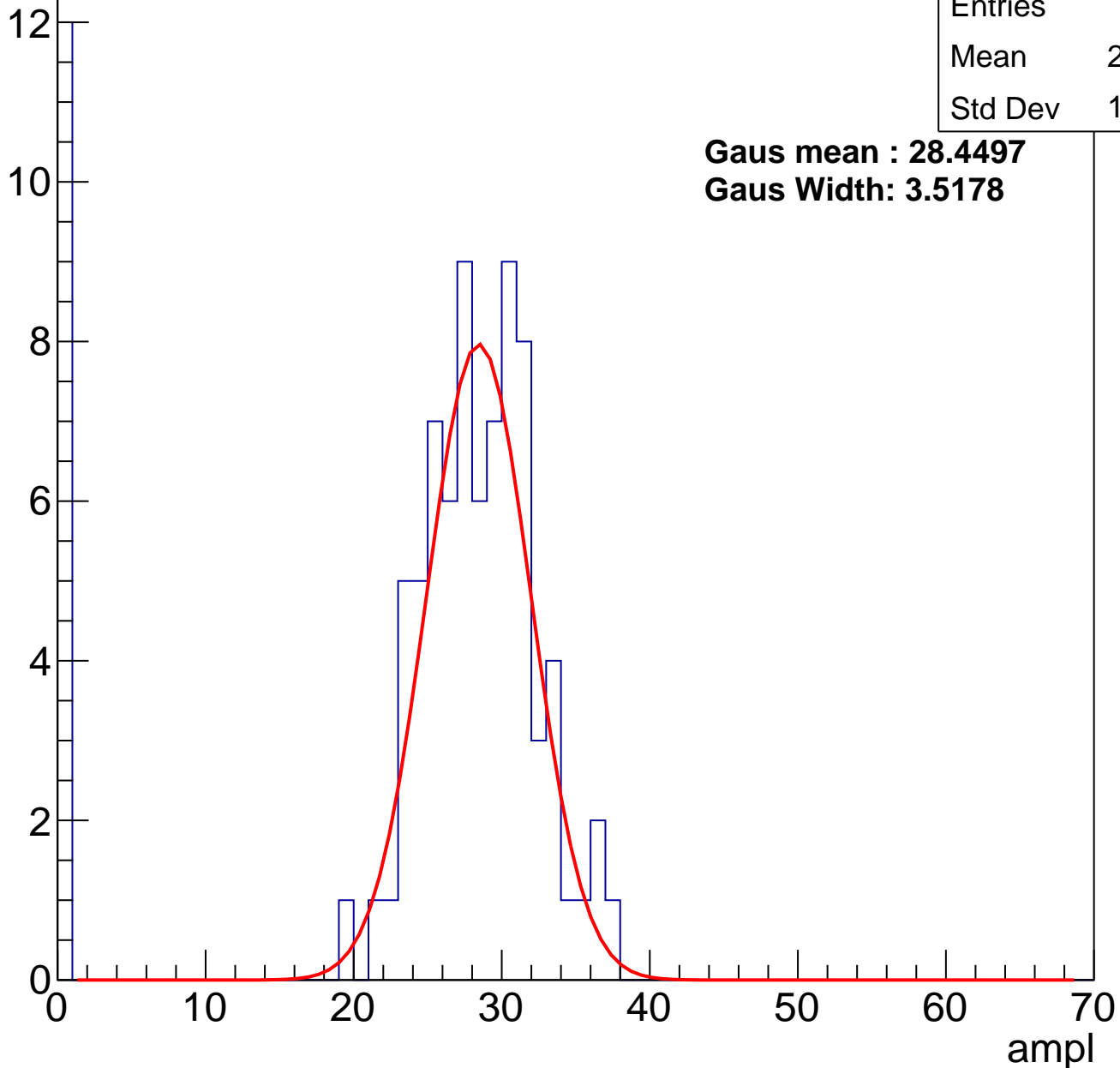
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	89
Mean	24.34
Std Dev	10.19

**Gaus mean : 28.4497**

**Gaus Width: 3.5178**

Entry



# B1L103S, U22-ch43, adc1

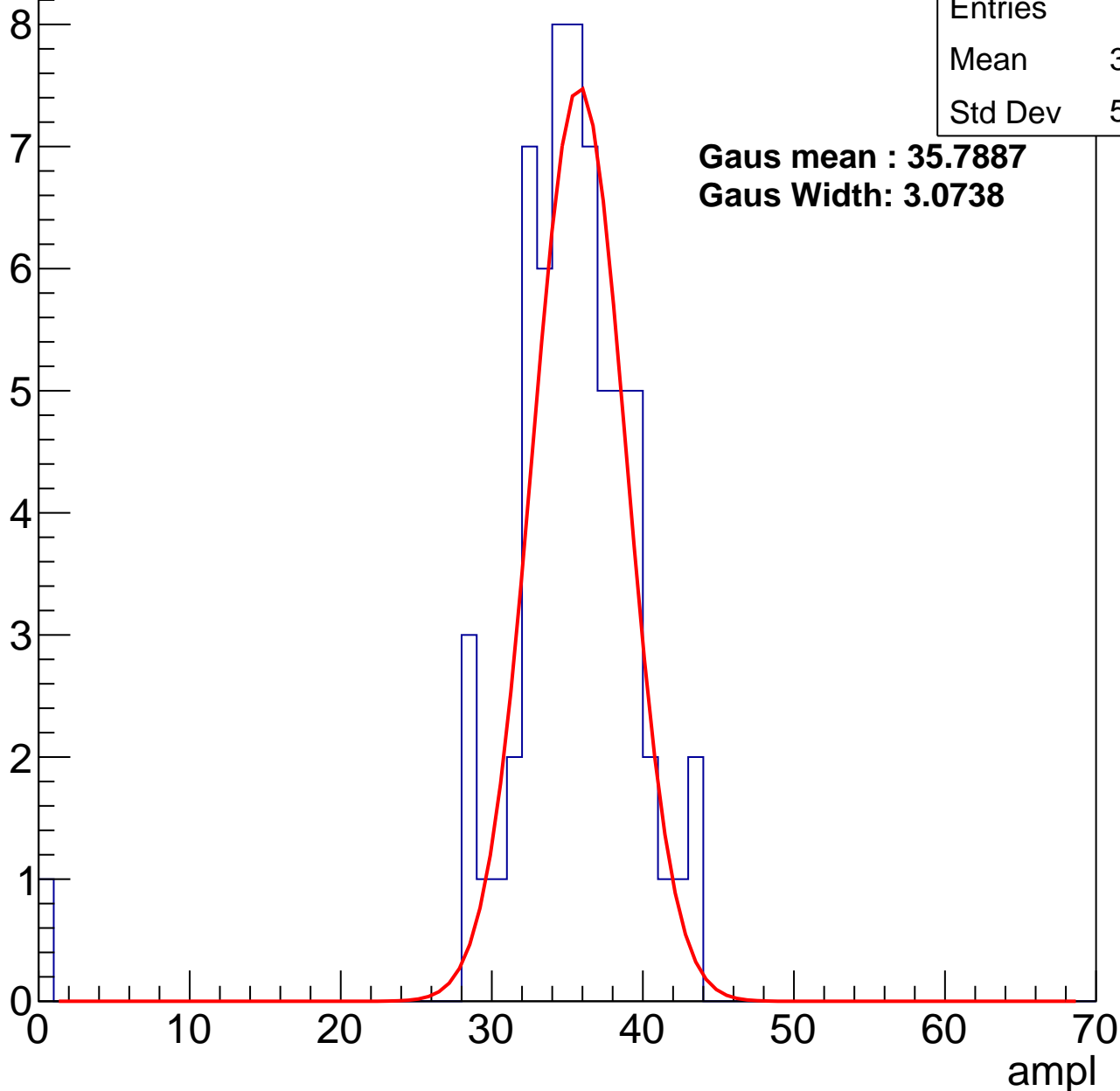
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	34.62
Std Dev	5.499

**Gaus mean : 35.7887**

**Gaus Width: 3.0738**



# B1L103S, U22-ch43, adc2

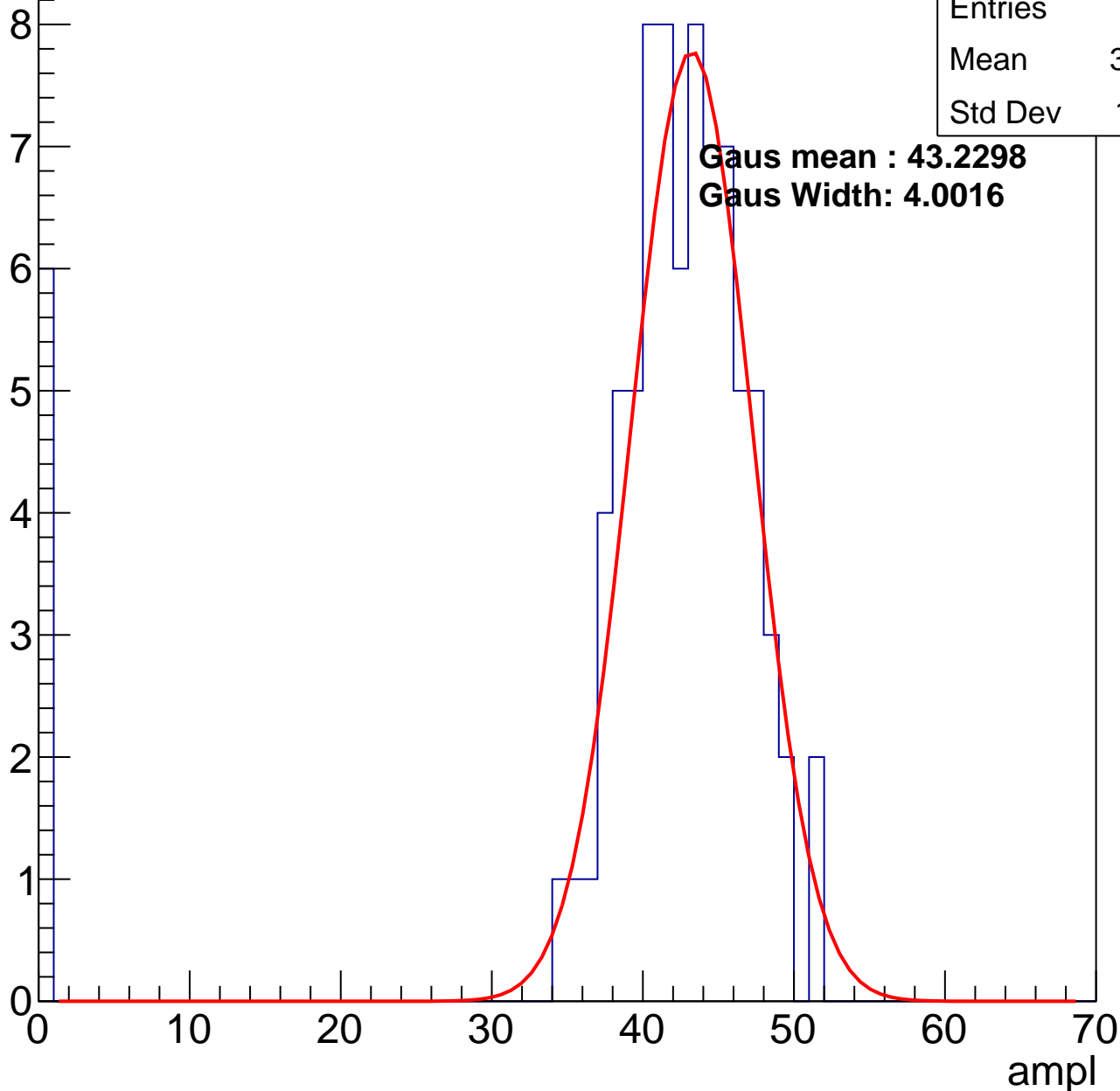
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	39.45
Std Dev	11.51

**Gaus mean : 43.2298**

**Gaus Width: 4.0016**

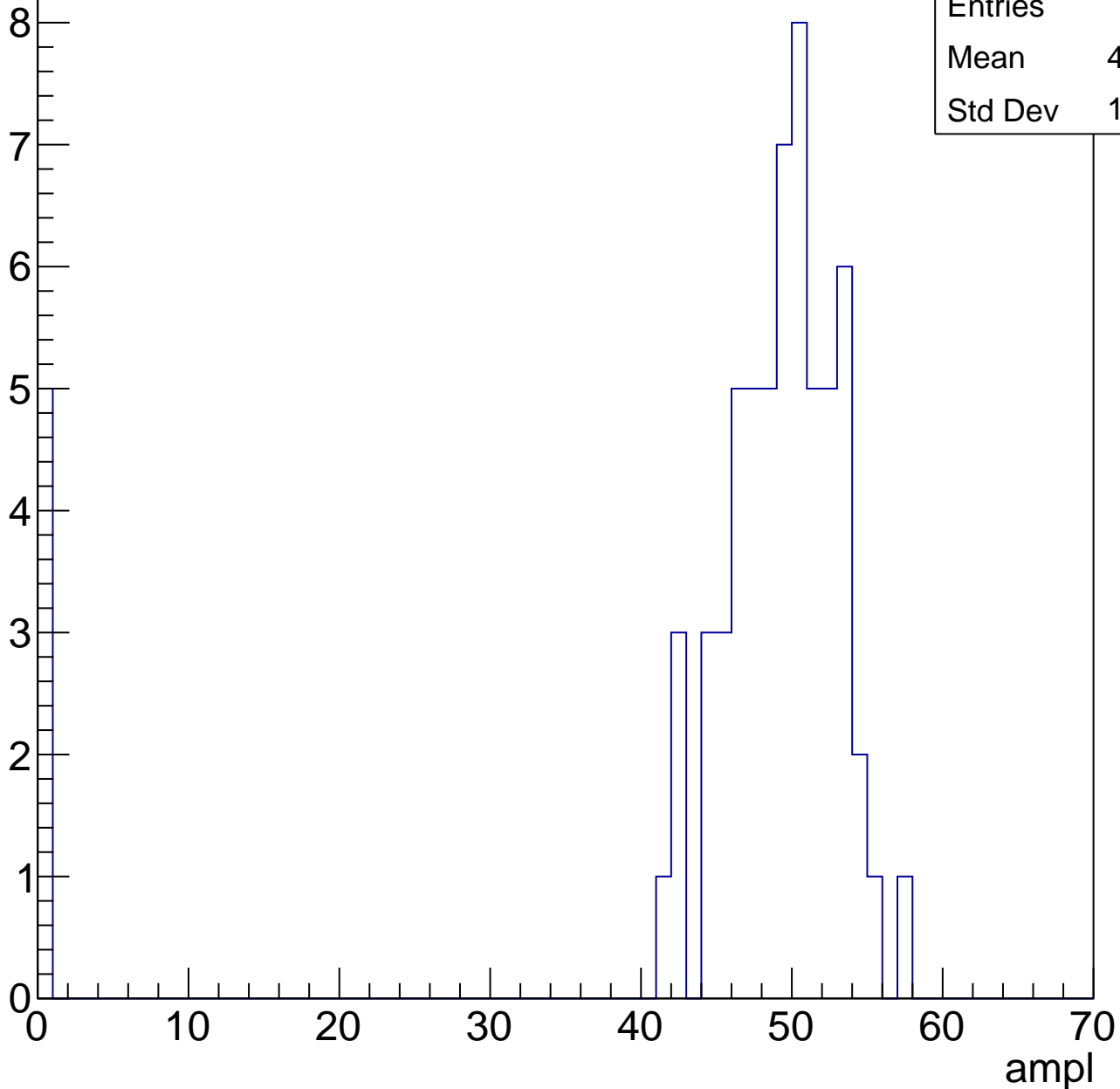


# B1L103S, U22-ch43, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	45.15
Std Dev	13.45

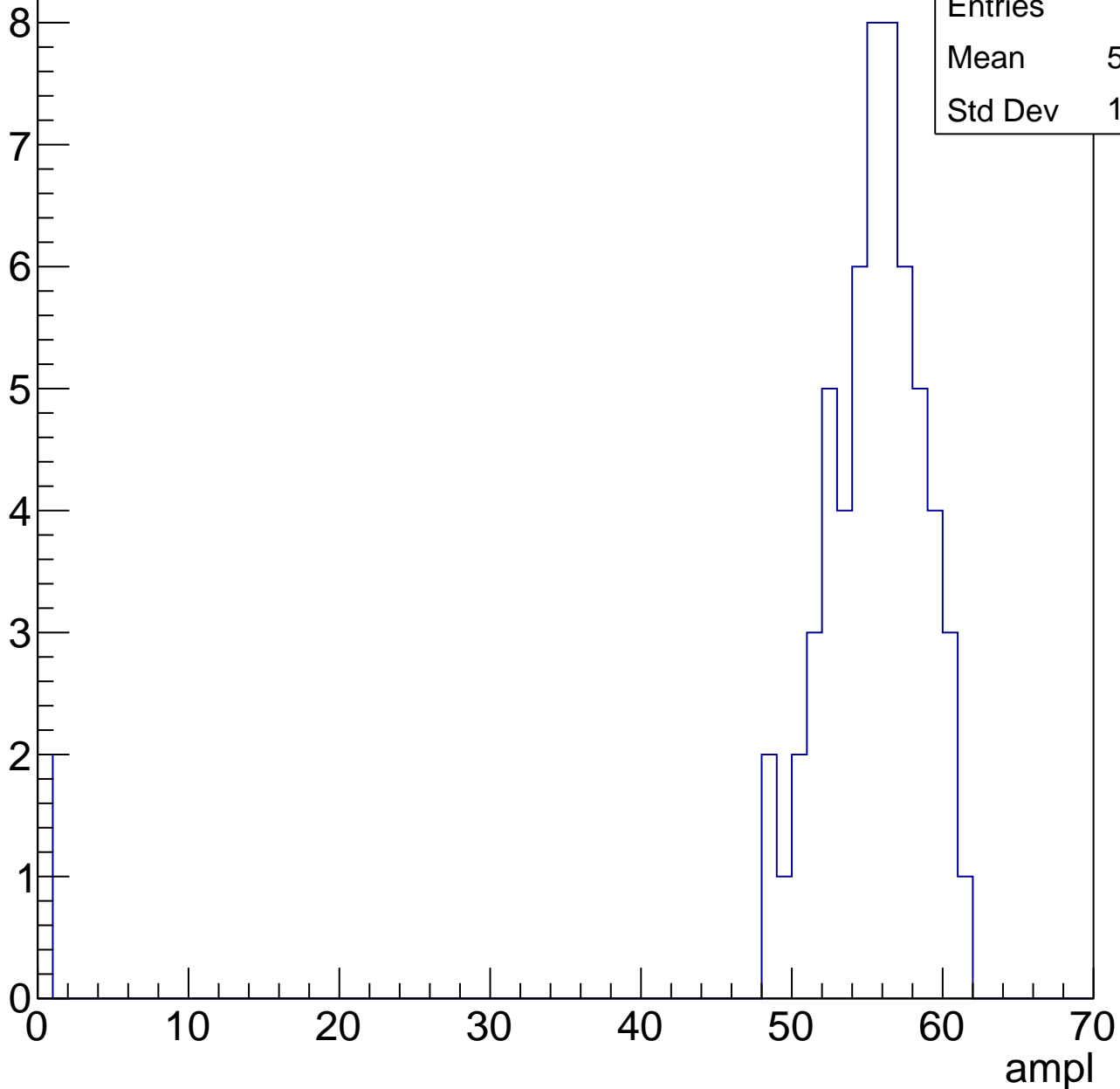


# B1L103S, U22-ch43, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	53.18
Std Dev	10.33



# B1L103S, U22-ch43, adc5

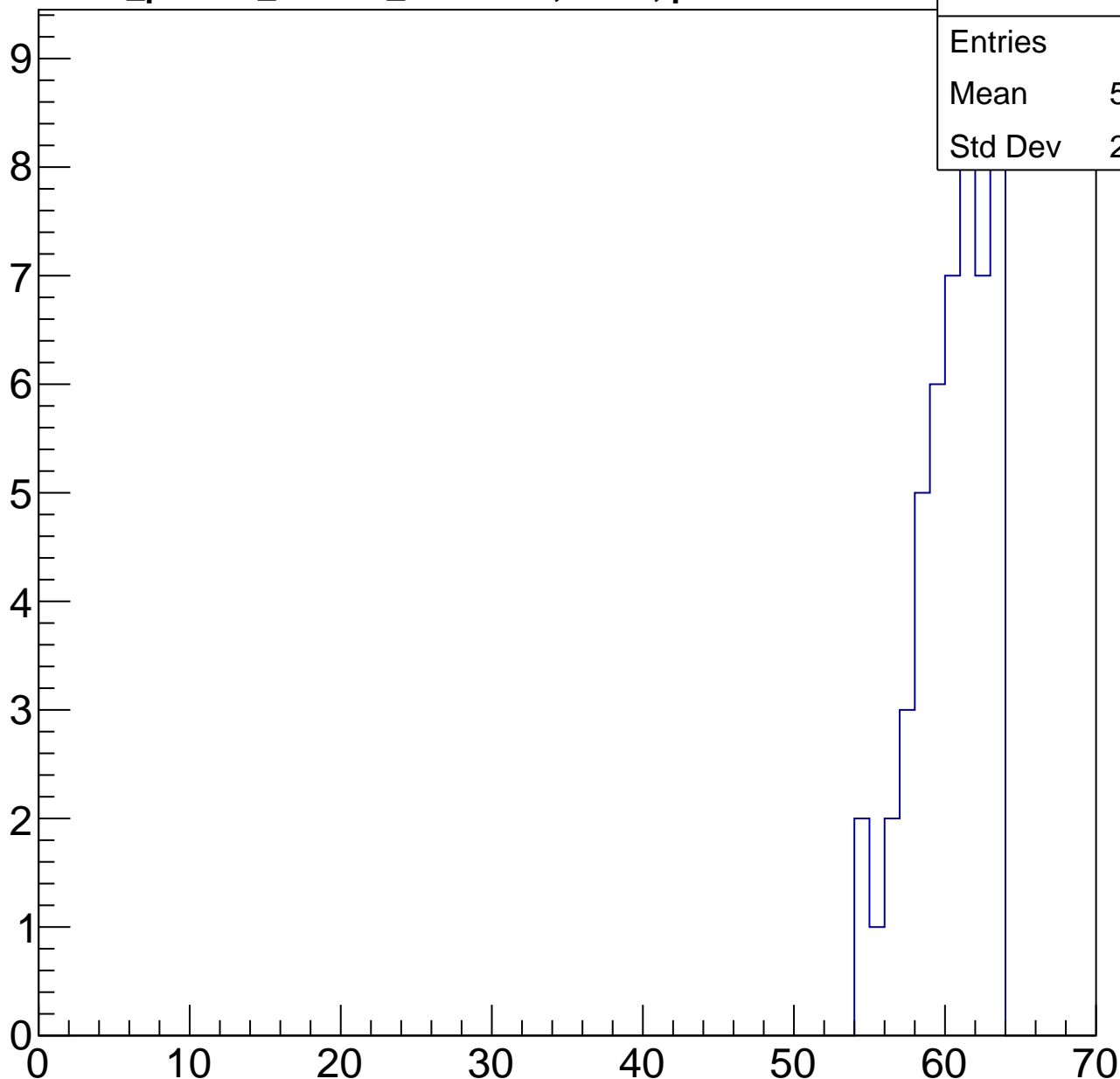
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	59.94
Std Dev	2.412

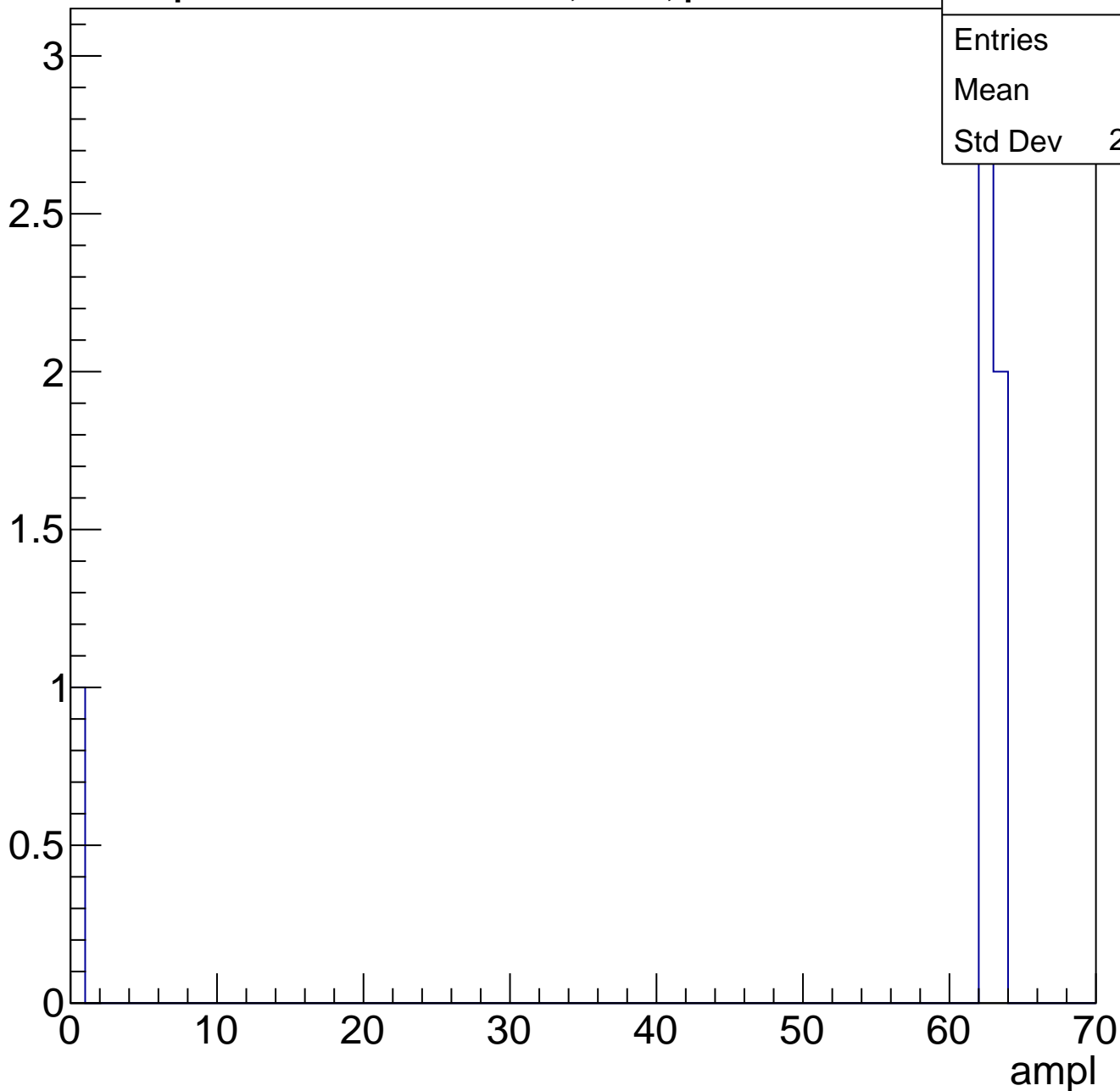
ampl



# B1L103S, U22-ch43, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch43, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch44, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	25
Std Dev	10.4

**Gaus mean : 29.4165**

**Gaus Width: 4.2068**

Entry

10

8

6

4

2

0

0

10

20

30

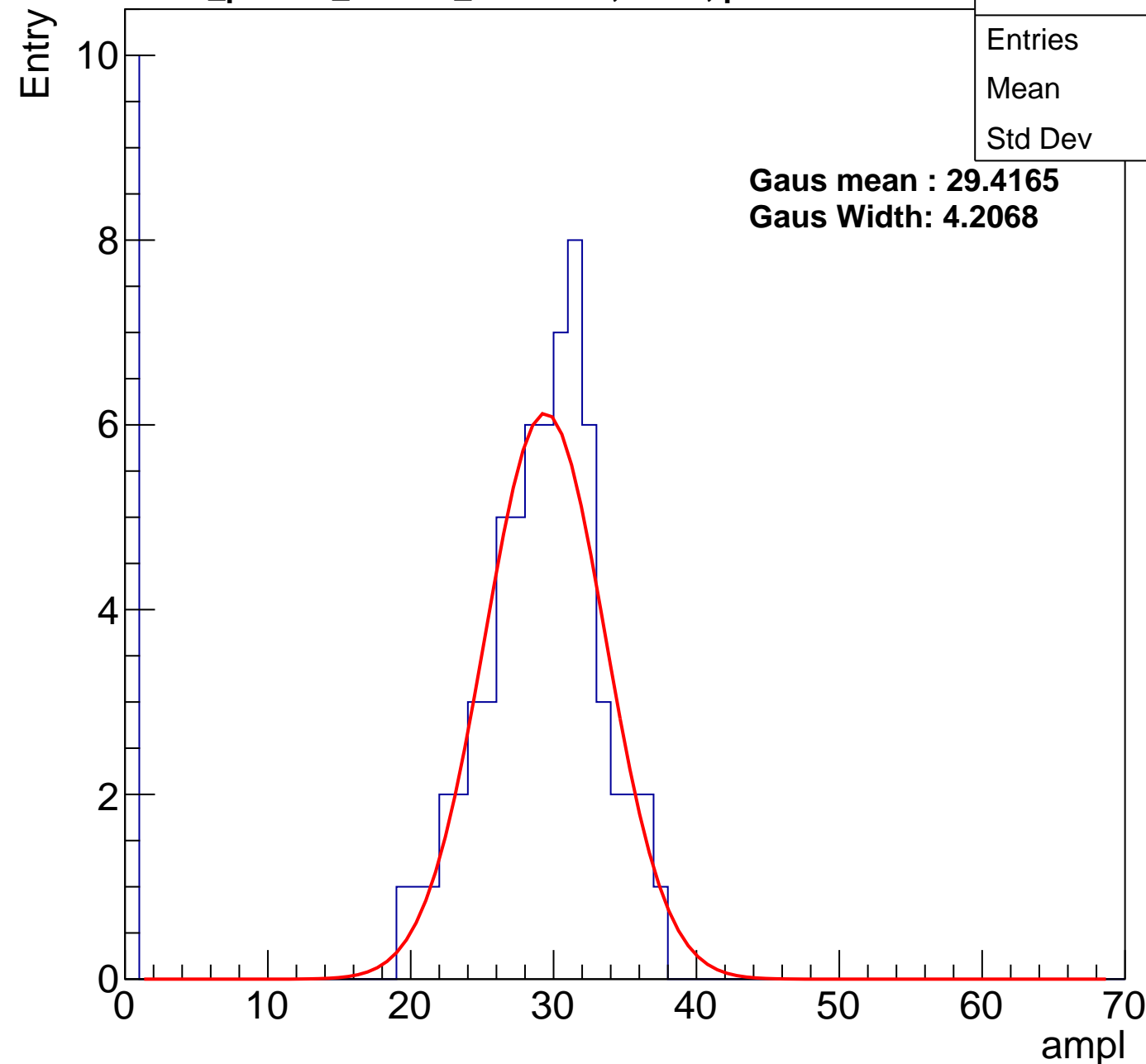
40

50

60

70

ampl



# B1L103S, U22-ch44, adc1

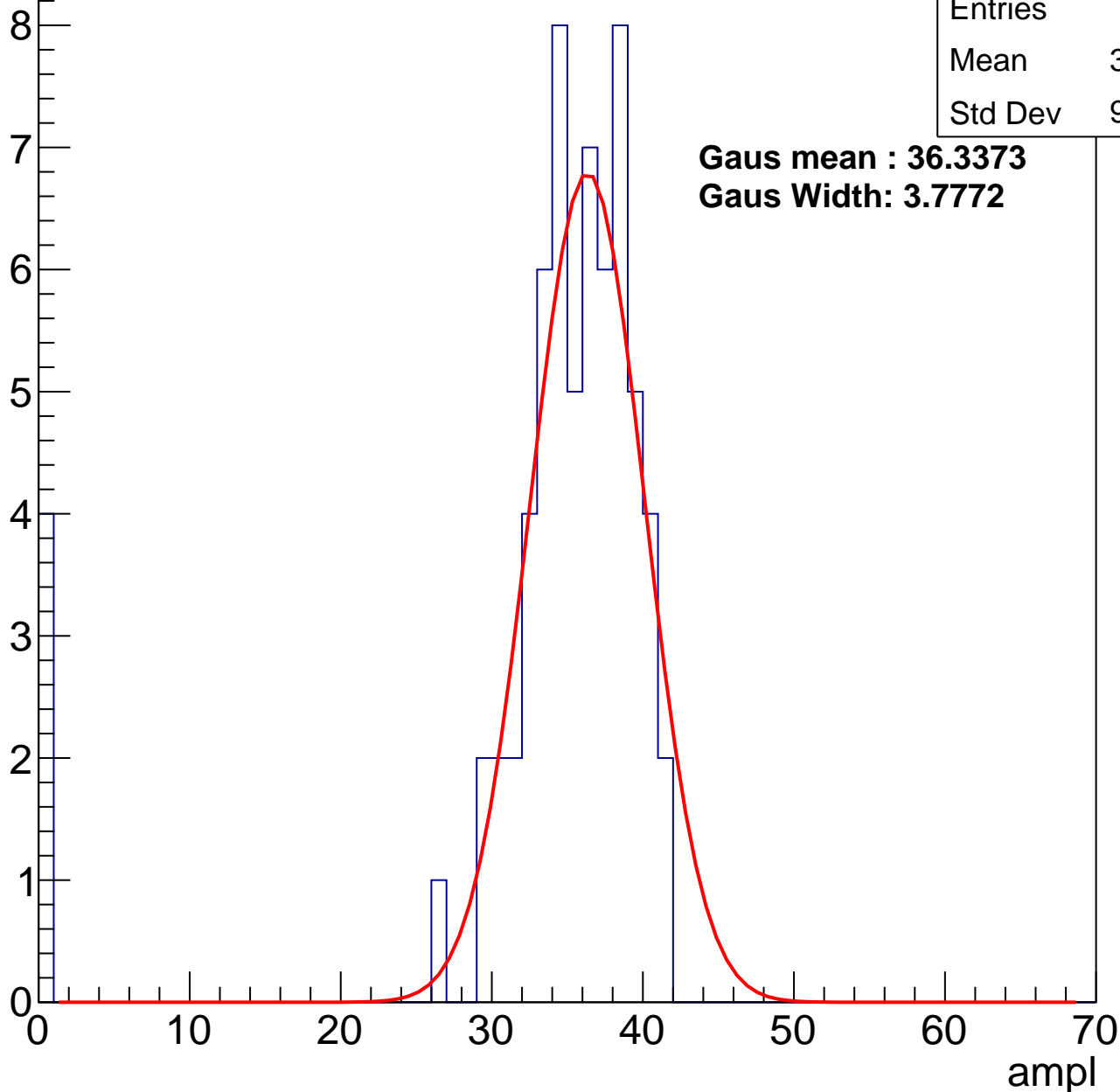
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	33.24
Std Dev	9.007

**Gaus mean : 36.3373**

**Gaus Width: 3.7772**



# B1L103S, U22-ch44, adc2

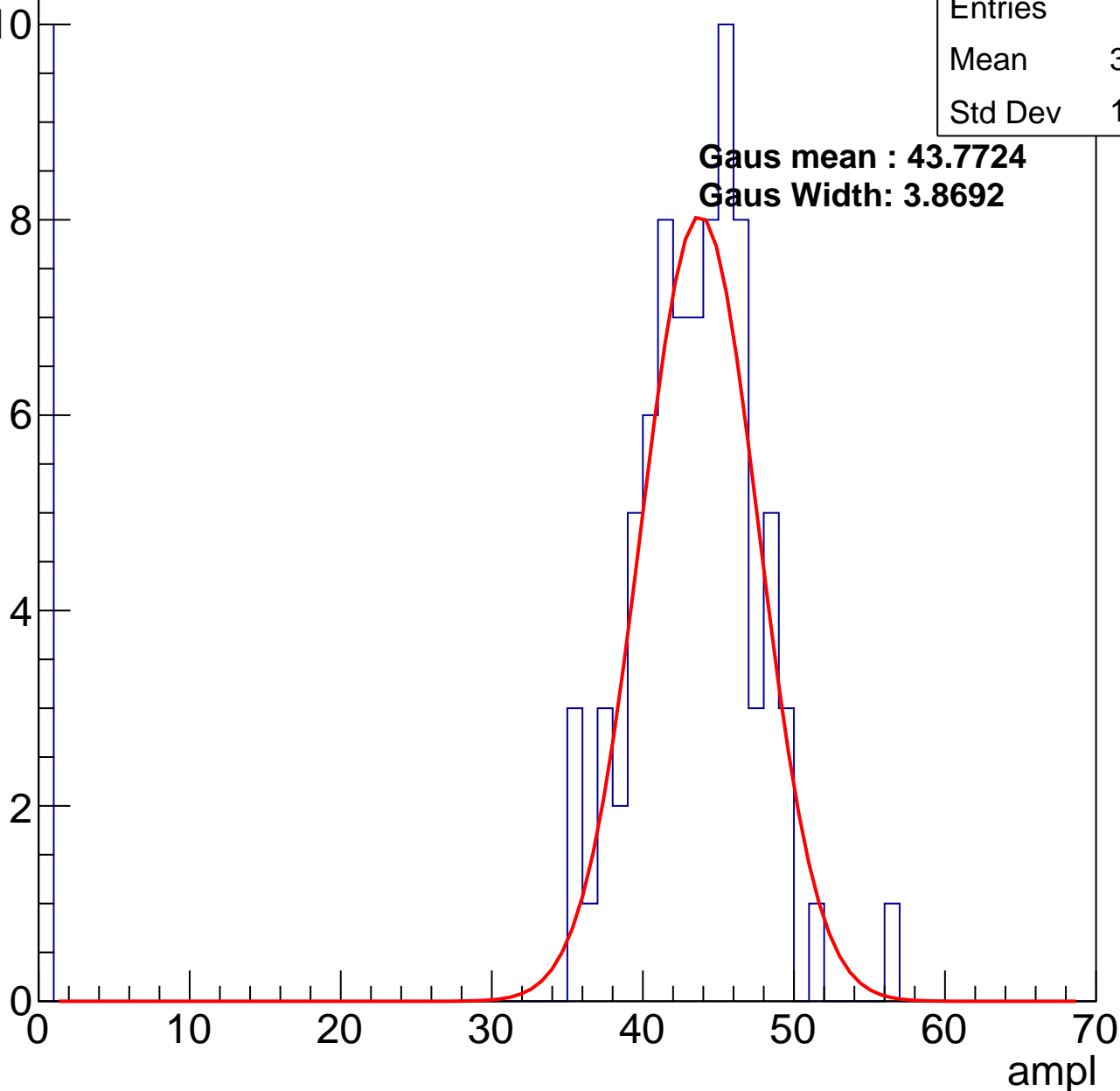
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	91
Mean	38.36
Std Dev	13.96

**Gaus mean : 43.7724**

**Gaus Width: 3.8692**

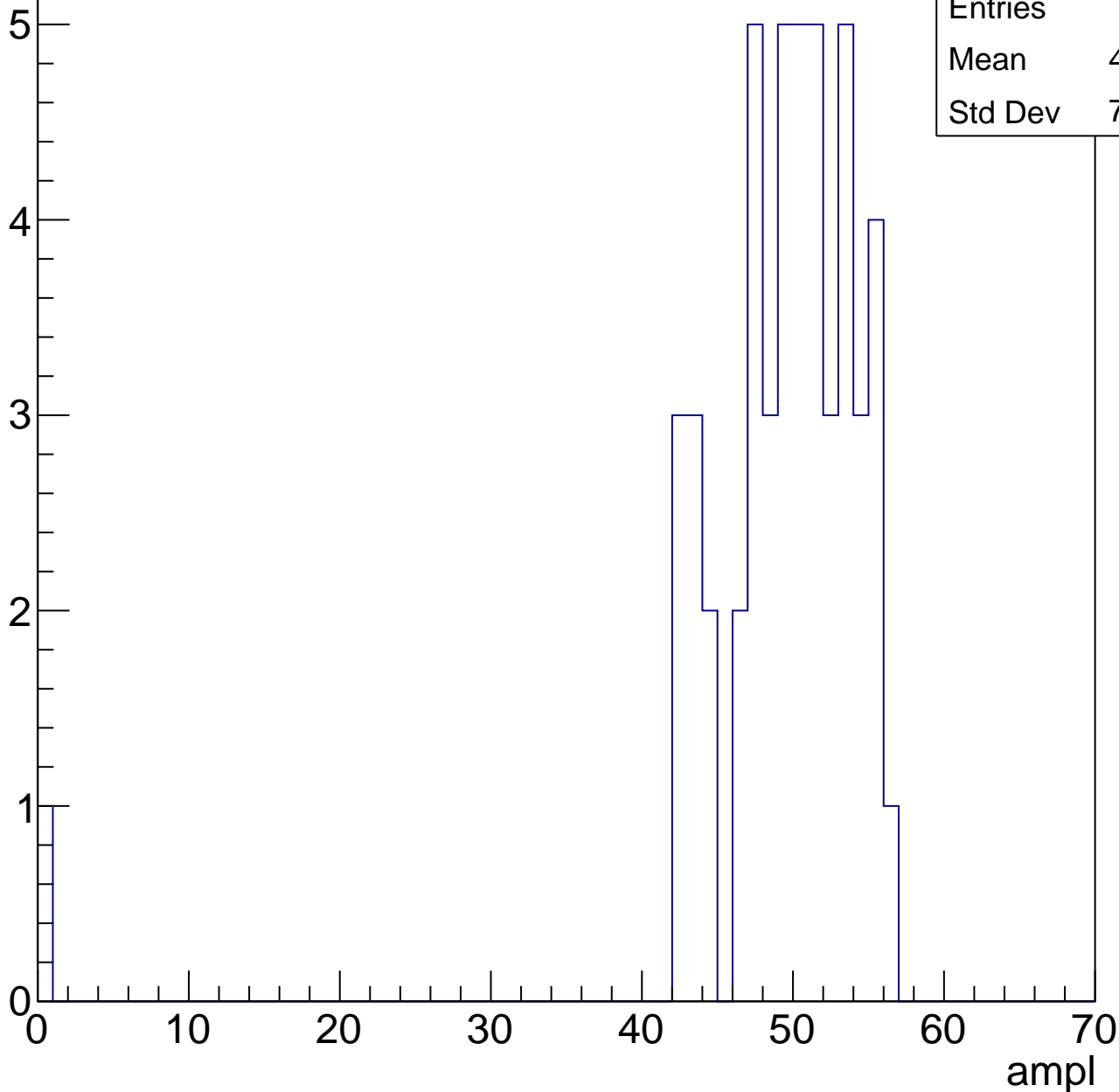


# B1L103S, U22-ch44, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	48.46
Std Dev	7.915

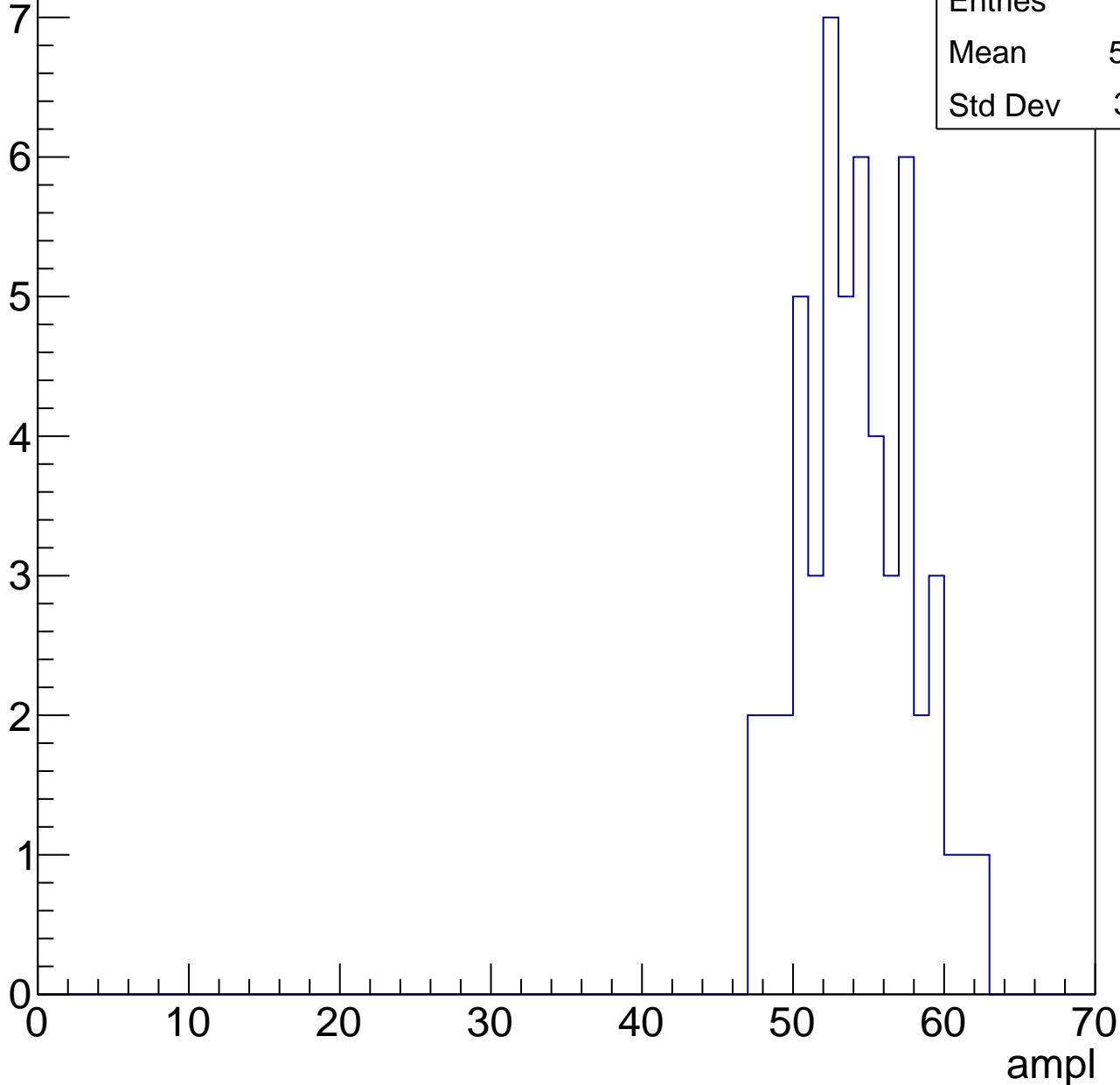


# B1L103S, U22-ch44, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.77
Std Dev	3.591

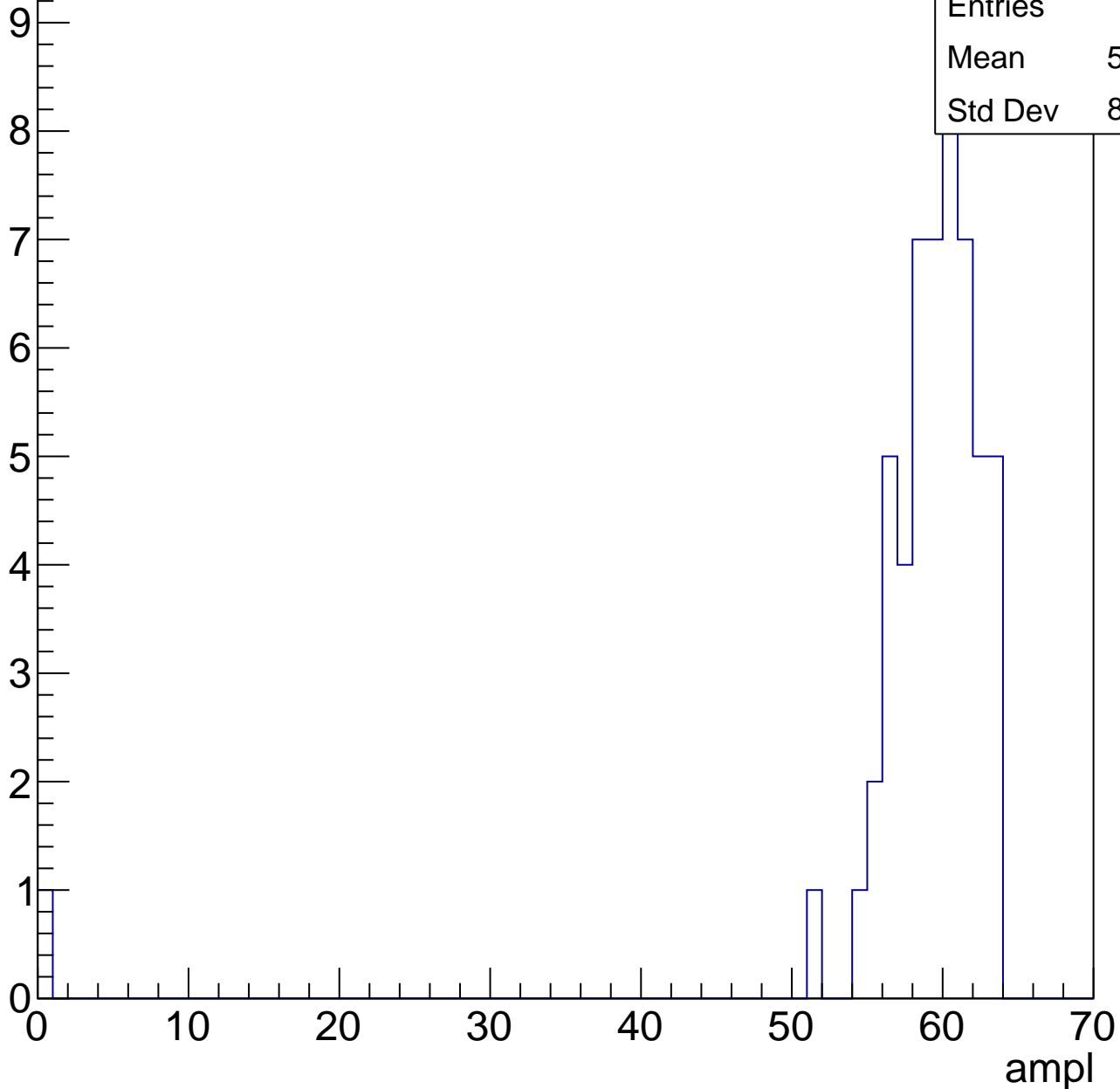


# B1L103S, U22-ch44, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

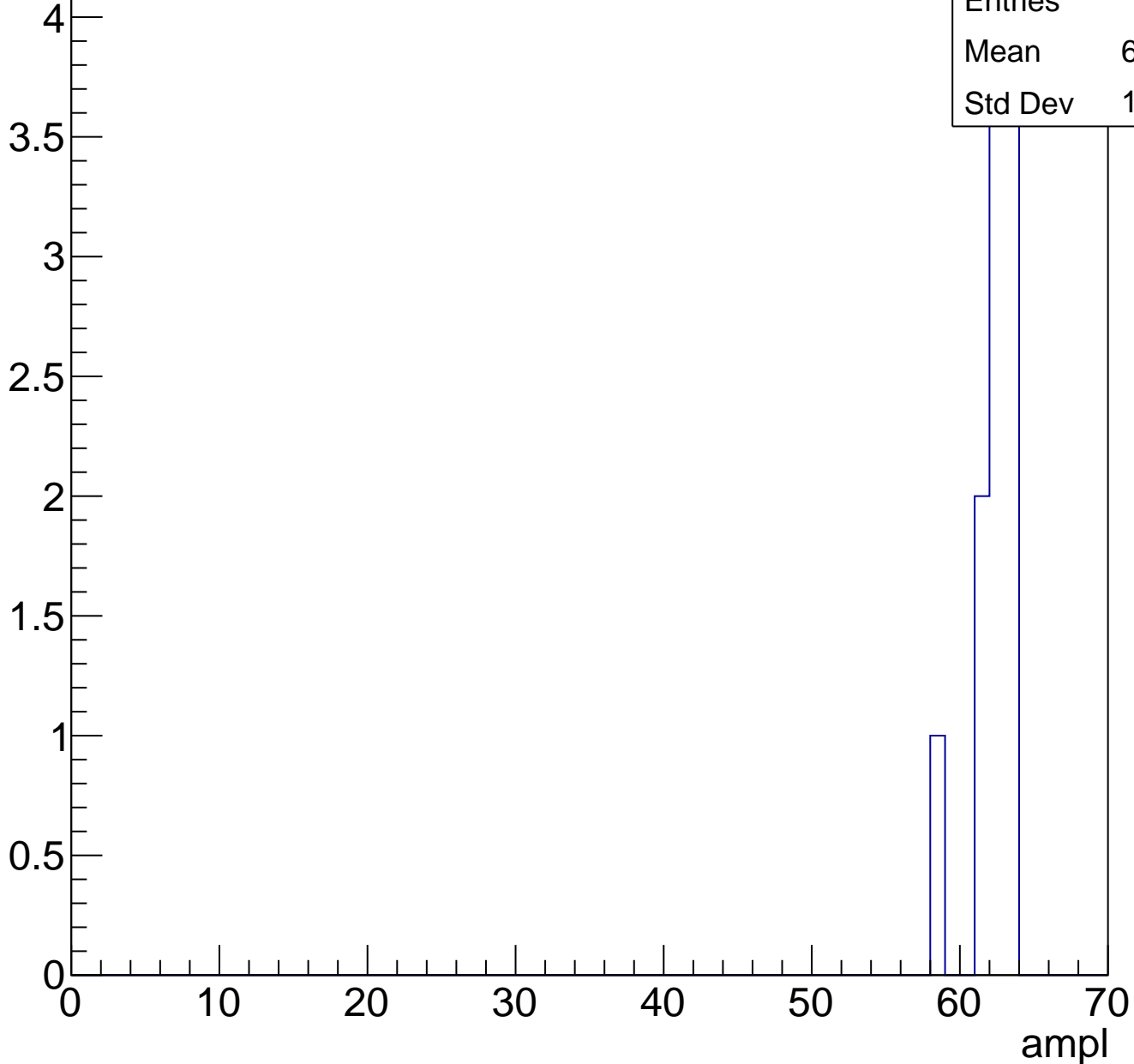
Entries	54
Mean	58.04
Std Dev	8.369



# B1L103S, U22-ch44, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch44, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



# B1L103S, U22-ch45, adc0

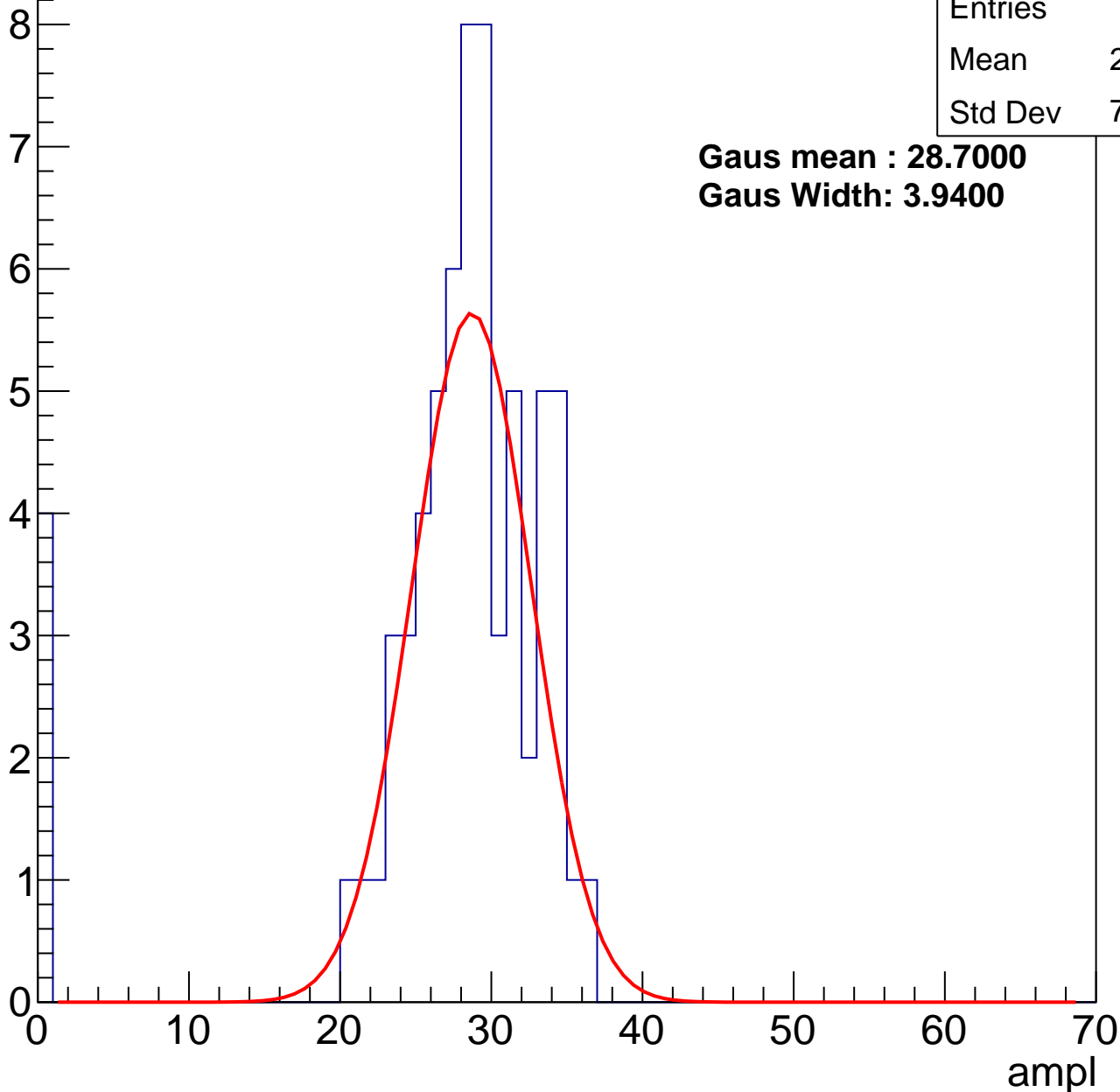
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	26.77
Std Dev	7.673

**Gaus mean : 28.7000**

**Gaus Width: 3.9400**



# B1L103S, U22-ch45, adc1

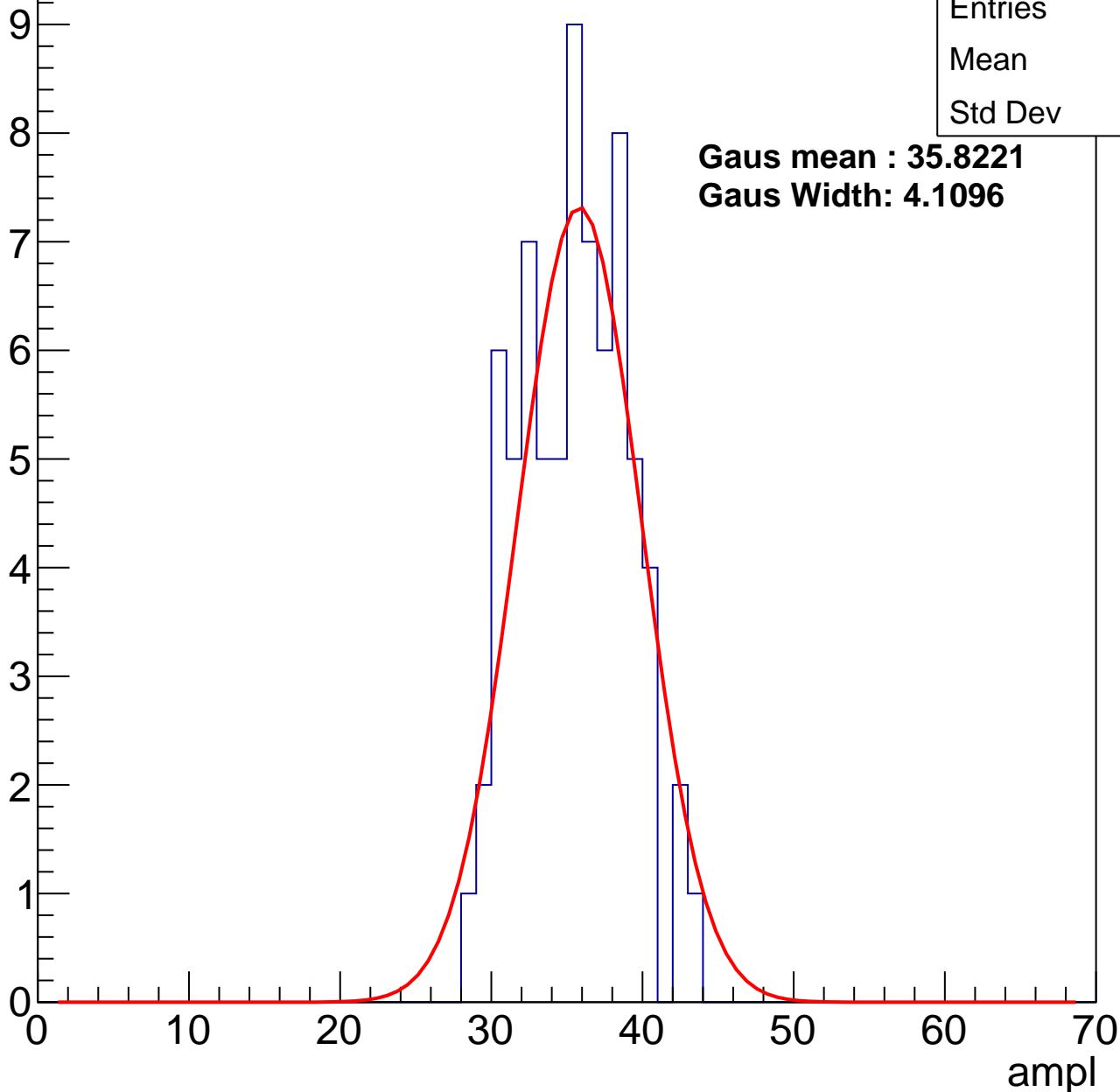
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35
Std Dev	3.48

**Gaus mean : 35.8221**

**Gaus Width: 4.1096**



# B1L103S, U22-ch45, adc2

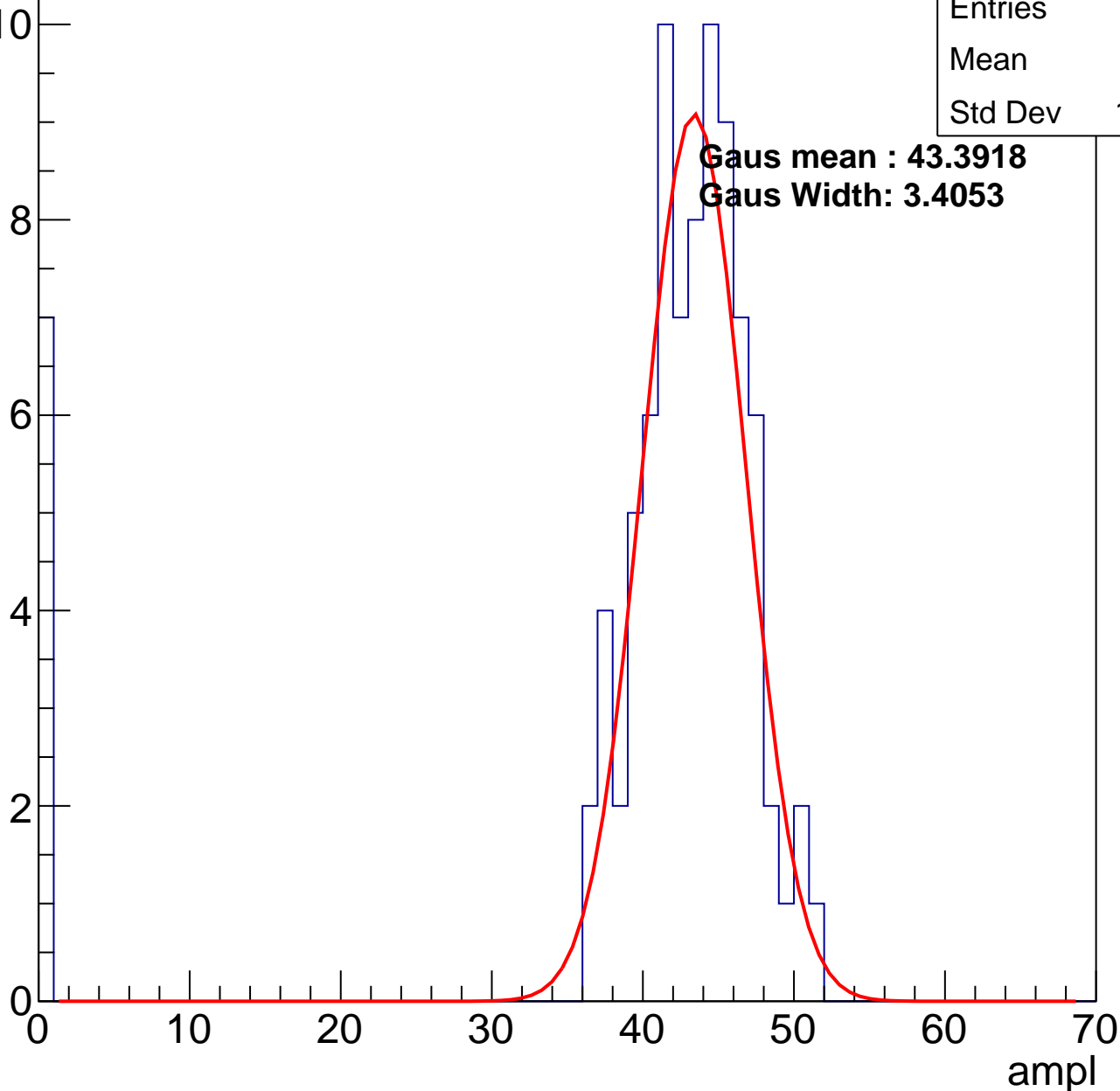
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	89
Mean	39.6
Std Dev	12.01

**Gaus mean : 43.3918**

**Gaus Width: 3.4053**

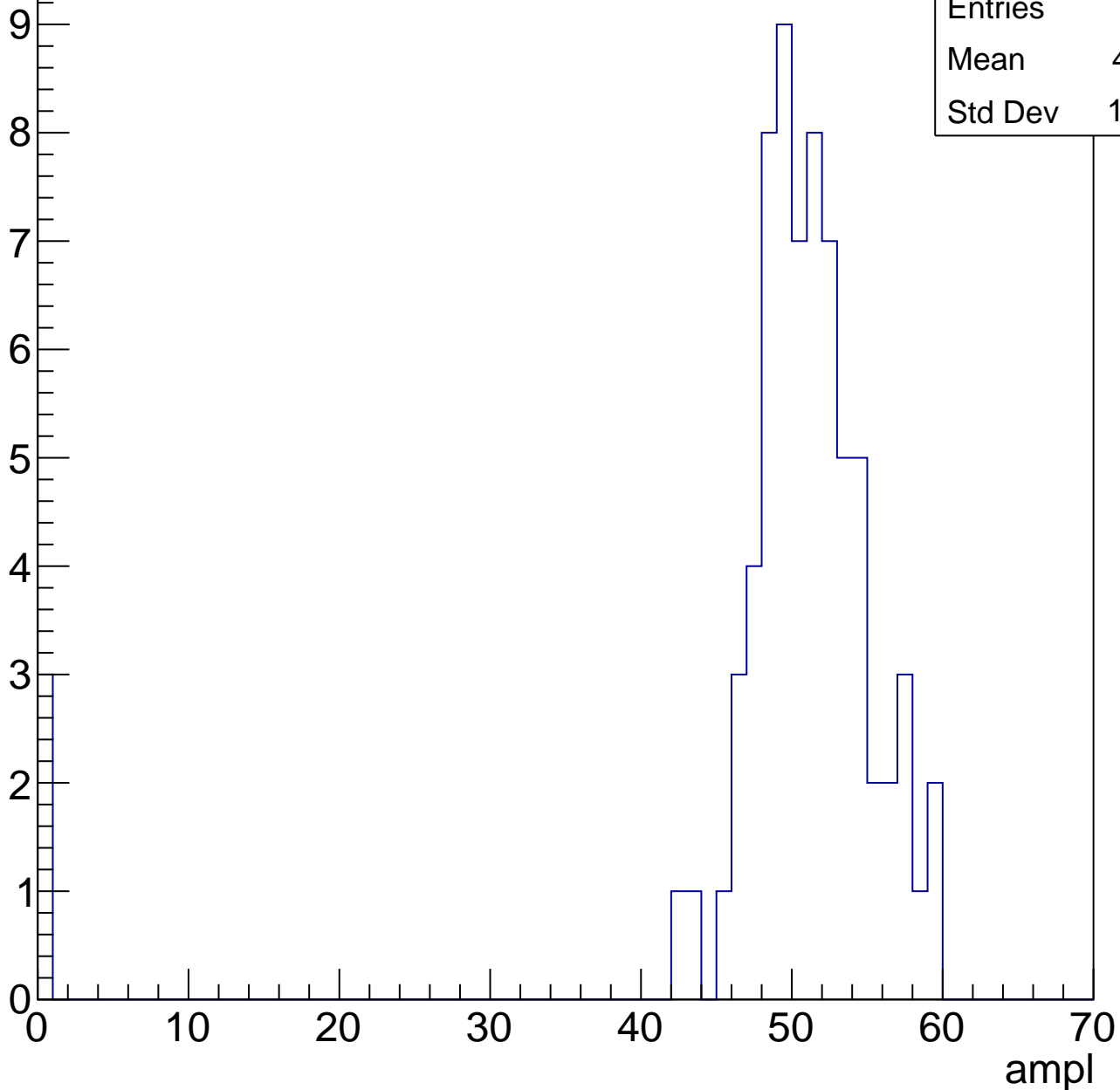


# B1L103S, U22-ch45, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	48.71
Std Dev	10.74

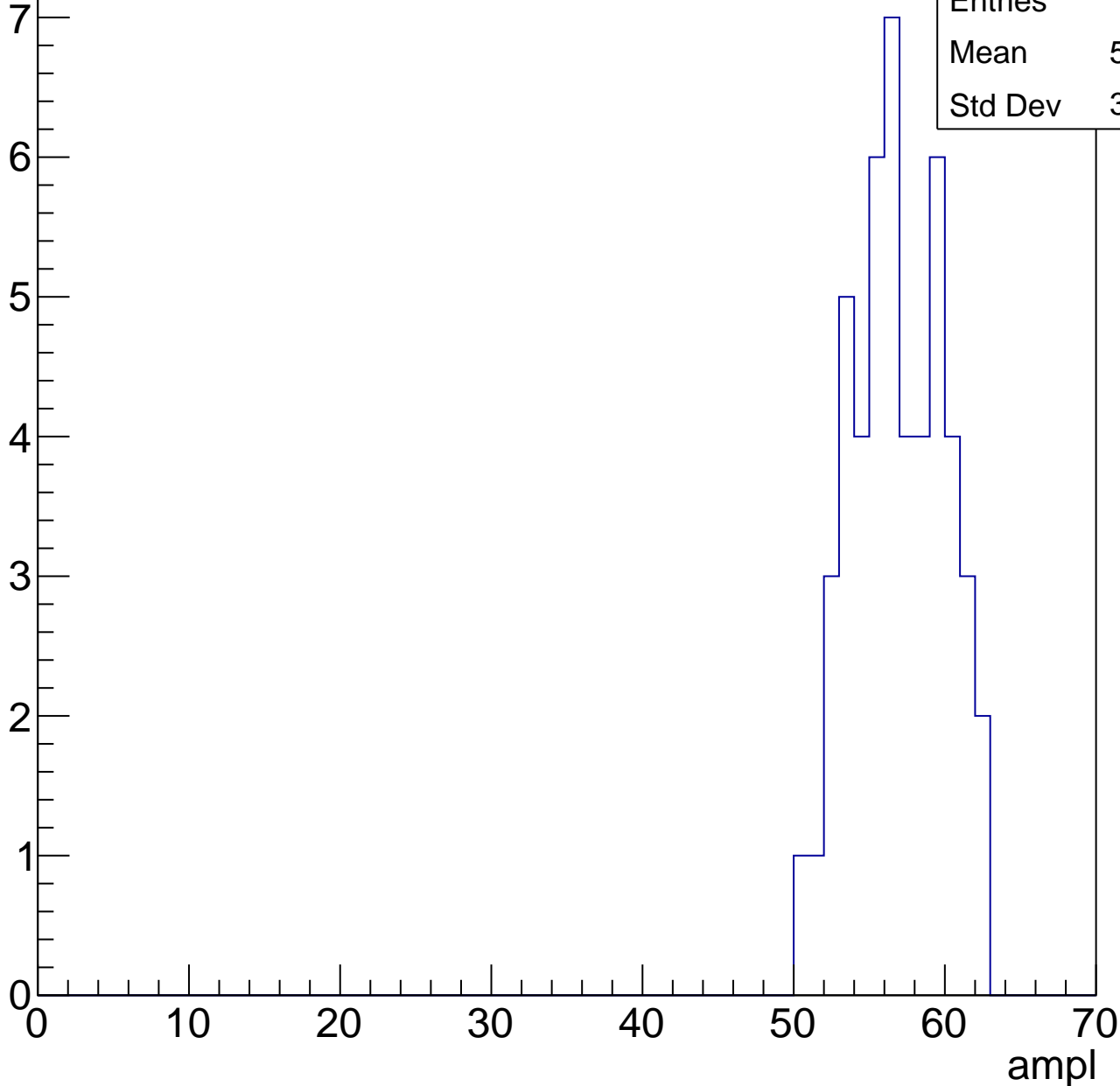


# B1L103S, U22-ch45, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	56.42
Std Dev	3.007

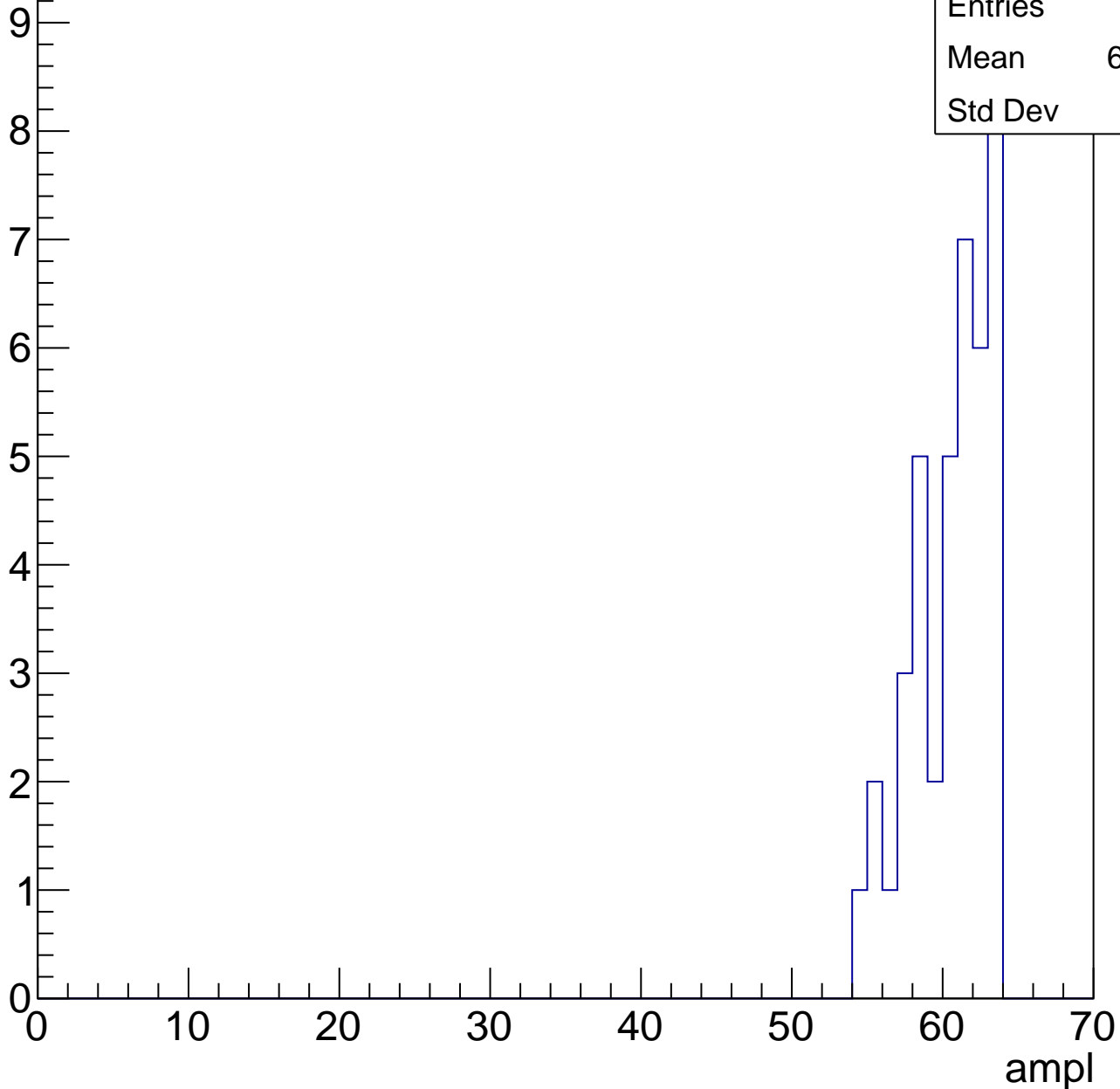


# B1L103S, U22-ch45, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

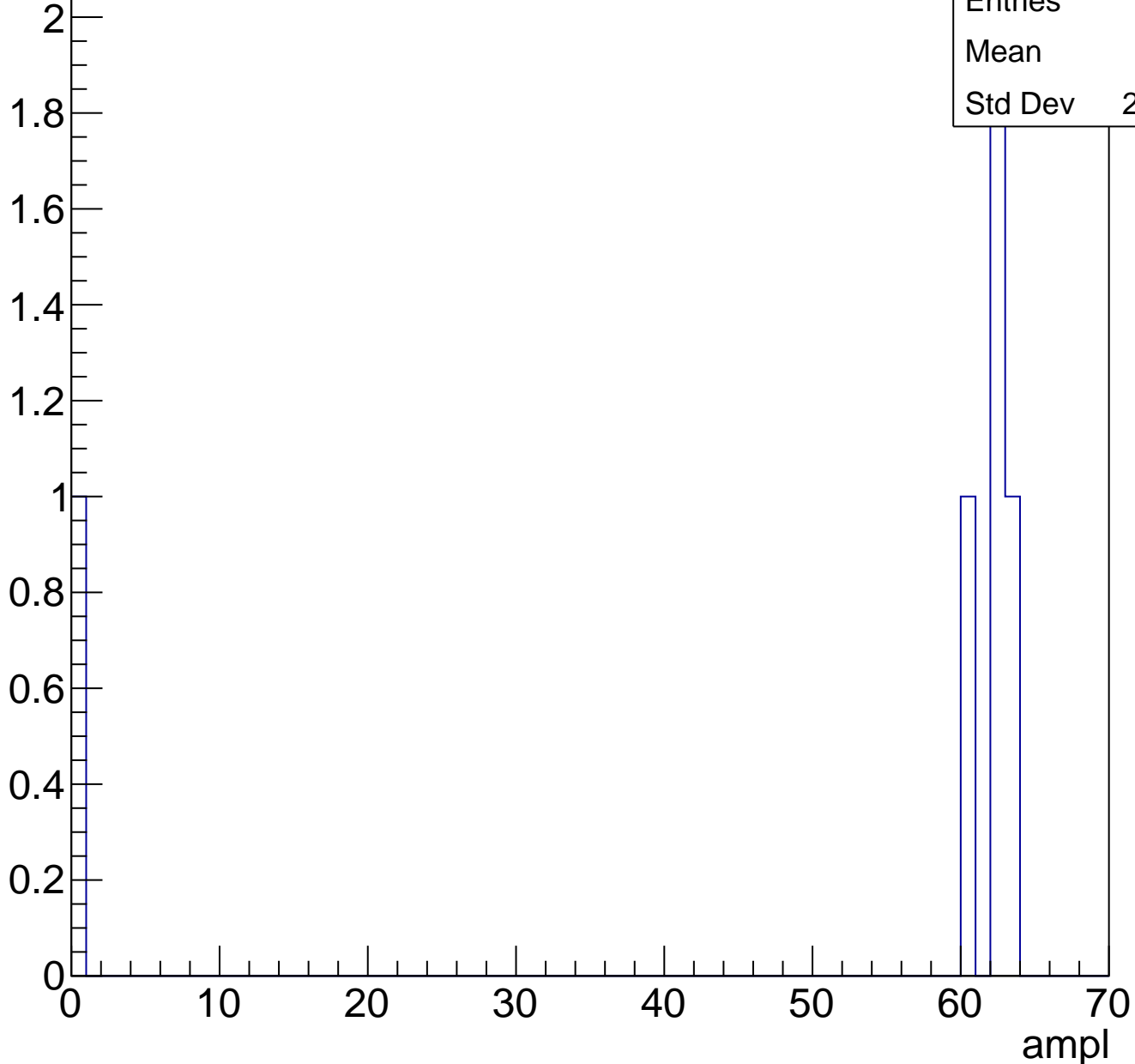
Entries	41
Mean	60.12
Std Dev	2.53



# B1L103S, U22-ch45, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch45, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch46, adc0

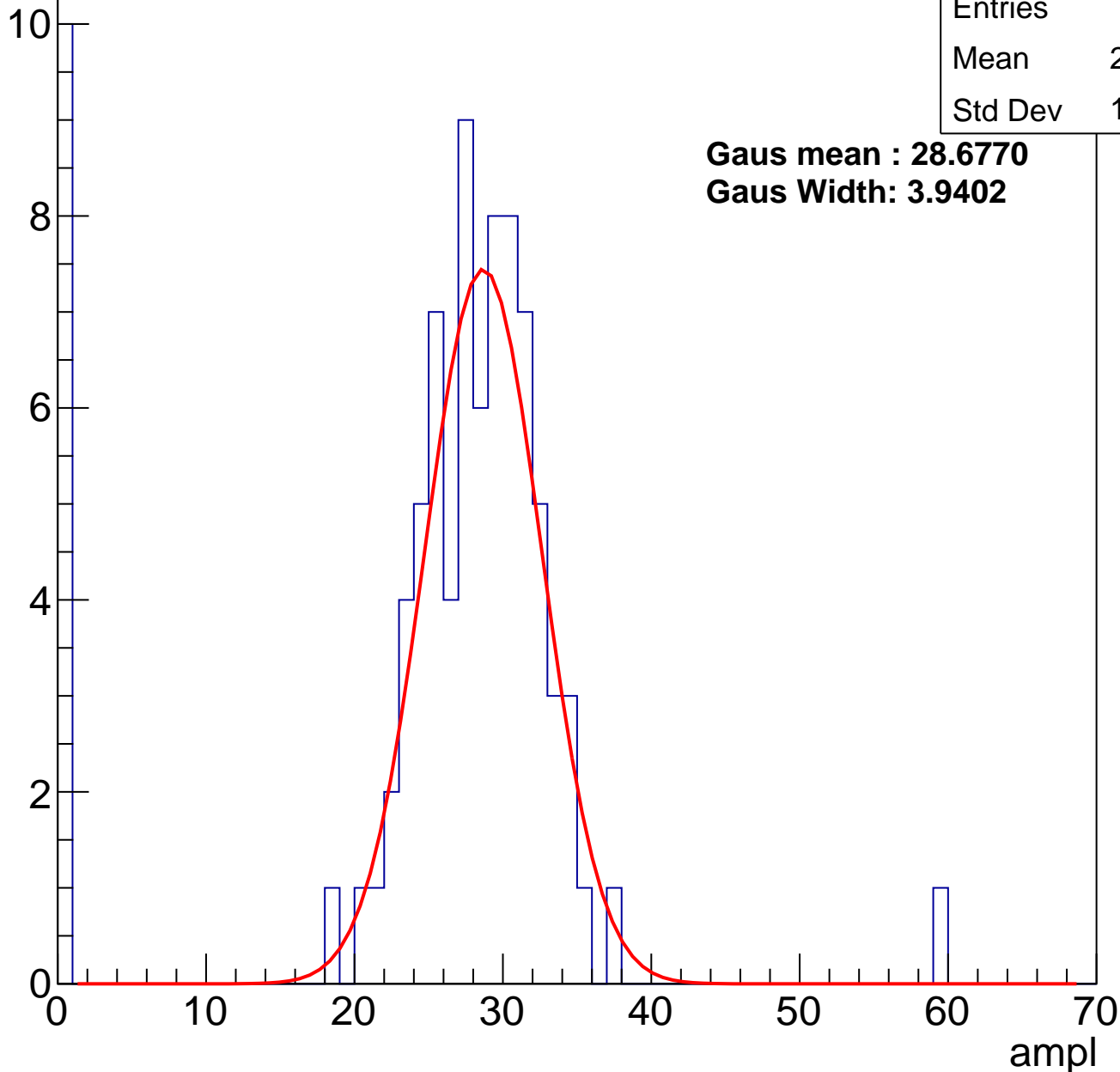
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	25.13
Std Dev	10.25

**Gaus mean : 28.6770**

**Gaus Width: 3.9402**

Entry



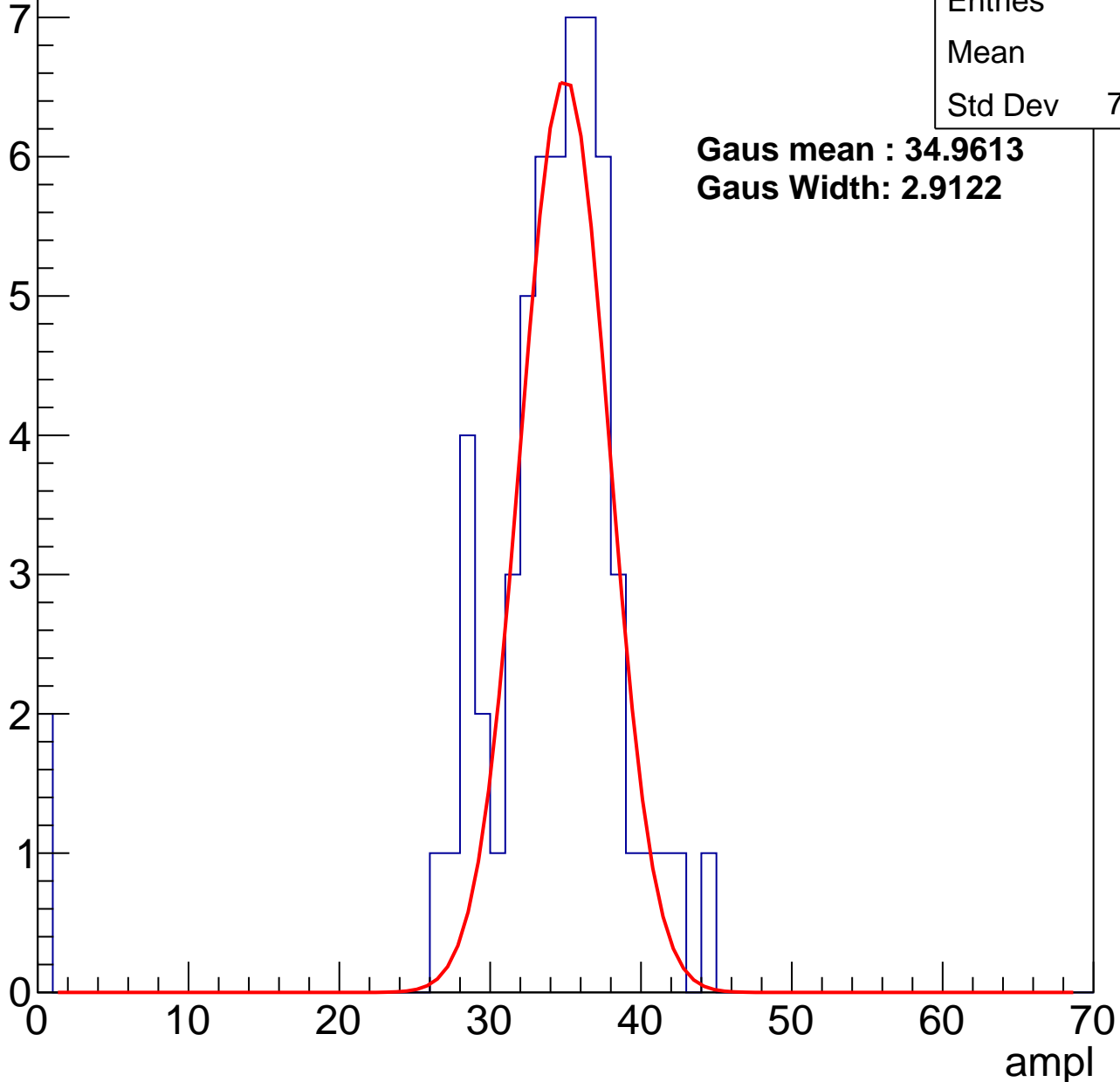
# B1L103S, U22-ch46, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	33
Std Dev	7.178

**Gaus mean : 34.9613**  
**Gaus Width: 2.9122**



# B1L103S, U22-ch46, adc2

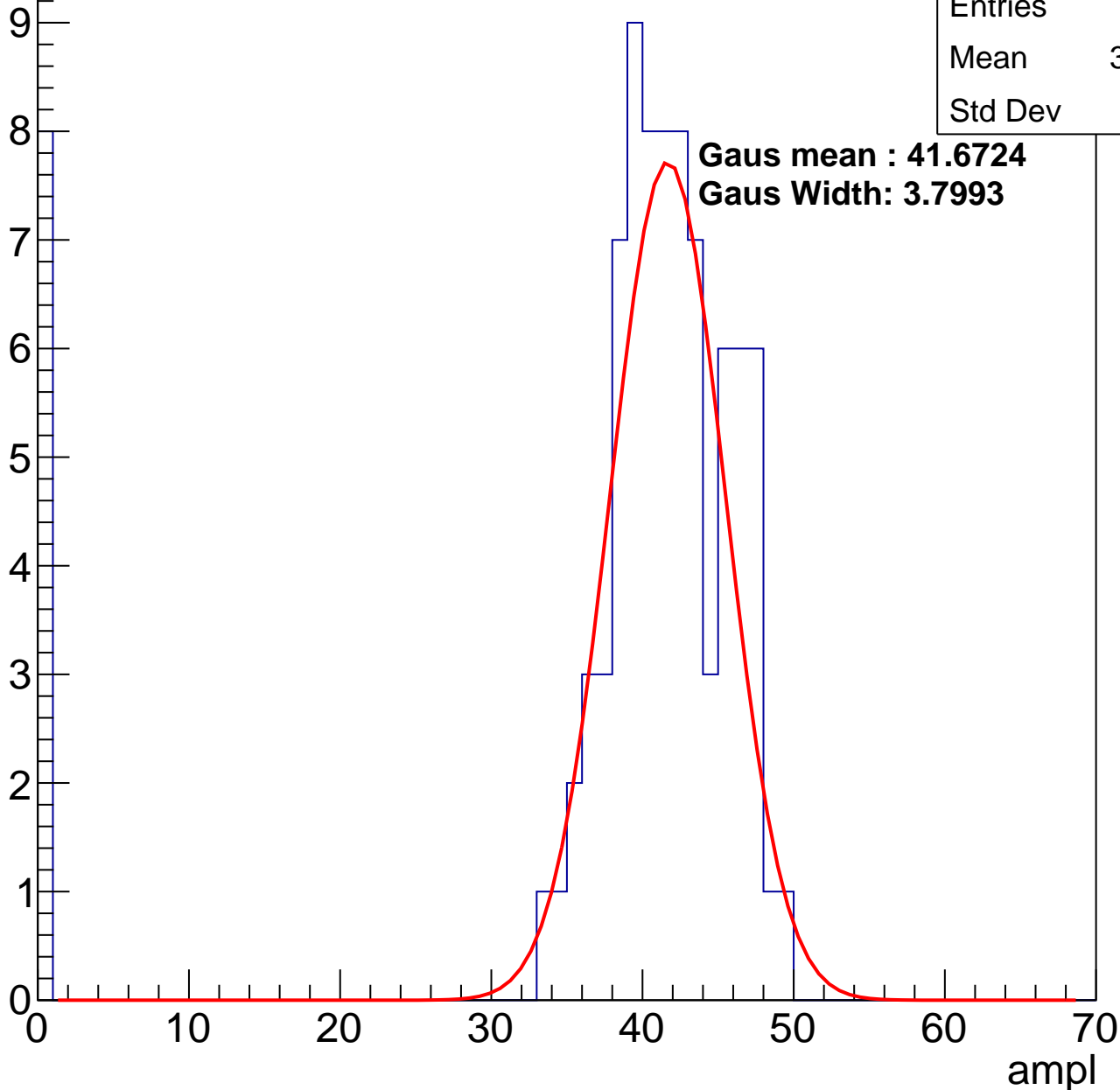
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	88
Mean	37.67
Std Dev	12.4

**Gaus mean : 41.6724**

**Gaus Width: 3.7993**

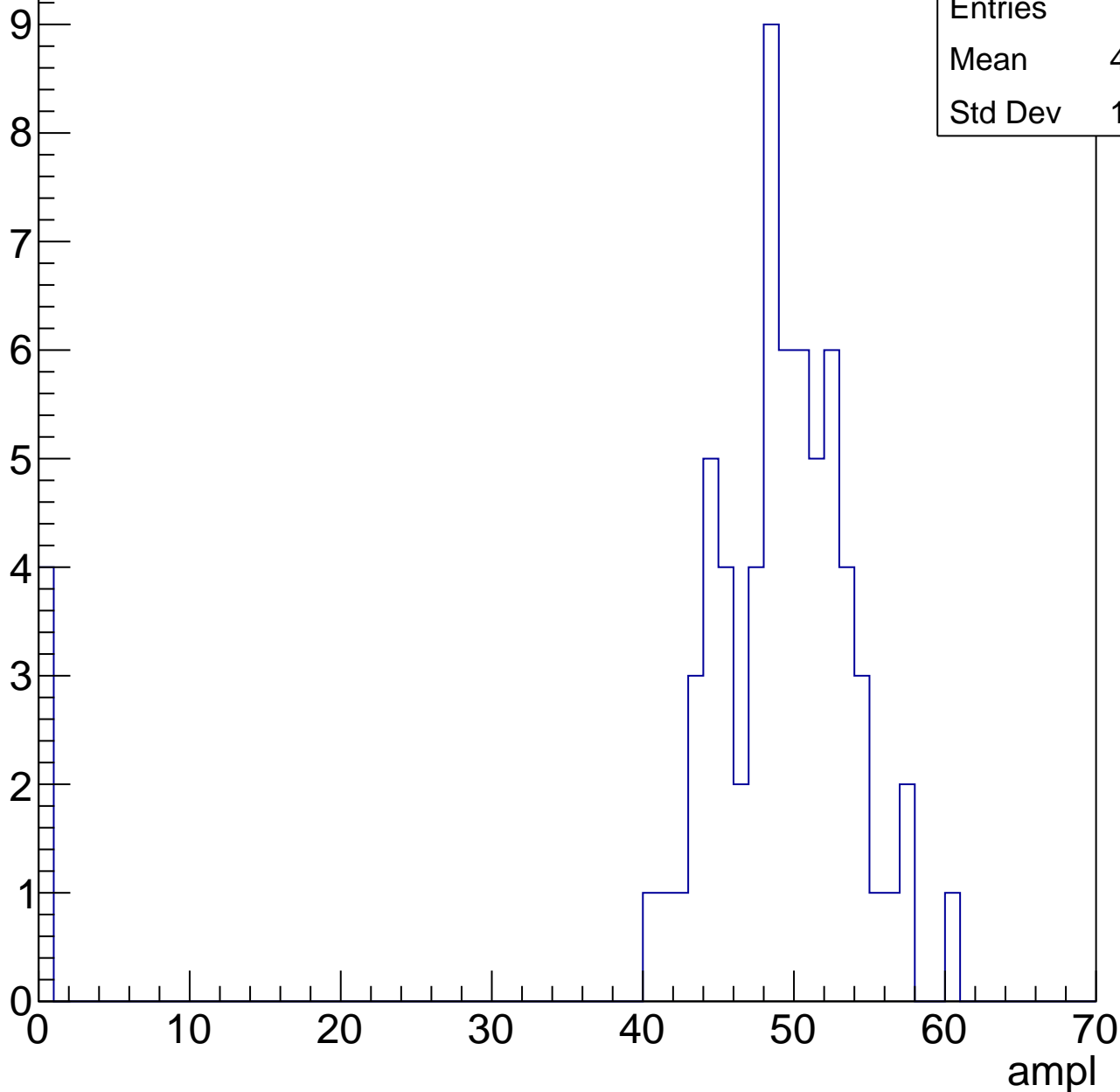


# B1L103S, U22-ch46, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.14
Std Dev	12.12

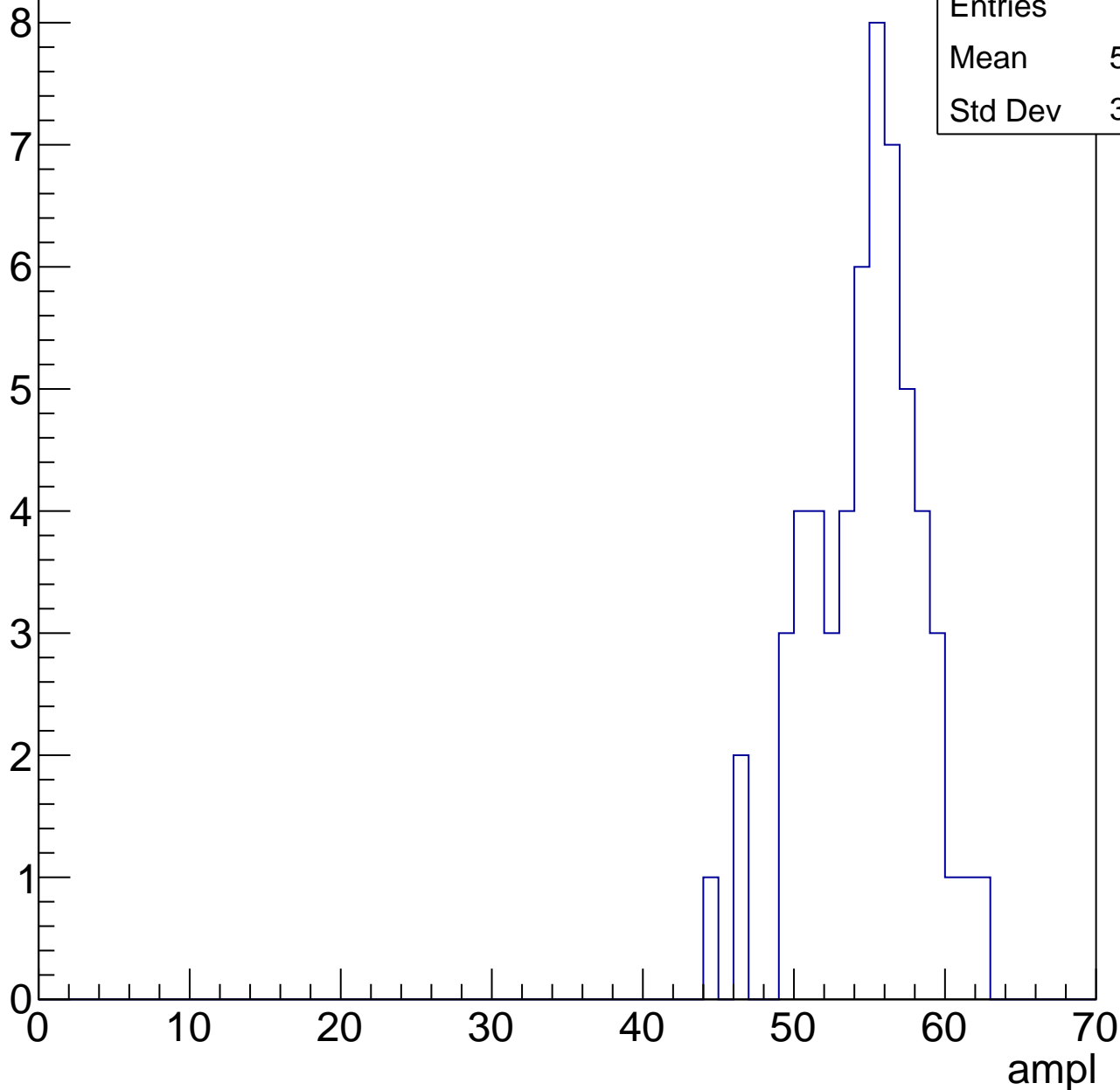


# B1L103S, U22-ch46, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.18
Std Dev	3.714

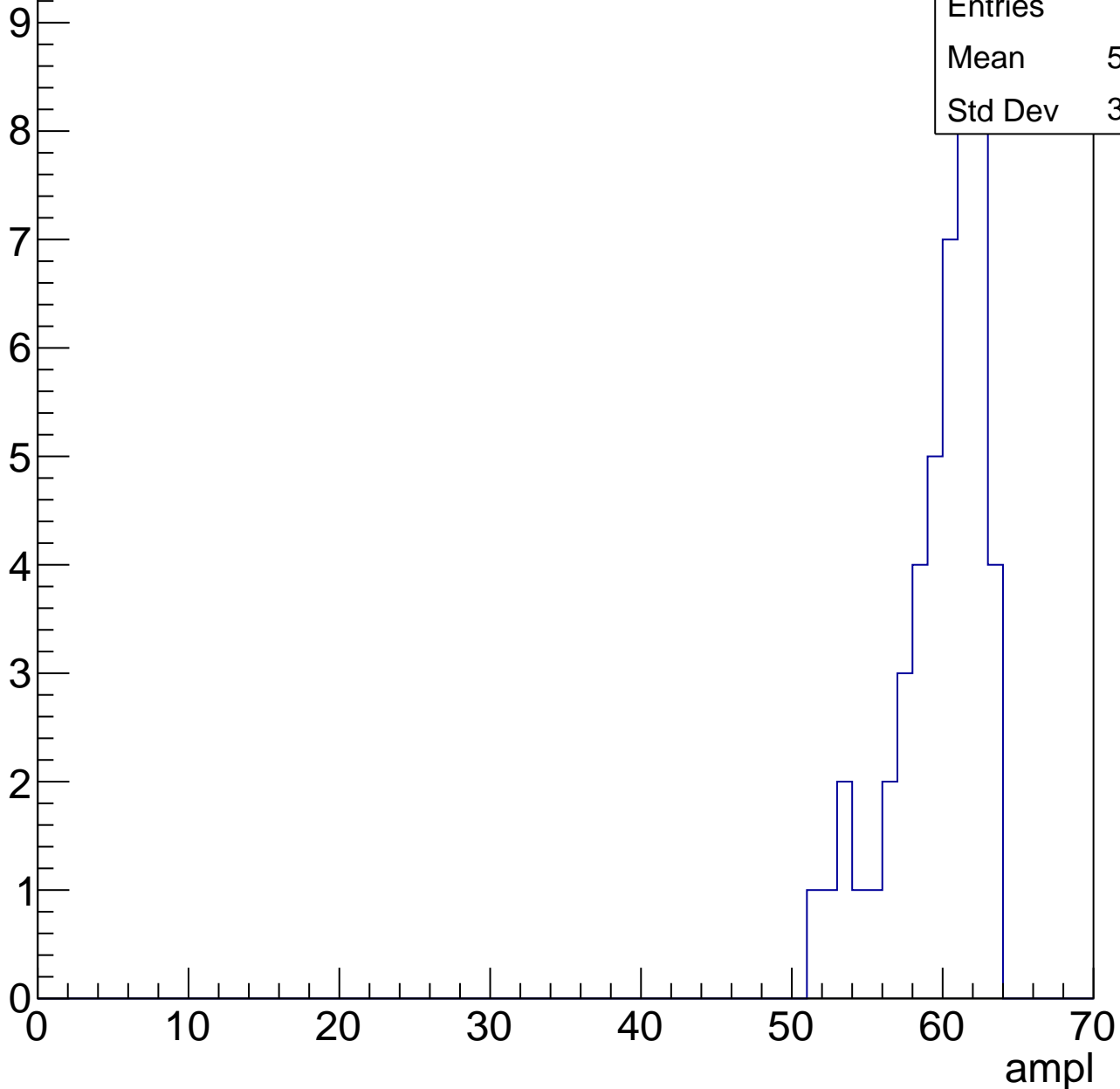


# B1L103S, U22-ch46, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

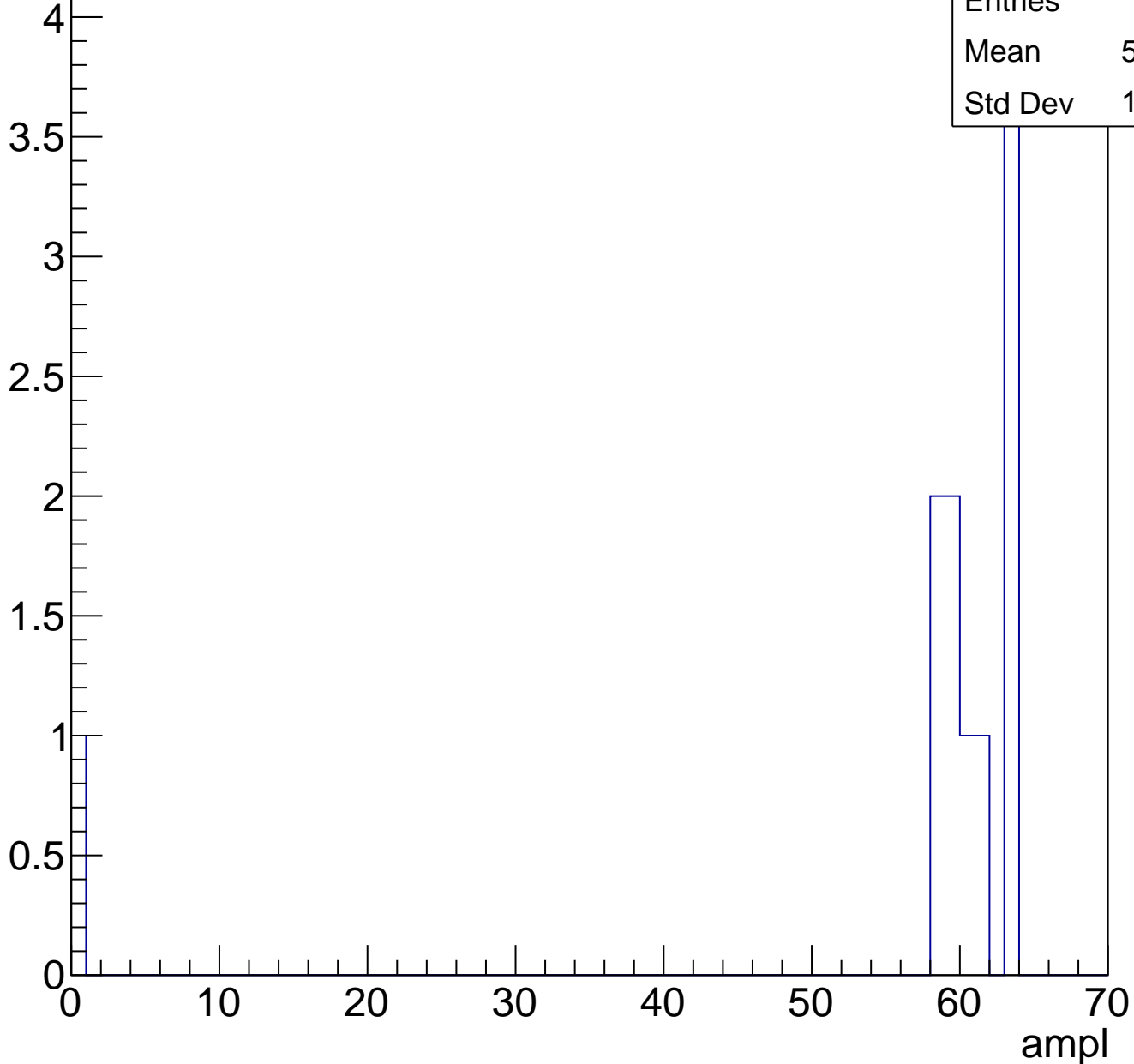
Entries	48
Mean	59.29
Std Dev	3.027



# B1L103S, U22-ch46, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch46, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	6
Std Dev	18.49

Entry



# B1L103S, U22-ch47, adc0

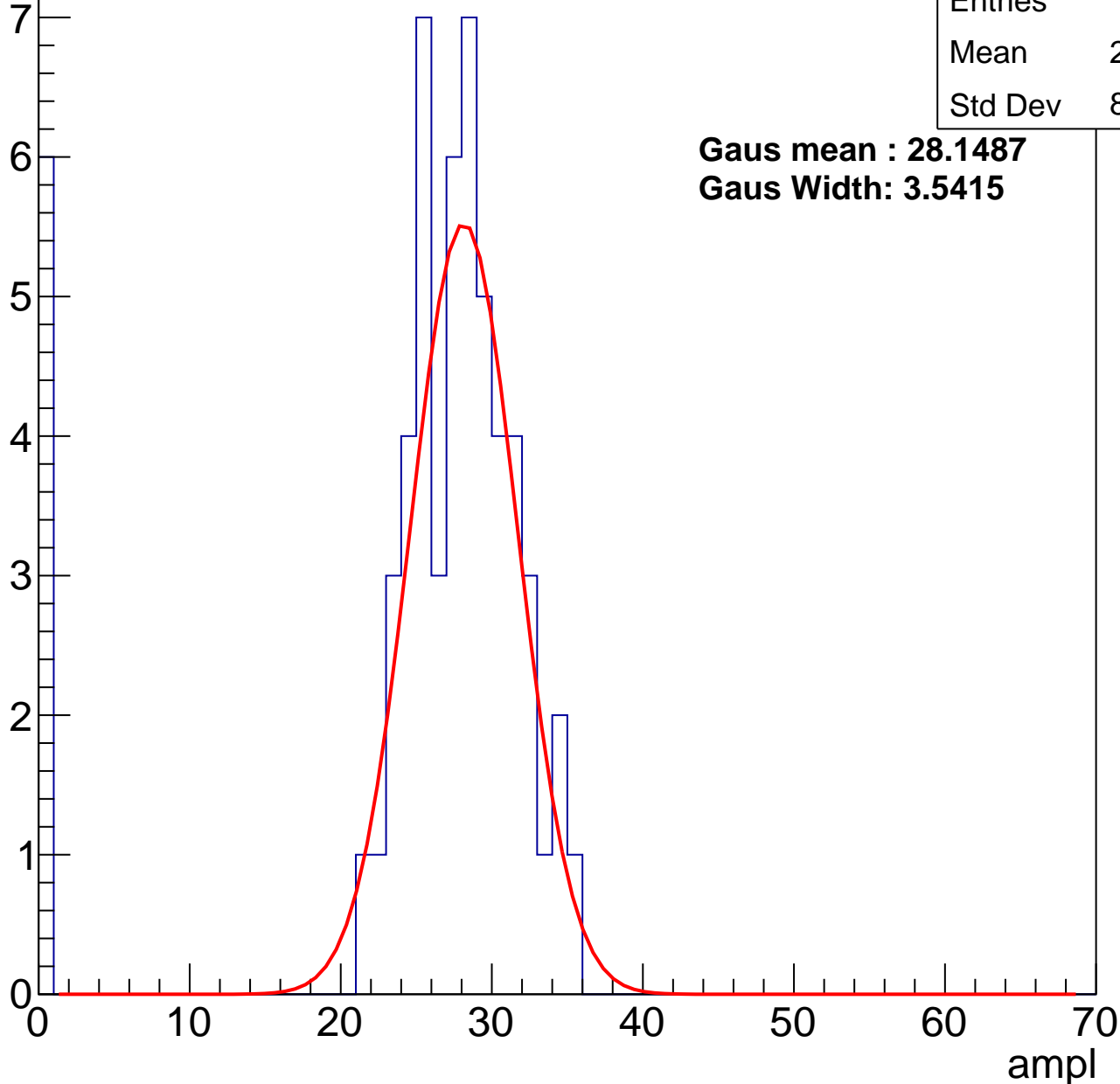
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	24.83
Std Dev	8.979

**Gaus mean : 28.1487**

**Gaus Width: 3.5415**



# B1L103S, U22-ch47, adc1

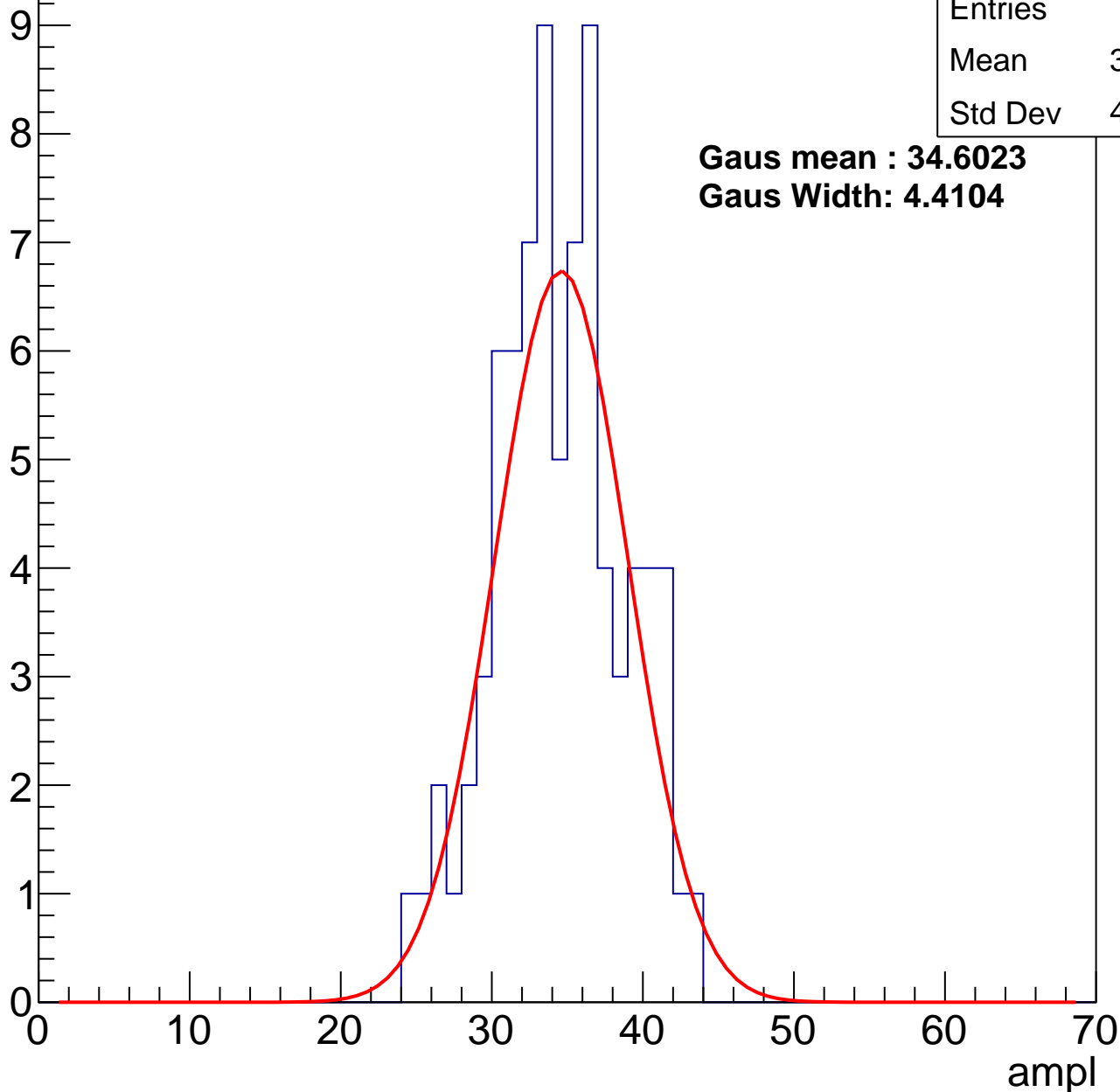
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	80
Mean	34.05
Std Dev	4.189

**Gaus mean : 34.6023**

**Gaus Width: 4.4104**



# B1L103S, U22-ch47, adc2

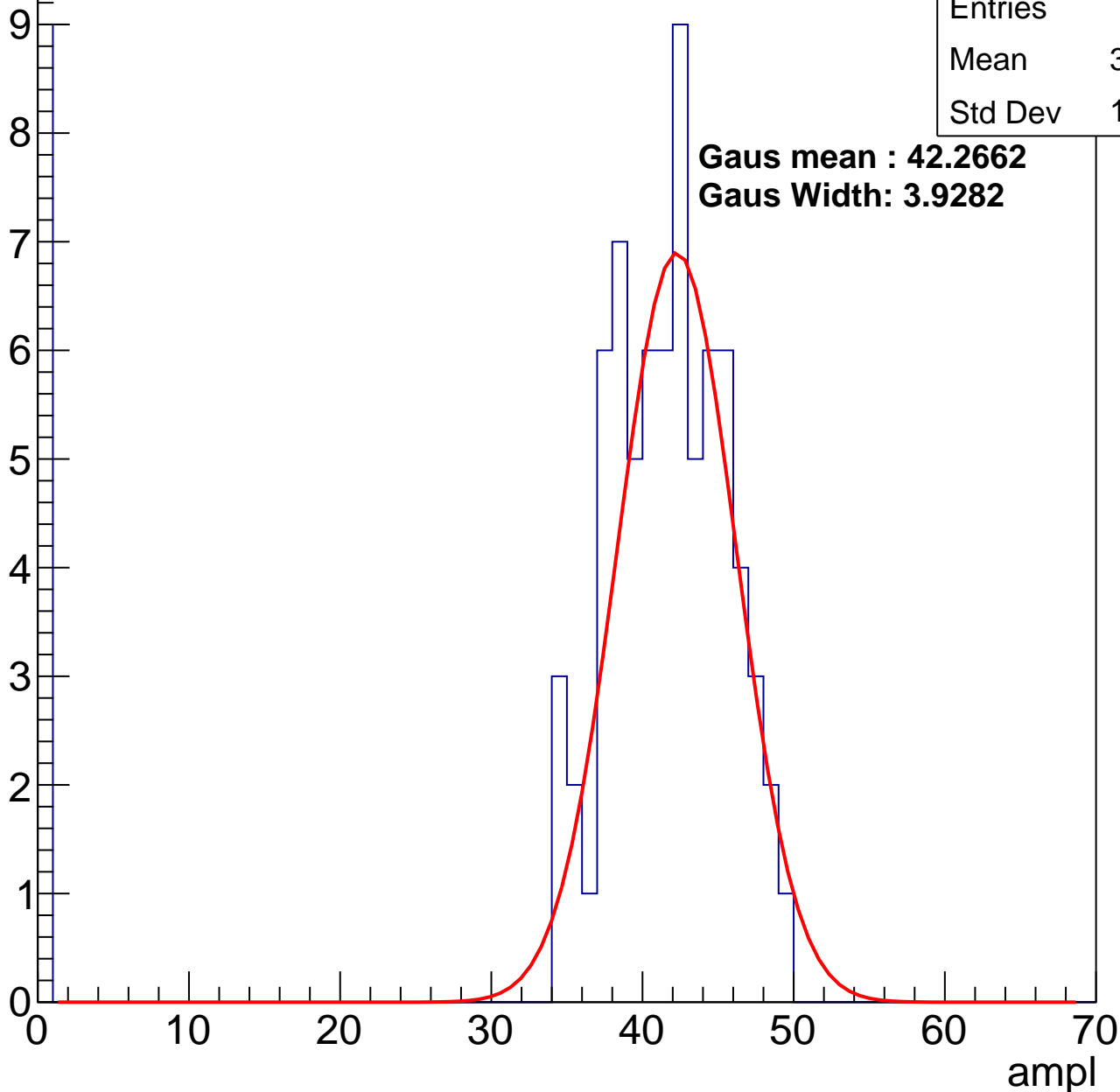
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	36.72
Std Dev	13.44

**Gaus mean : 42.2662**

**Gaus Width: 3.9282**

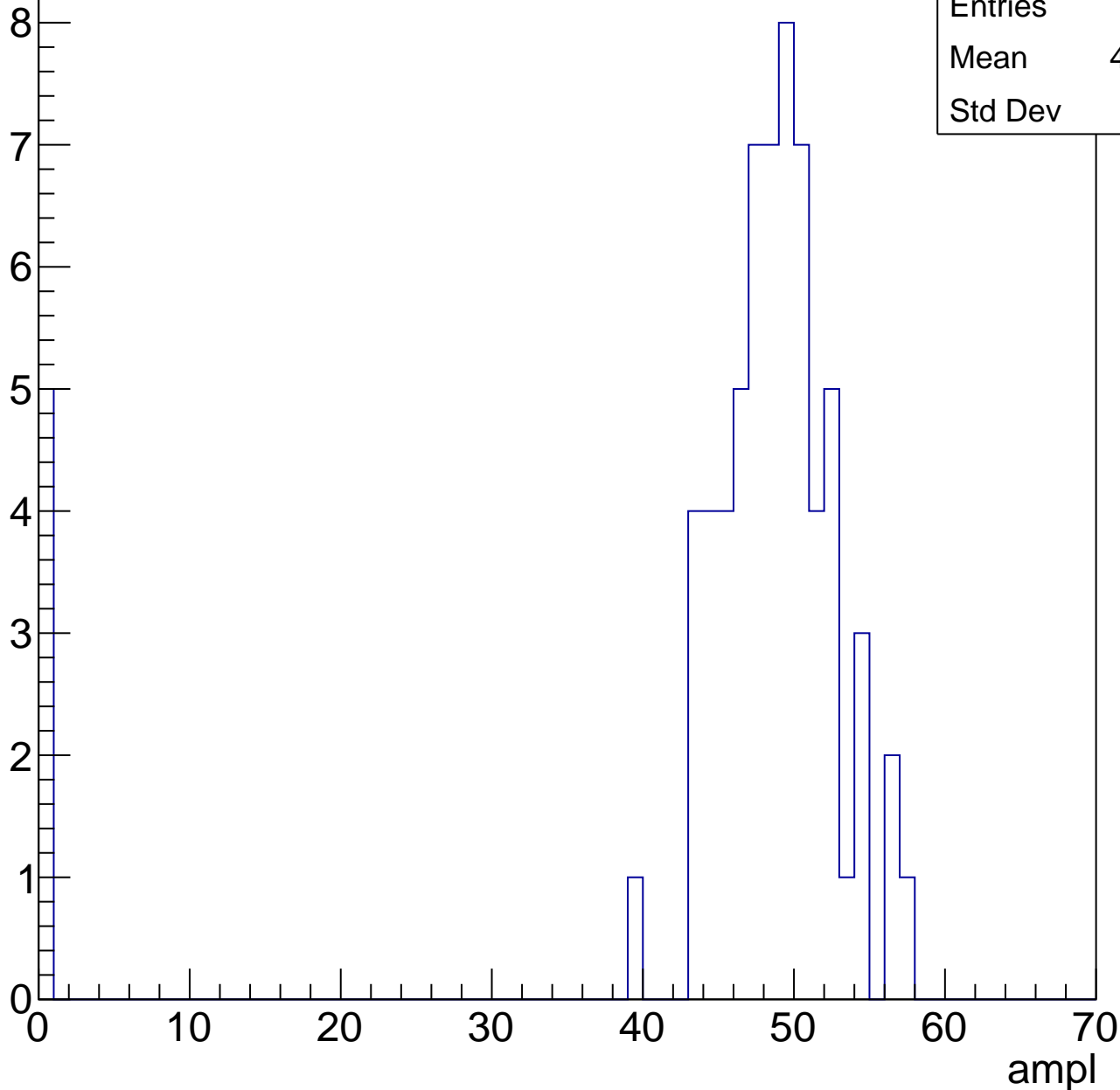


# B1L103S, U22-ch47, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	44.88
Std Dev	13.1

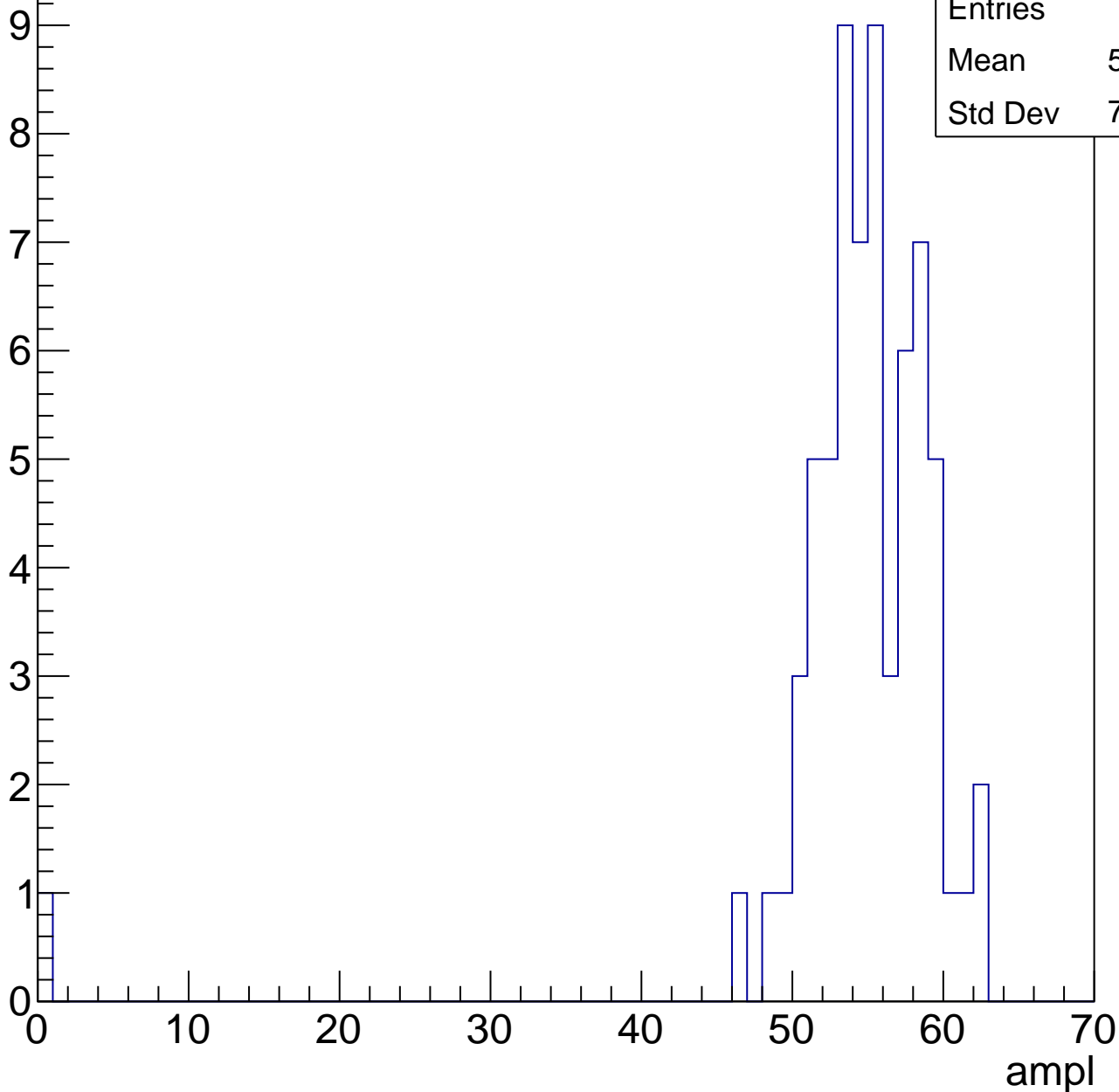


# B1L103S, U22-ch47, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	53.94
Std Dev	7.419

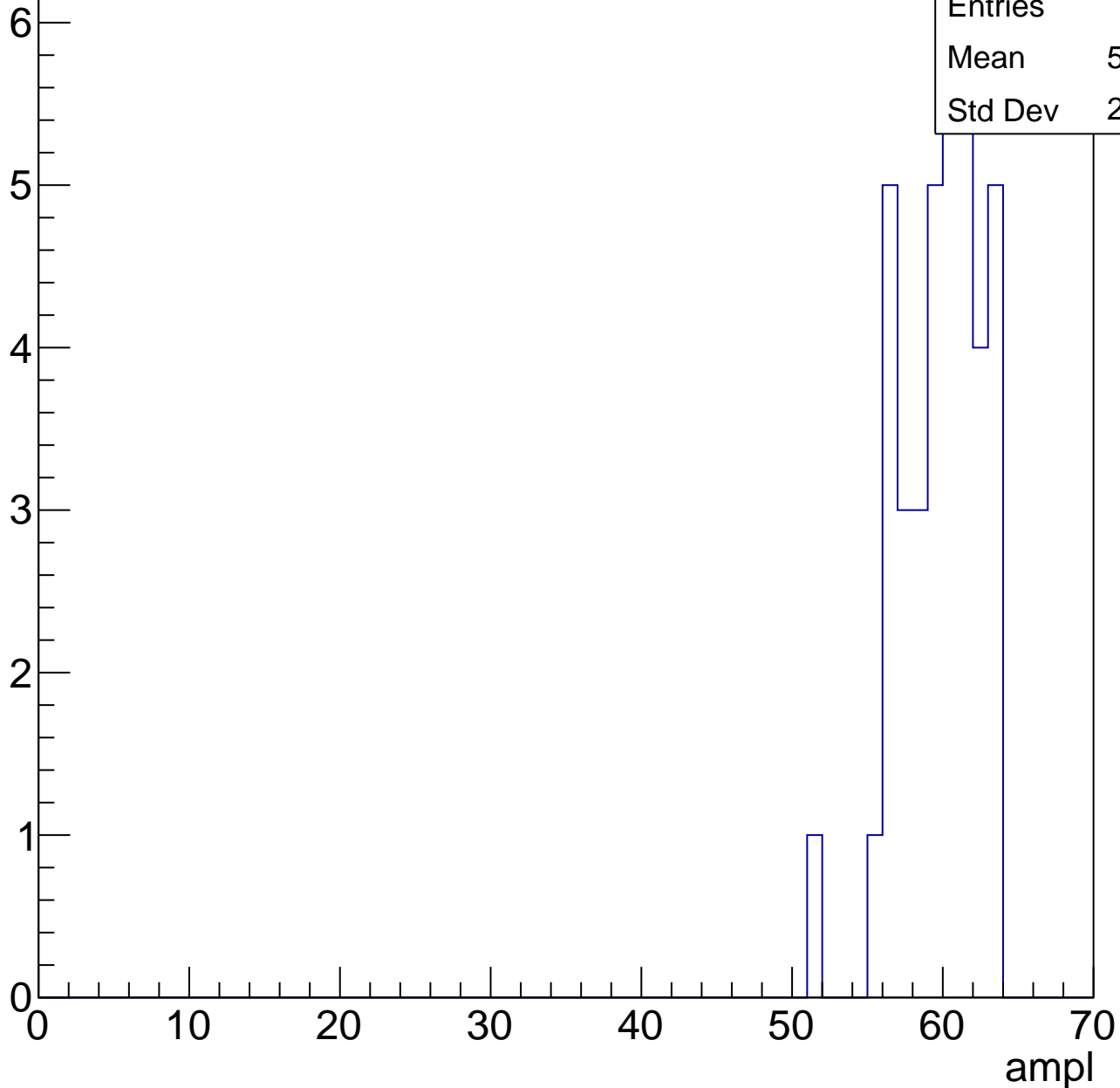


# B1L103S, U22-ch47, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	39
Mean	59.36
Std Dev	2.684



# B1L103S, U22-ch47, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	12
Mean	61.5
Std Dev	1.118



# B1L103S, U22-ch47, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	22
Mean	8.545
Std Dev	21.51

Entry



# B1L103S, U22-ch48, adc0

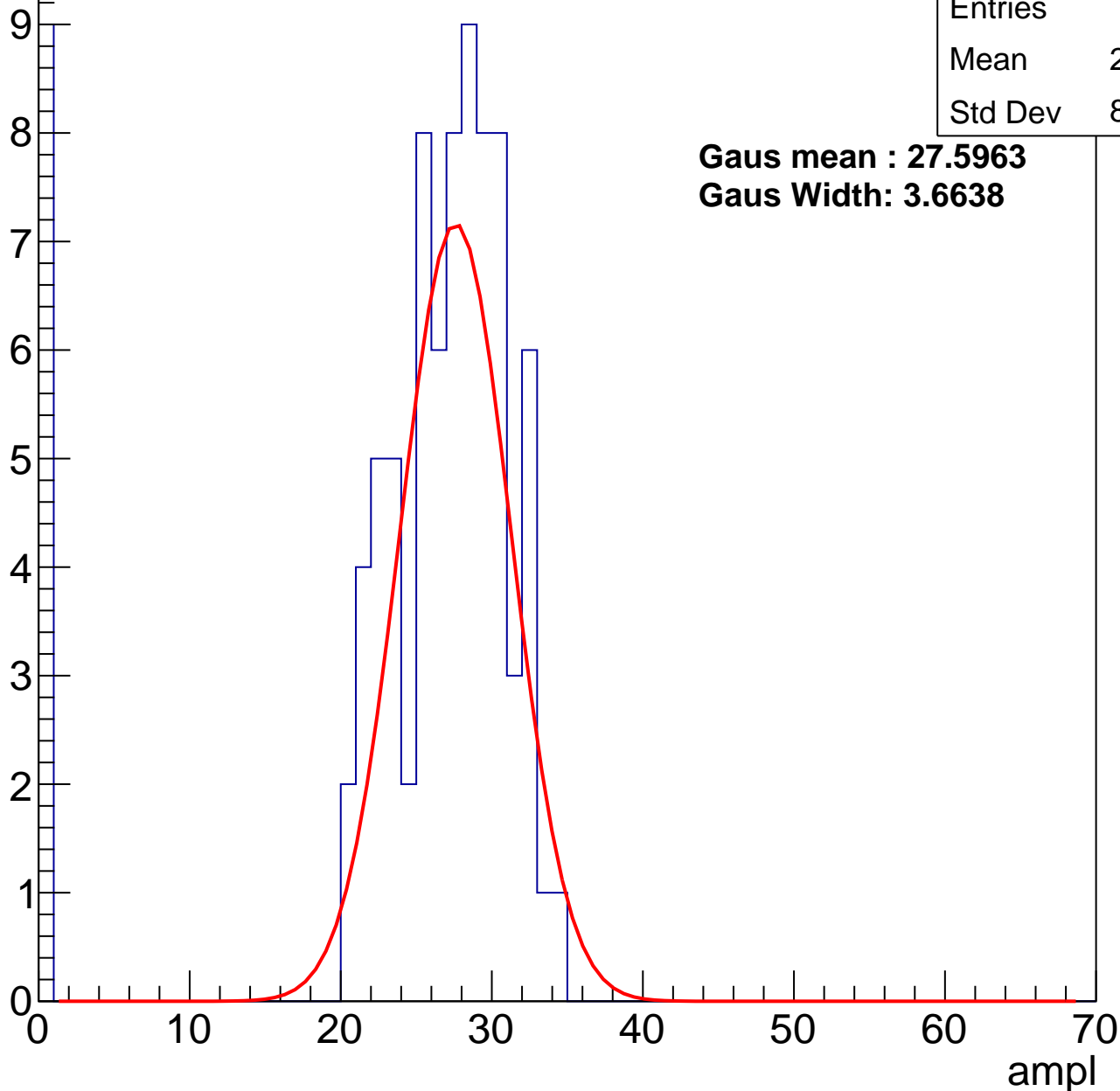
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	85
Mean	24.06
Std Dev	8.895

**Gaus mean : 27.5963**

**Gaus Width: 3.6638**

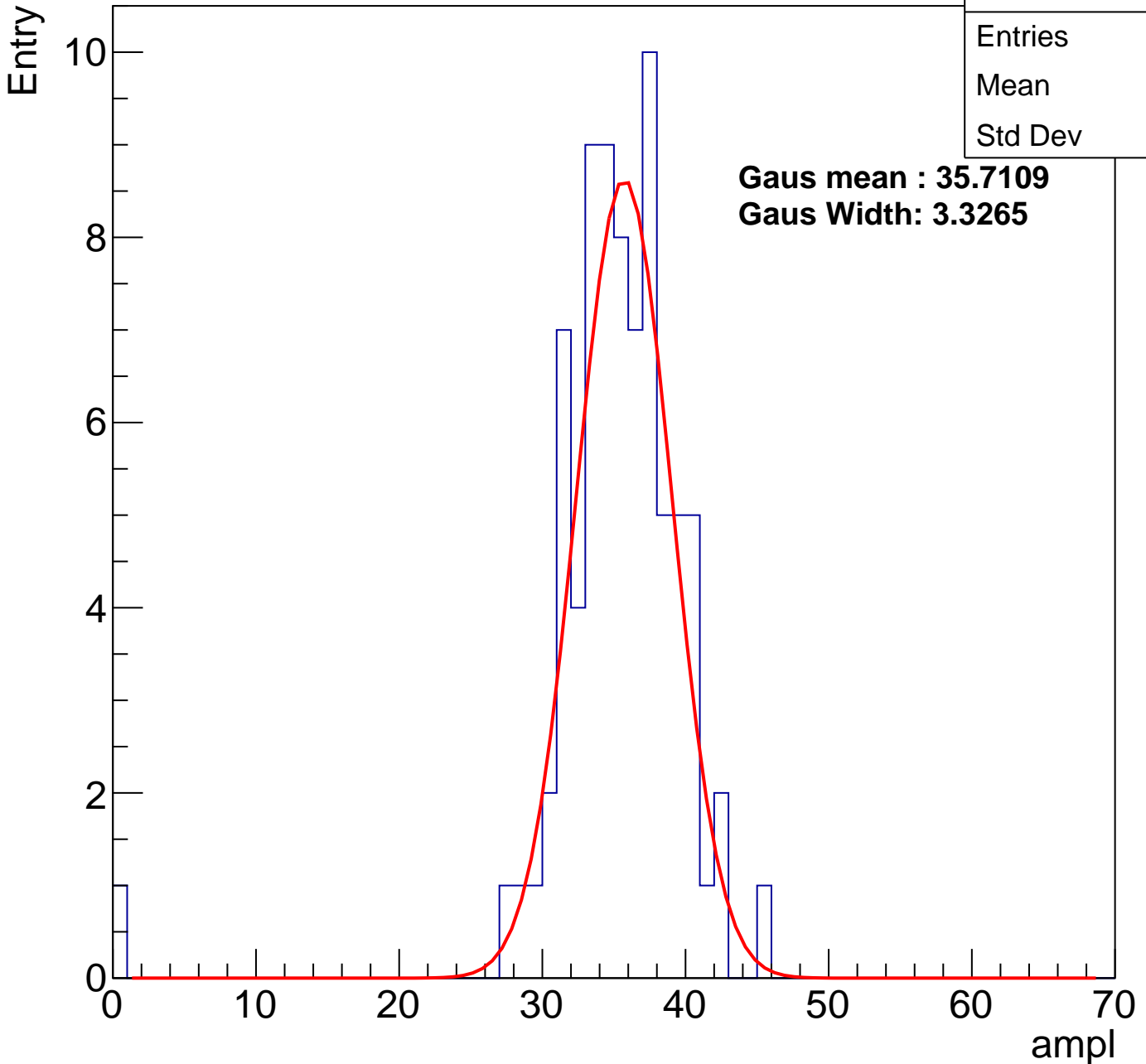


# B1L103S, U22-ch48, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	34.8
Std Dev	5.21

**Gaus mean : 35.7109**  
**Gaus Width: 3.3265**



# B1L103S, U22-ch48, adc2

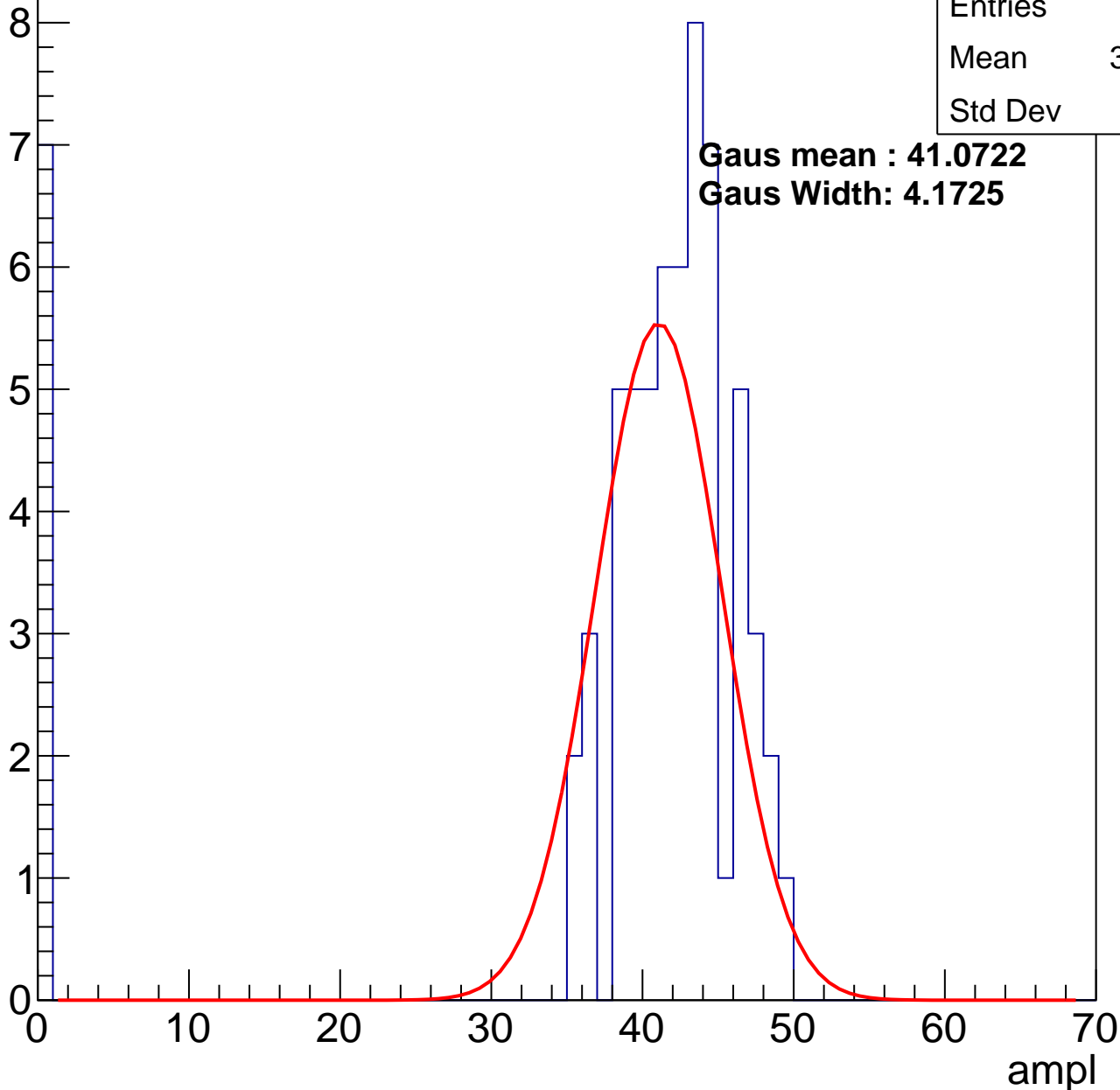
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	37.48
Std Dev	13.3

**Gaus mean : 41.0722**

**Gaus Width: 4.1725**

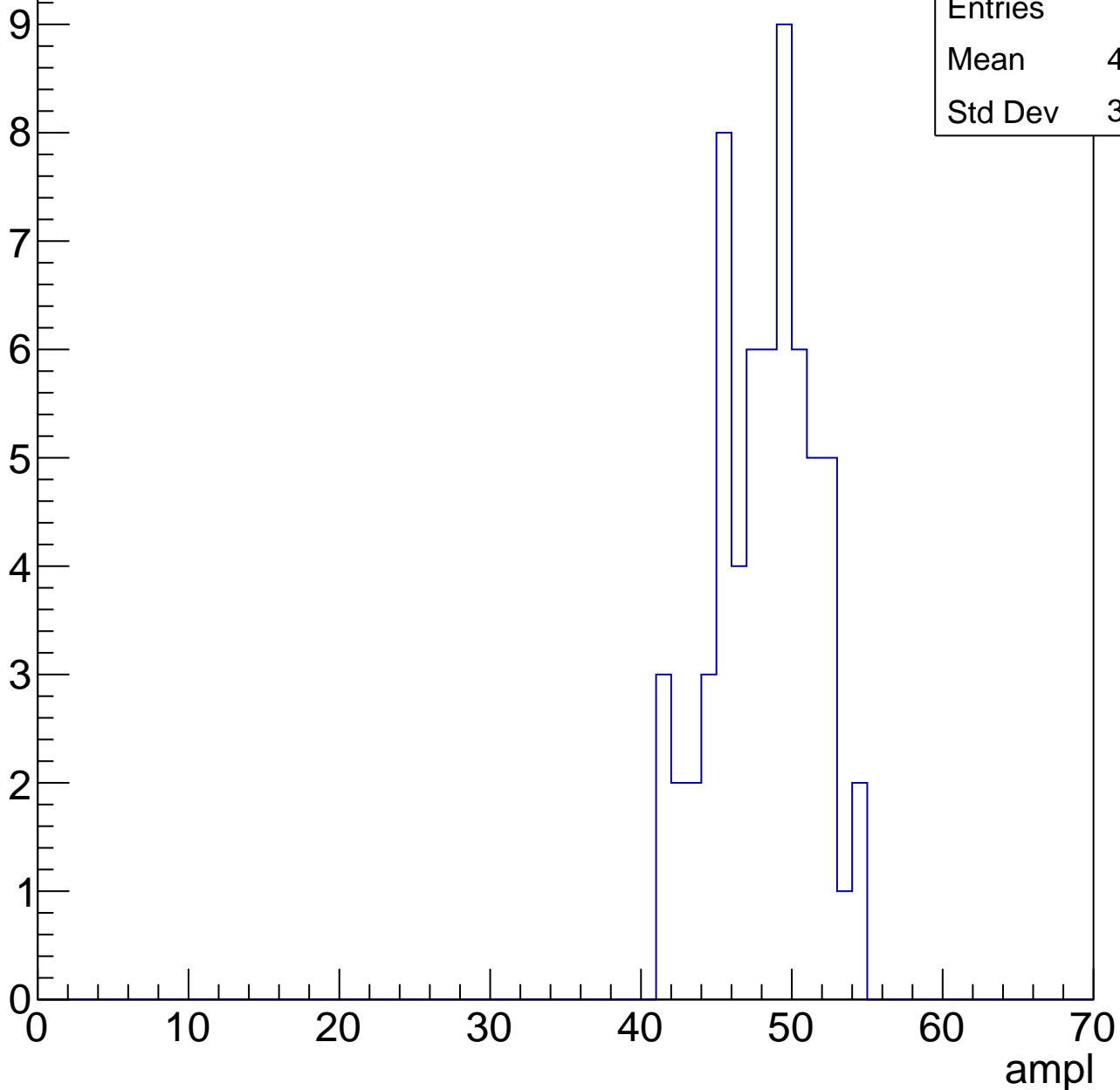


# B1L103S, U22-ch48, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	47.68
Std Dev	3.262

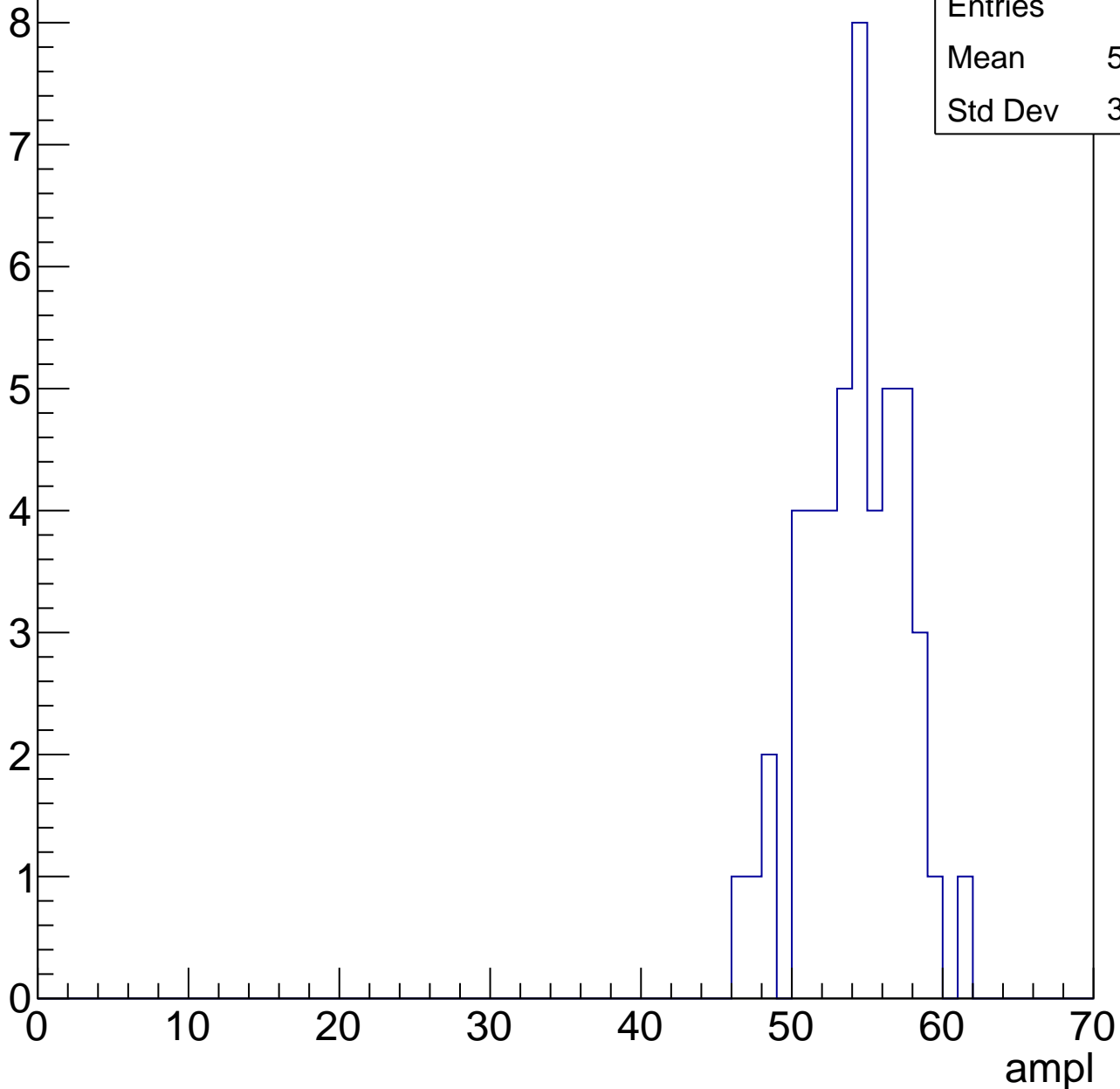


# B1L103S, U22-ch48, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.69
Std Dev	3.203

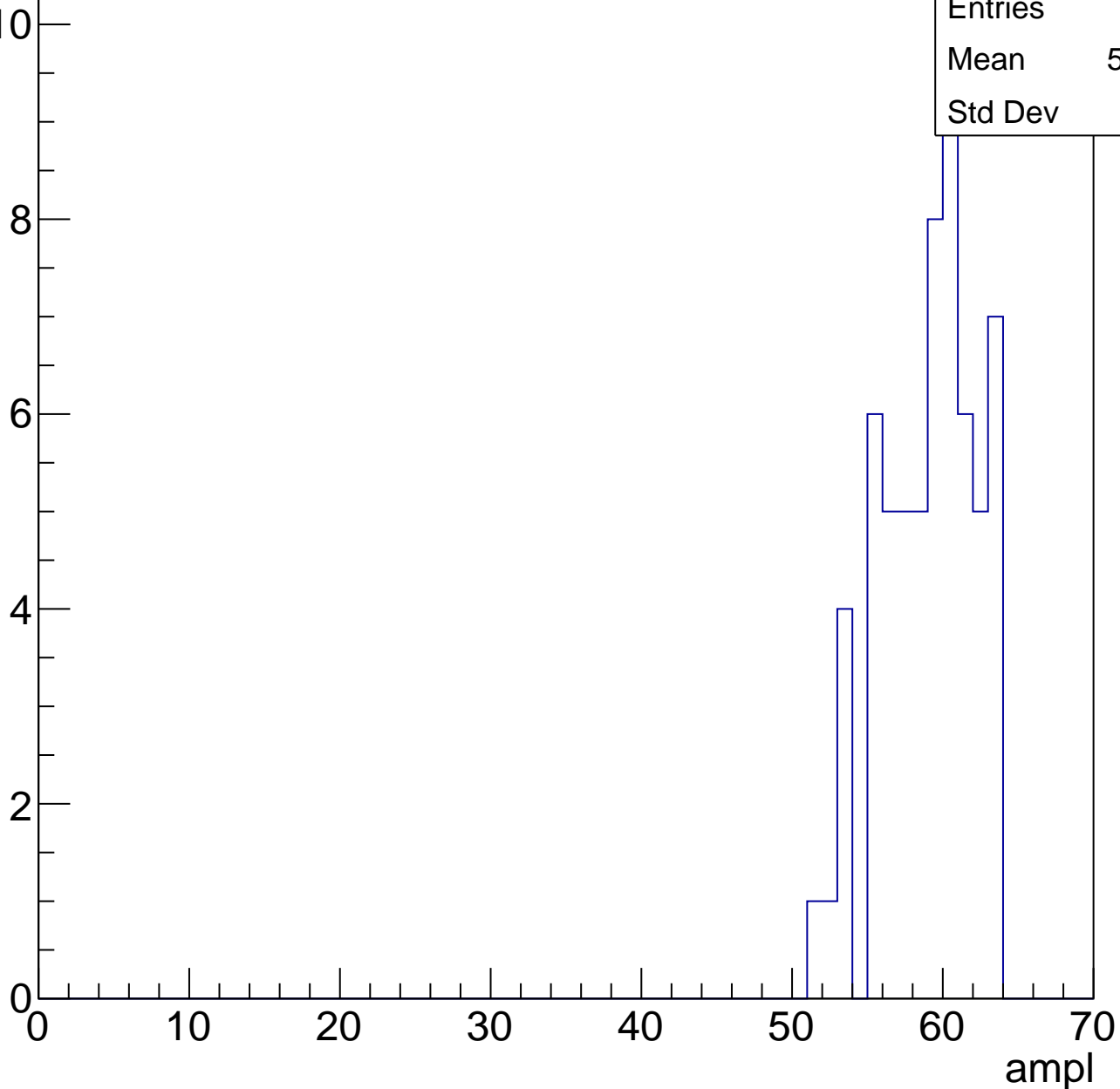


# B1L103S, U22-ch48, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	58.56
Std Dev	3.09

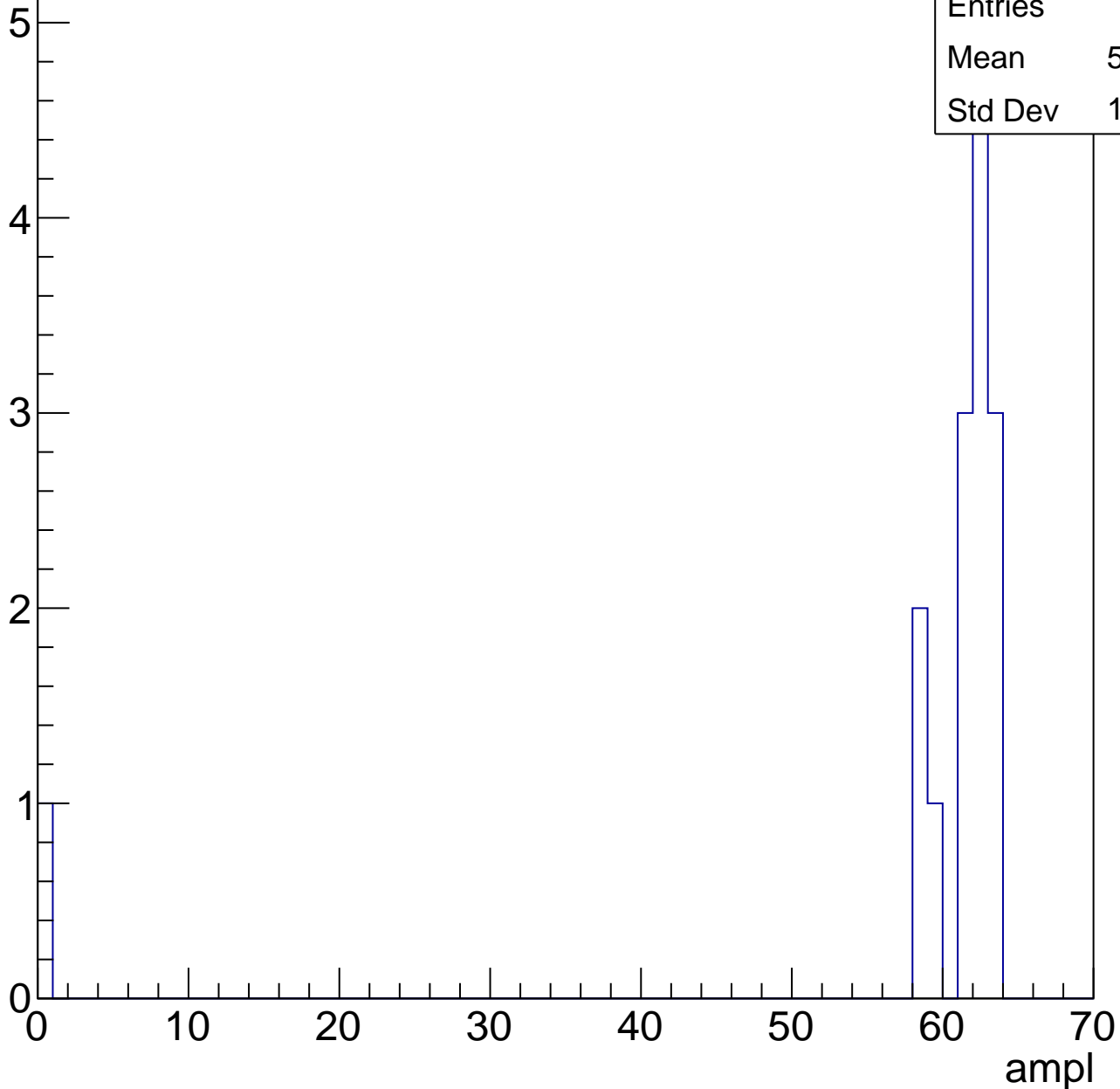


# B1L103S, U22-ch48, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.13
Std Dev	15.35





# B1L103S, U22-ch48, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch49, adc0

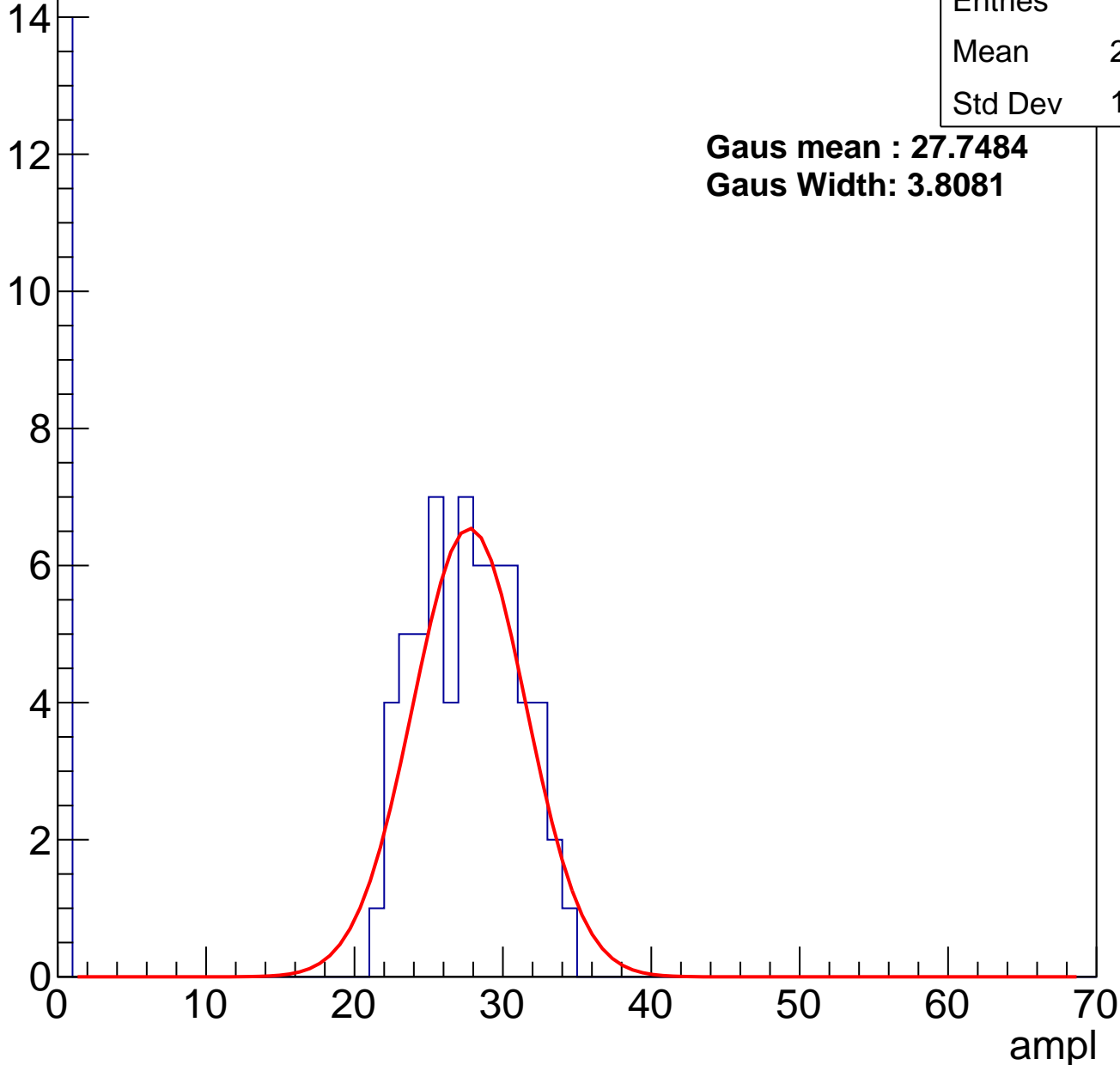
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	22.18
Std Dev	10.95

**Gaus mean : 27.7484**

**Gaus Width: 3.8081**

Entry



# B1L103S, U22-ch49, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	32.19
Std Dev	8.018

**Gaus mean : 34.2441**

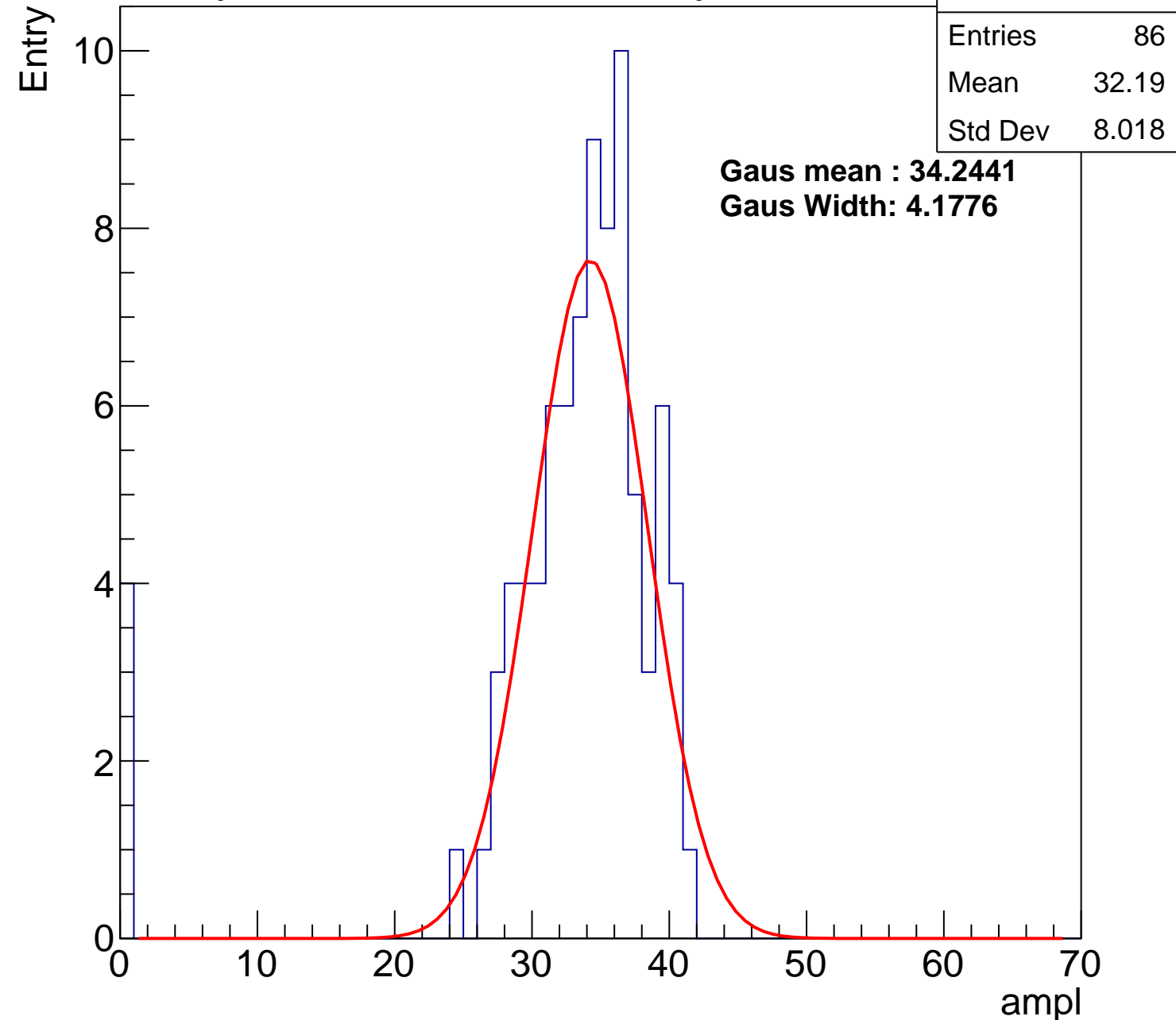
**Gaus Width: 4.1776**

Entry

10  
8  
6  
4  
2  
0

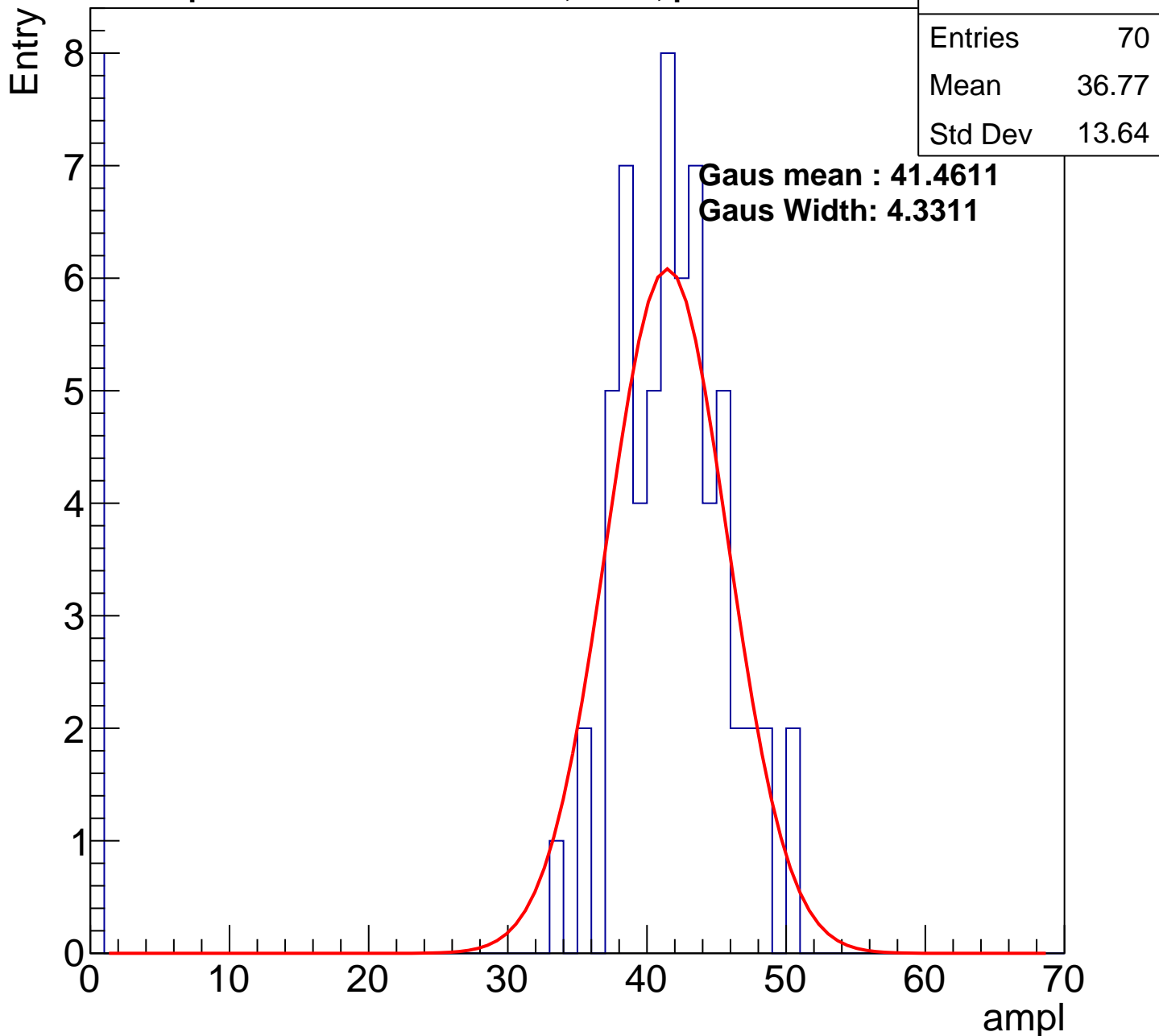
ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch49, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

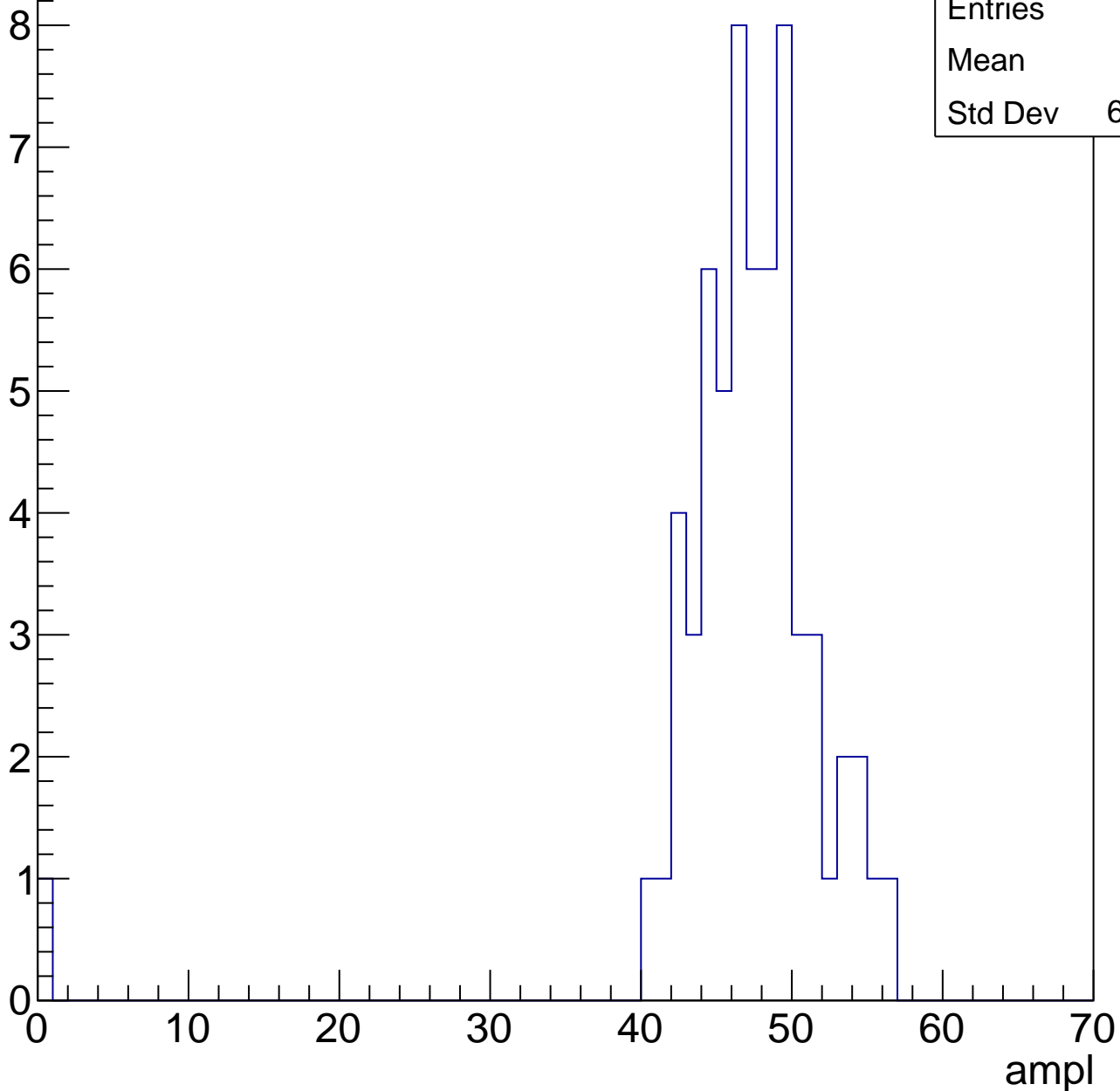


# B1L103S, U22-ch49, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

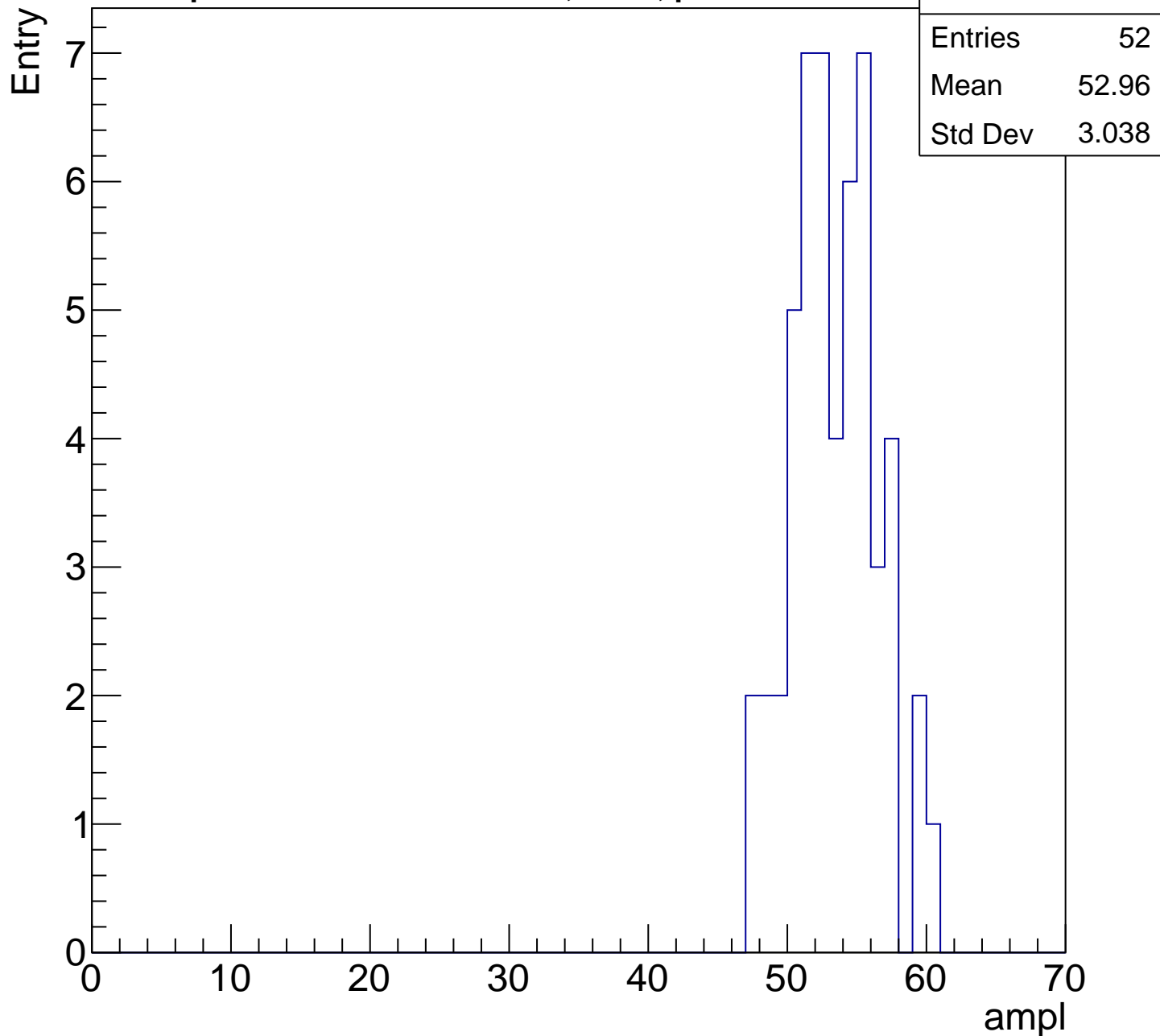
Entry

Entries	62
Mean	46.4
Std Dev	6.904



# B1L103S, U22-ch49, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2



# B1L103S, U22-ch49, adc5

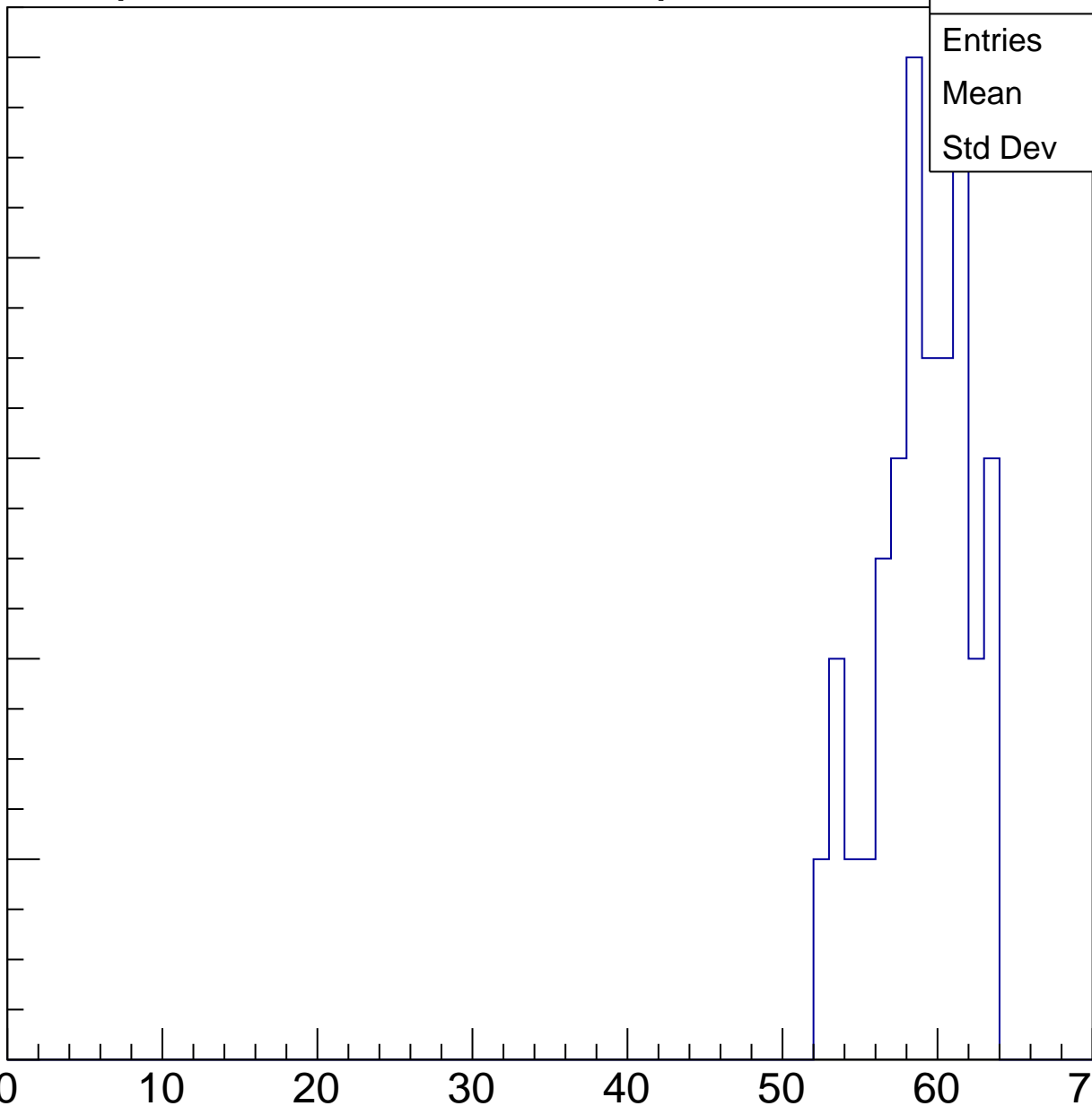
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

10  
8  
6  
4  
2  
0

Entries	65
Mean	58.54
Std Dev	2.956

ampl

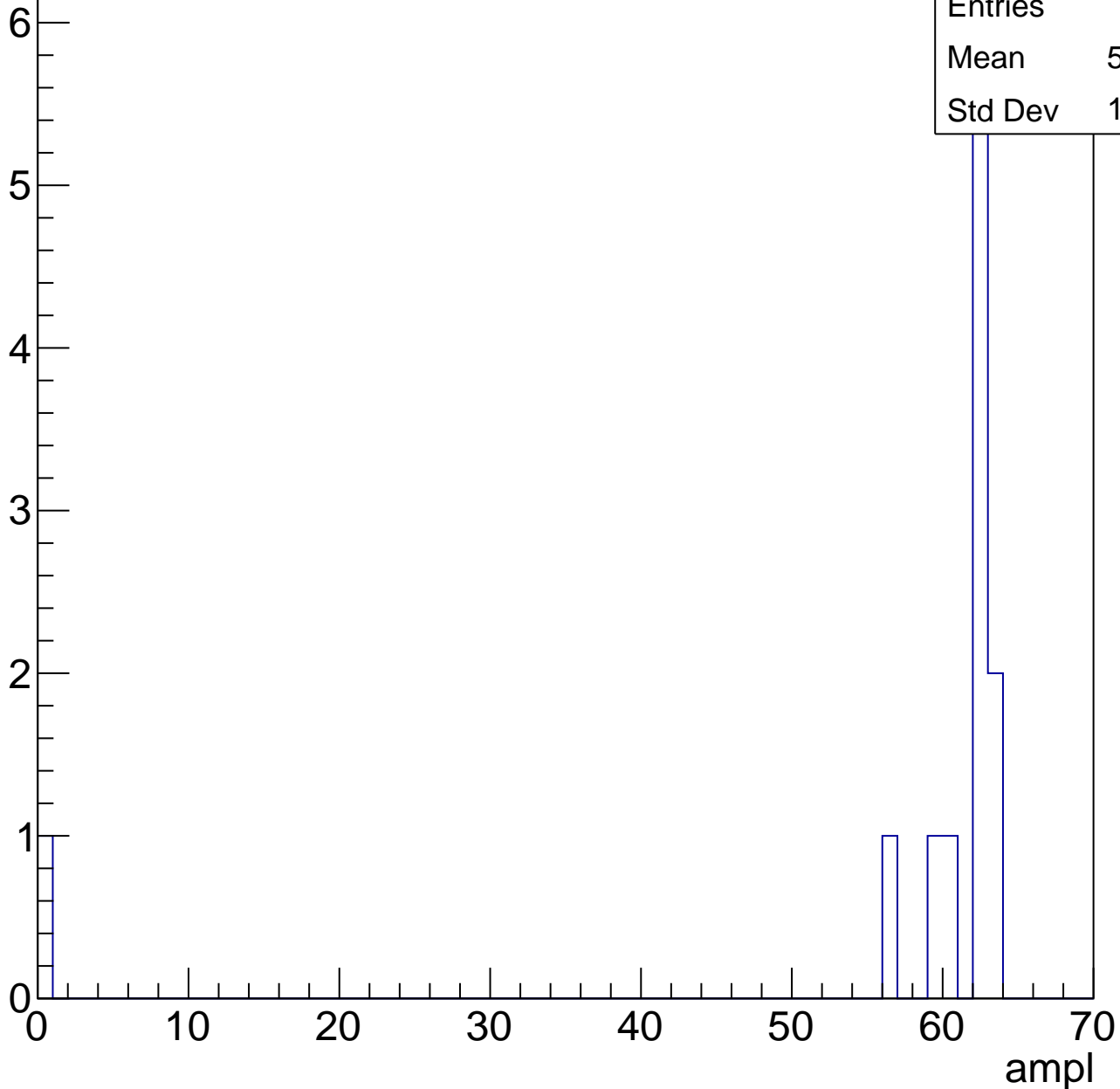


# B1L103S, U22-ch49, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.08
Std Dev	17.02



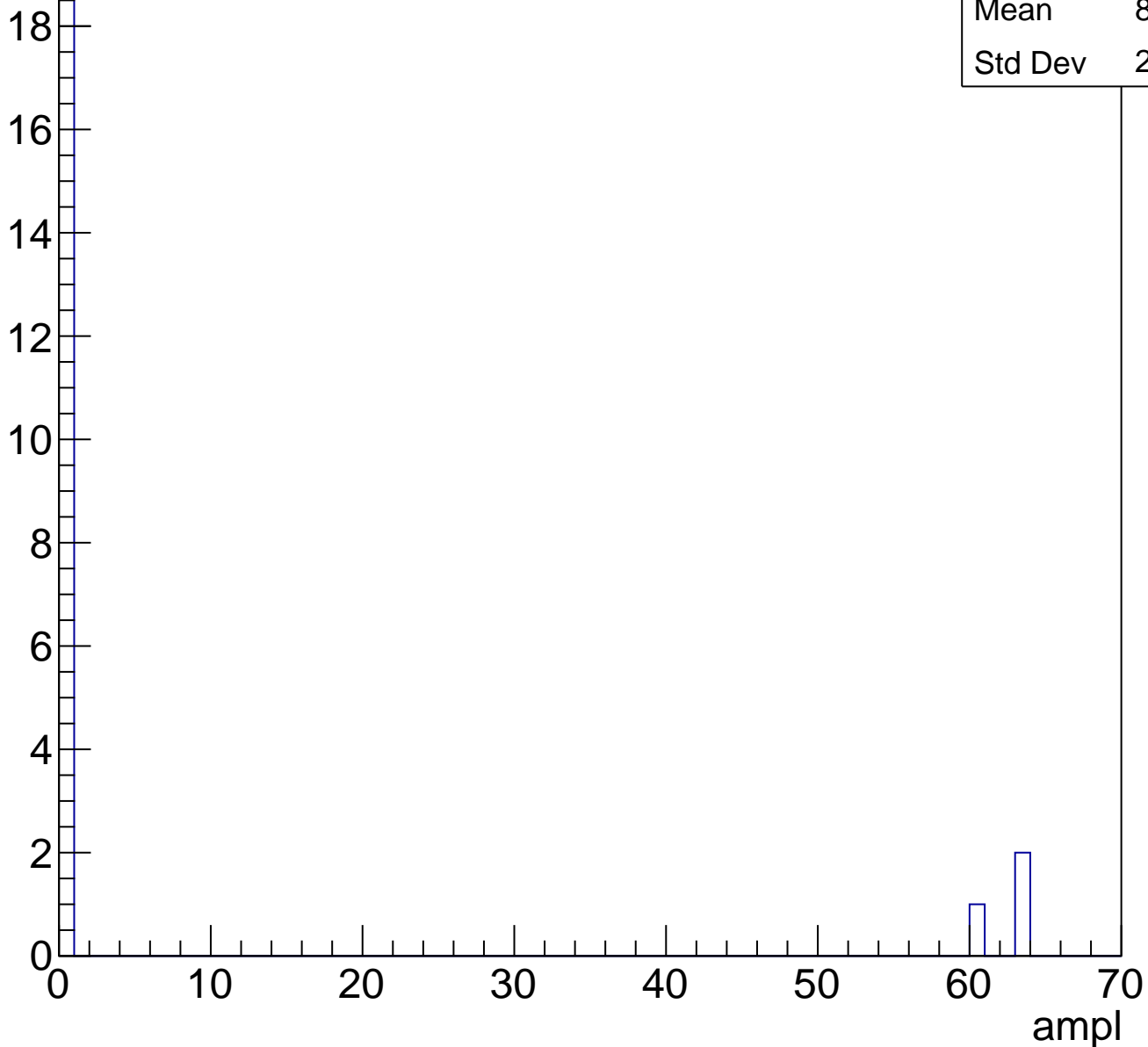


# B1L103S, U22-ch49, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	22
Mean	8.455
Std Dev	21.28

Entry



# B1L103S, U22-ch50, adc0

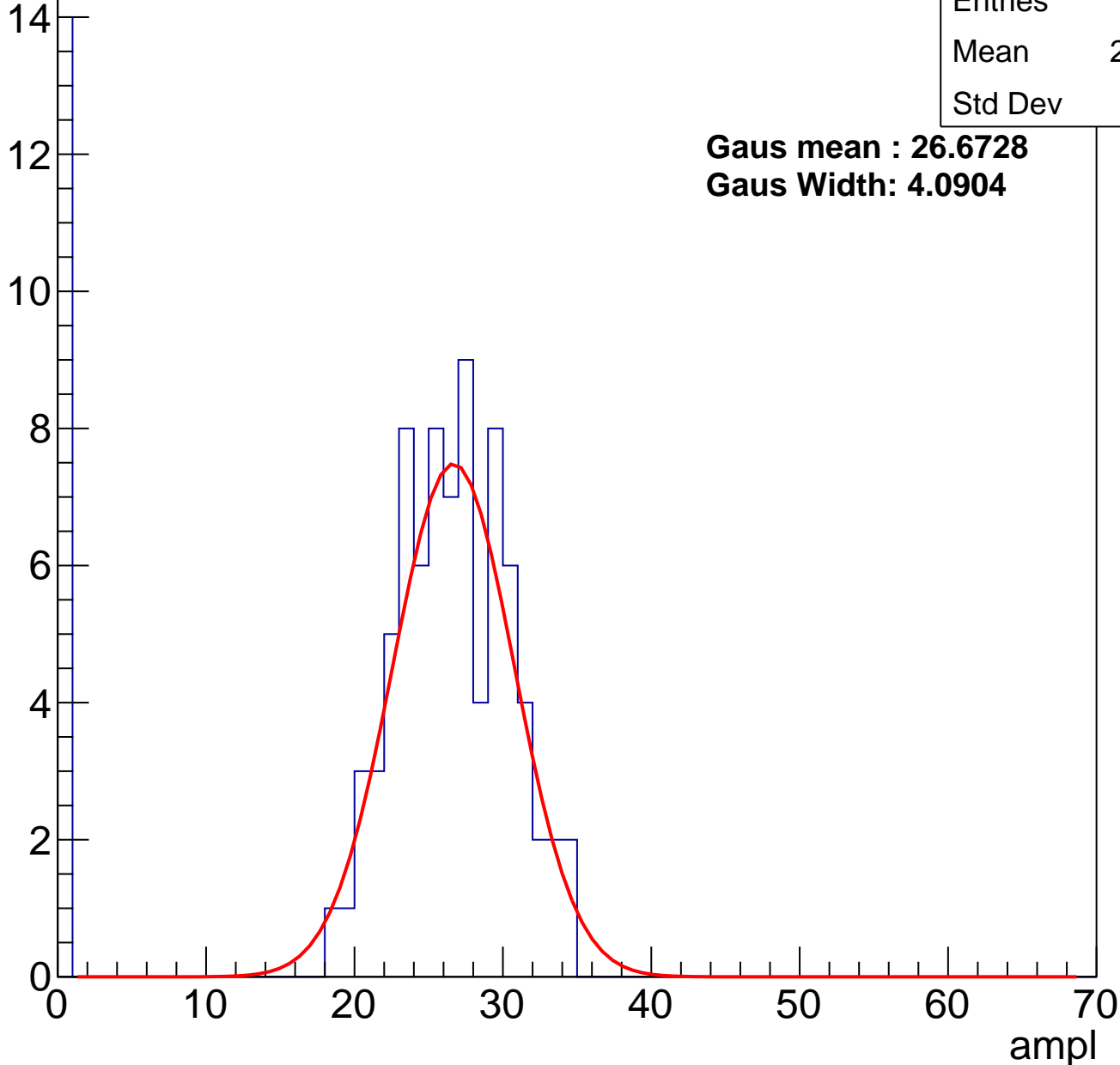
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	93
Mean	22.25
Std Dev	9.96

**Gaus mean : 26.6728**

**Gaus Width: 4.0904**

Entry



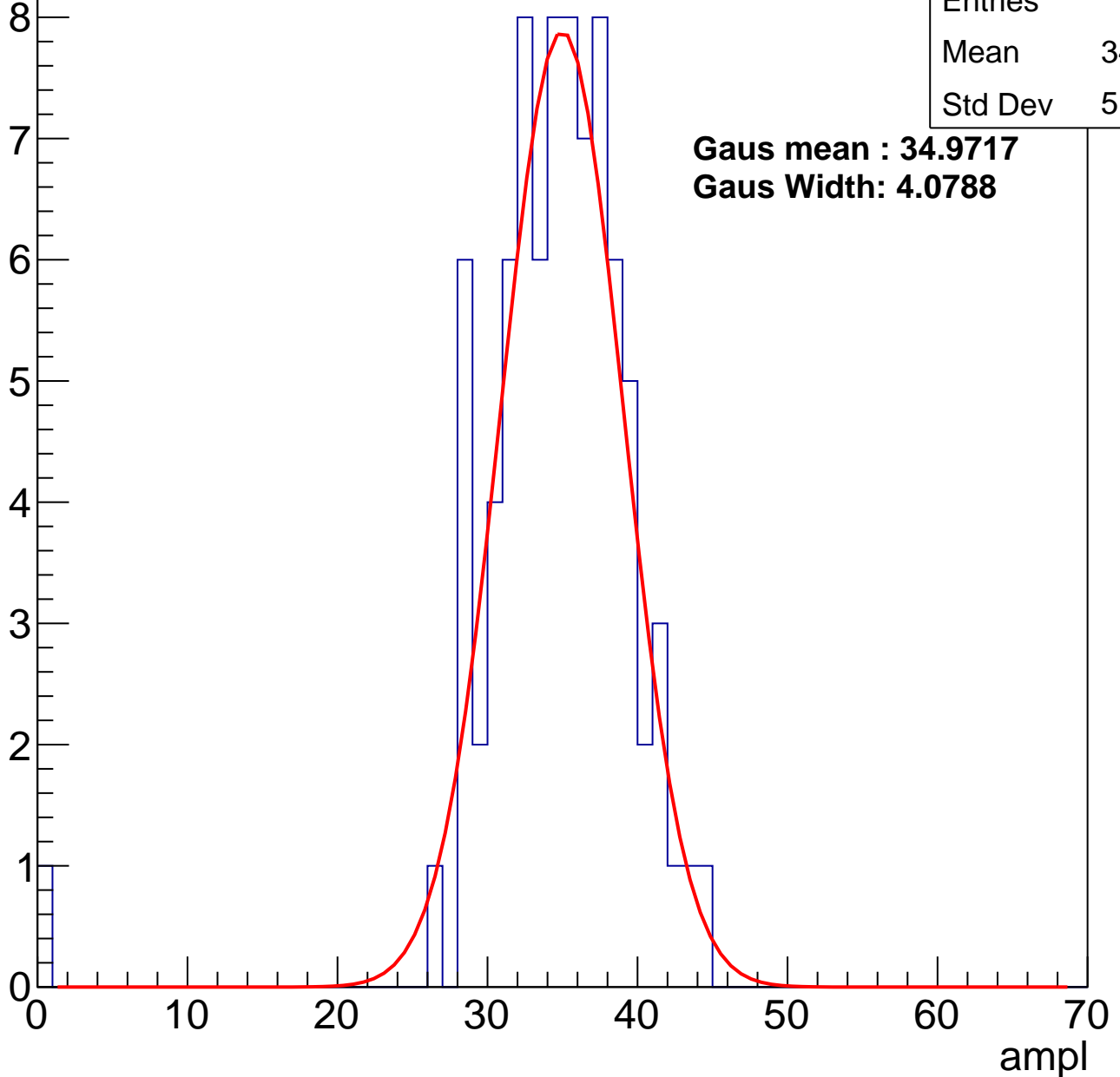
# B1L103S, U22-ch50, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	34.13
Std Dev	5.382

**Gaus mean : 34.9717**  
**Gaus Width: 4.0788**



# B1L103S, U22-ch50, adc2

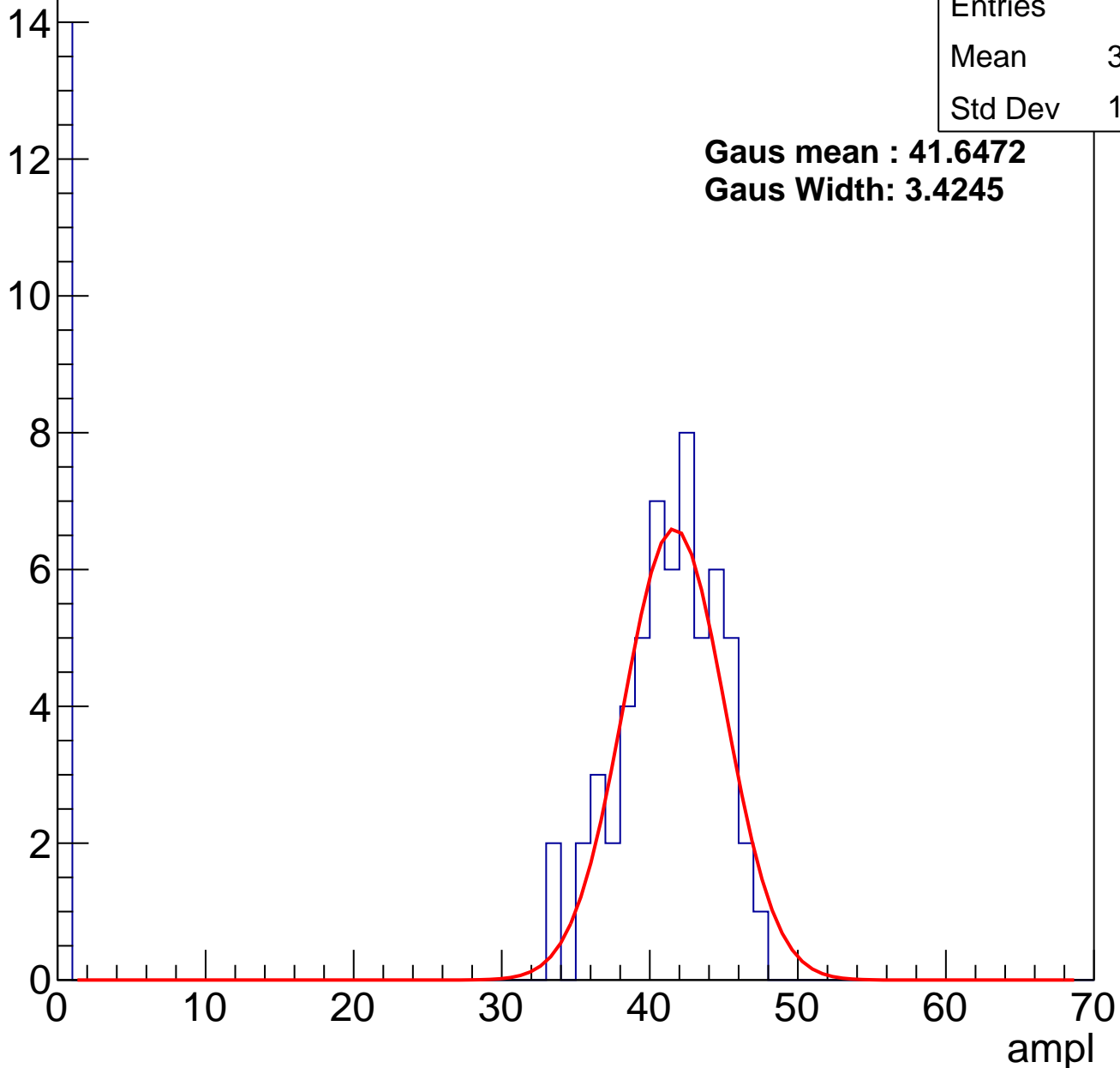
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	72
Mean	32.92
Std Dev	16.43

**Gaus mean : 41.6472**

**Gaus Width: 3.4245**

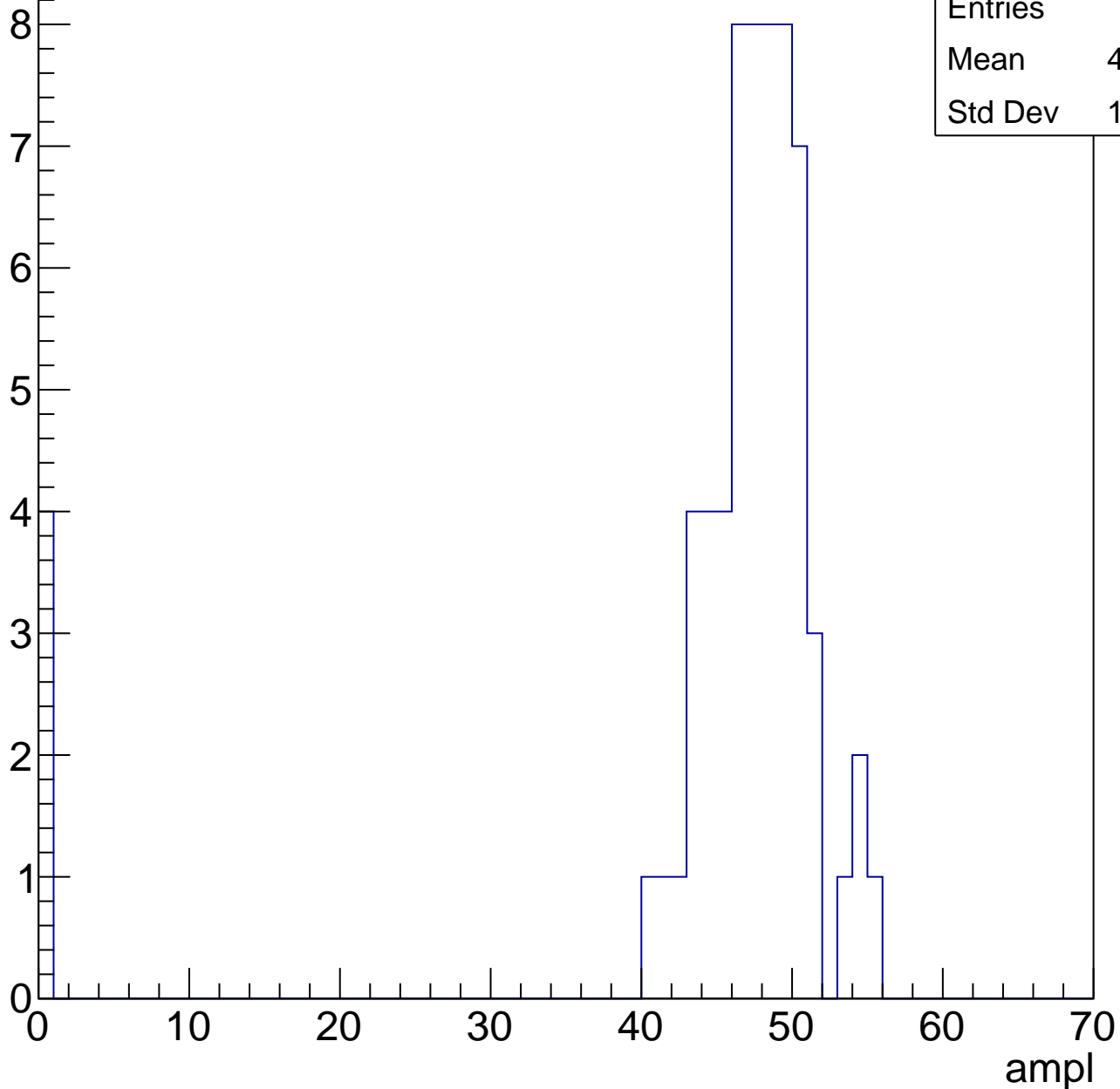
Entry



# B1L103S, U22-ch50, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



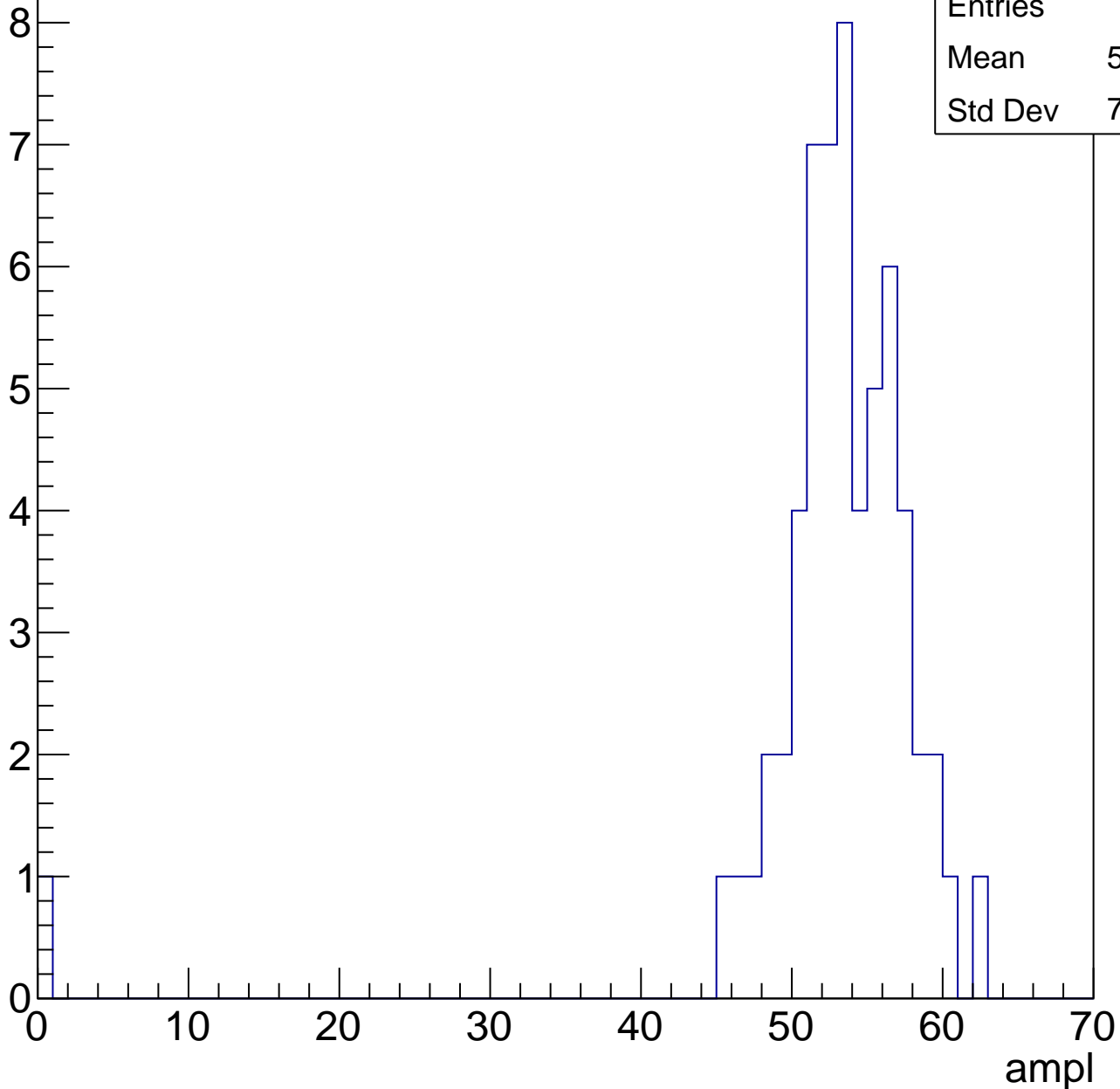
Entries	65
Mean	44.46
Std Dev	11.77

# B1L103S, U22-ch50, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	52.34
Std Dev	7.679

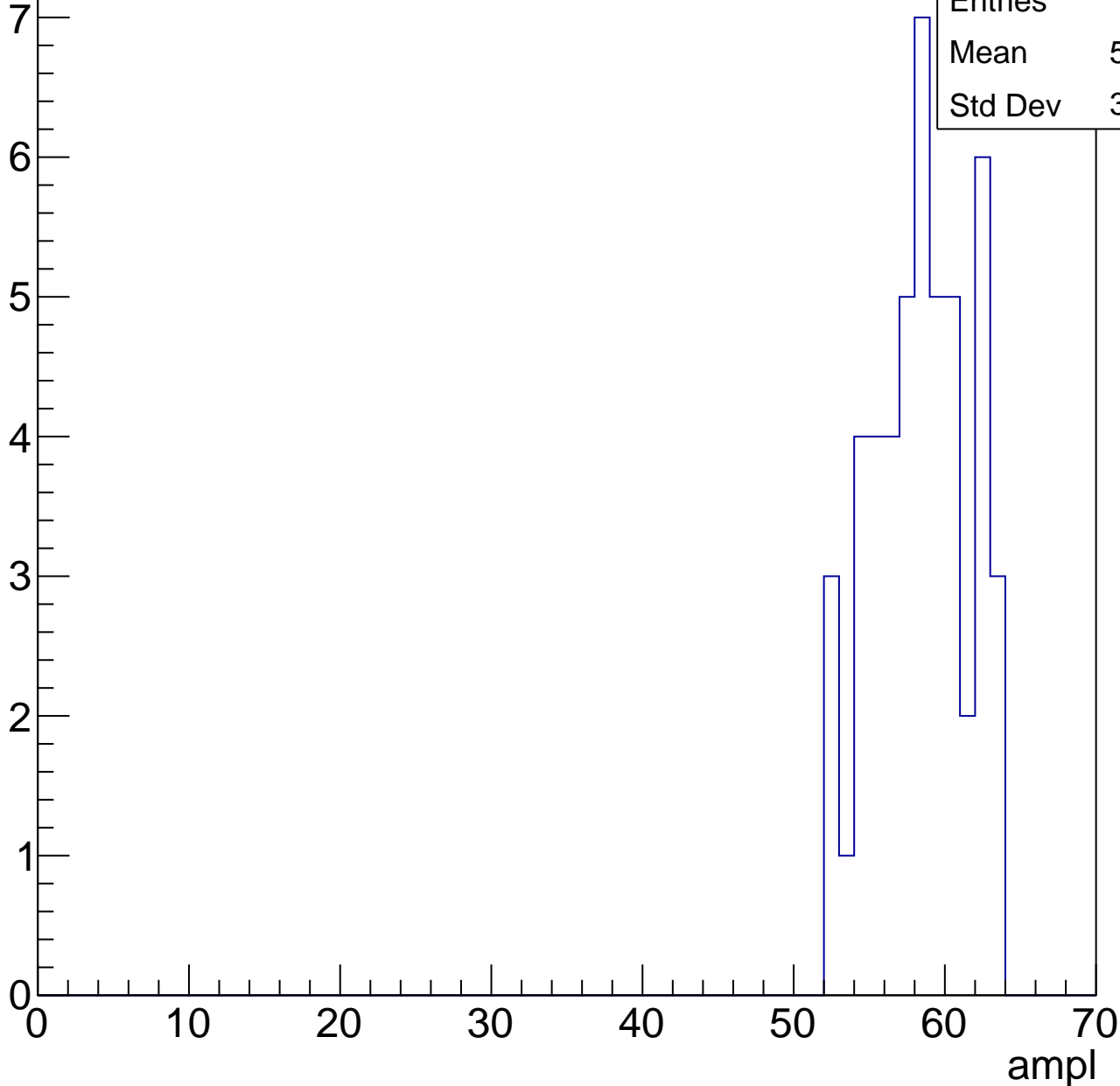


# B1L103S, U22-ch50, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	57.92
Std Dev	3.089

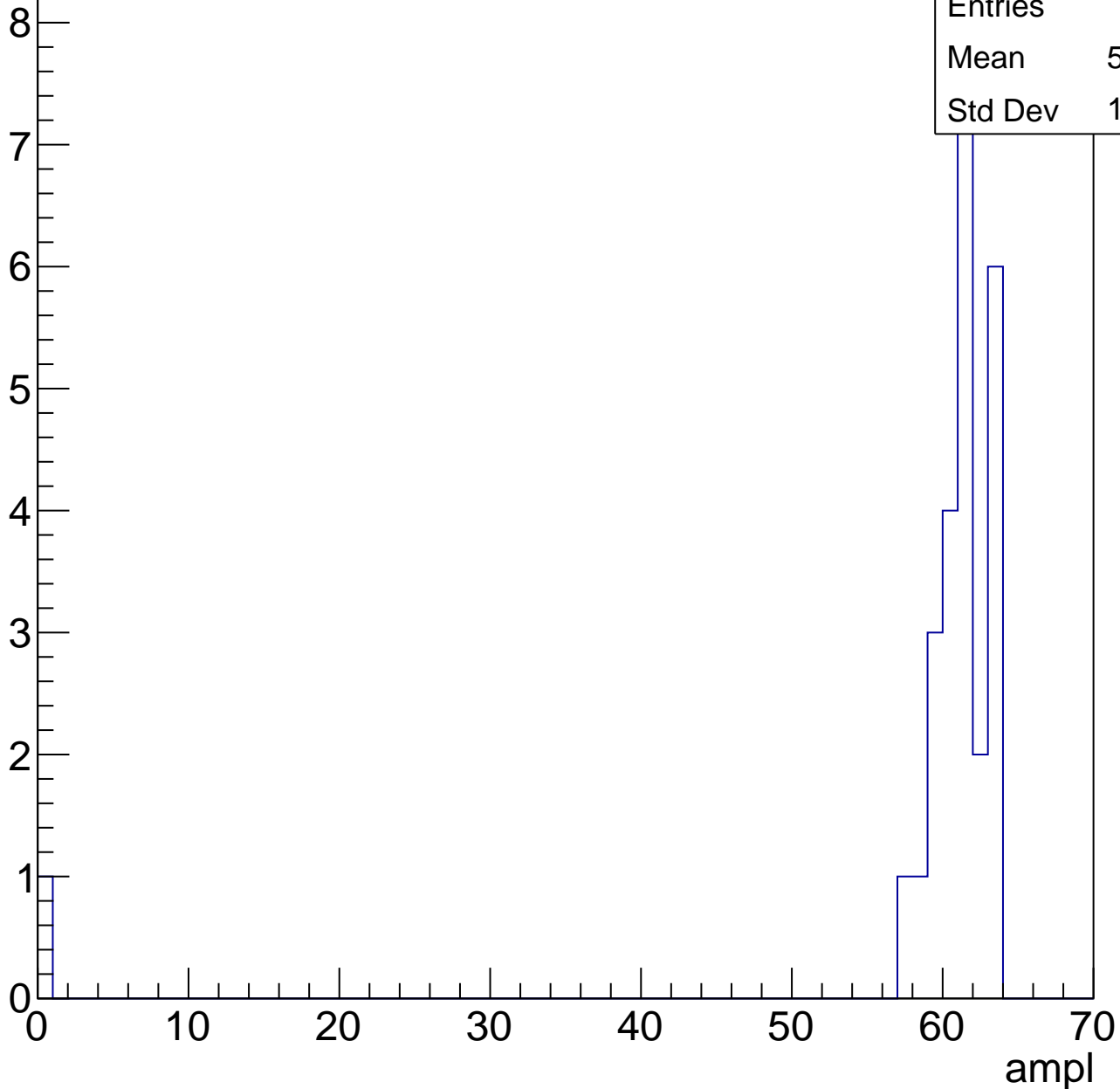


# B1L103S, U22-ch50, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	26
Mean	58.54
Std Dev	11.82



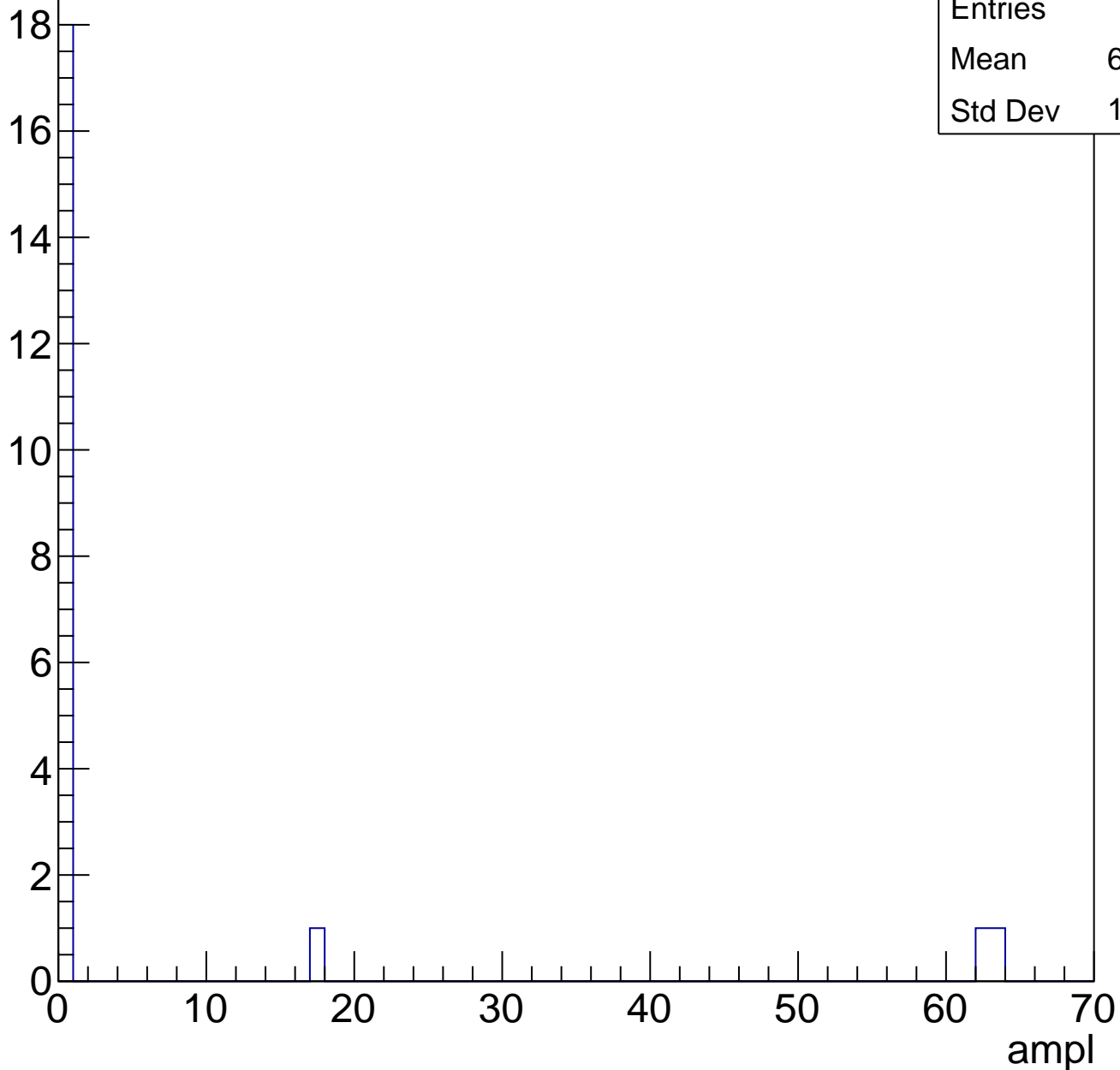


# B1L103S, U22-ch50, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	6.762
Std Dev	18.44

Entry



# B1L103S, U22-ch51, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

12  
10  
8  
6  
4  
2  
0

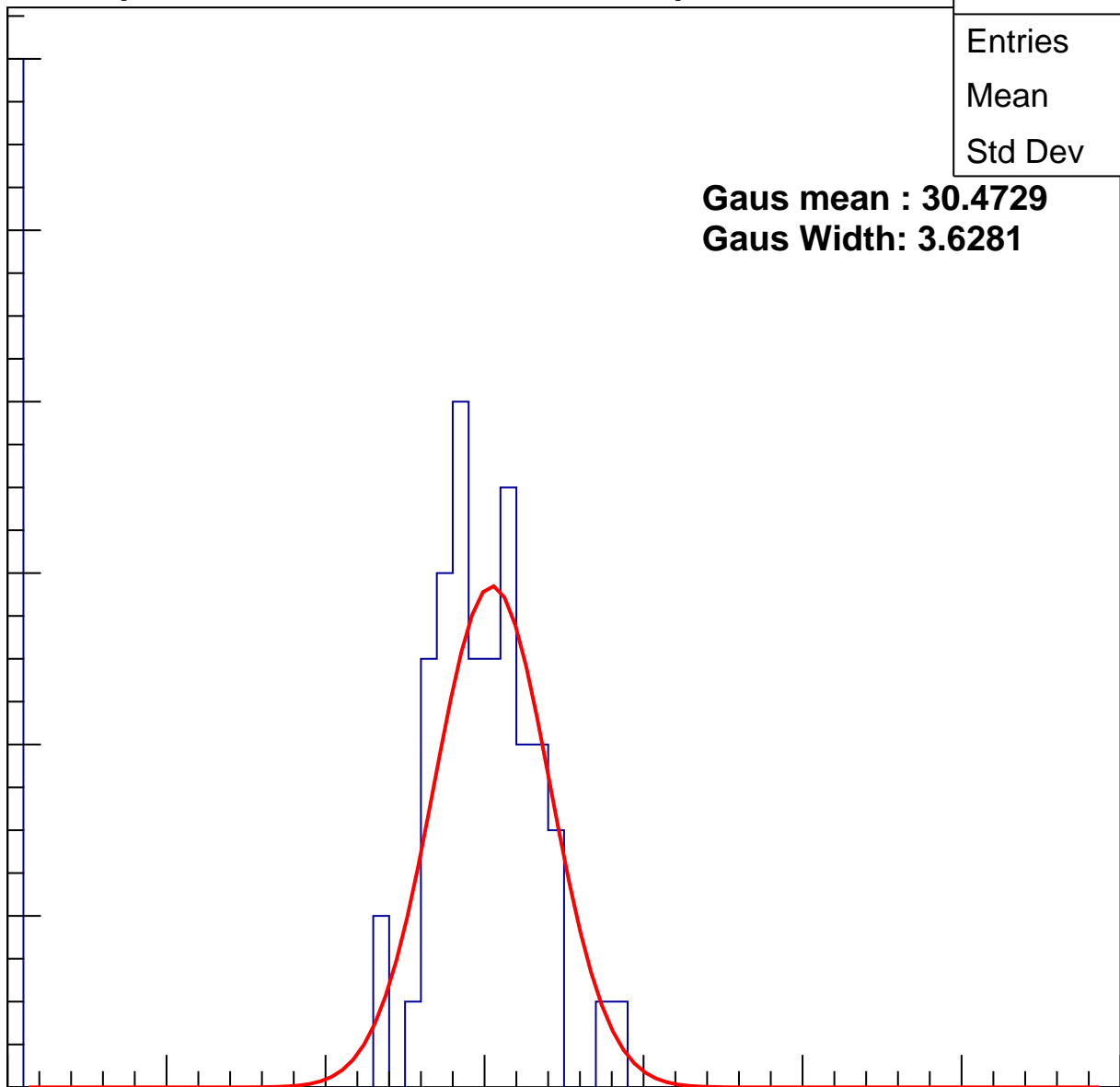
Entries	64
Mean	24
Std Dev	11.87

**Gaus mean : 30.4729**

**Gaus Width: 3.6281**

0 10 20 30 40 50 60 70

ampl



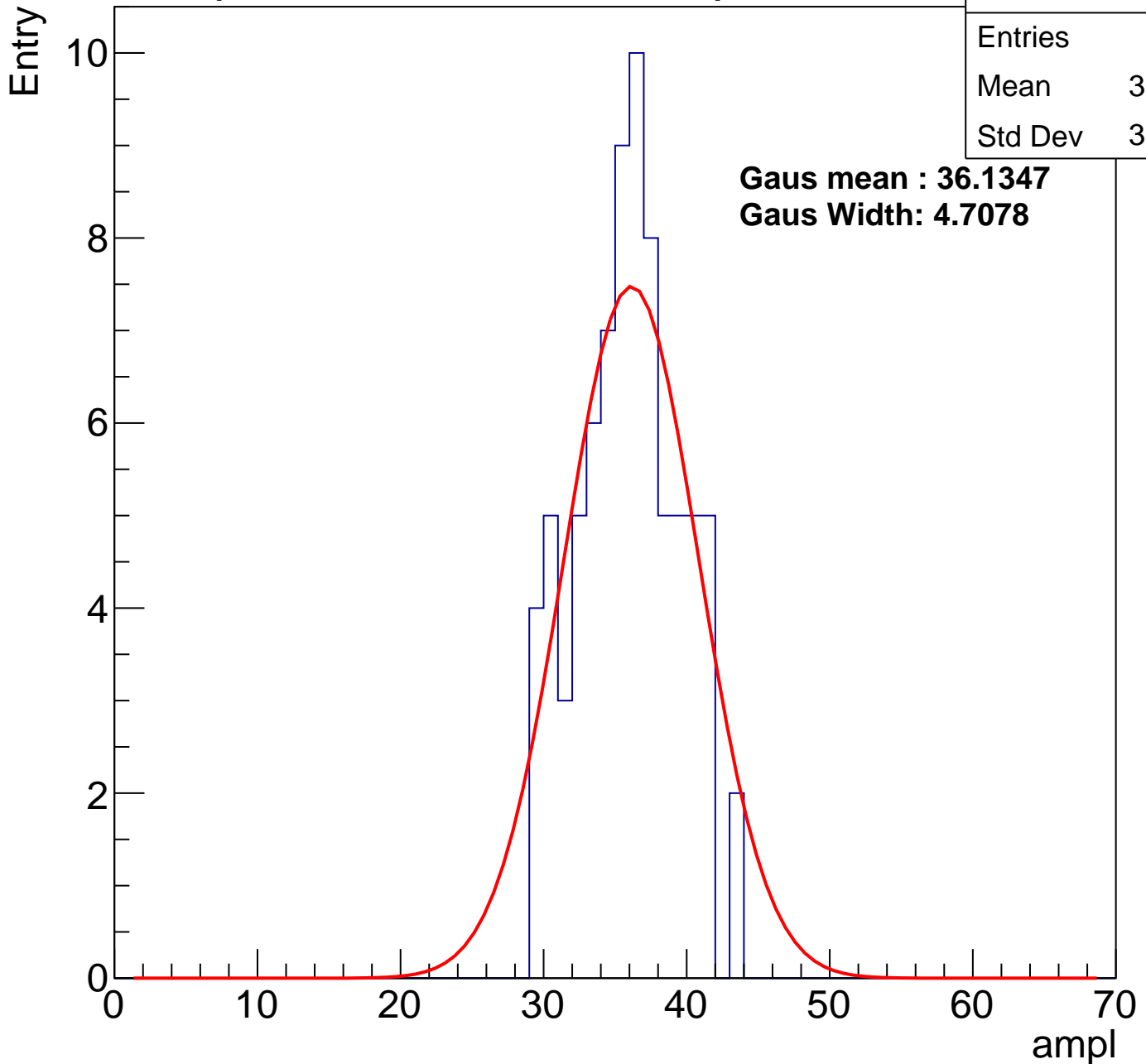
# B1L103S, U22-ch51, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	35.47
Std Dev	3.514

**Gaus mean : 36.1347**

**Gaus Width: 4.7078**



# B1L103S, U22-ch51, adc2

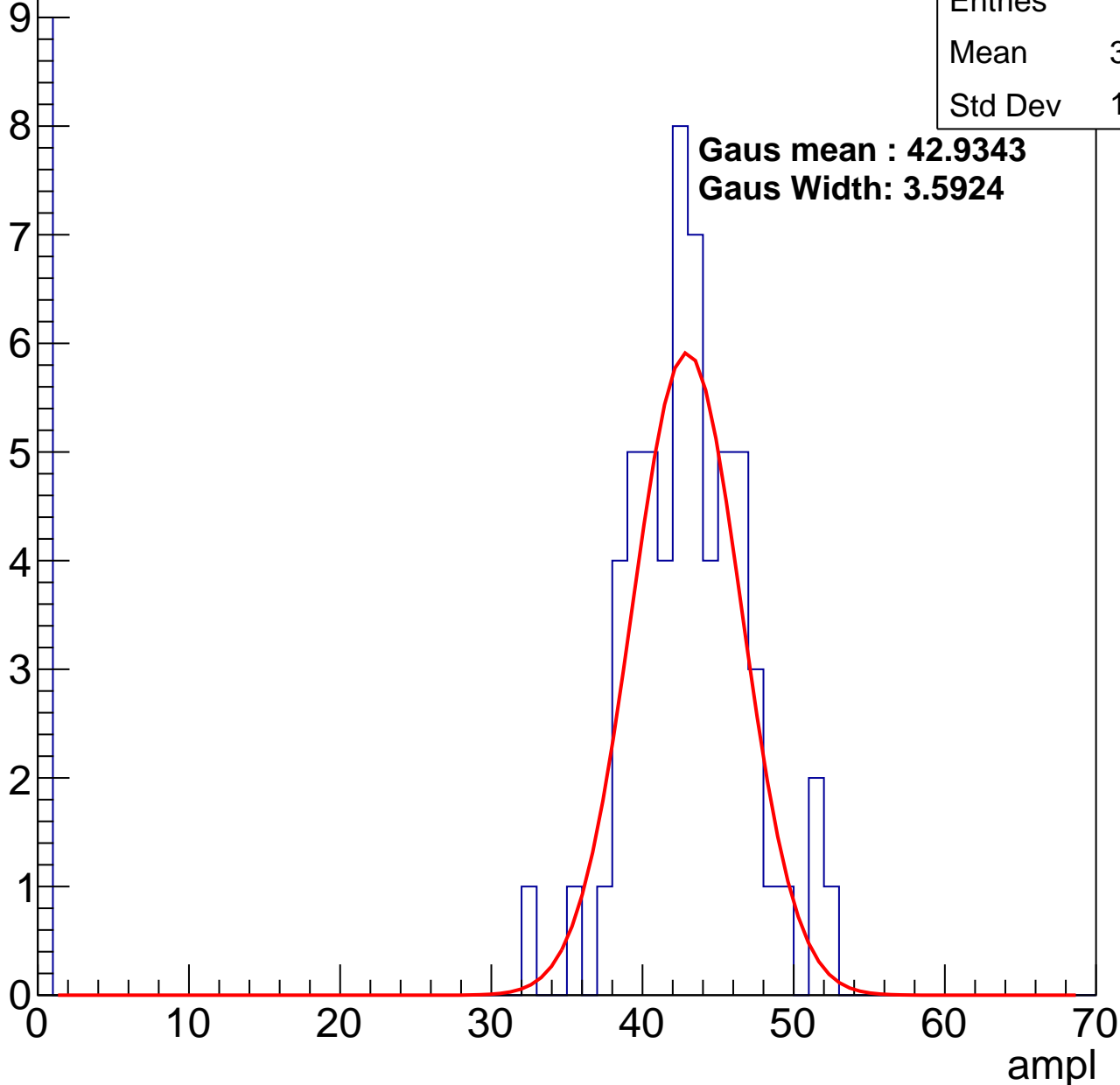
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	36.94
Std Dev	14.98

**Gaus mean : 42.9343**

**Gaus Width: 3.5924**

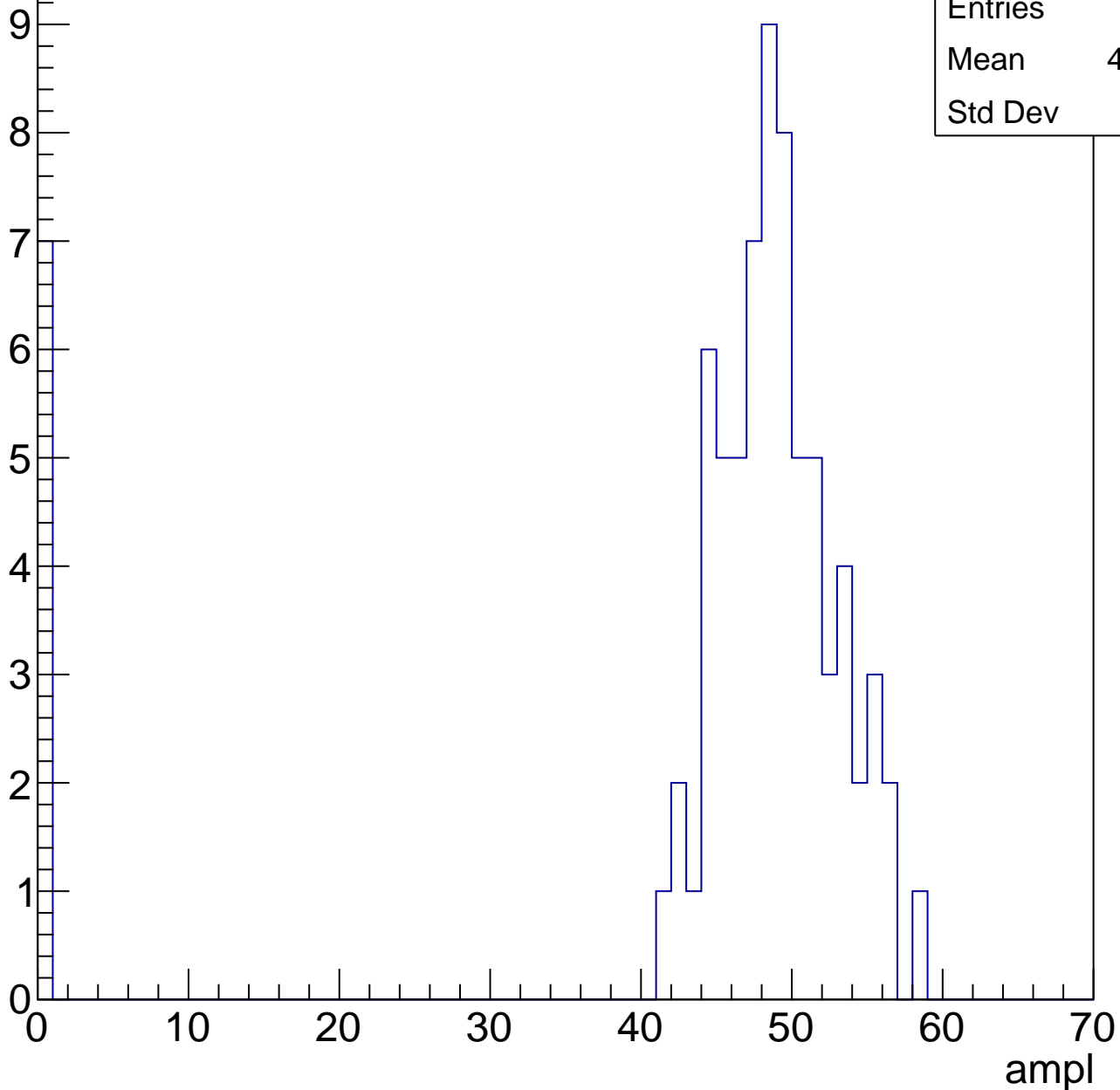


# B1L103S, U22-ch51, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	44.16
Std Dev	14.5

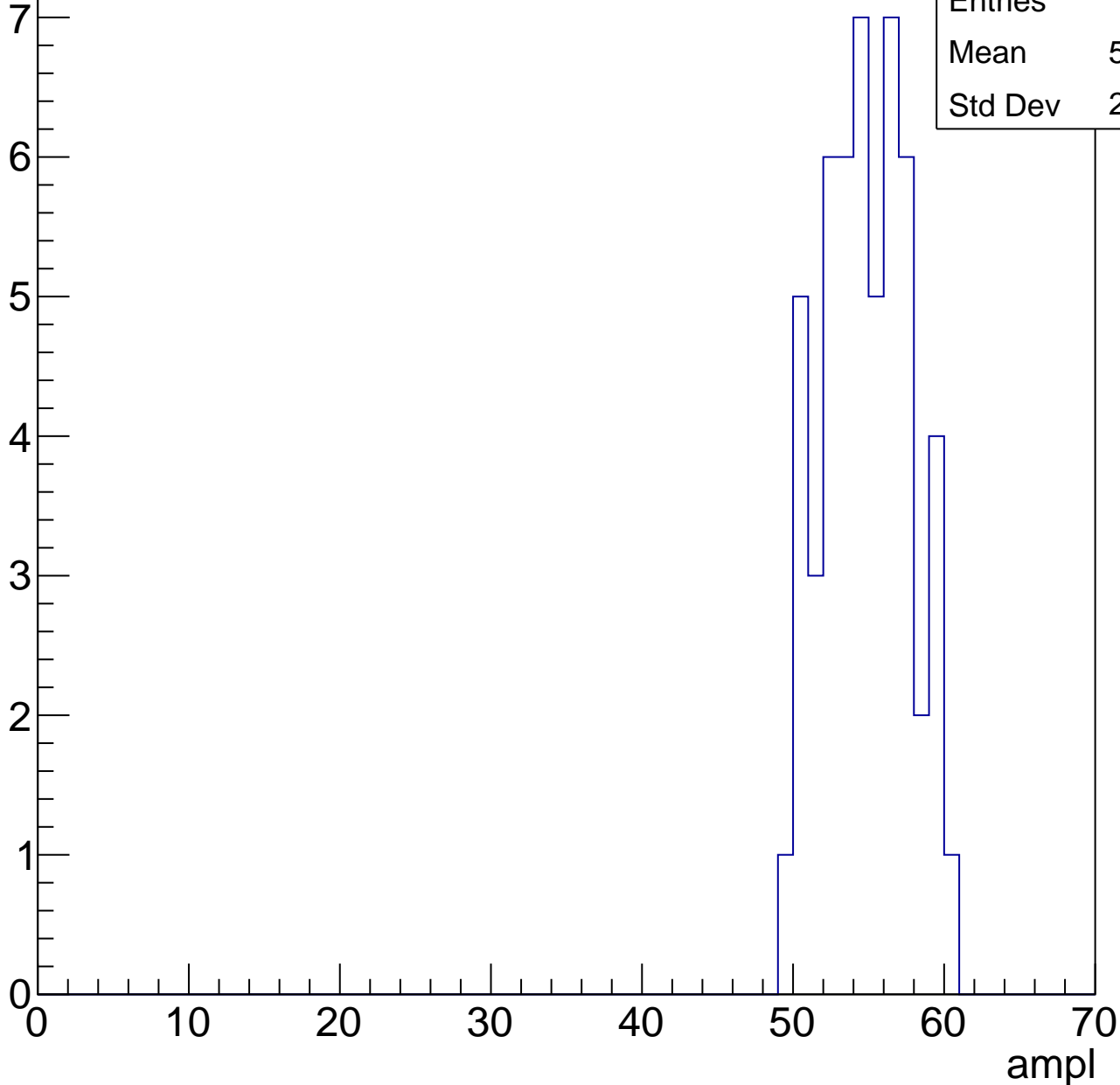


# B1L103S, U22-ch51, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

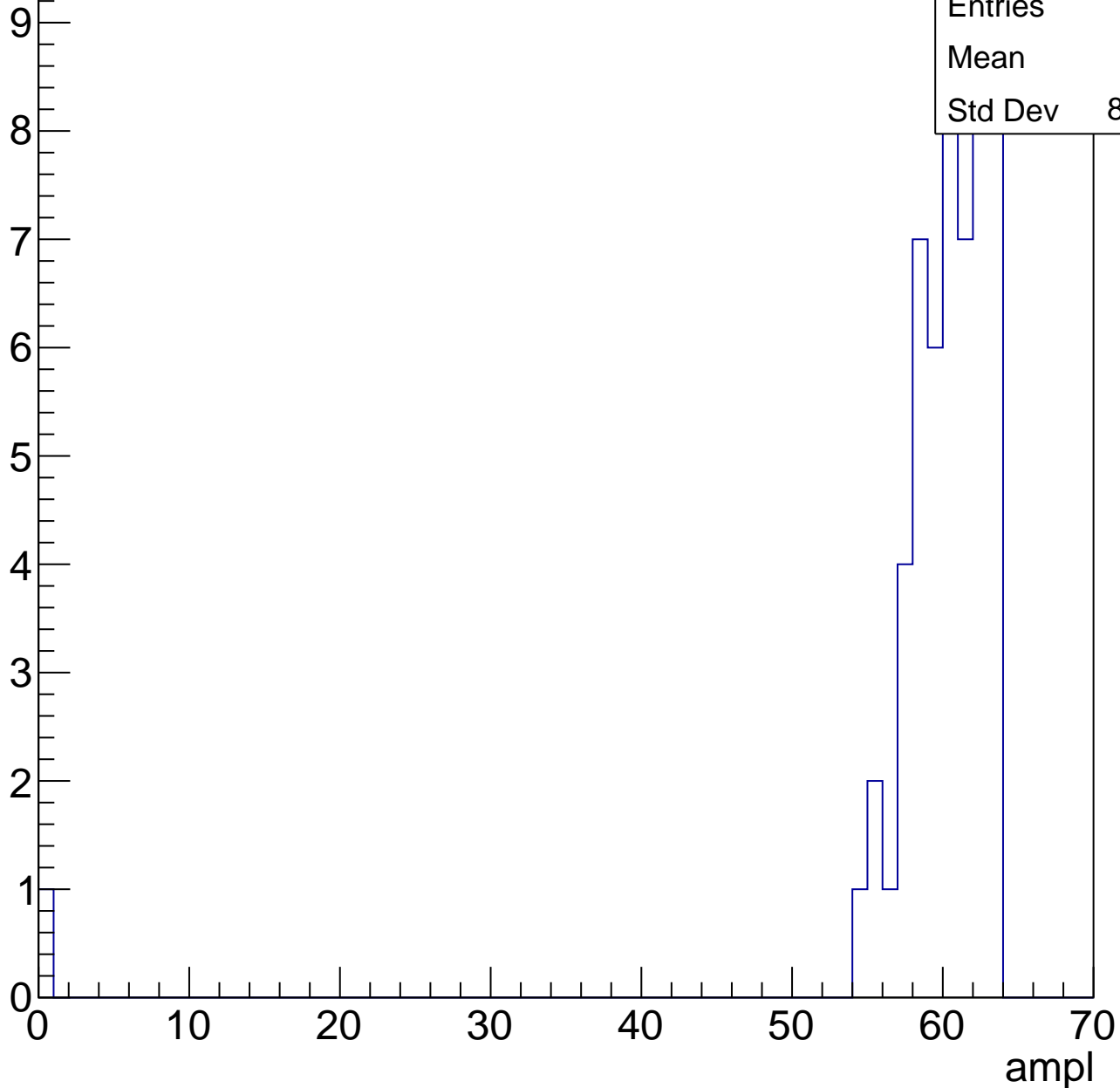
Entries	53
Mean	54.36
Std Dev	2.782



# B1L103S, U22-ch51, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch51, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch51, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch52, adc0

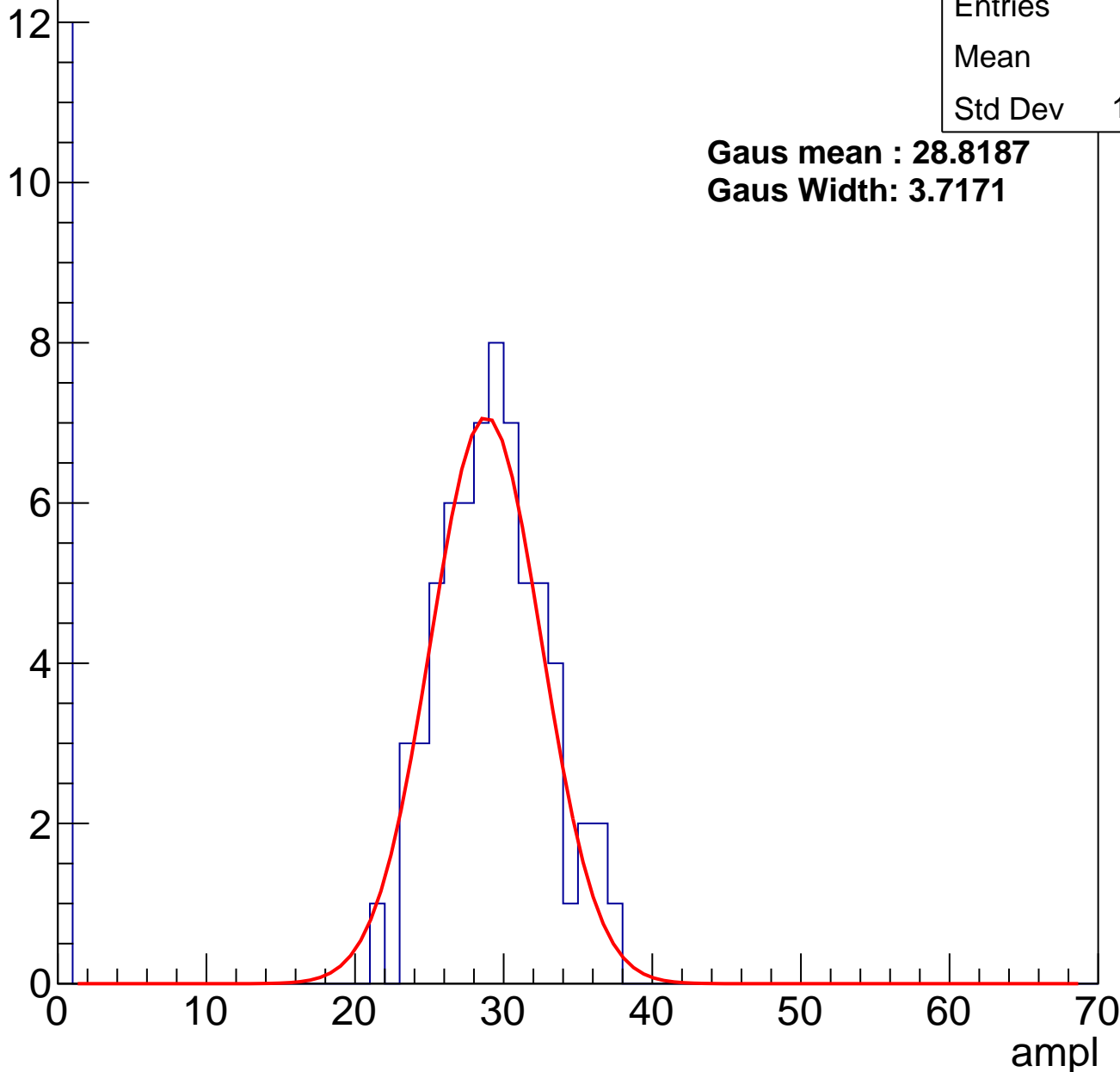
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	24.4
Std Dev	10.89

**Gaus mean : 28.8187**

**Gaus Width: 3.7171**

Entry



# B1L103S, U22-ch52, adc1

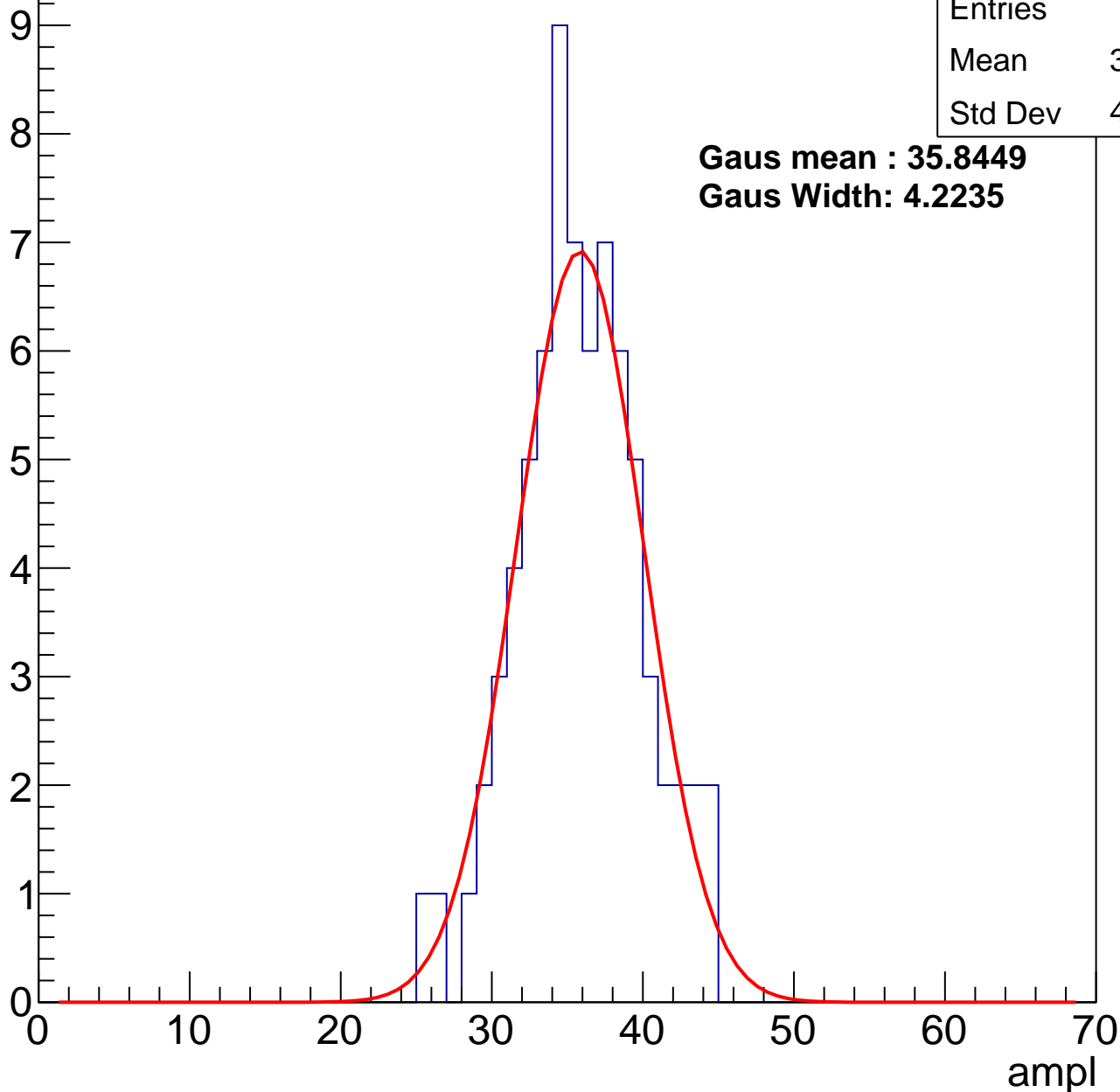
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	35.38
Std Dev	4.046

**Gaus mean : 35.8449**

**Gaus Width: 4.2235**



# B1L103S, U22-ch52, adc2

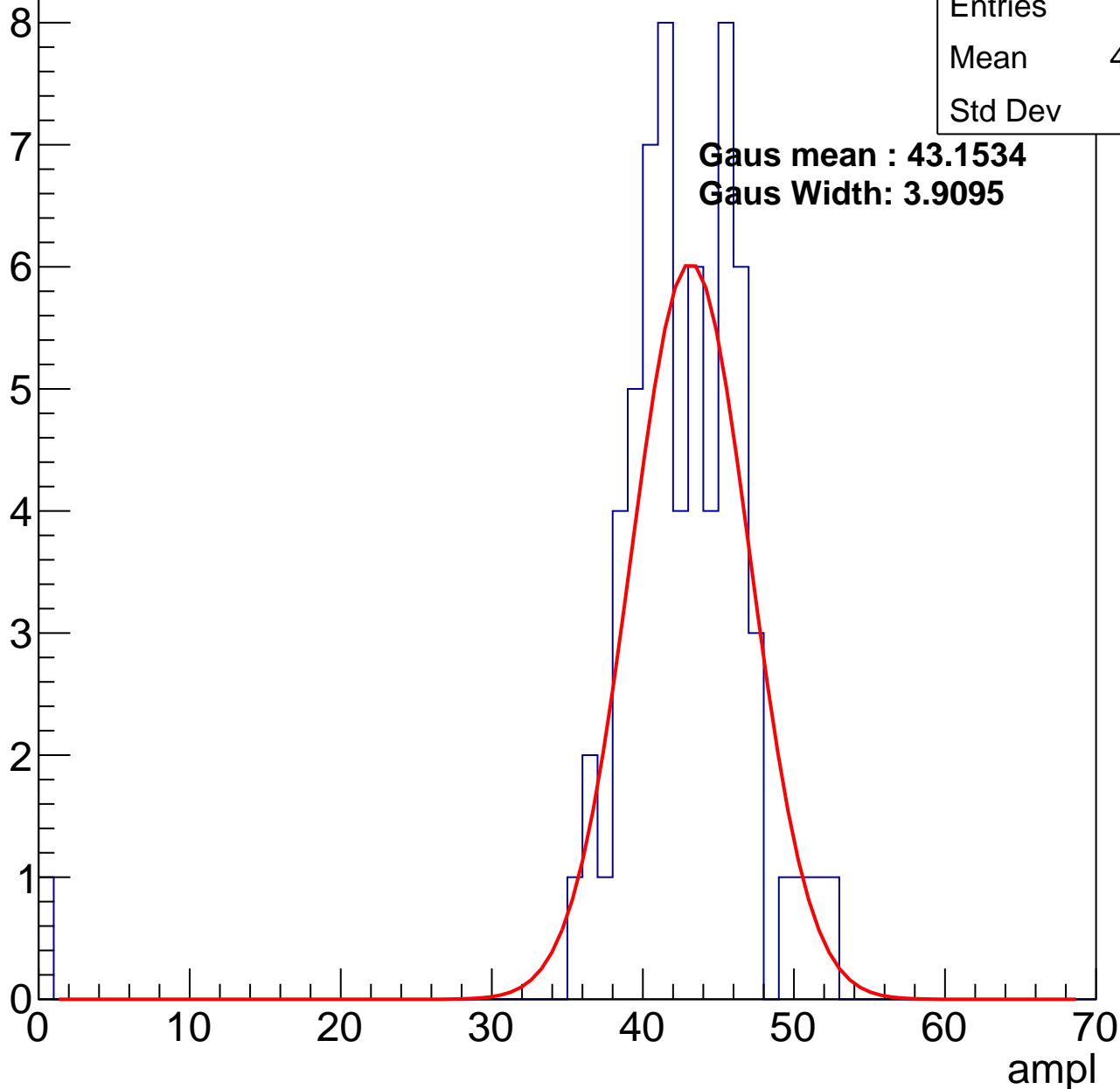
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	41.88
Std Dev	6.39

**Gaus mean : 43.1534**

**Gaus Width: 3.9095**



# B1L103S, U22-ch52, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

10

8

6

4

2

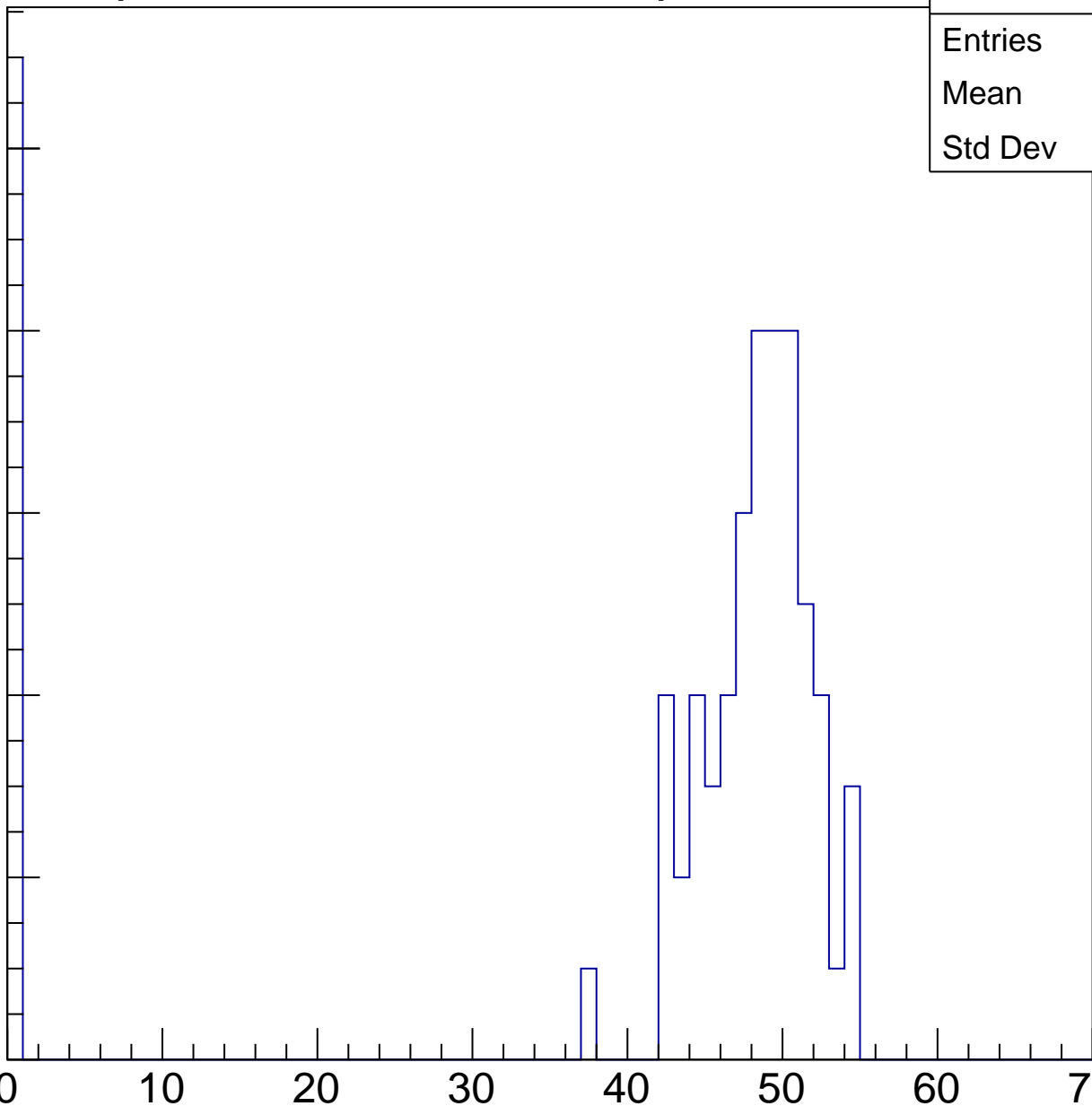
0

Entries 72

Mean 40.58

Std Dev 17.52

ampl

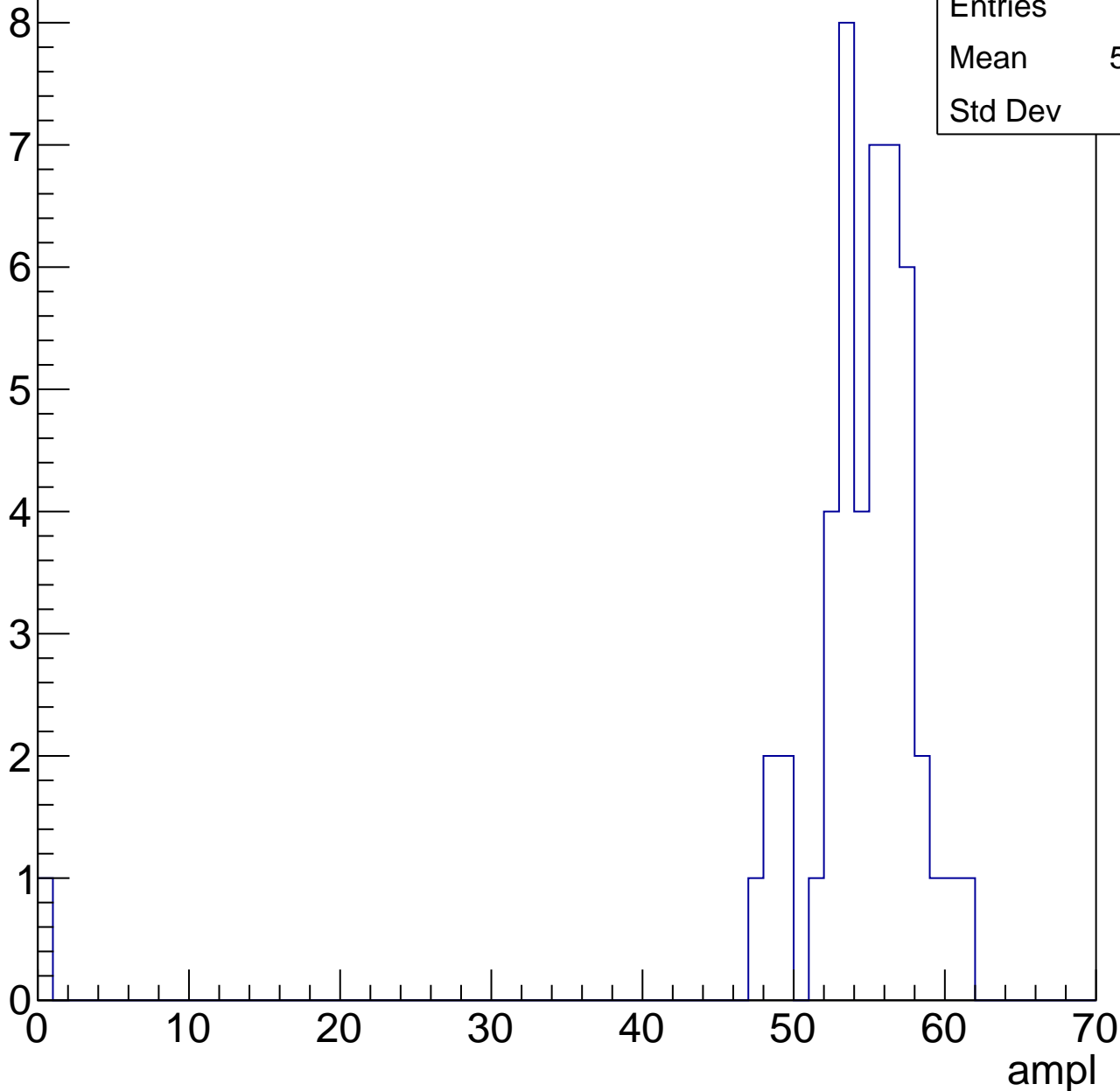


# B1L103S, U22-ch52, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	53.23
Std Dev	8.32

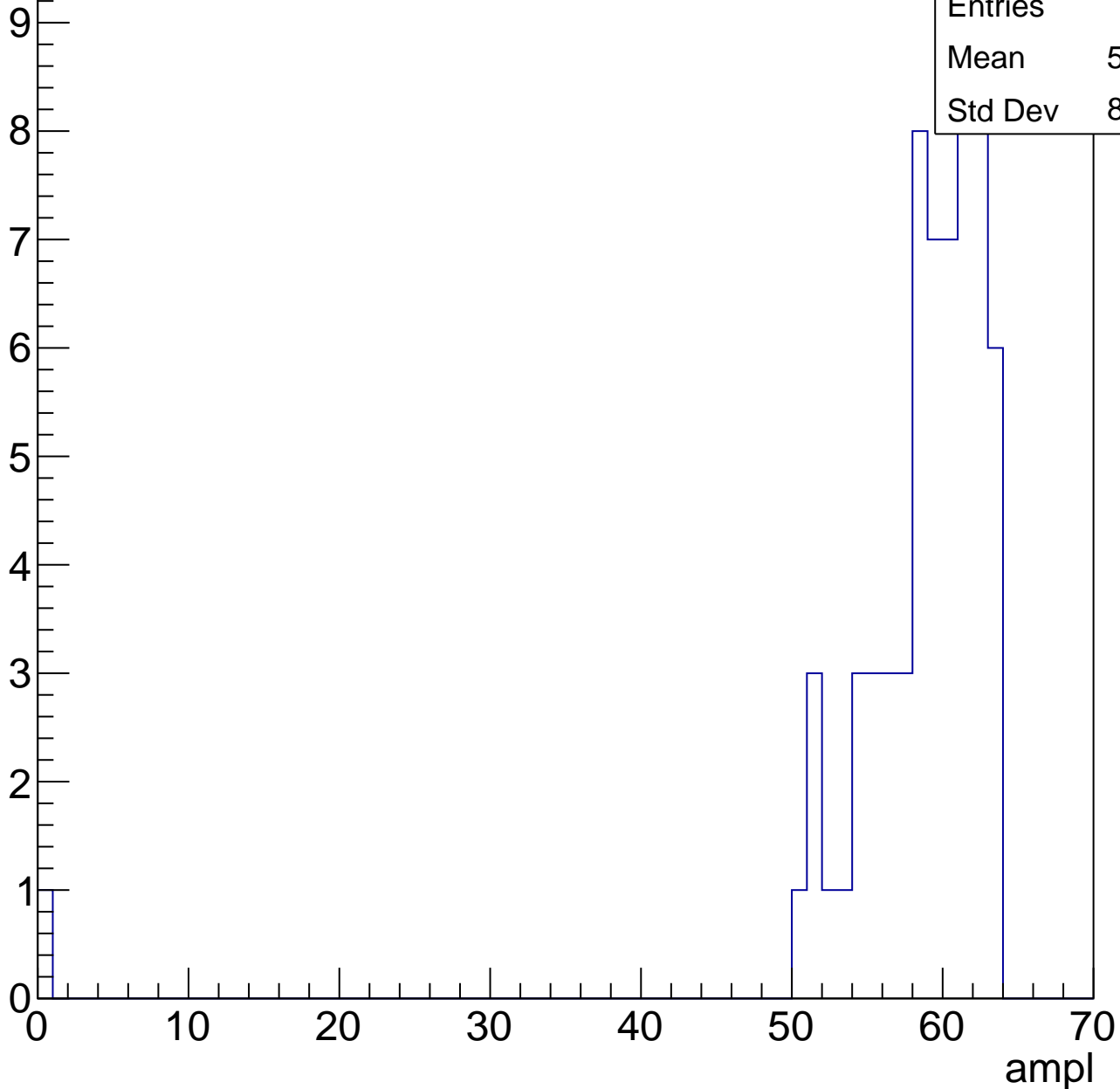


# B1L103S, U22-ch52, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

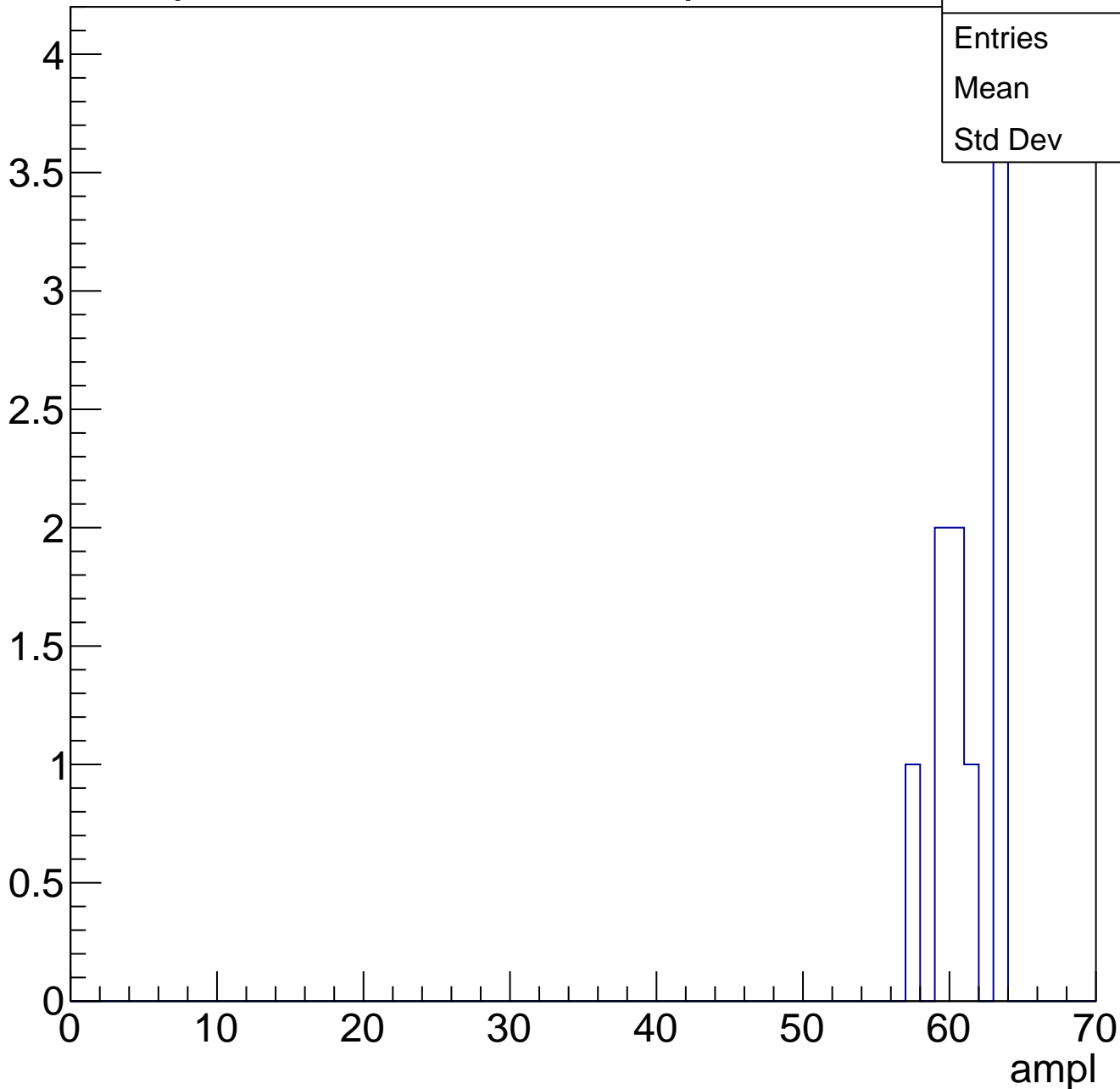
Entries	64
Mean	57.73
Std Dev	8.026



# B1L103S, U22-ch52, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

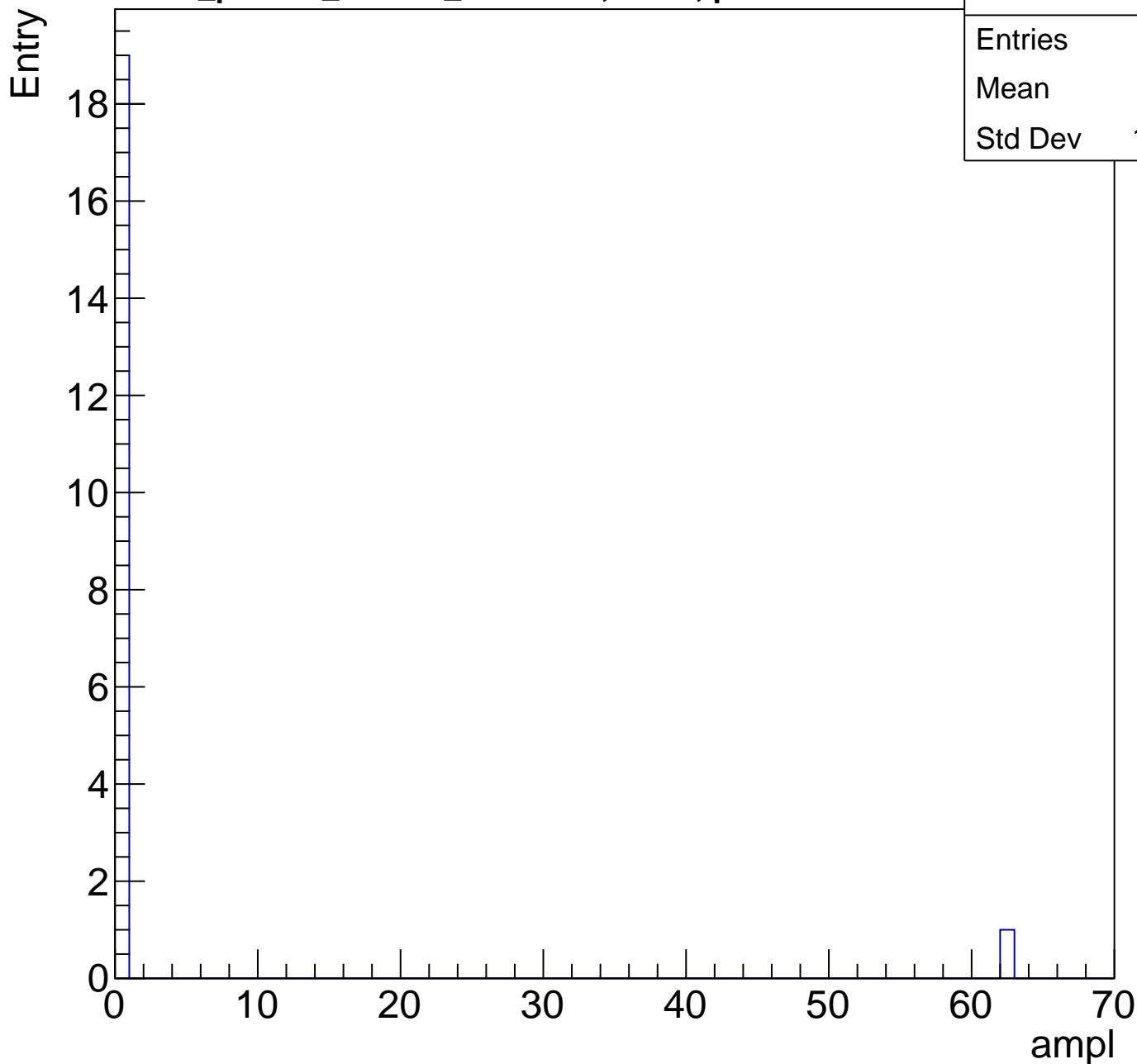




# B1L103S, U22-ch52, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51



# B1L103S, U22-ch53, adc0

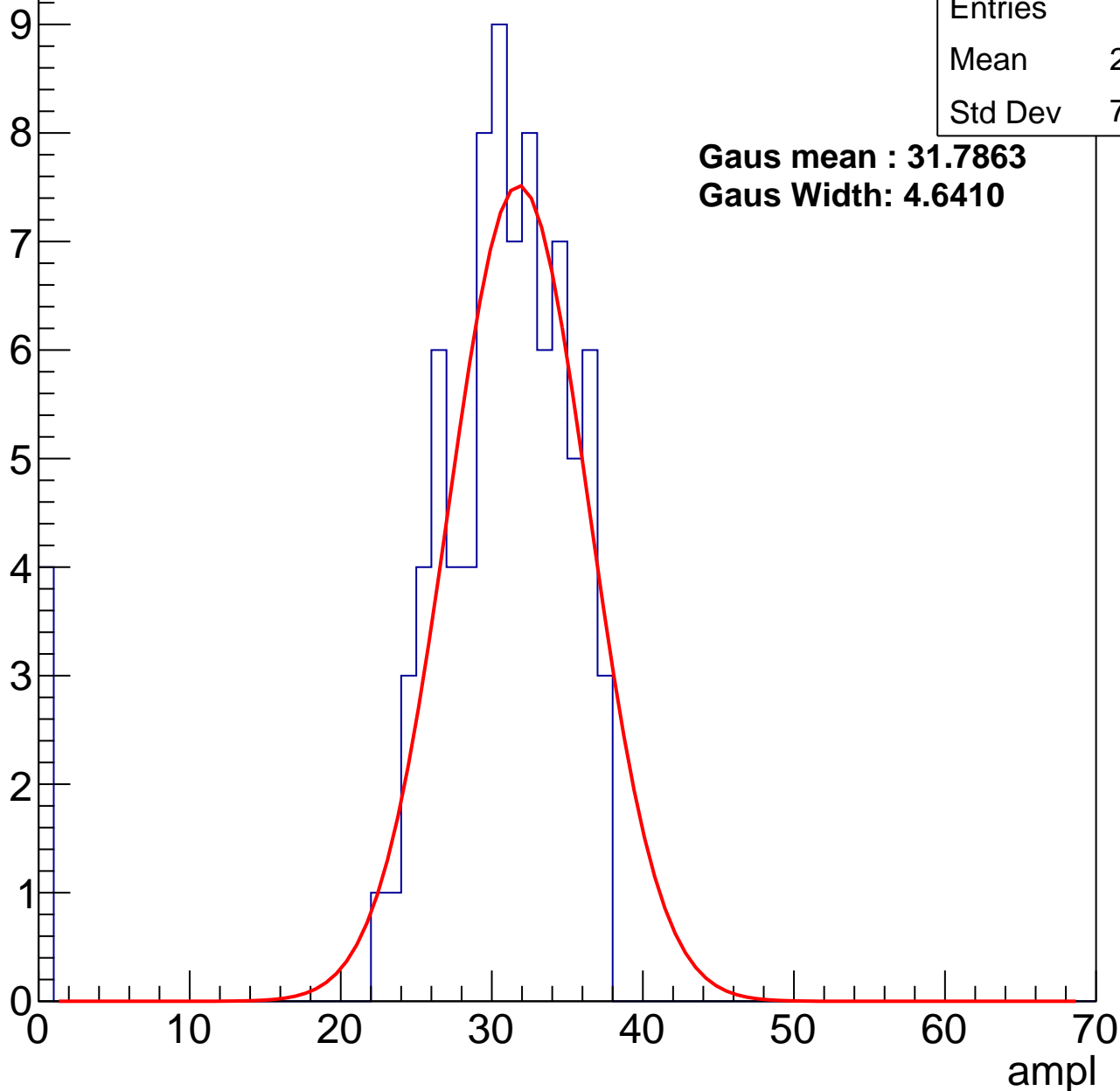
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	86
Mean	29.14
Std Dev	7.398

**Gaus mean : 31.7863**

**Gaus Width: 4.6410**



# B1L103S, U22-ch53, adc1

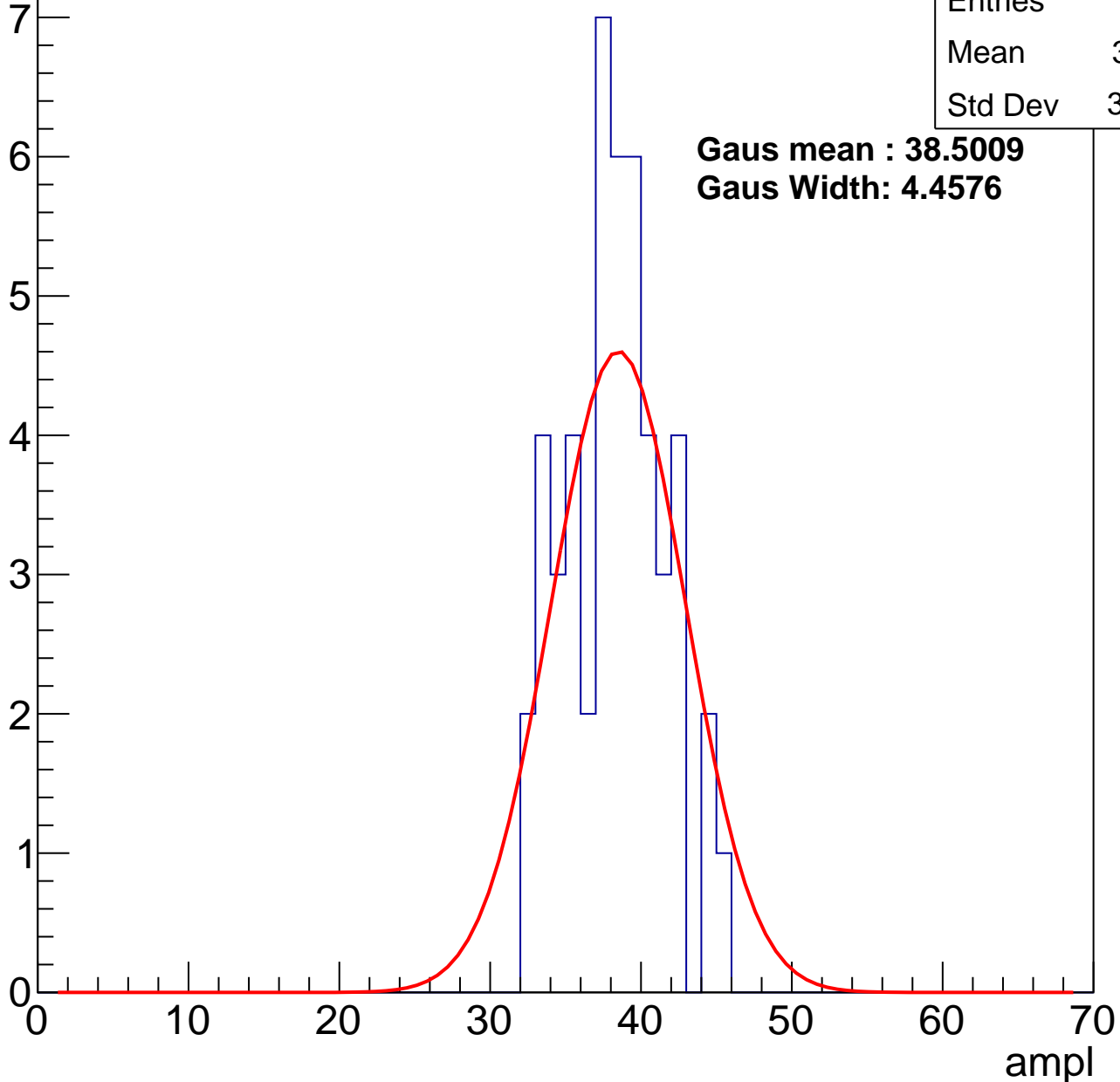
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	37.81
Std Dev	3.238

**Gaus mean : 38.5009**

**Gaus Width: 4.4576**



# B1L103S, U22-ch53, adc2

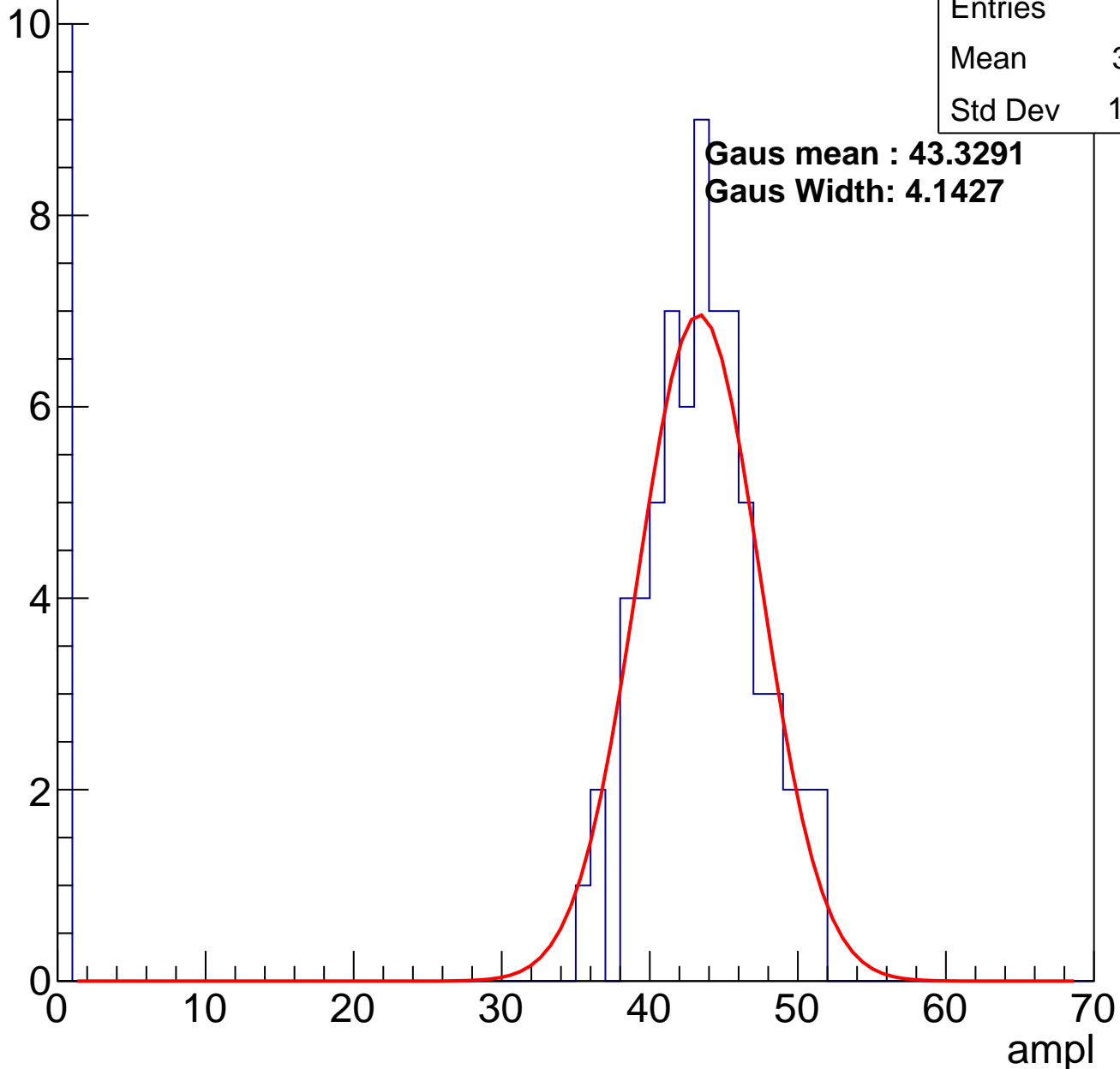
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	37.71
Std Dev	14.75

**Gaus mean : 43.3291**

**Gaus Width: 4.1427**

Entry

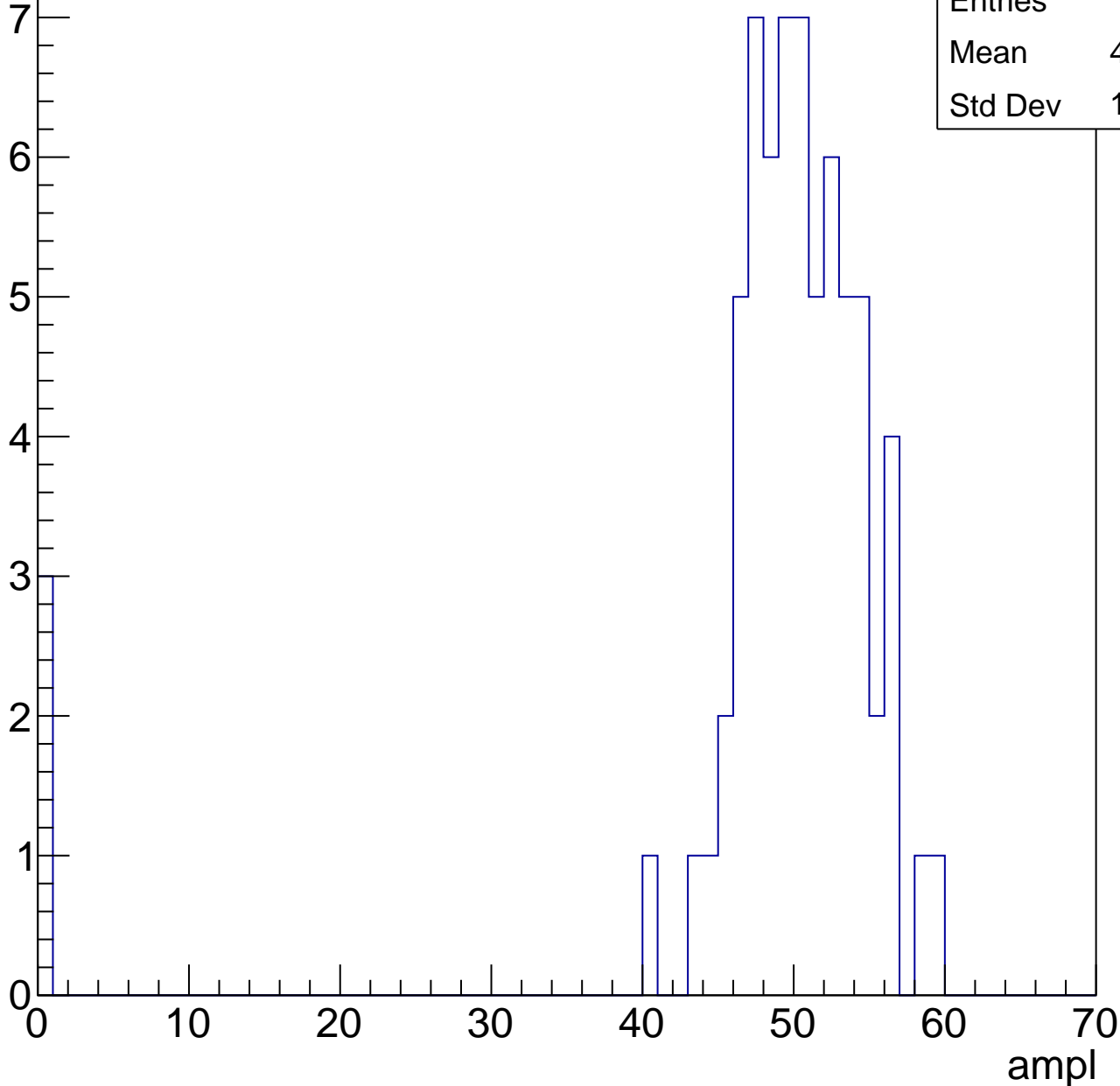


# B1L103S, U22-ch53, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

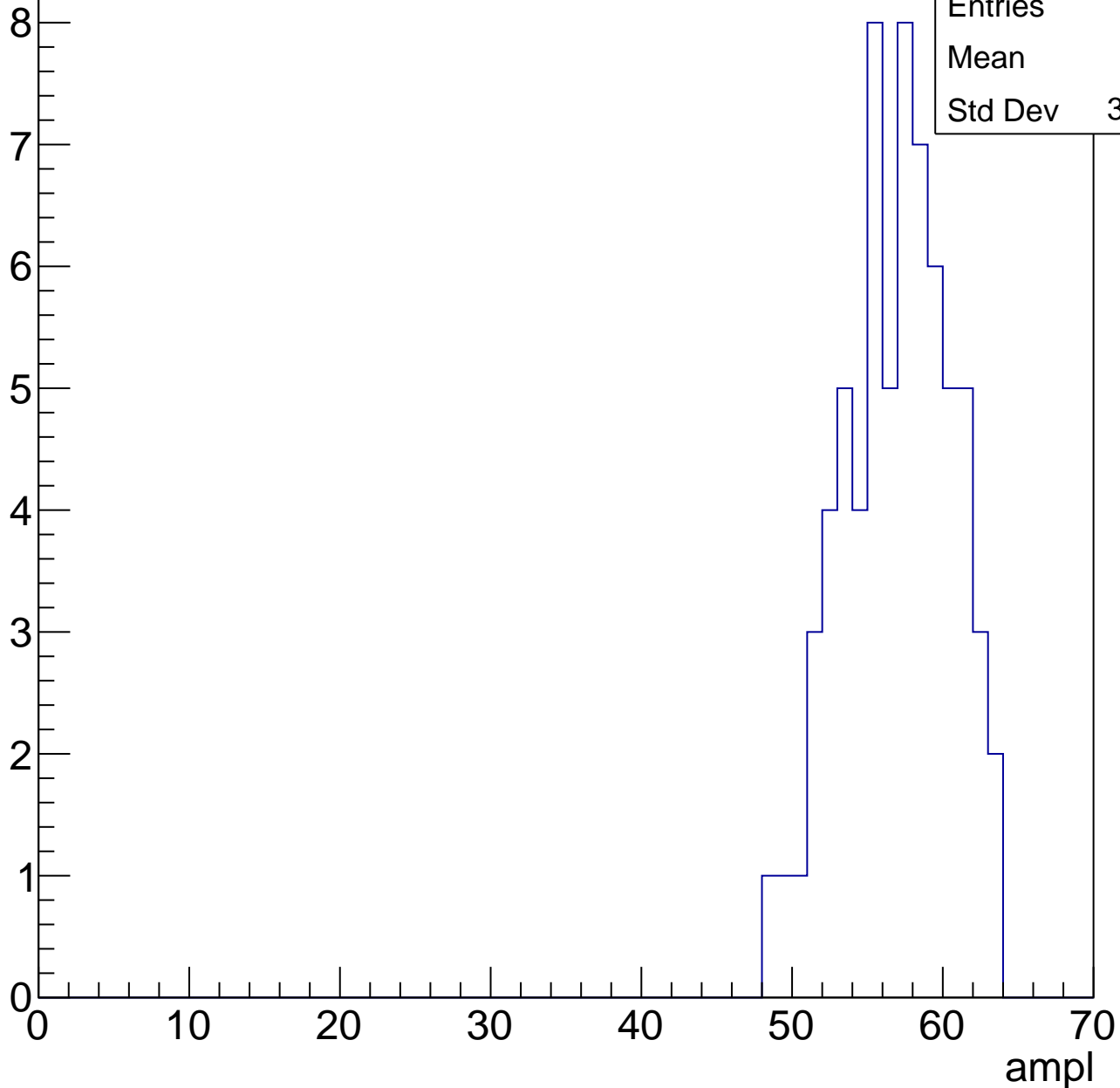
Entries	69
Mean	47.97
Std Dev	10.85



# B1L103S, U22-ch53, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

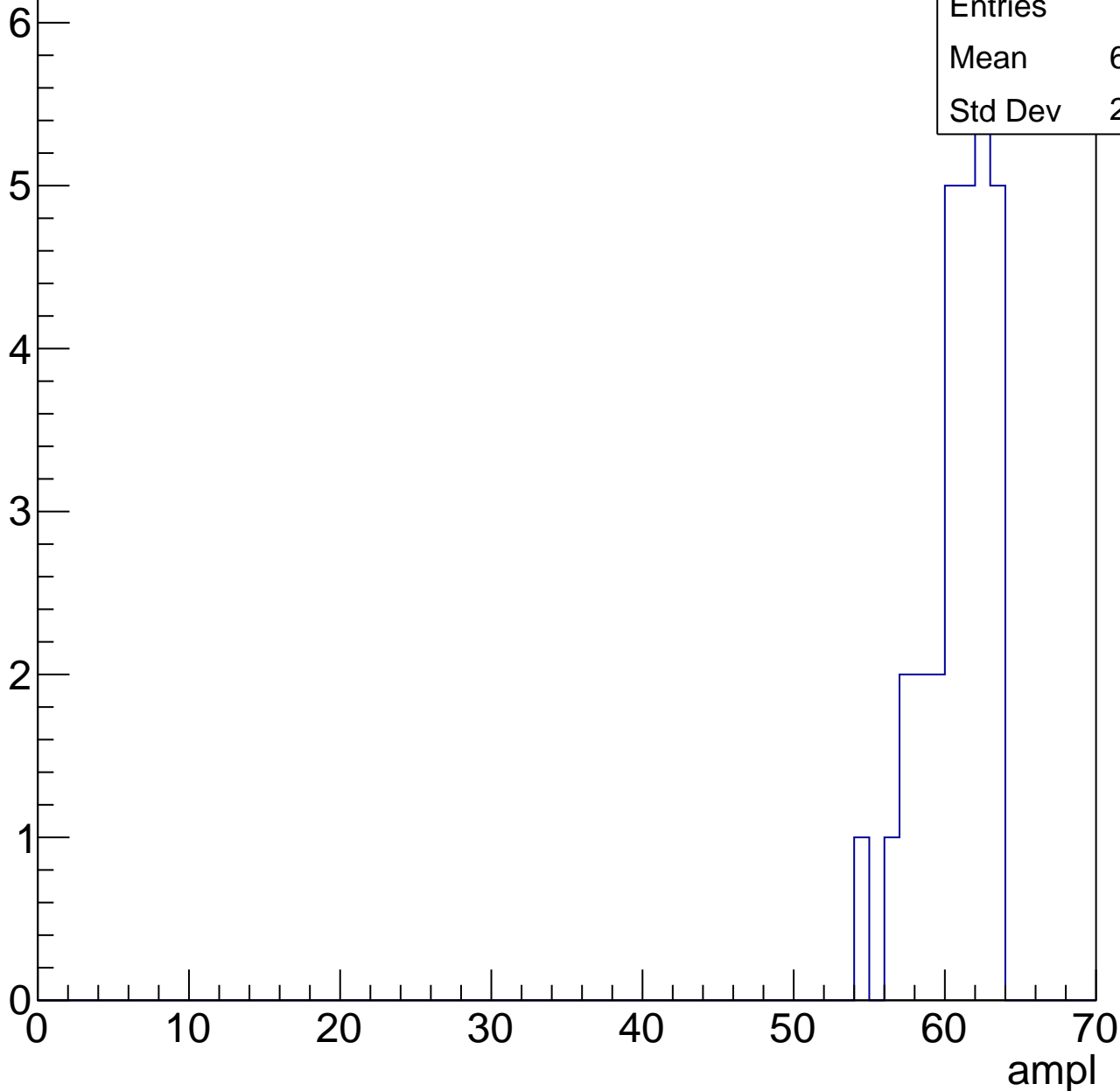


# B1L103S, U22-ch53, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

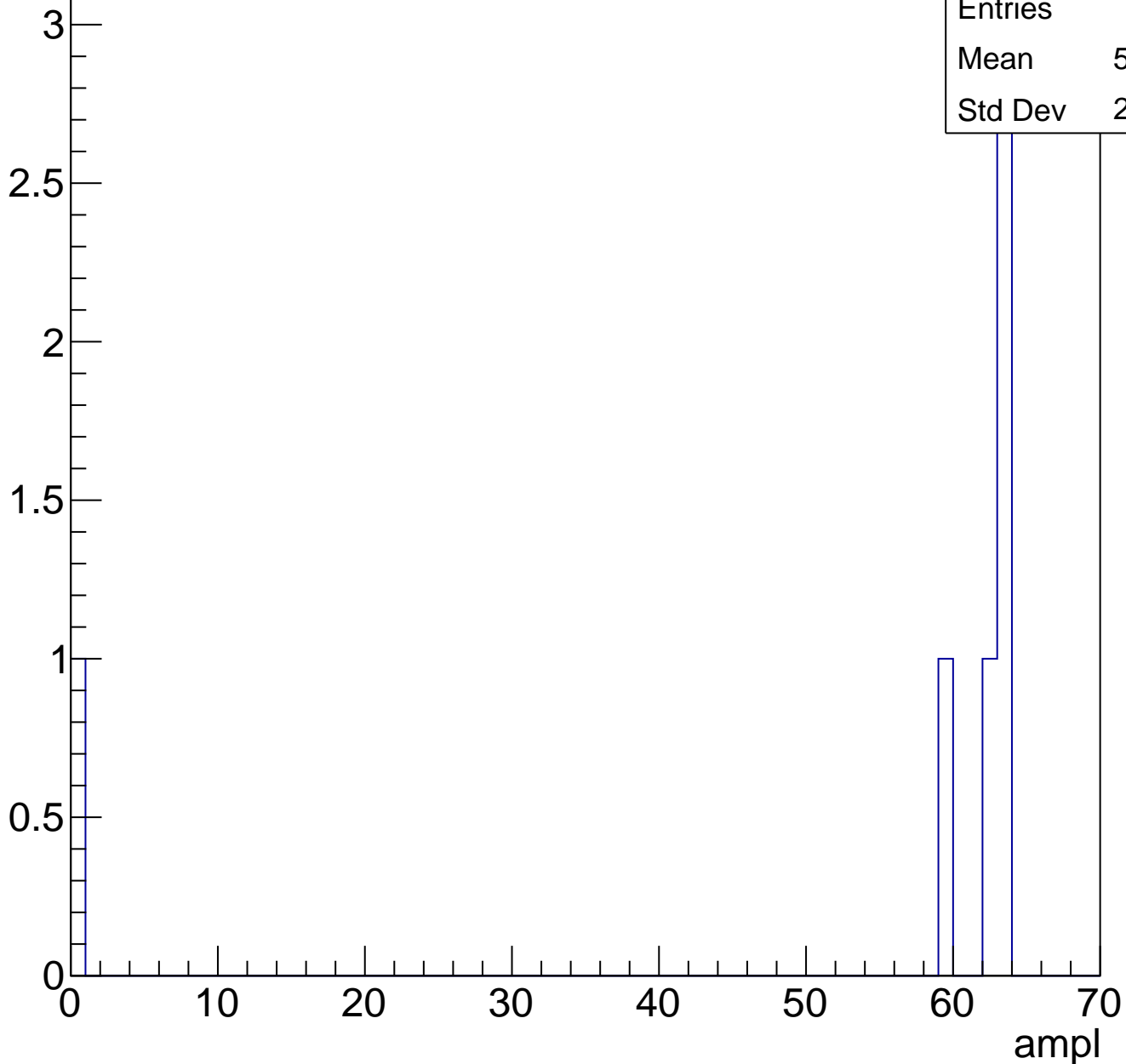
Entries	29
Mean	60.34
Std Dev	2.278



# B1L103S, U22-ch53, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch53, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch54, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	99
Mean	26.27
Std Dev	8.41

**Gaus mean : 28.8372**

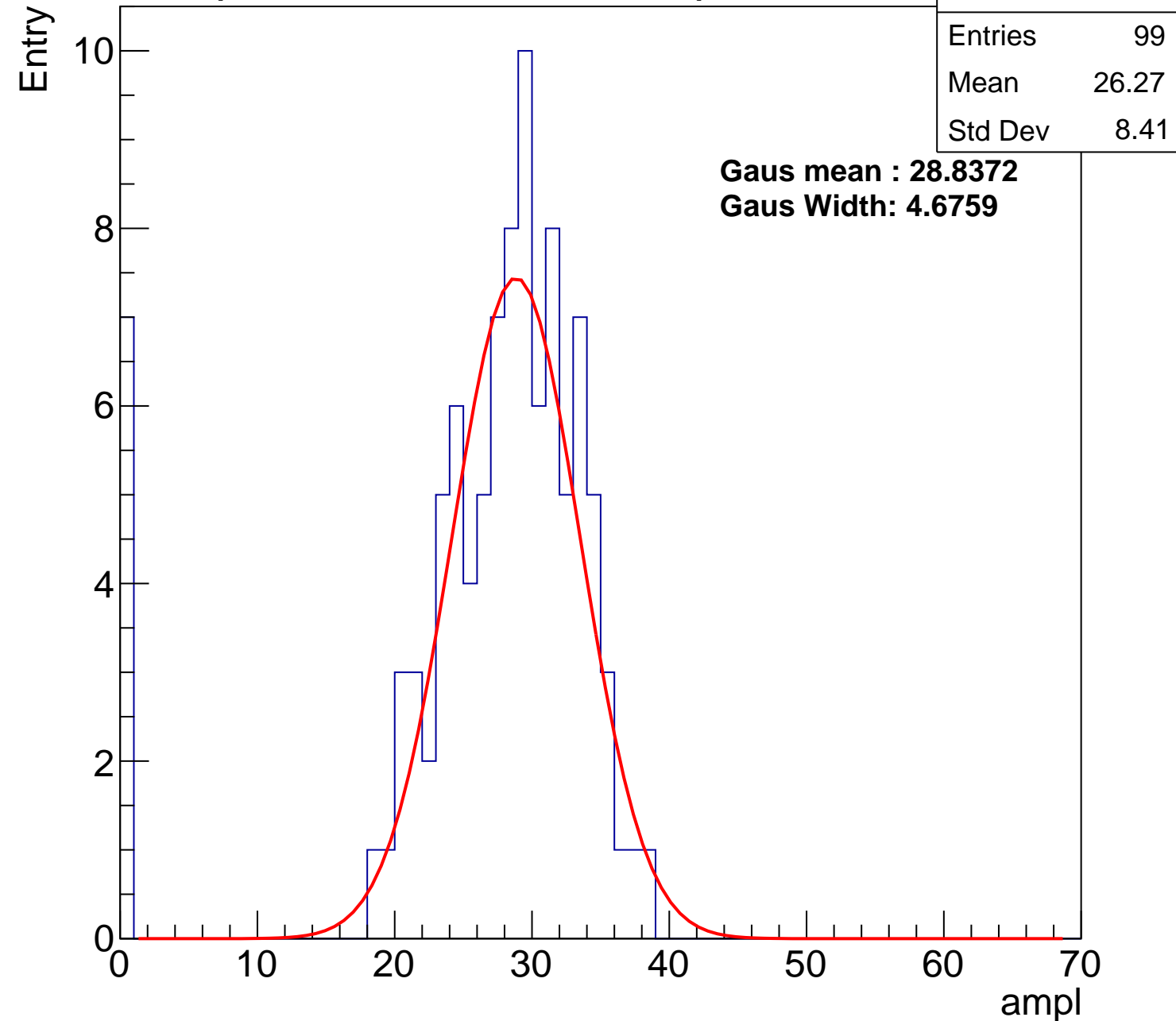
**Gaus Width: 4.6759**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch54, adc1

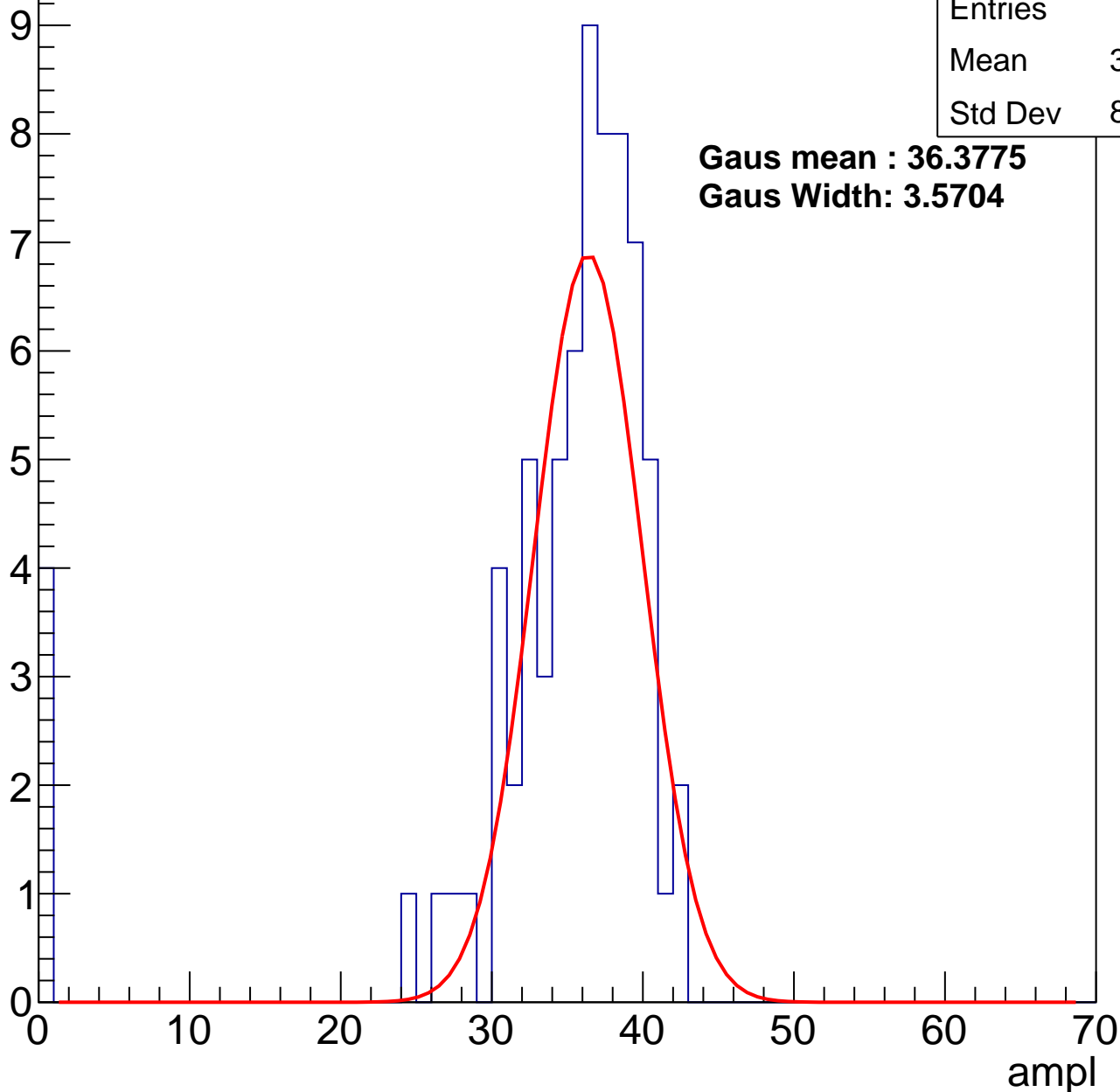
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.53
Std Dev	8.864

**Gaus mean : 36.3775**

**Gaus Width: 3.5704**



# B1L103S, U22-ch54, adc2

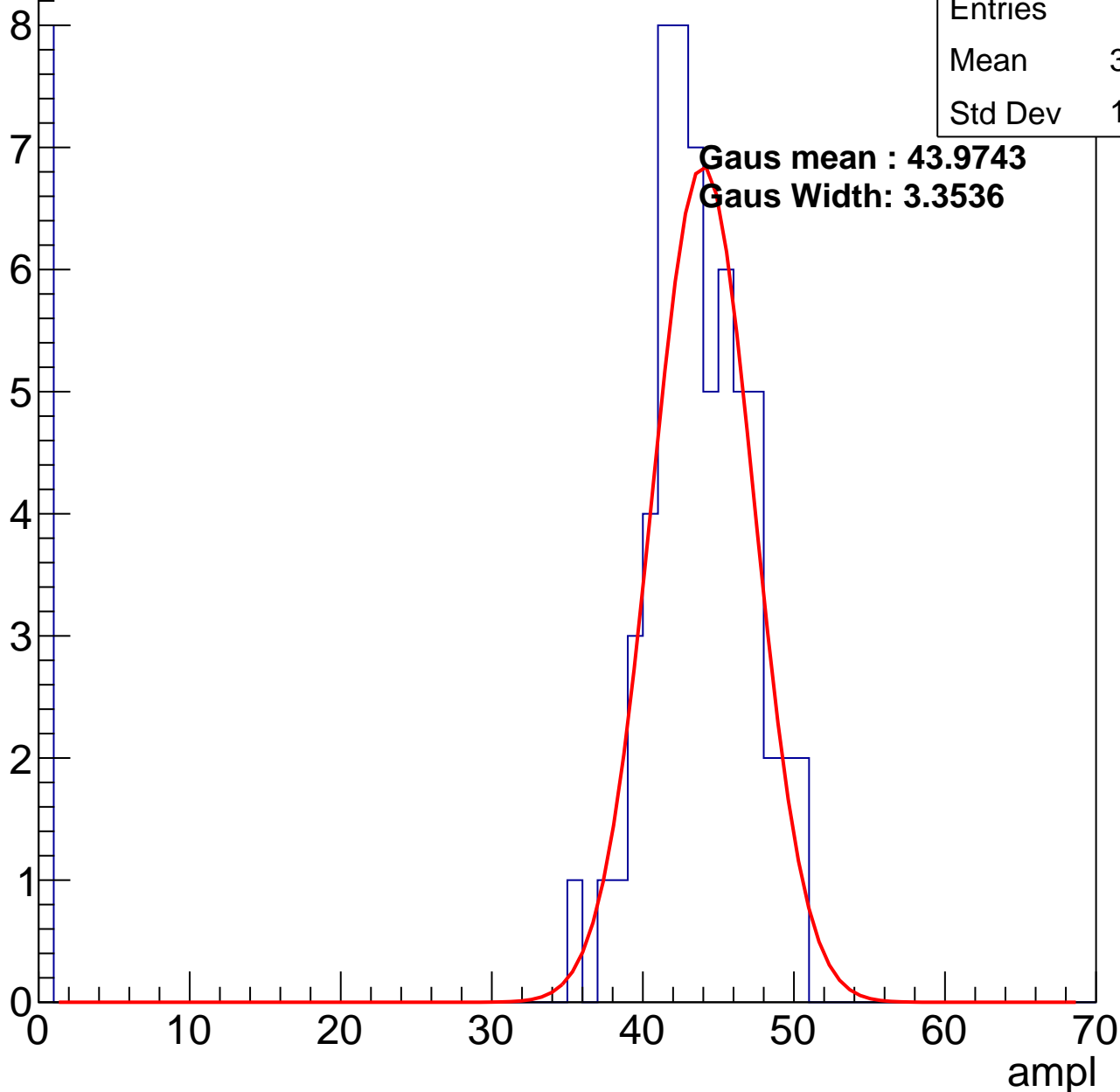
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.25
Std Dev	14.29

**Gaus mean : 43.9743**

**Gaus Width: 3.3536**

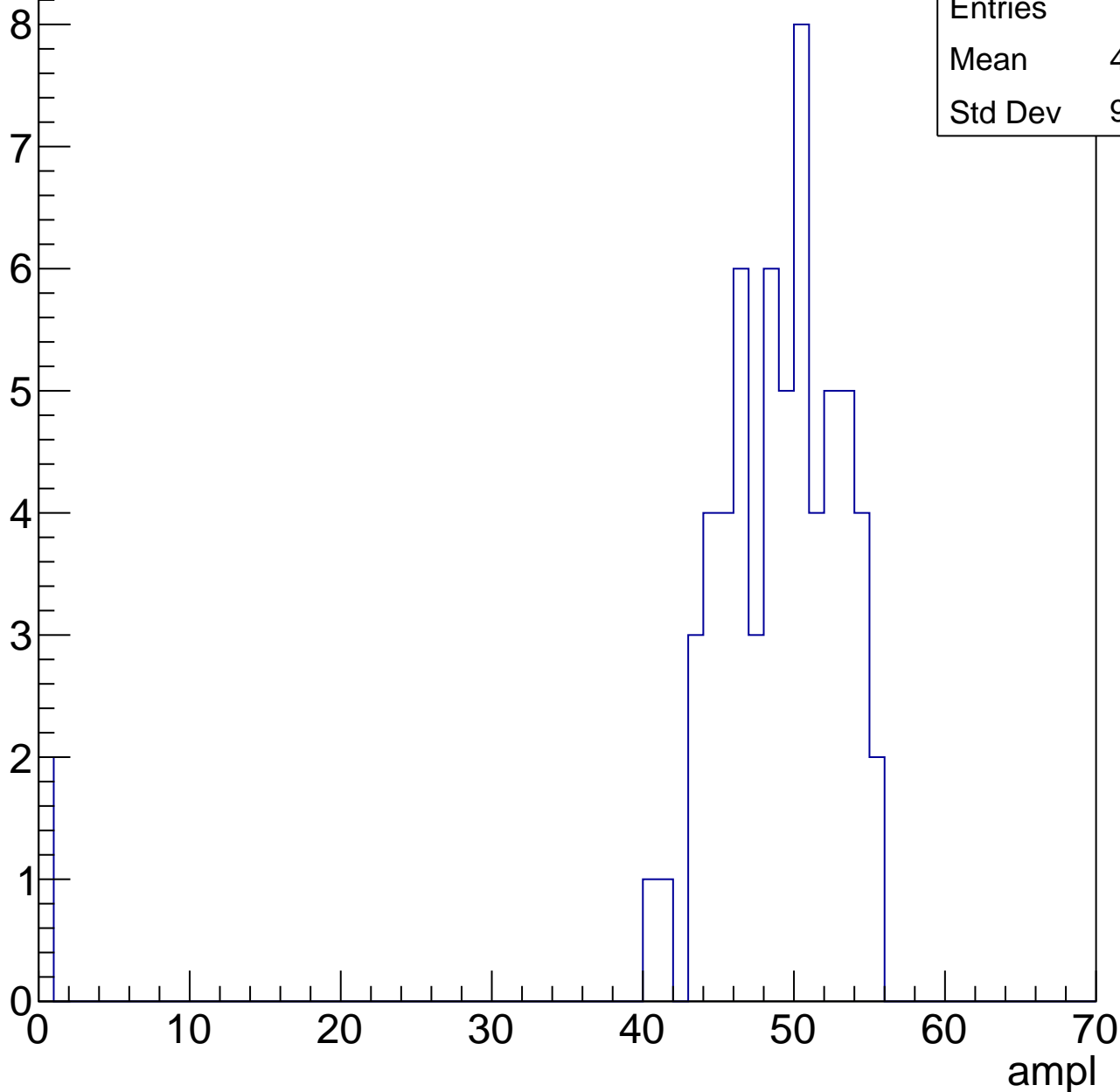


# B1L103S, U22-ch54, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	47.16
Std Dev	9.257

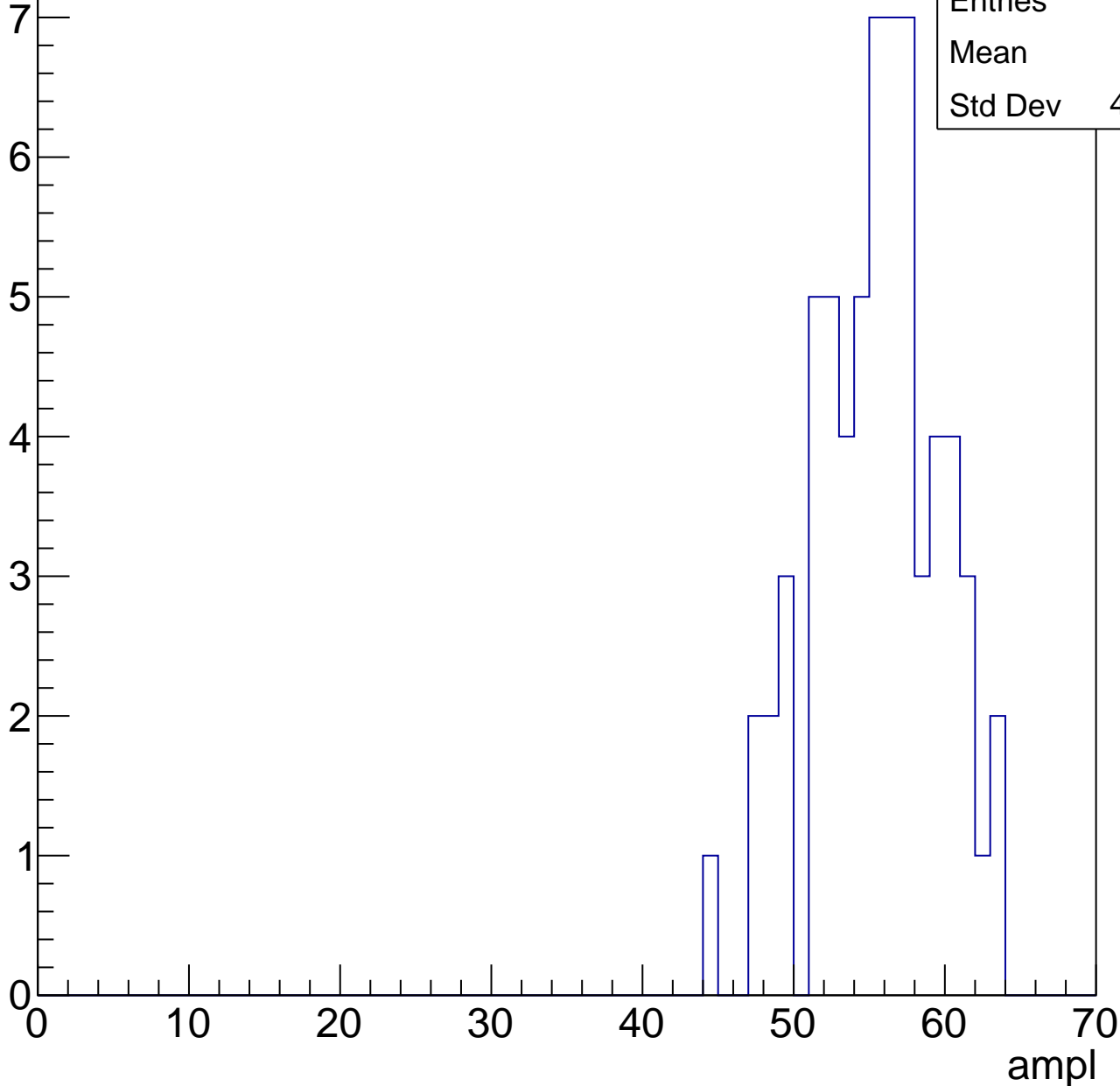


# B1L103S, U22-ch54, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

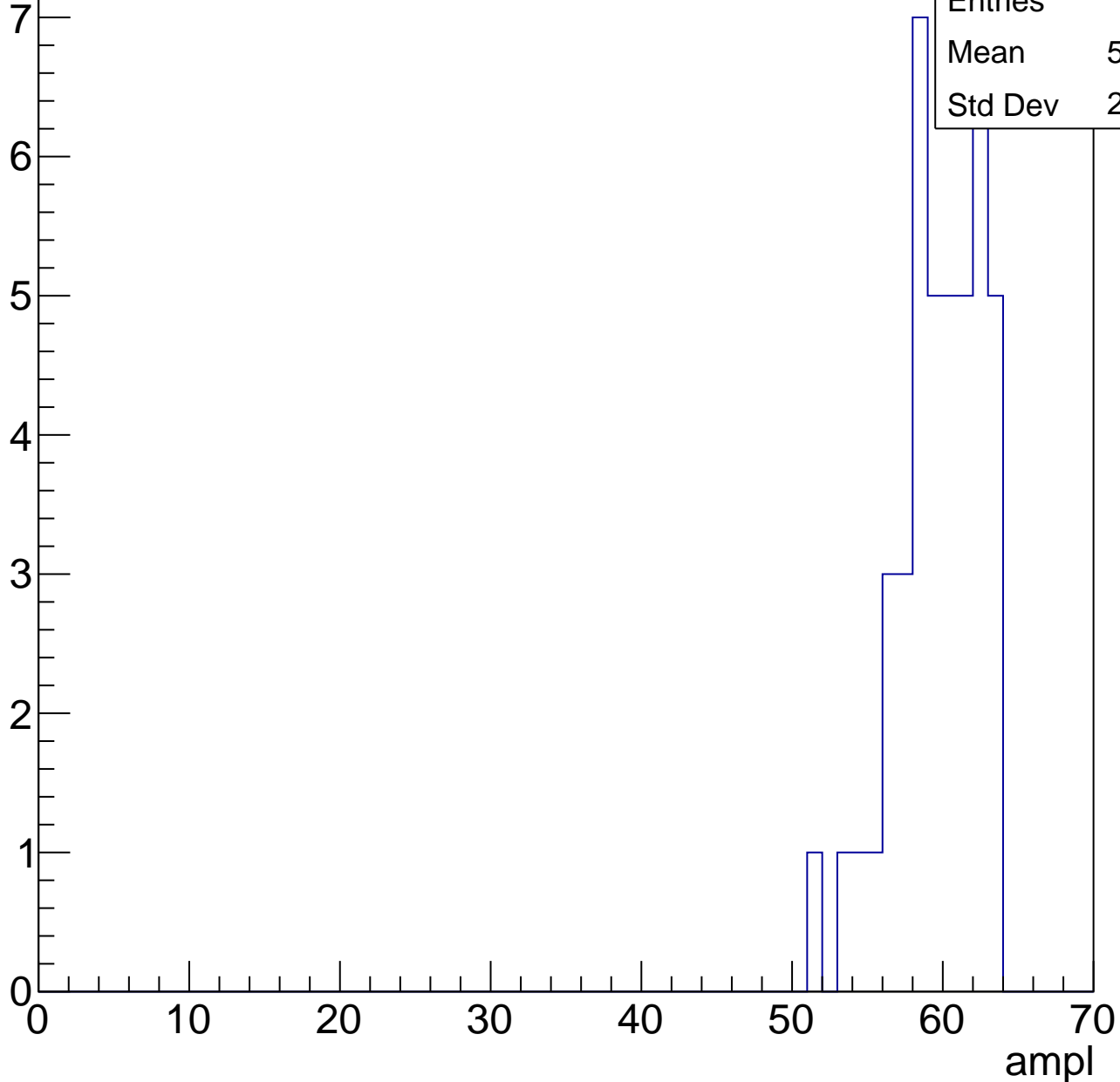
Entries	65
Mean	55
Std Dev	4.136



# B1L103S, U22-ch54, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

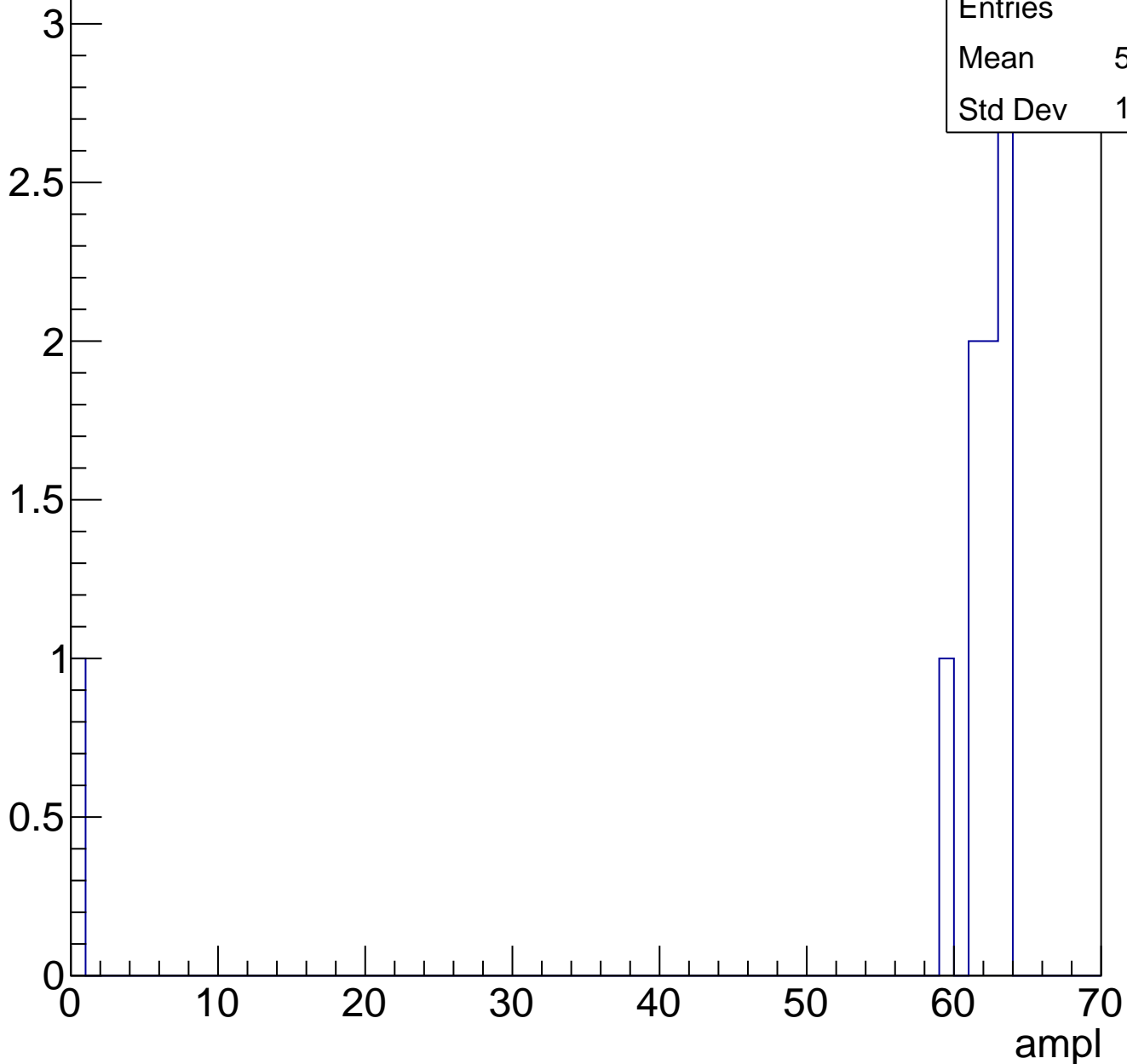
Entry



# B1L103S, U22-ch54, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	9
Mean	54.89
Std Dev	19.44

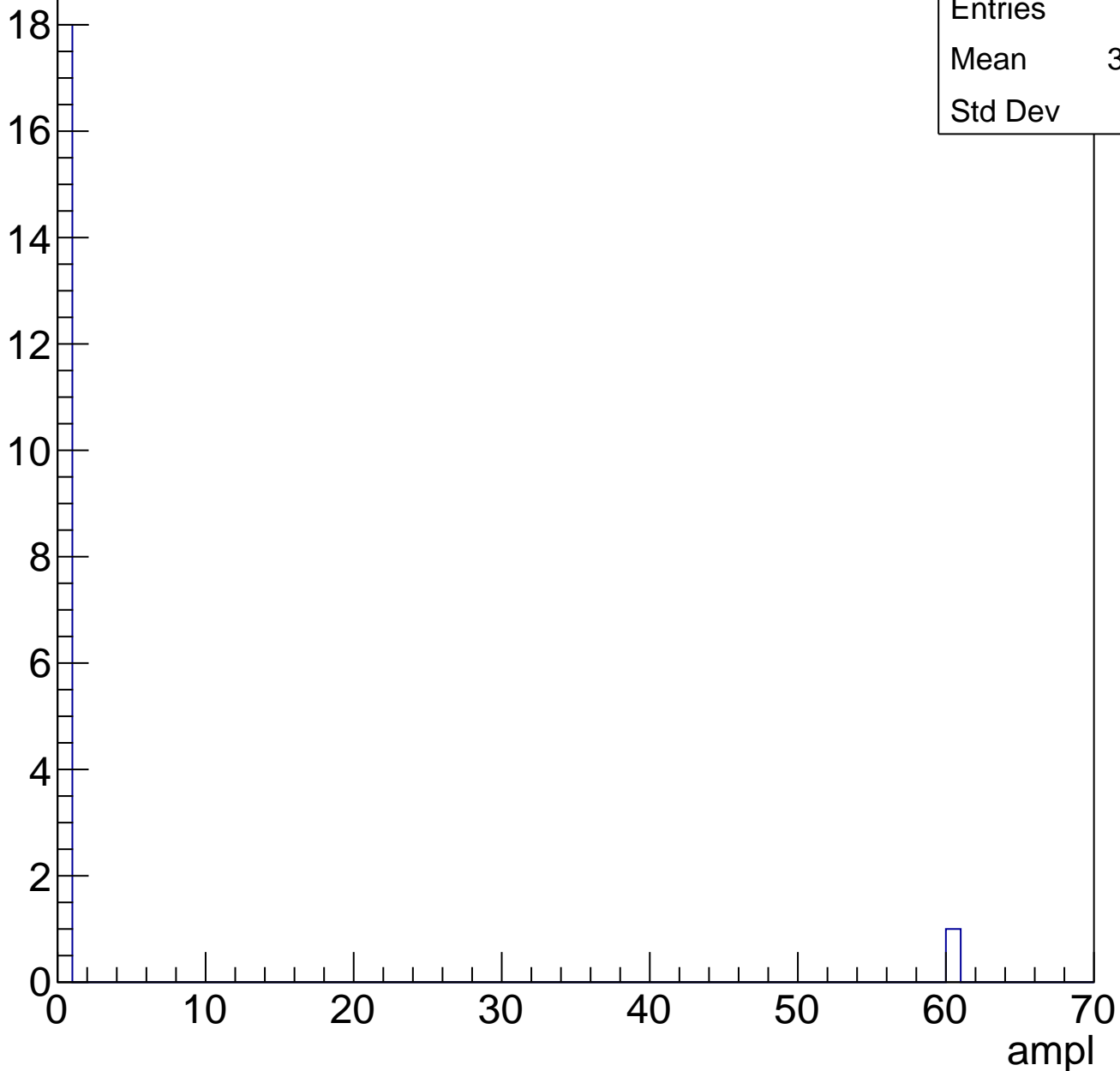


# B1L103S, U22-ch54, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	3.158
Std Dev	13.4

Entry



# B1L103S, U22-ch55, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	92
Mean	24.51
Std Dev	10.94

**Gaus mean : 29.1557**

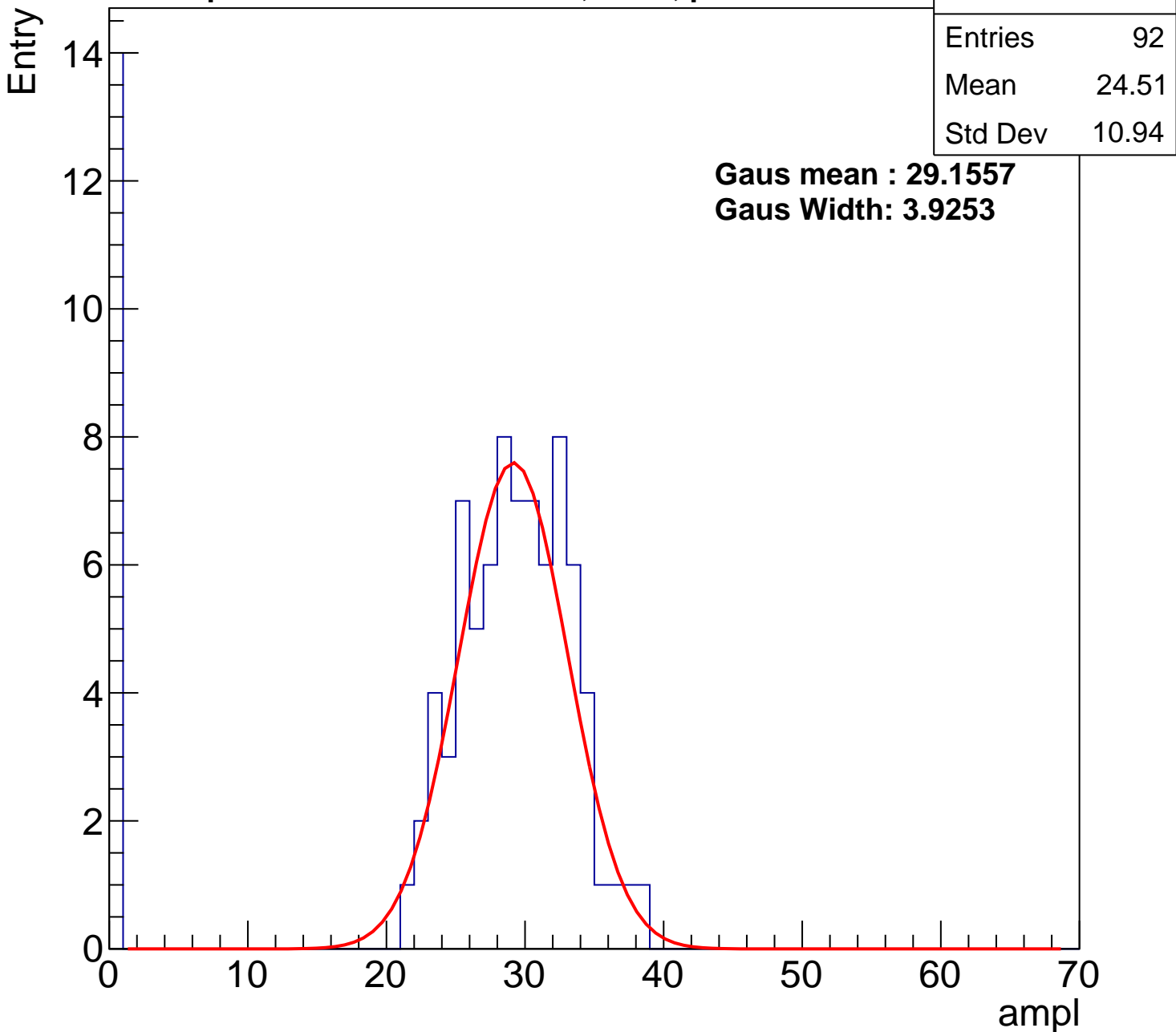
**Gaus Width: 3.9253**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch55, adc1

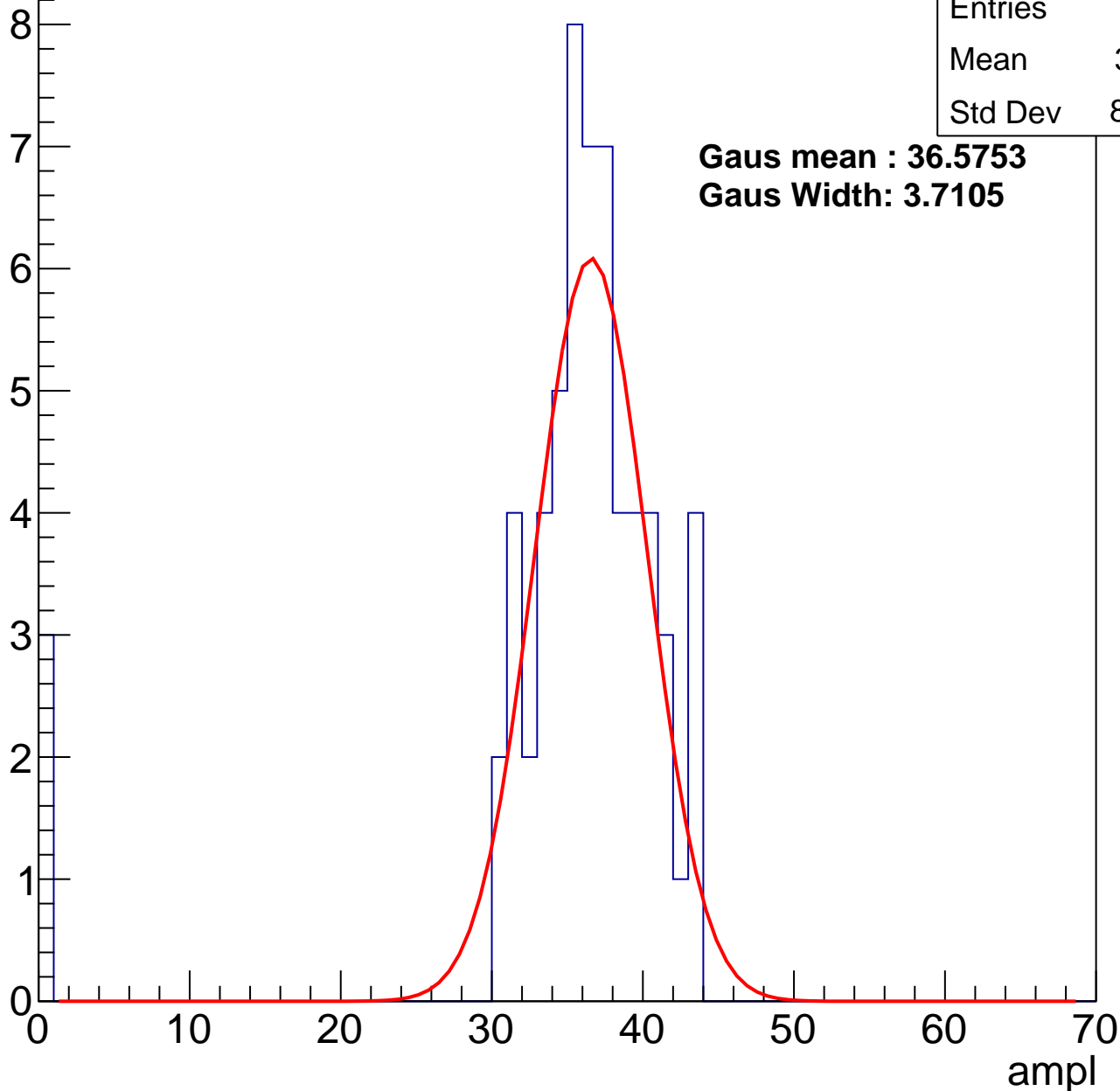
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	34.61
Std Dev	8.488

**Gaus mean : 36.5753**

**Gaus Width: 3.7105**

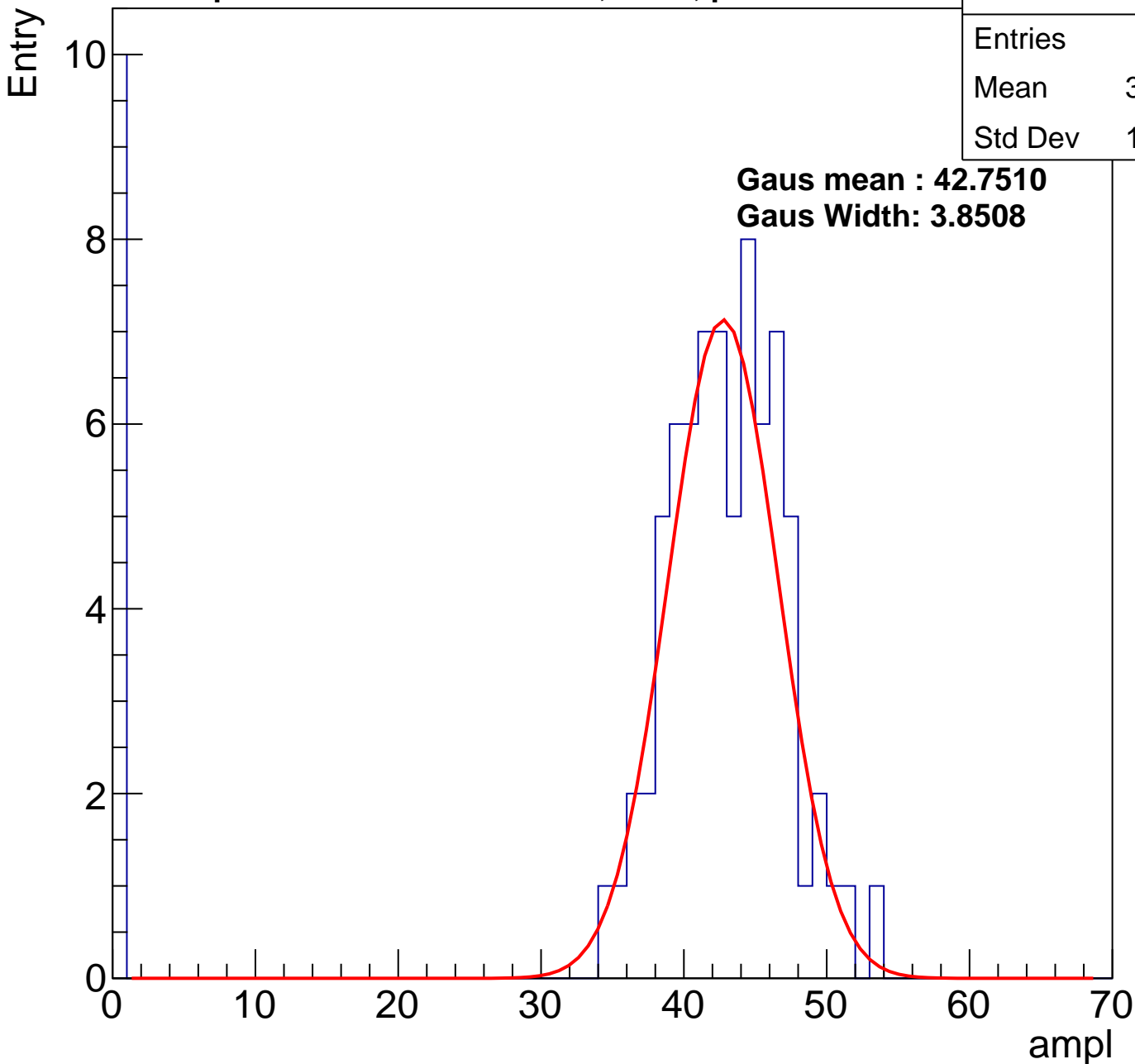


# B1L103S, U22-ch55, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	37.55
Std Dev	14.27

**Gaus mean : 42.7510**  
**Gaus Width: 3.8508**

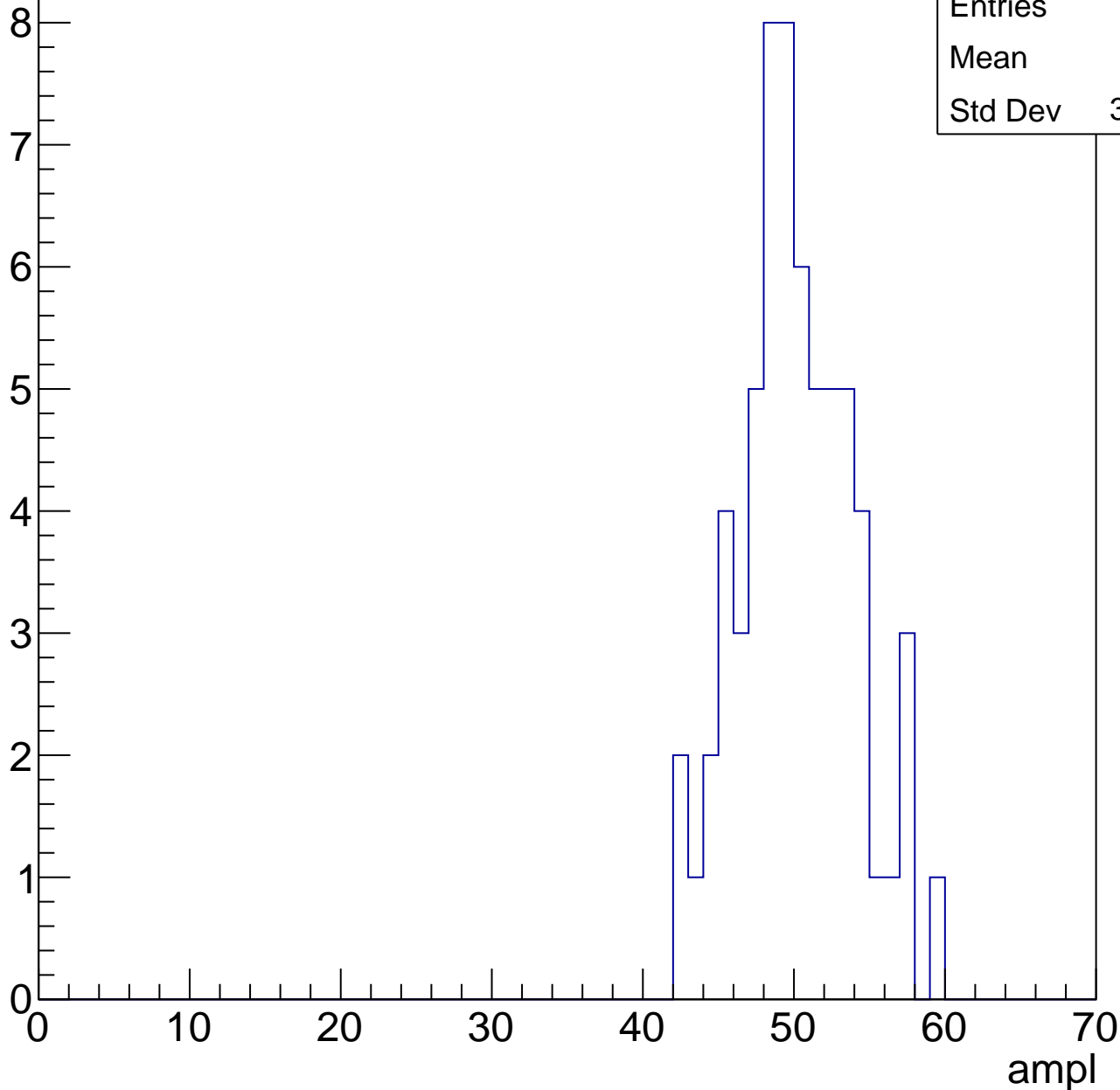


# B1L103S, U22-ch55, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	49.7
Std Dev	3.749

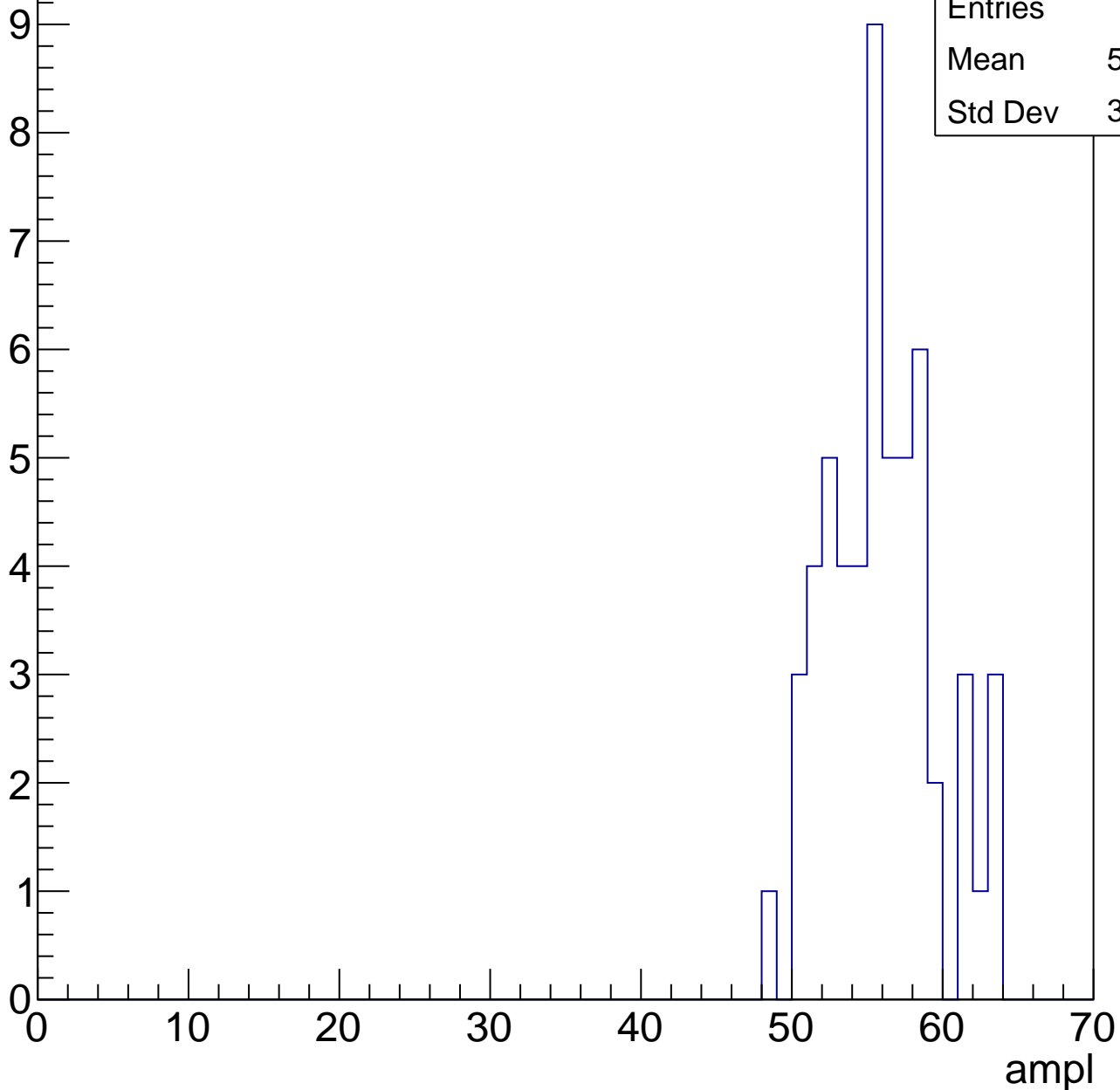


# B1L103S, U22-ch55, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	55.45
Std Dev	3.572

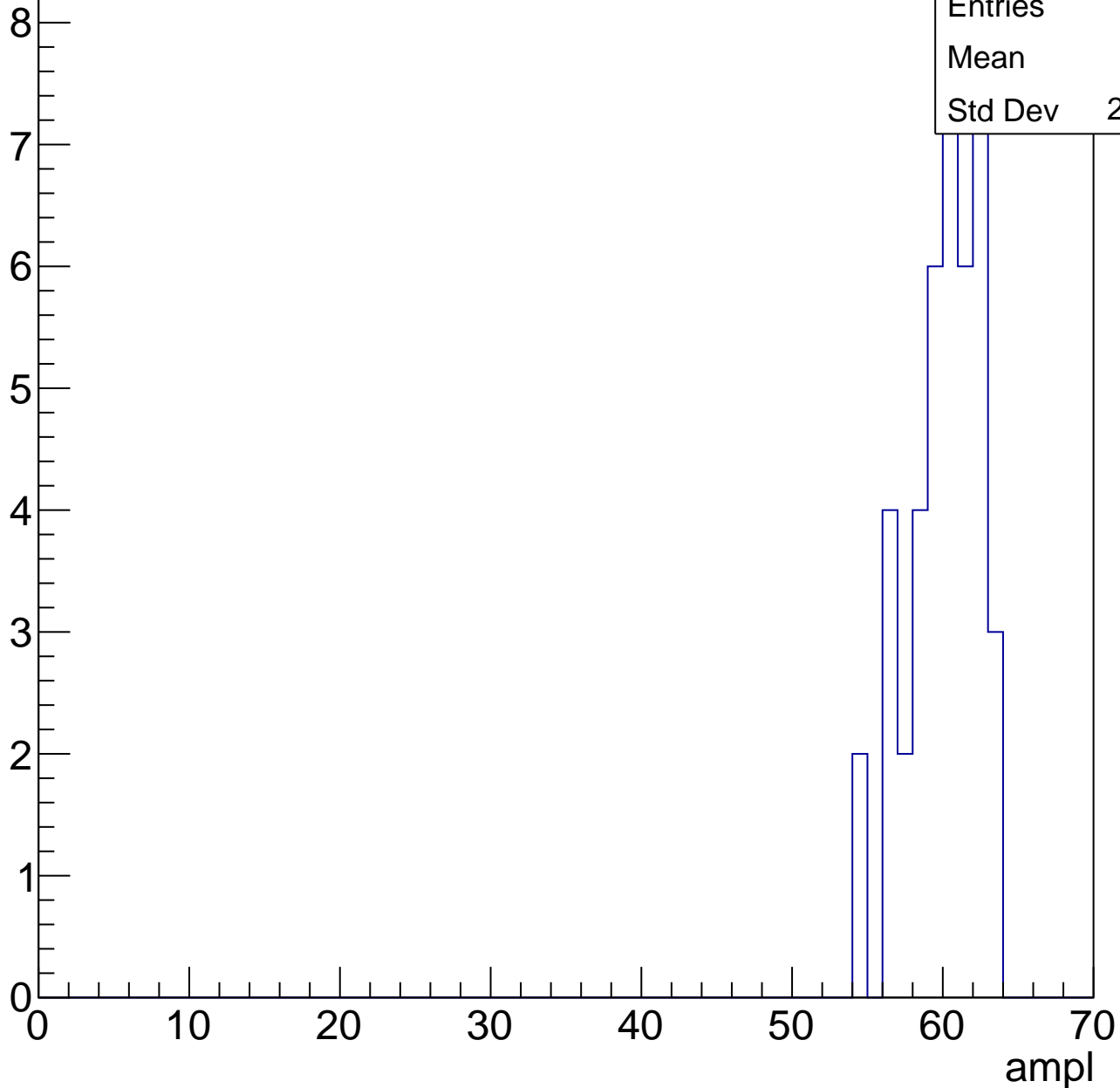


# B1L103S, U22-ch55, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

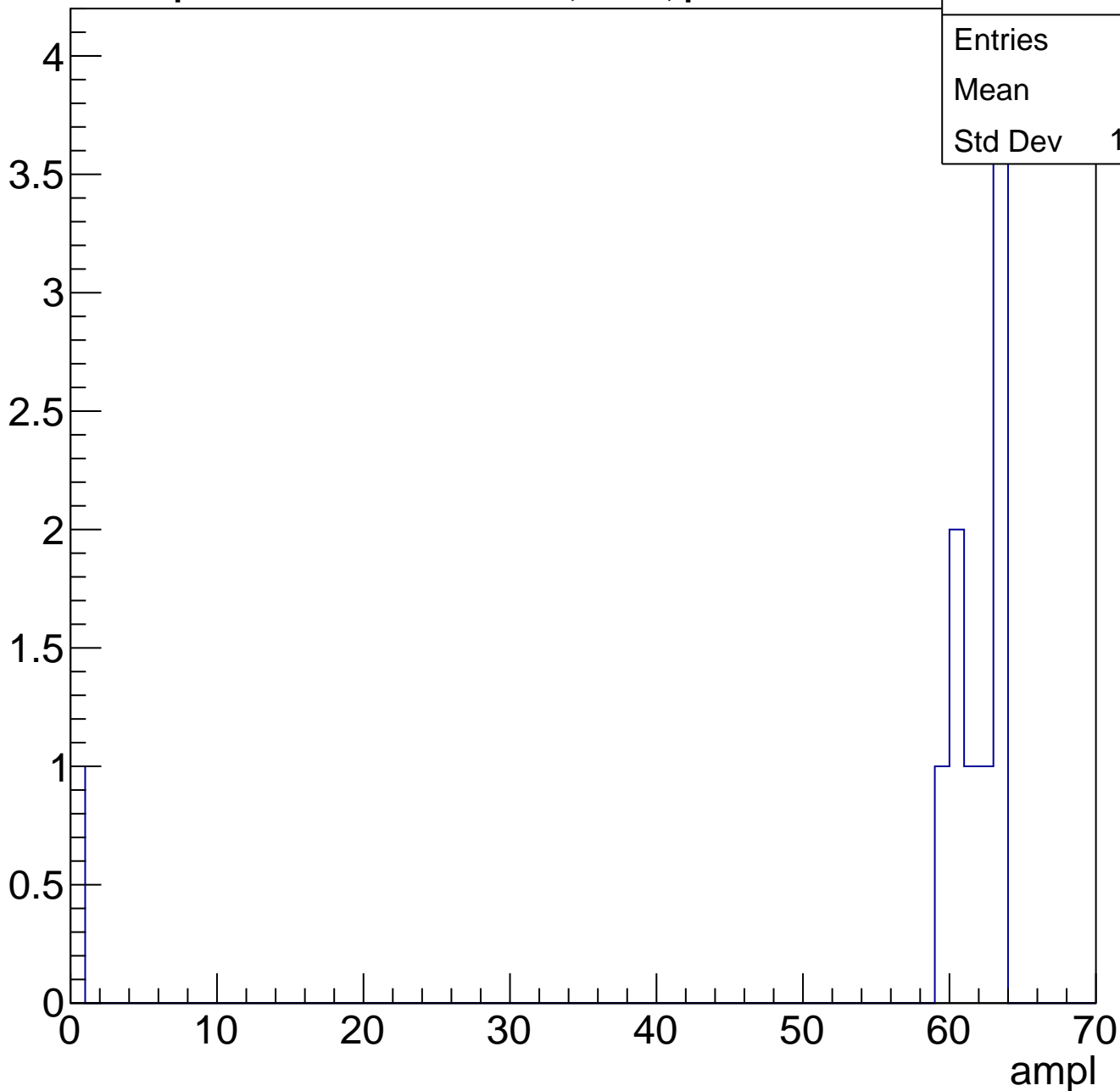
Entries	43
Mean	59.6
Std Dev	2.334



# B1L103S, U22-ch55, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch55, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch56, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	97
Mean	24.99
Std Dev	9.692

**Gaus mean : 28.5541**

**Gaus Width: 4.2791**

Entry

10

8

6

4

2

0

0

10

20

30

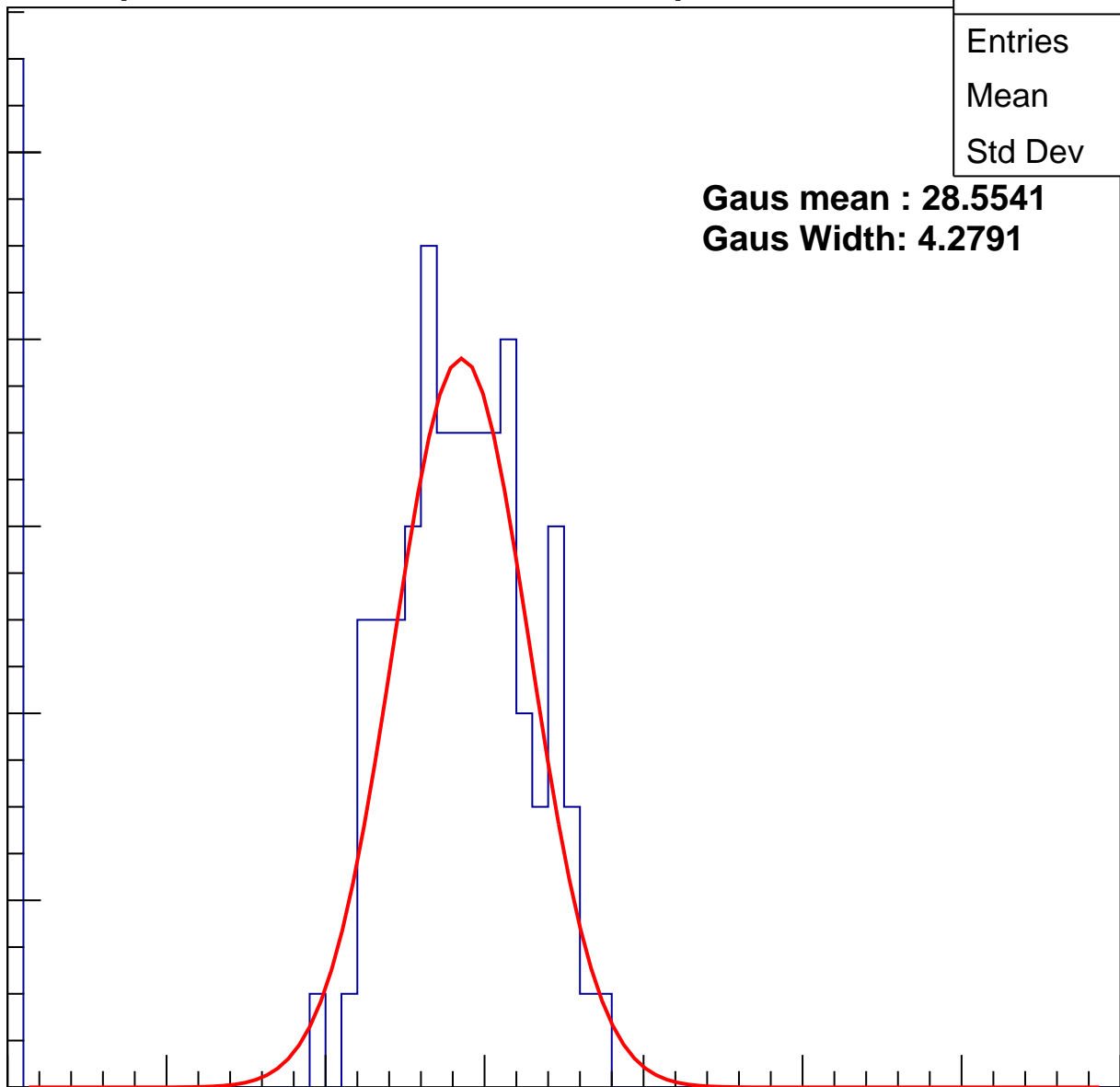
40

50

60

70

ampl



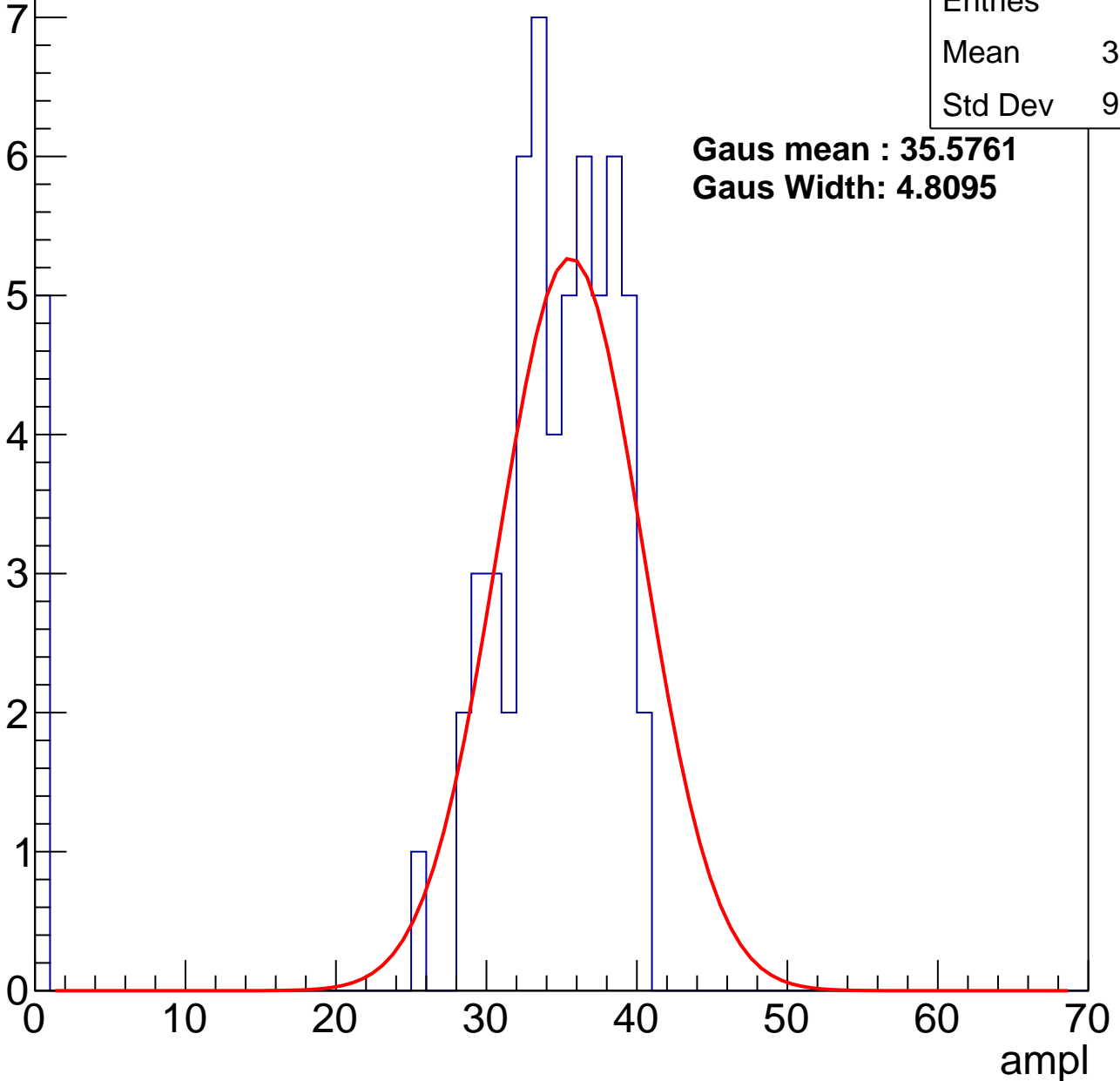
# B1L103S, U22-ch56, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	31.58
Std Dev	9.926

**Gaus mean : 35.5761**  
**Gaus Width: 4.8095**



# B1L103S, U22-ch56, adc2

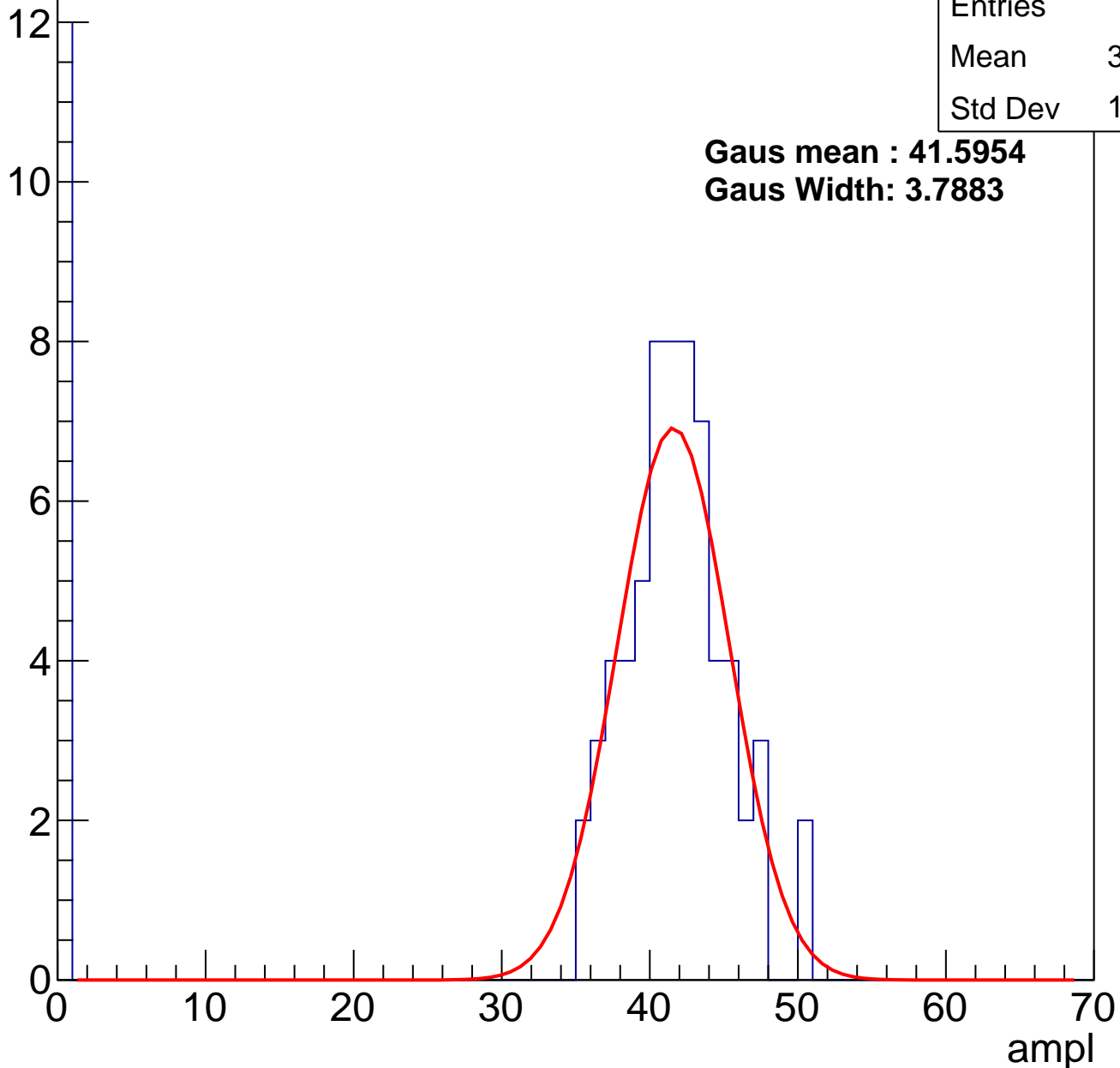
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	34.83
Std Dev	15.39

**Gaus mean : 41.5954**

**Gaus Width: 3.7883**

Entry

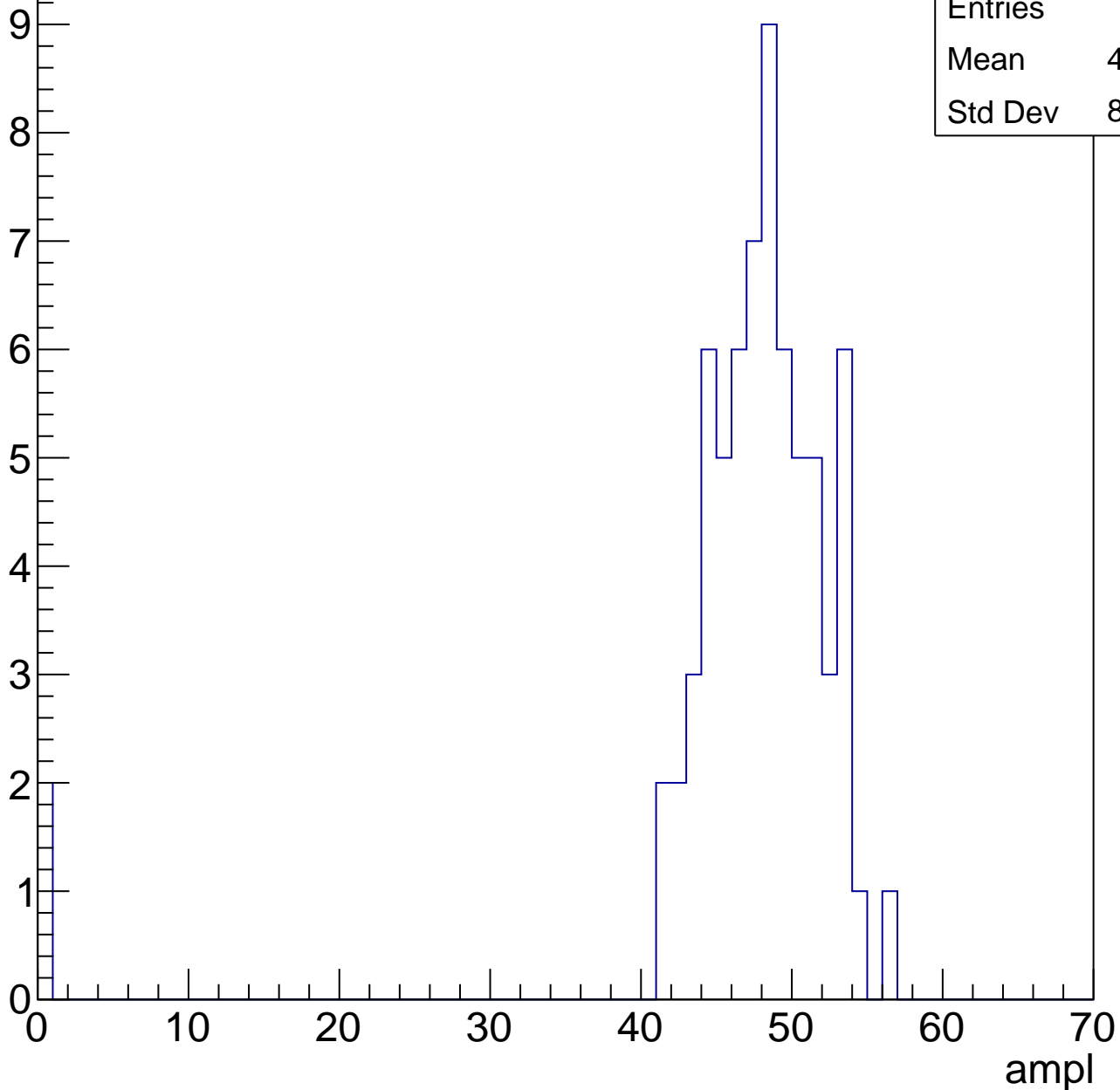


# B1L103S, U22-ch56, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	46.43
Std Dev	8.702

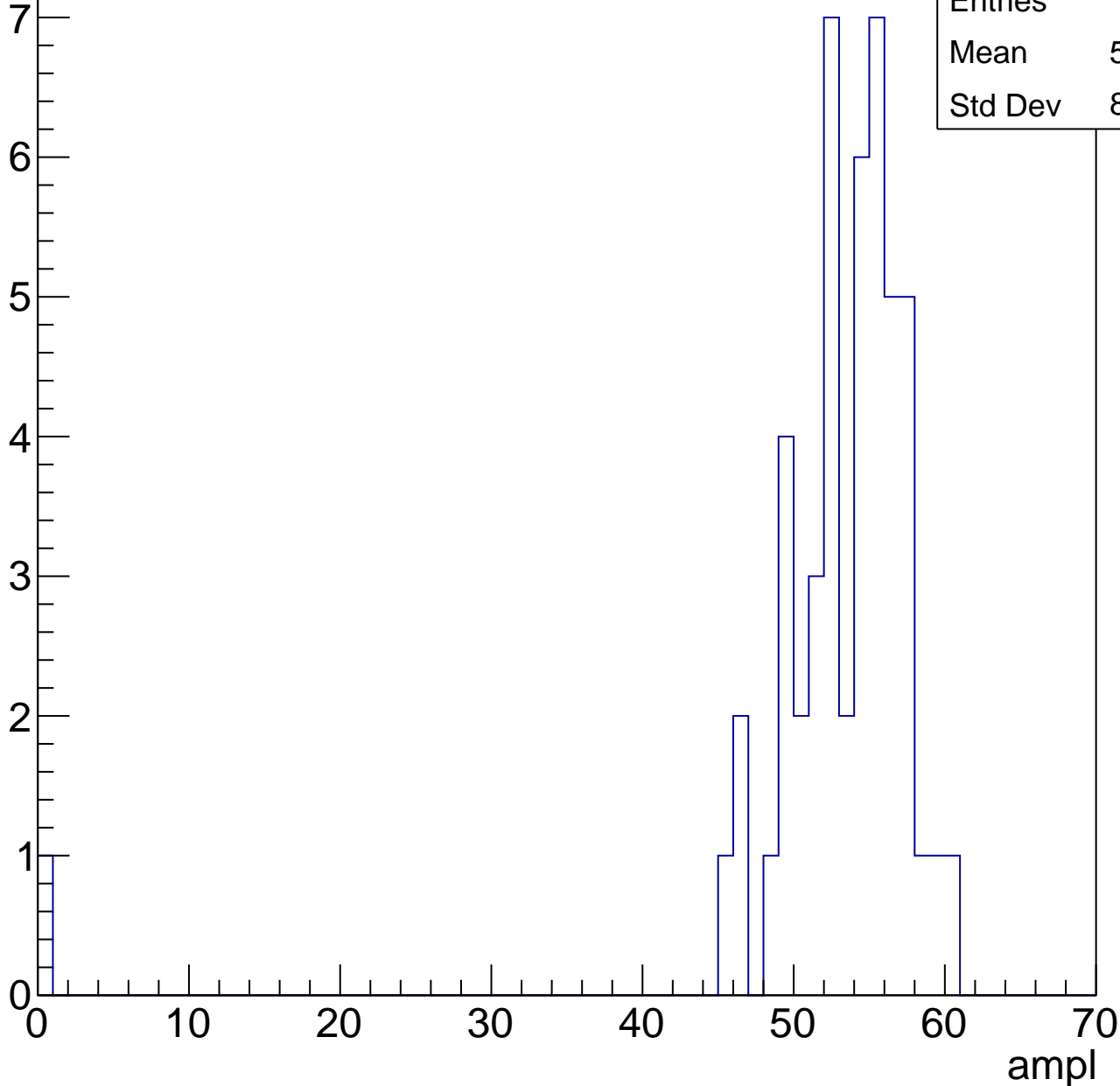


# B1L103S, U22-ch56, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	52.14
Std Dev	8.244

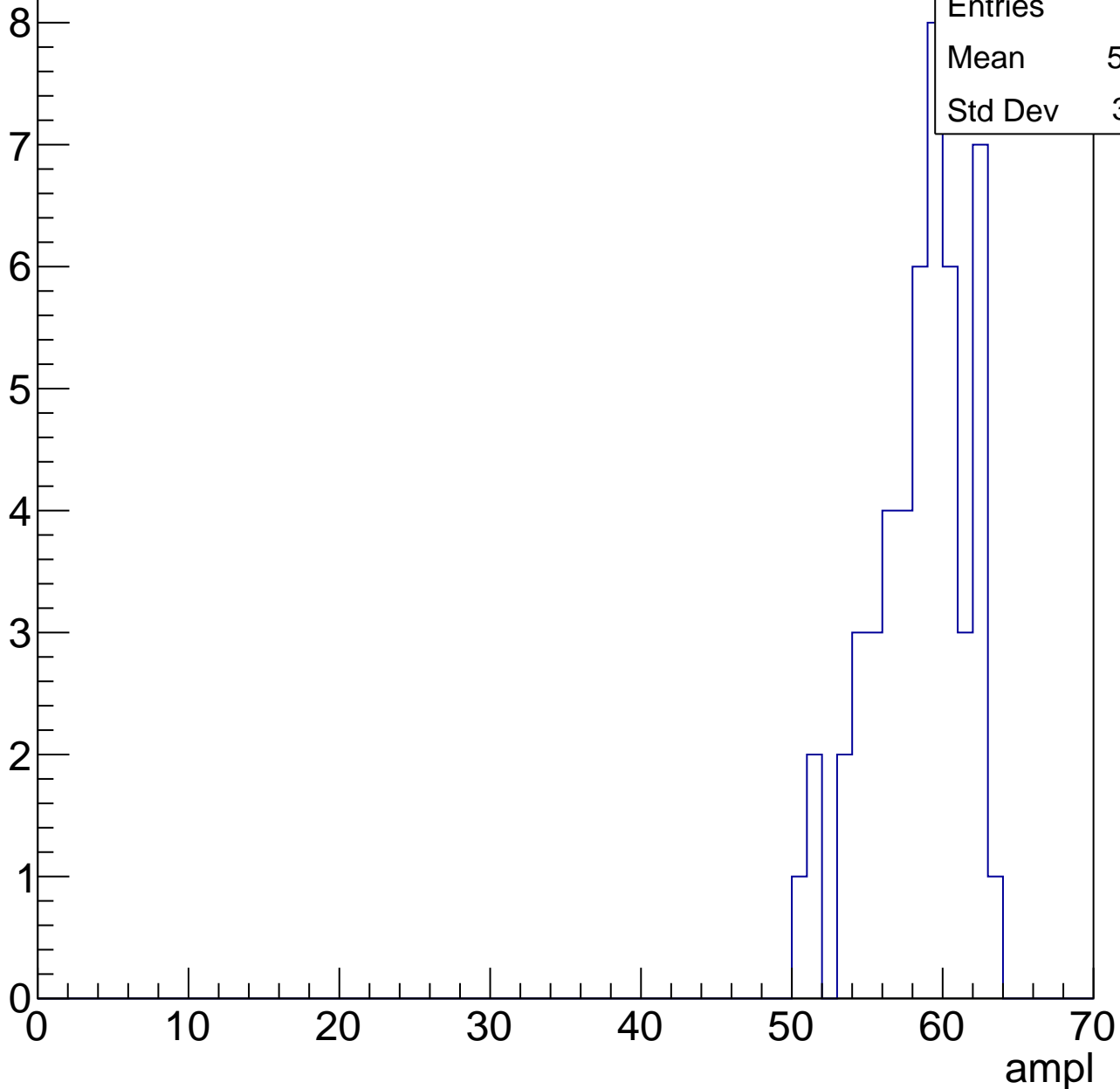


# B1L103S, U22-ch56, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	57.94
Std Dev	3.171

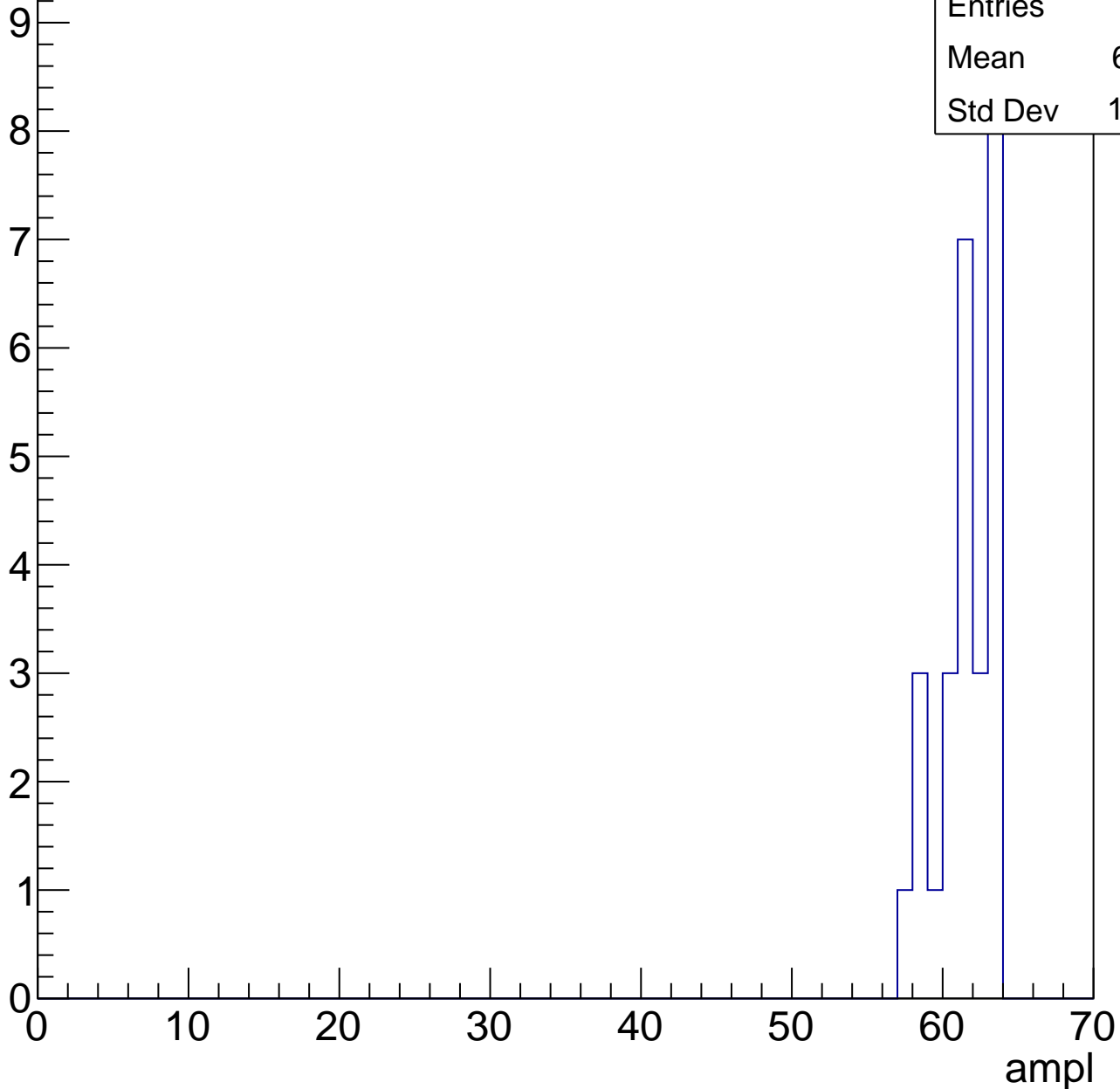


# B1L103S, U22-ch56, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	27
Mean	61.11
Std Dev	1.812





# B1L103S, U22-ch56, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch57, adc0

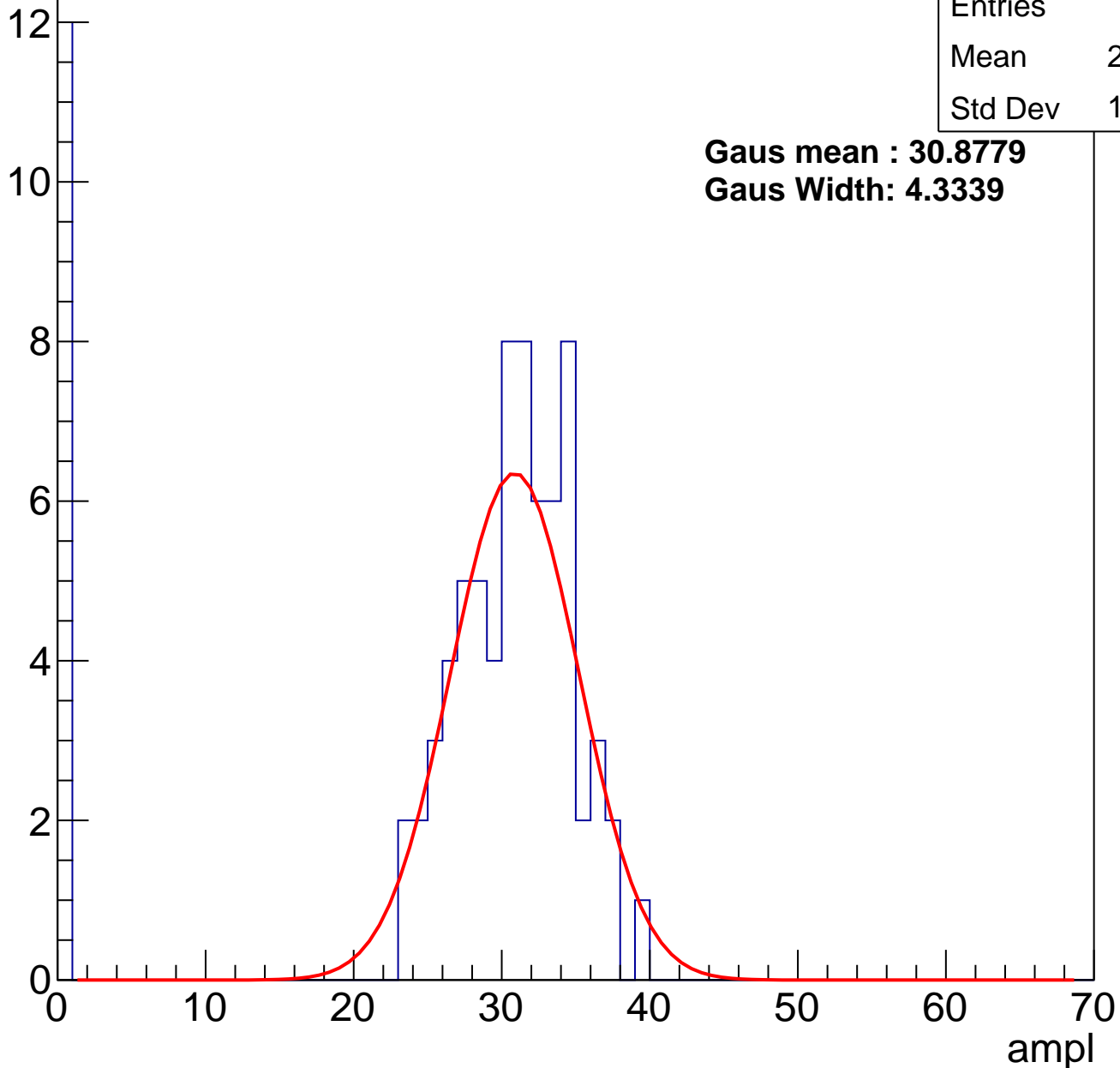
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	25.99
Std Dev	11.34

**Gaus mean : 30.8779**

**Gaus Width: 4.3339**

Entry



# B1L103S, U22-ch57, adc1

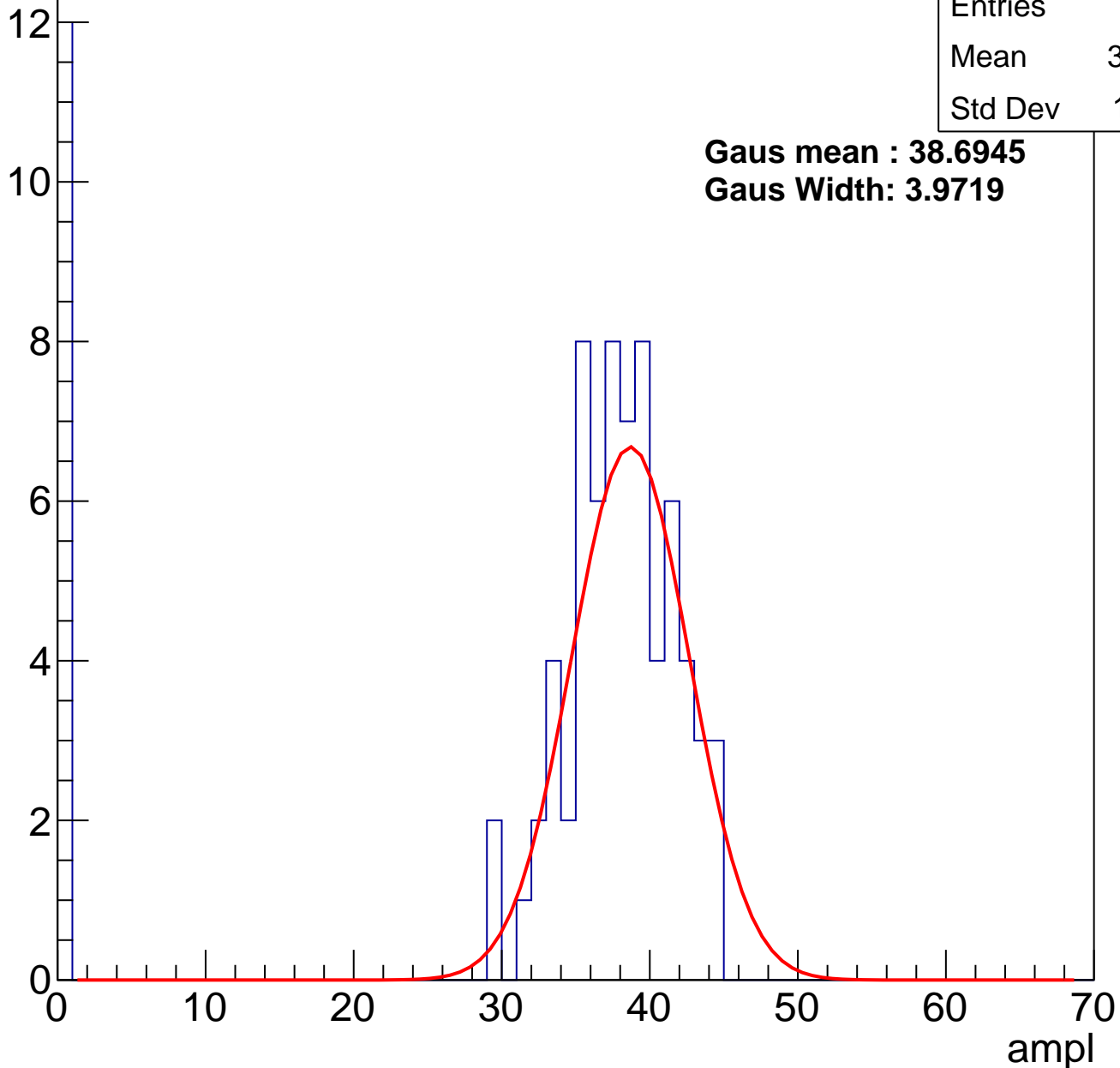
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	31.98
Std Dev	13.81

**Gaus mean : 38.6945**

**Gaus Width: 3.9719**

Entry



# B1L103S, U22-ch57, adc2

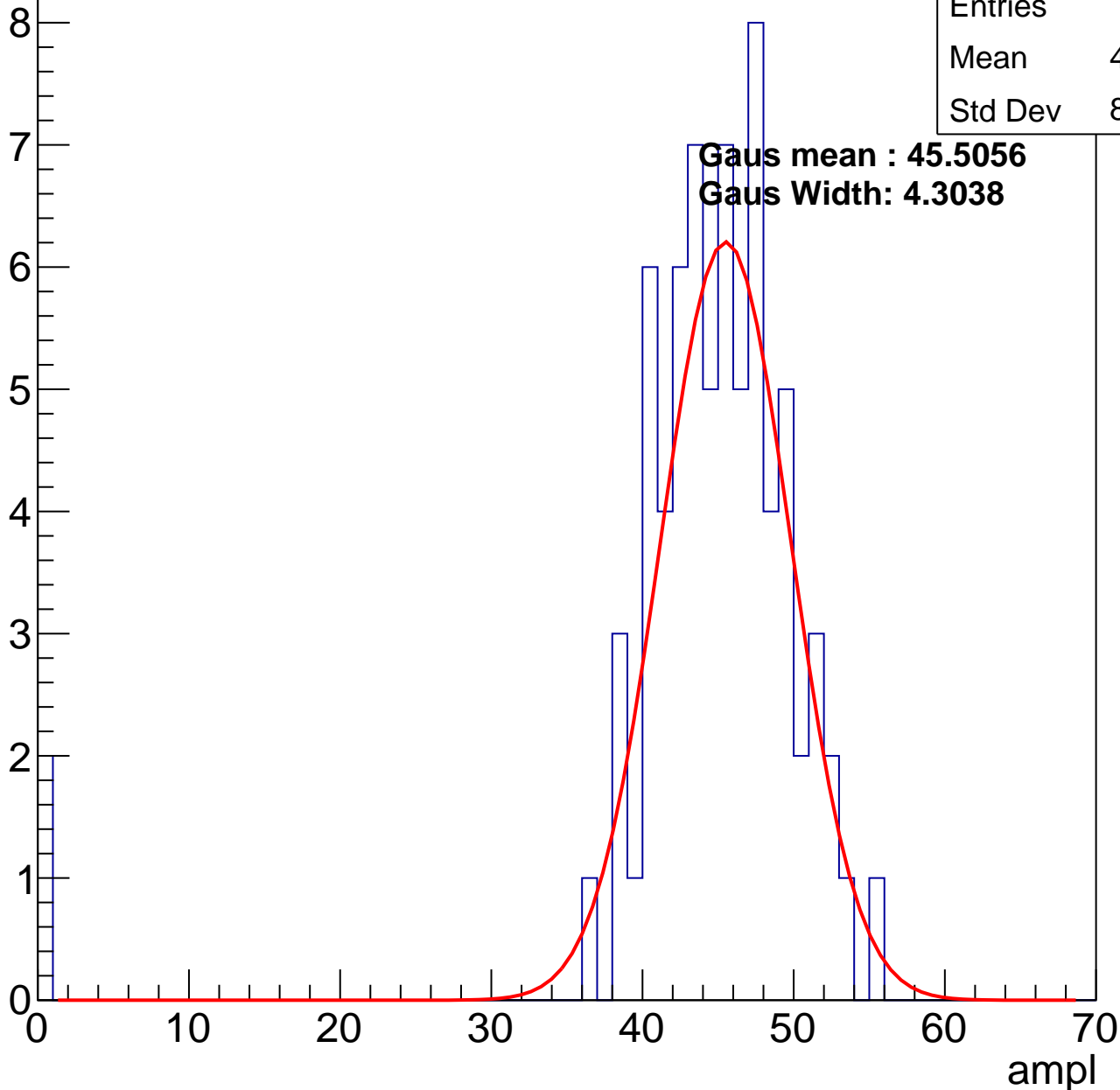
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	43.68
Std Dev	8.327

**Gaus mean : 45.5056**

**Gaus Width: 4.3038**

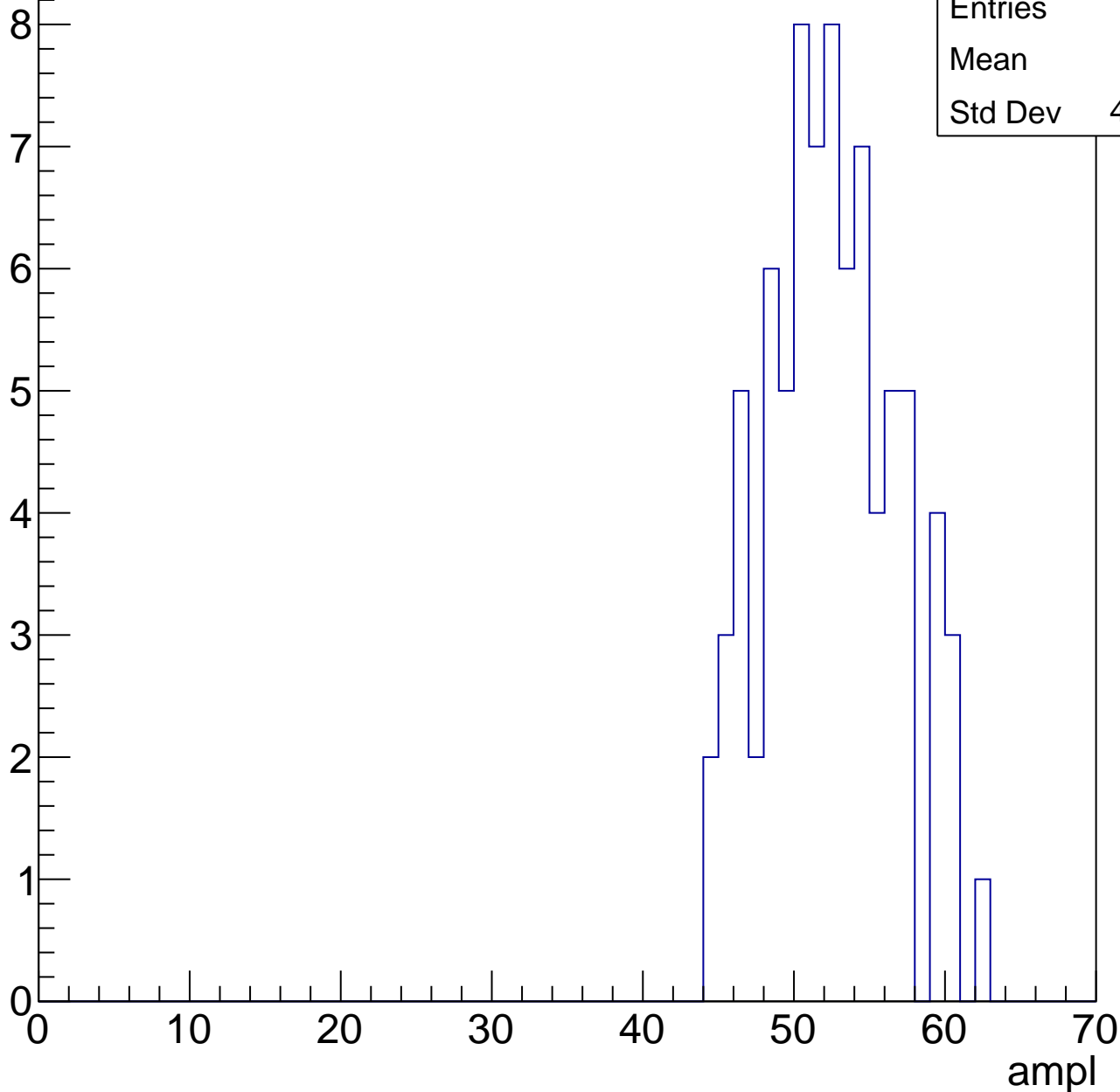


# B1L103S, U22-ch57, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	52
Std Dev	4.228

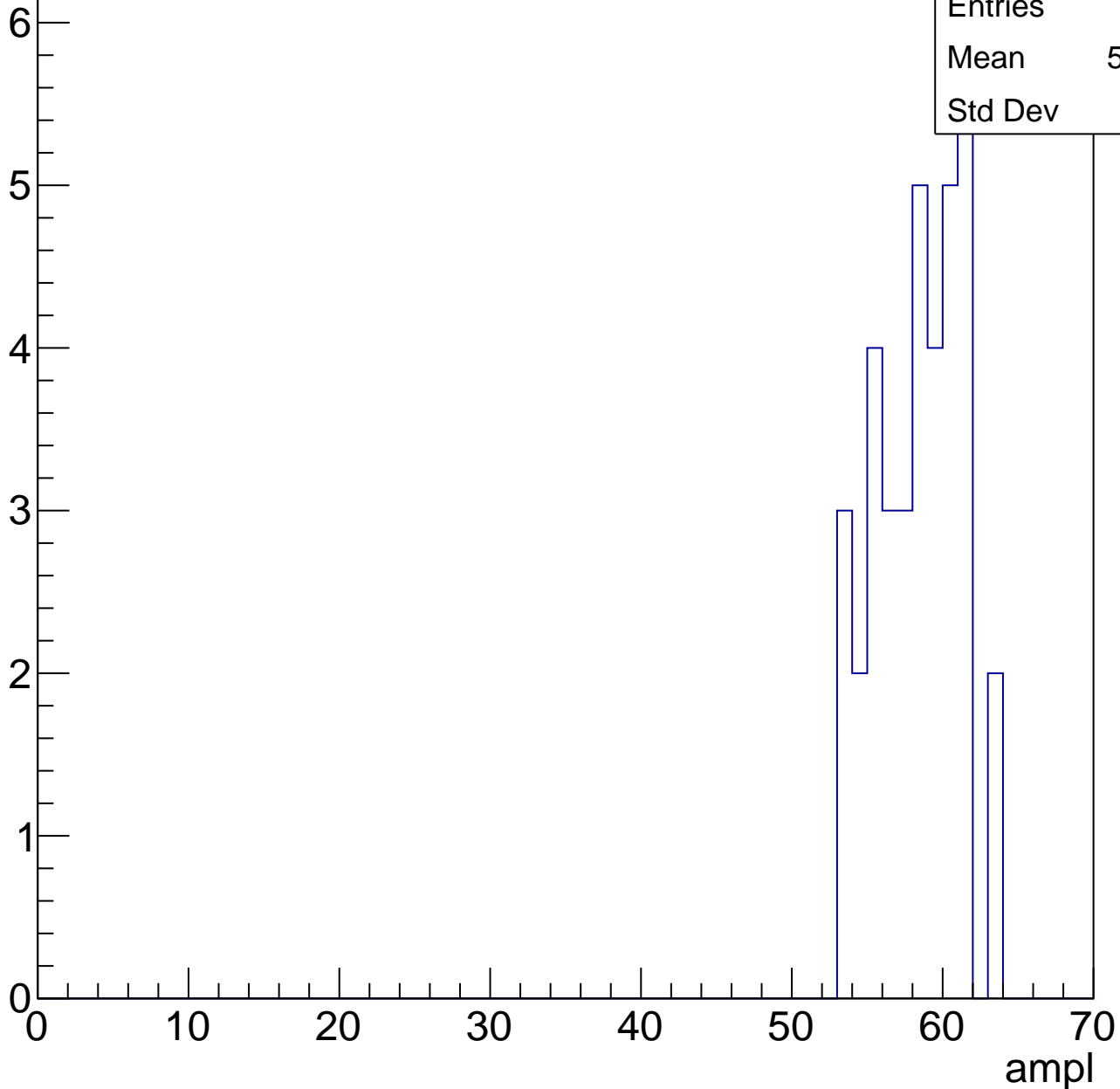


# B1L103S, U22-ch57, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	37
Mean	57.95
Std Dev	2.78

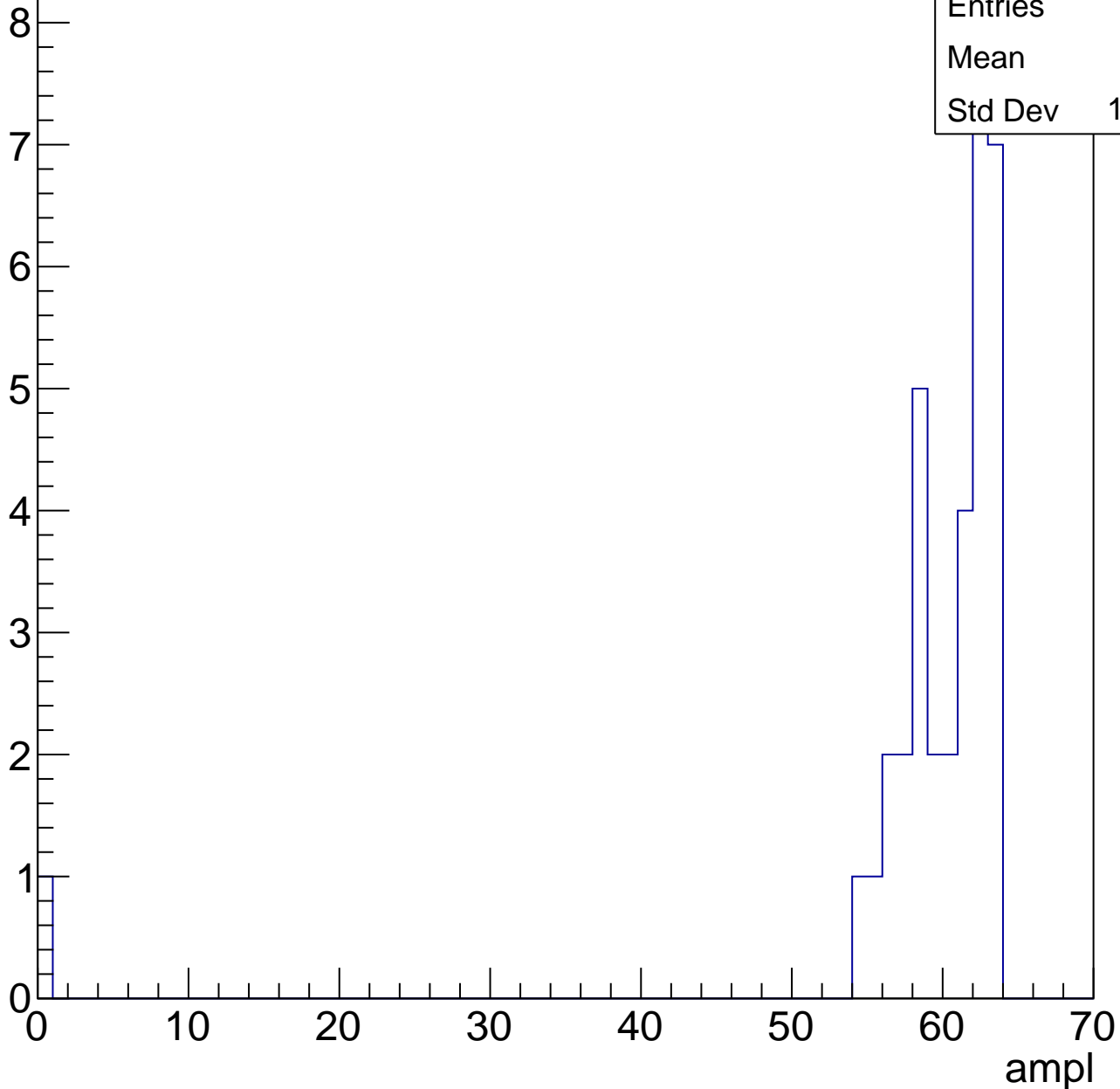


# B1L103S, U22-ch57, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	35
Mean	58.4
Std Dev	10.34



# B1L103S, U22-ch57, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch57, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



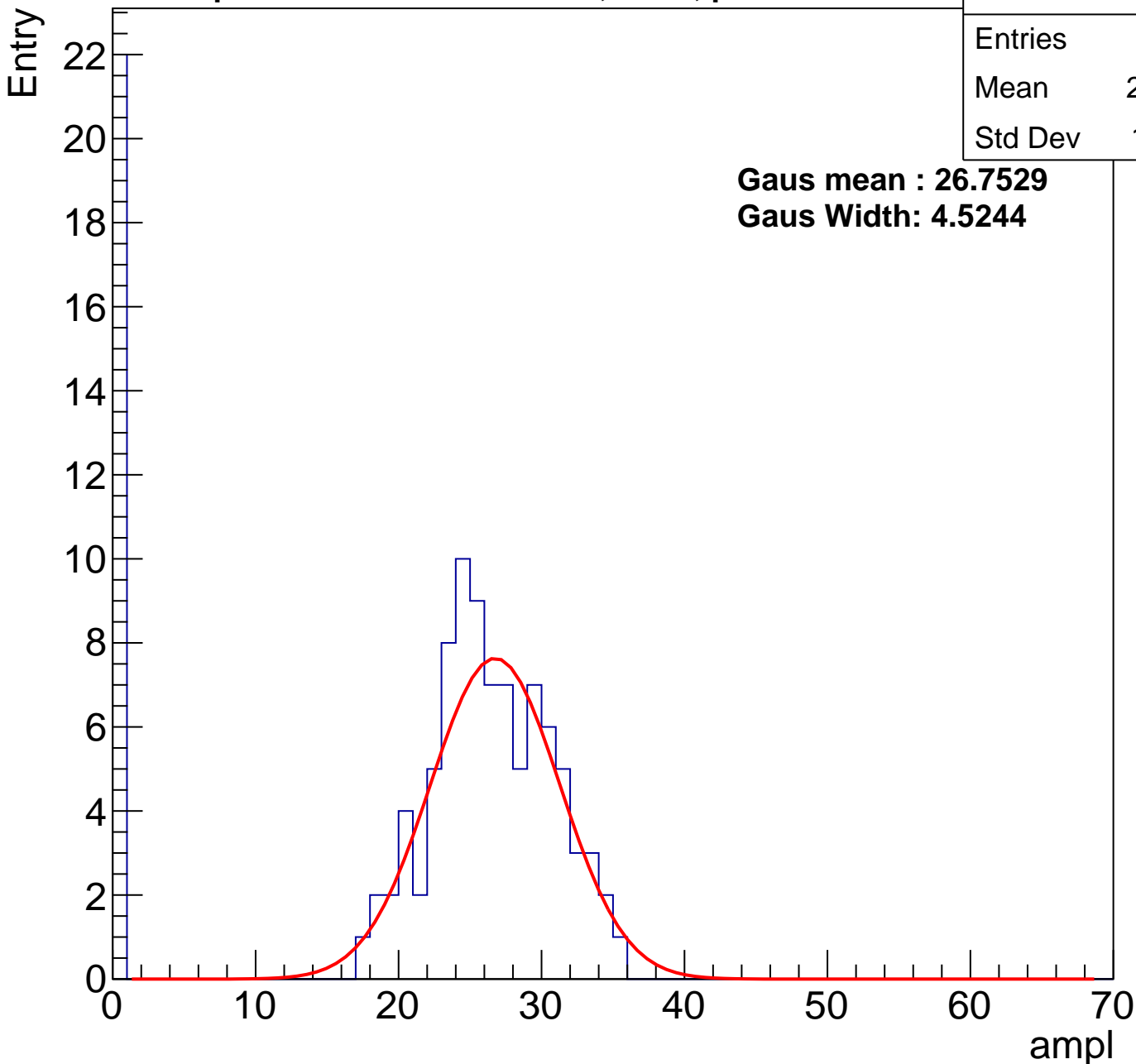
# B1L103S, U22-ch58, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	111
Mean	20.89
Std Dev	11.01

**Gaus mean : 26.7529**

**Gaus Width: 4.5244**



# B1L103S, U22-ch58, adc1

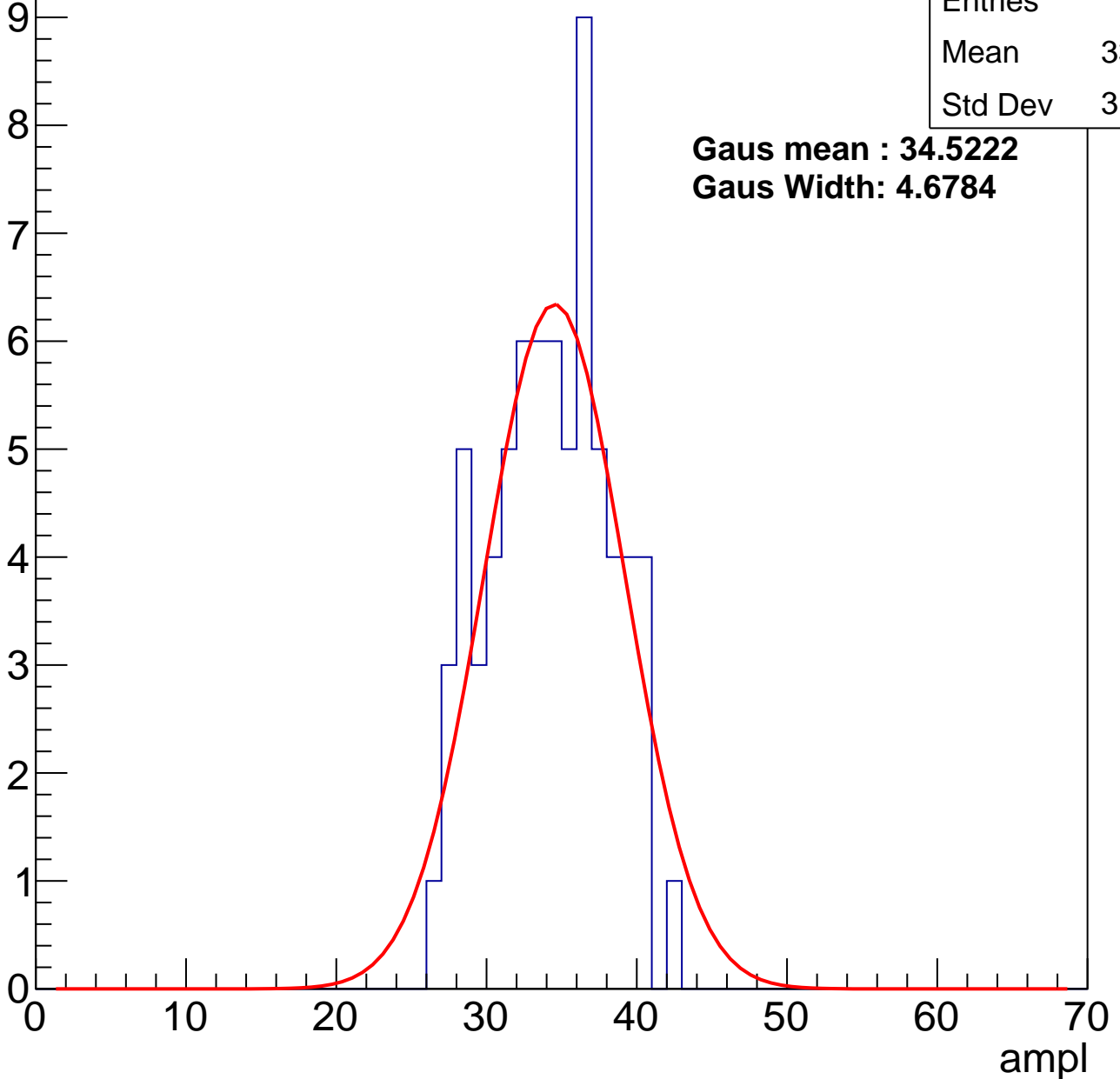
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	33.76
Std Dev	3.862

**Gaus mean : 34.5222**

**Gaus Width: 4.6784**



# B1L103S, U22-ch58, adc2

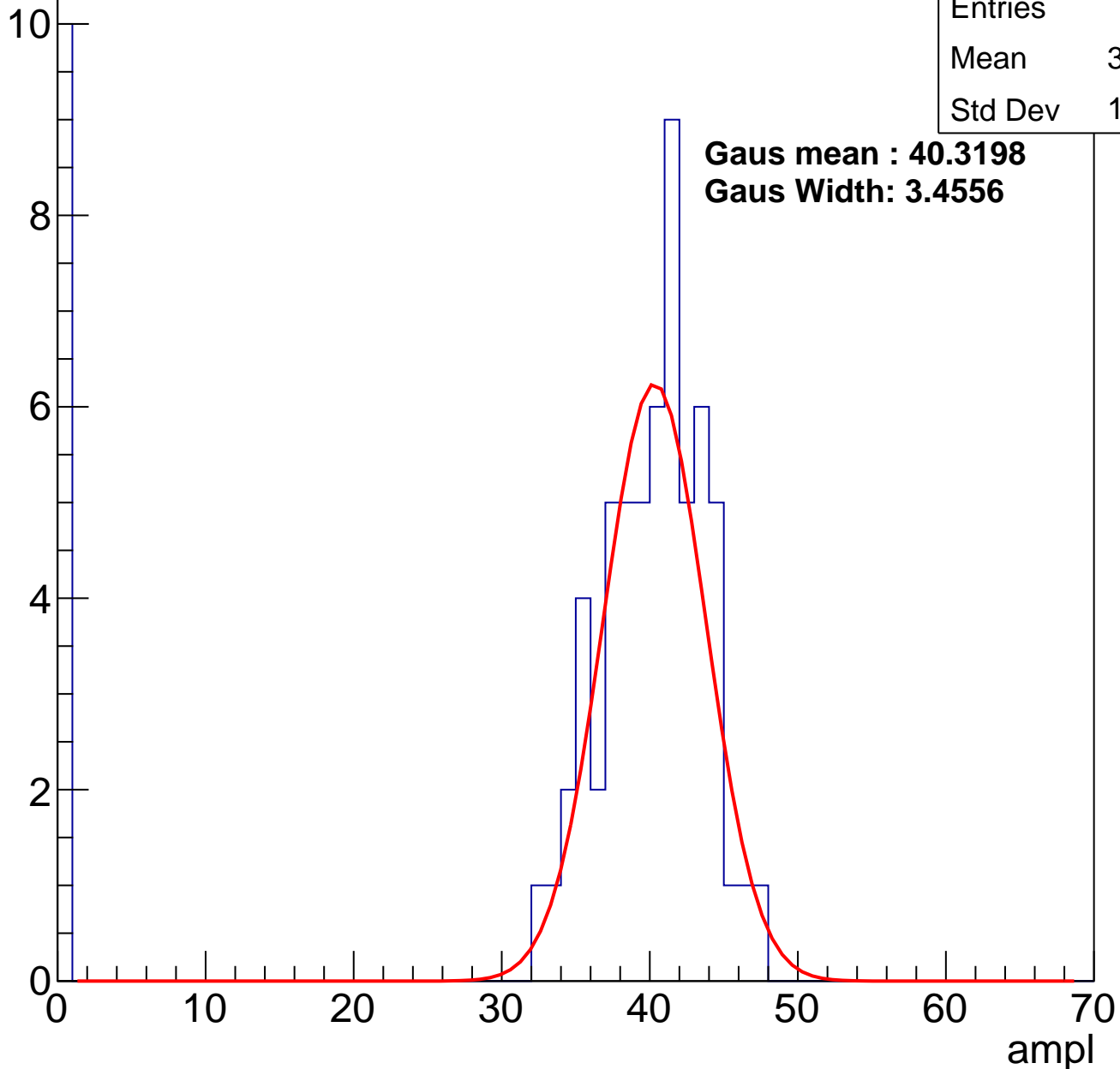
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	69
Mean	34.06
Std Dev	14.36

**Gaus mean : 40.3198**

**Gaus Width: 3.4556**

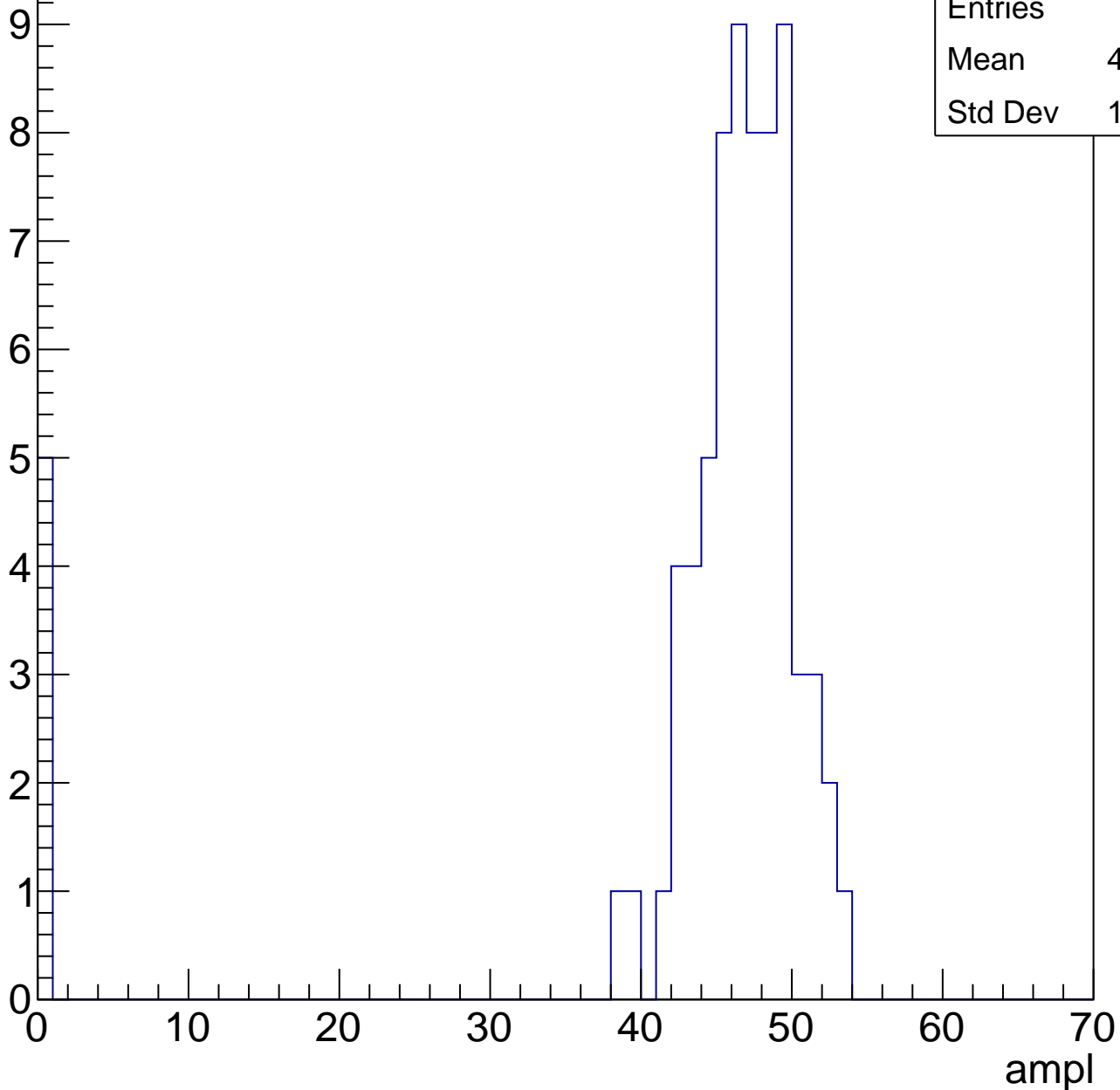
Entry



# B1L103S, U22-ch58, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

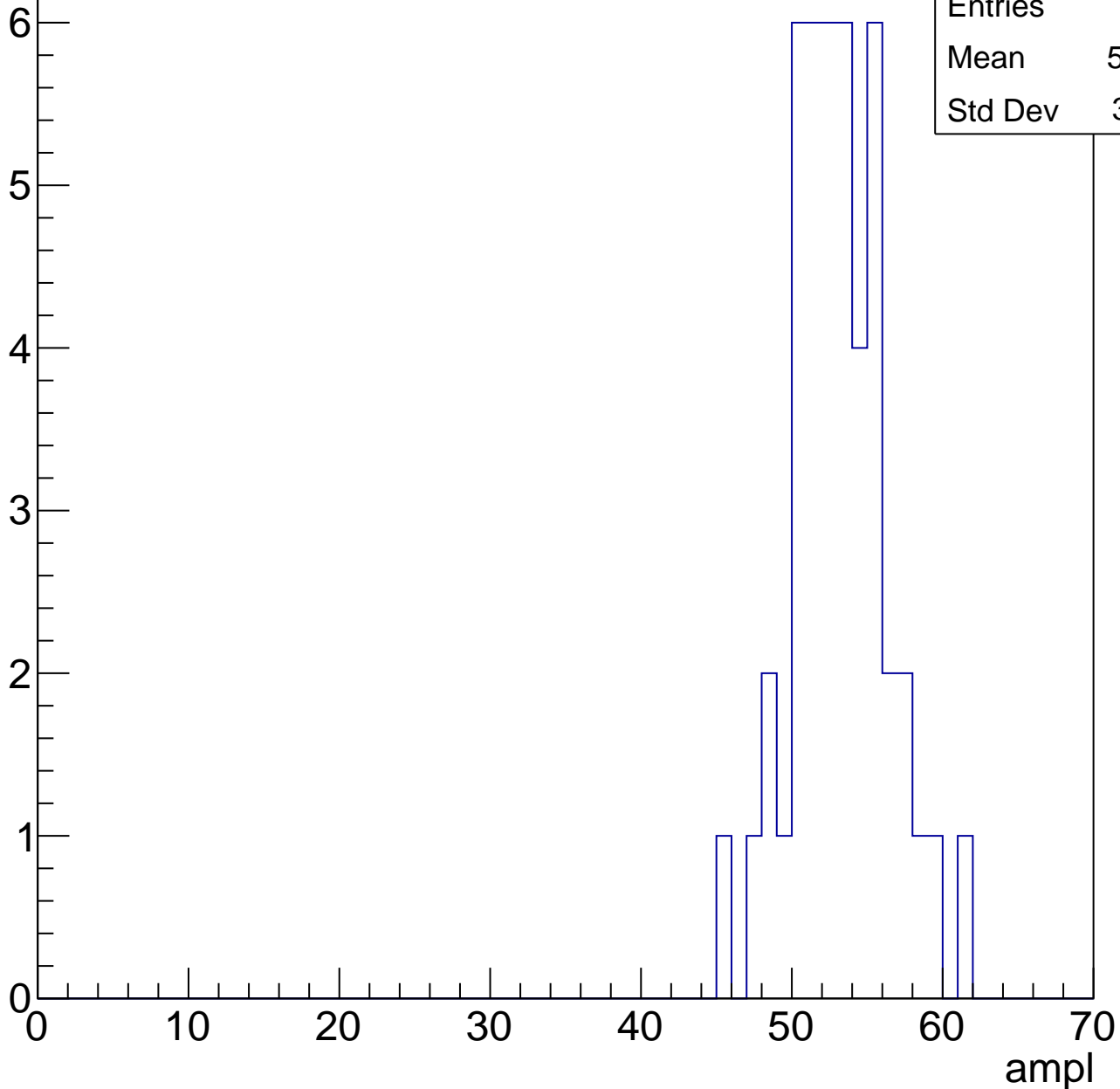


# B1L103S, U22-ch58, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

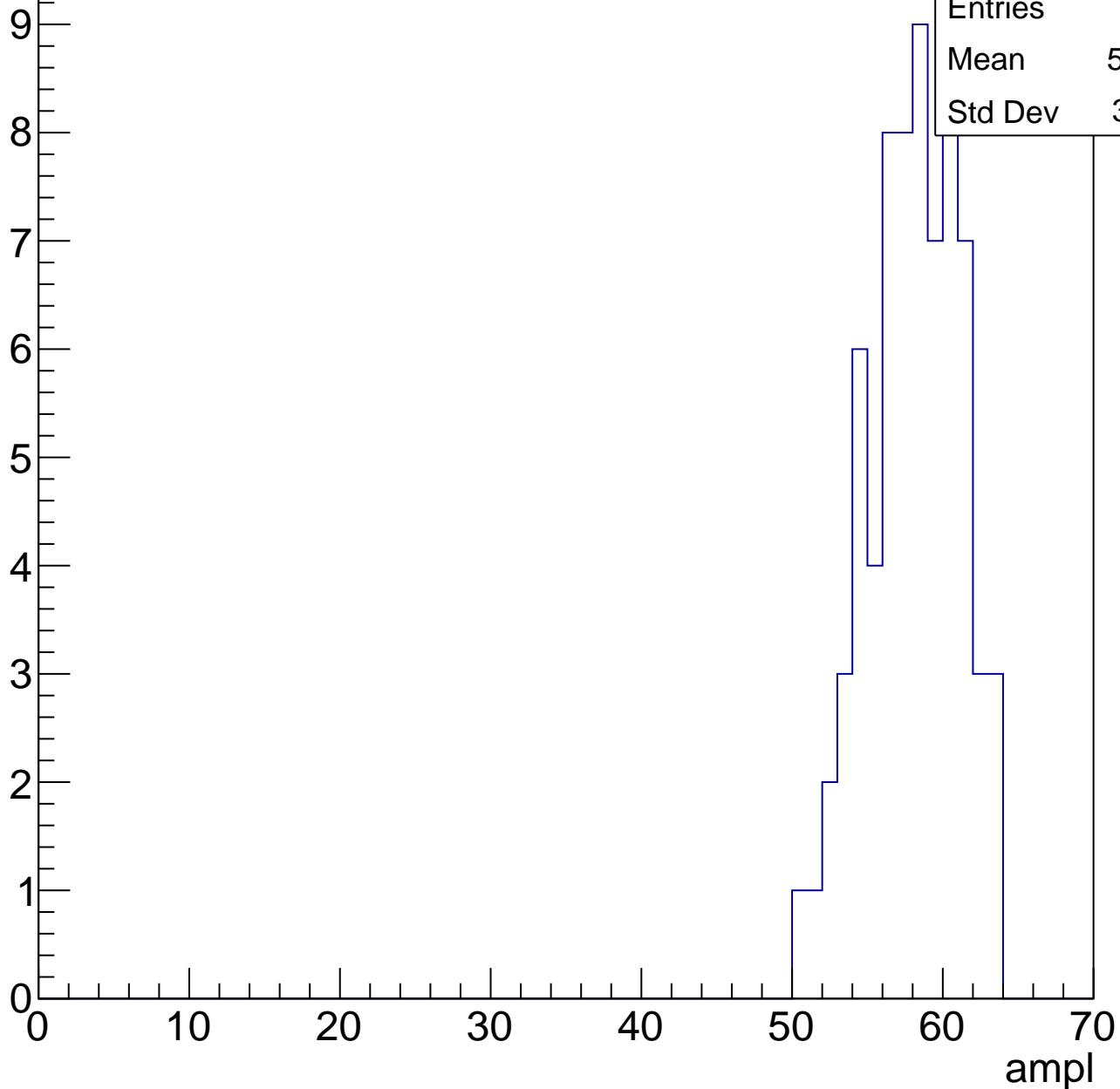
Entries	46
Mean	52.67
Std Dev	3.121



# B1L103S, U22-ch58, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

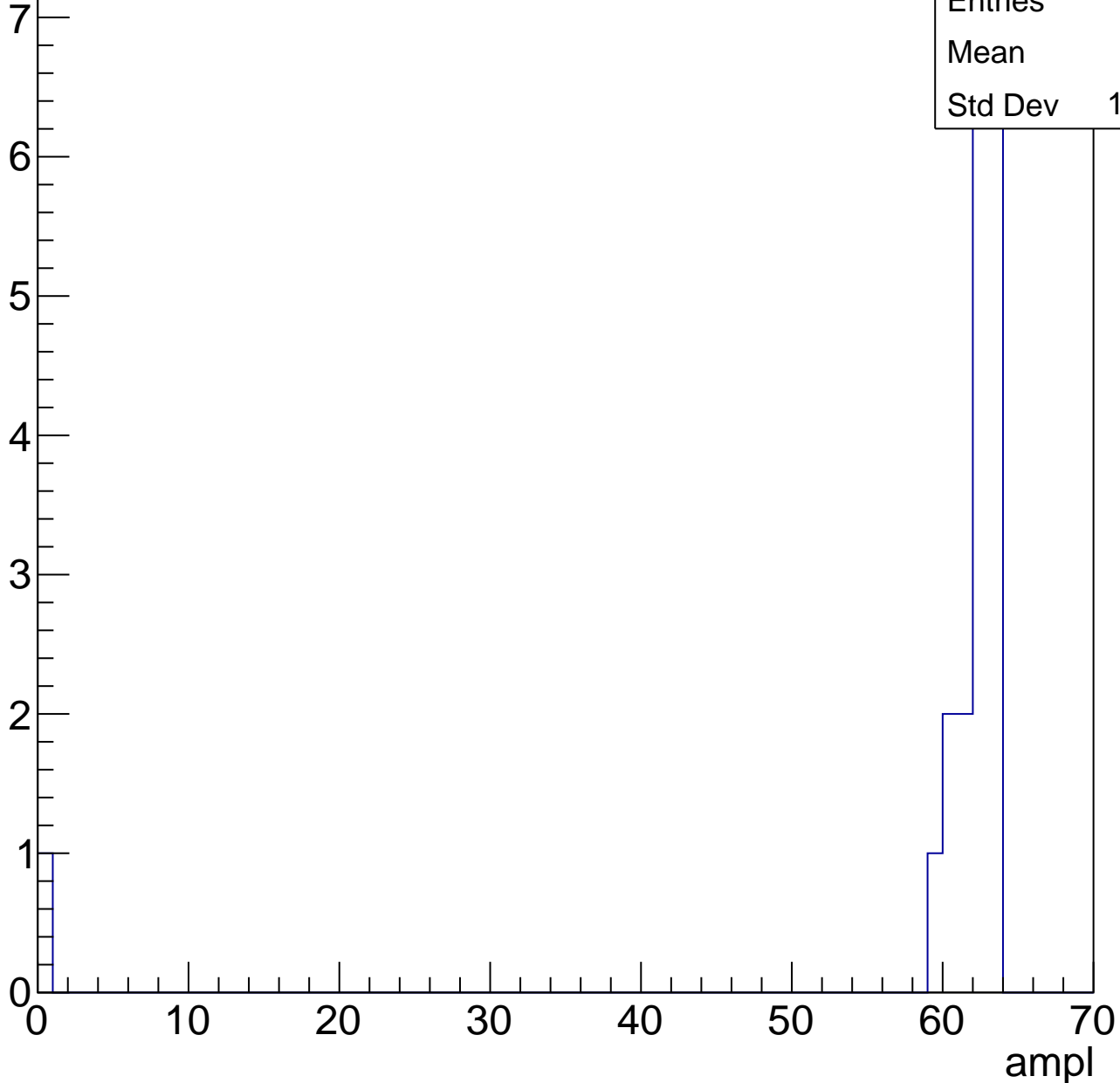


# B1L103S, U22-ch58, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.8
Std Dev	13.54



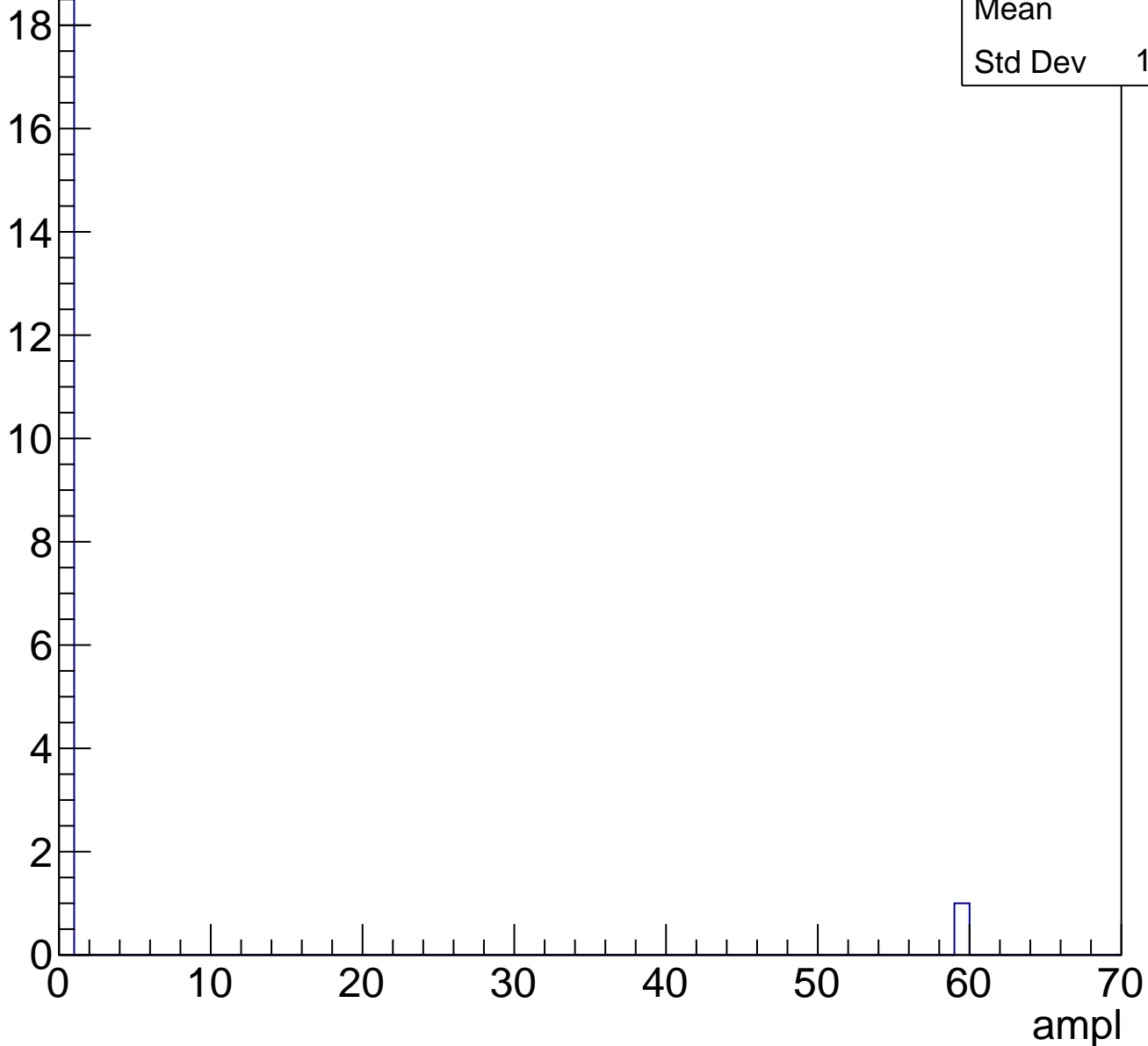


# B1L103S, U22-ch58, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	2.95
Std Dev	12.86

Entry



# B1L103S, U22-ch59, adc0

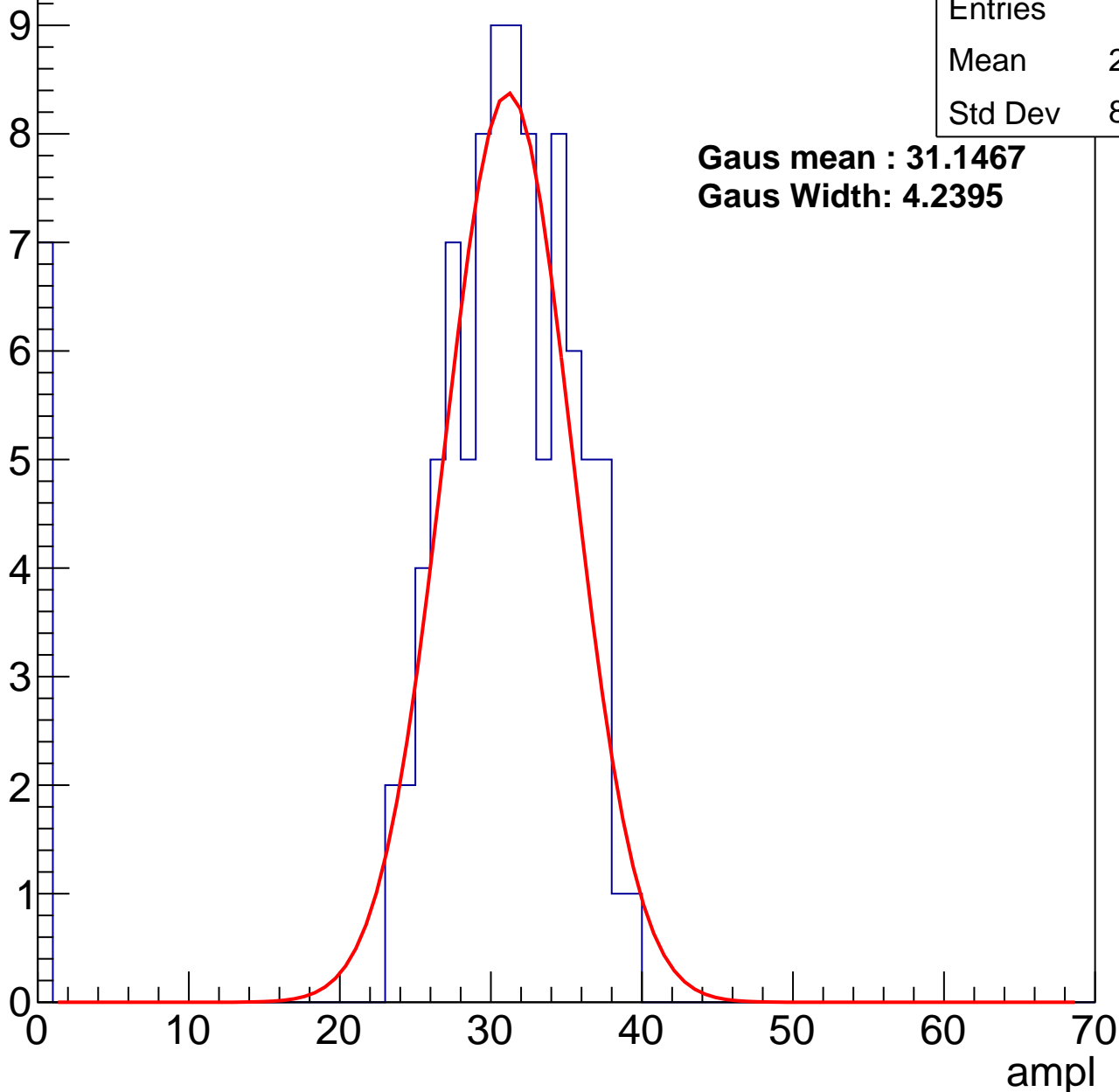
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	97
Mean	28.65
Std Dev	8.792

**Gaus mean : 31.1467**

**Gaus Width: 4.2395**



# B1L103S, U22-ch59, adc1

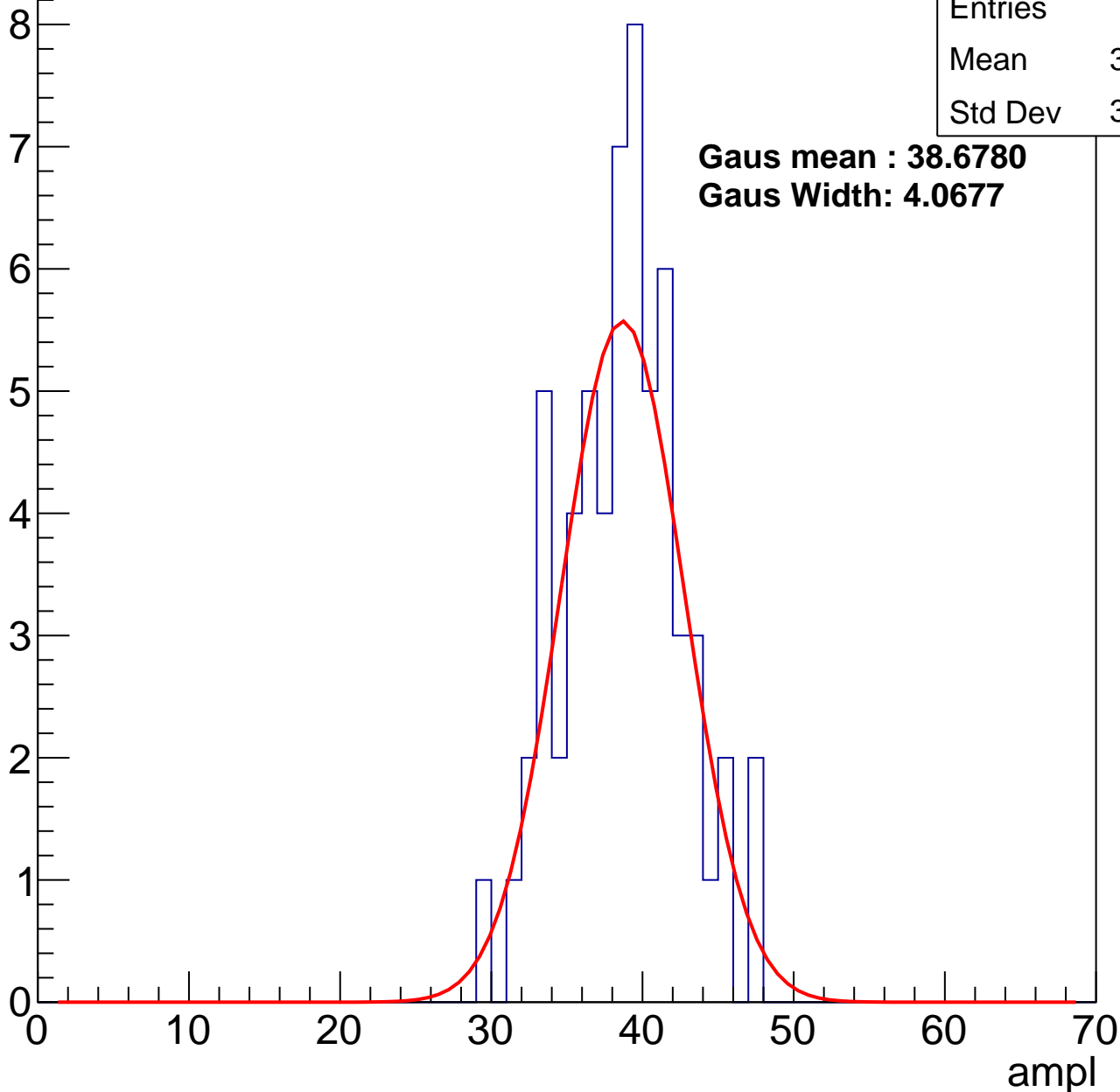
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	38.23
Std Dev	3.877

**Gaus mean : 38.6780**

**Gaus Width: 4.0677**



# B1L103S, U22-ch59, adc2

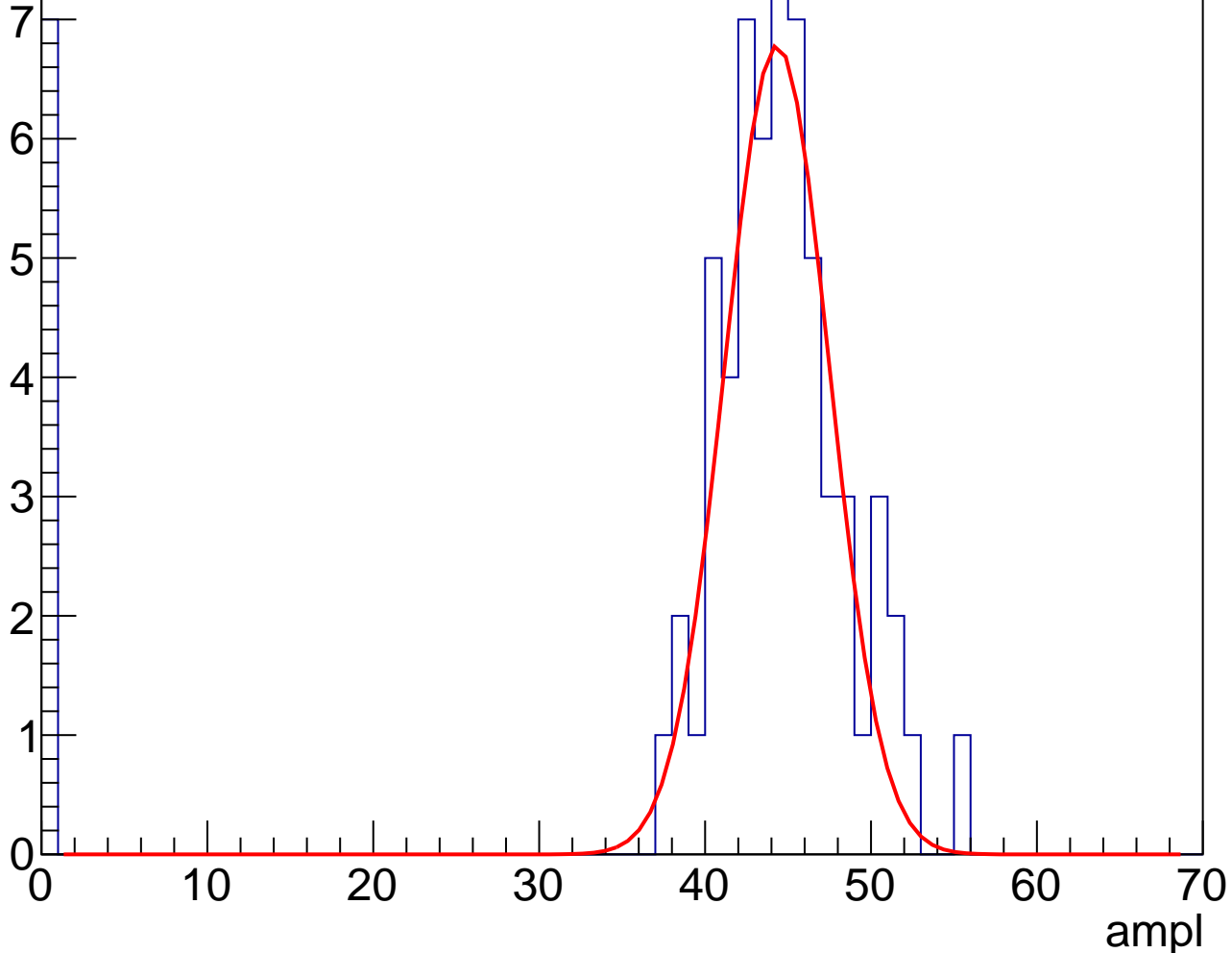
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	39.74
Std Dev	13.9

**Gaus mean : 44.3348**

**Gaus Width: 3.1423**

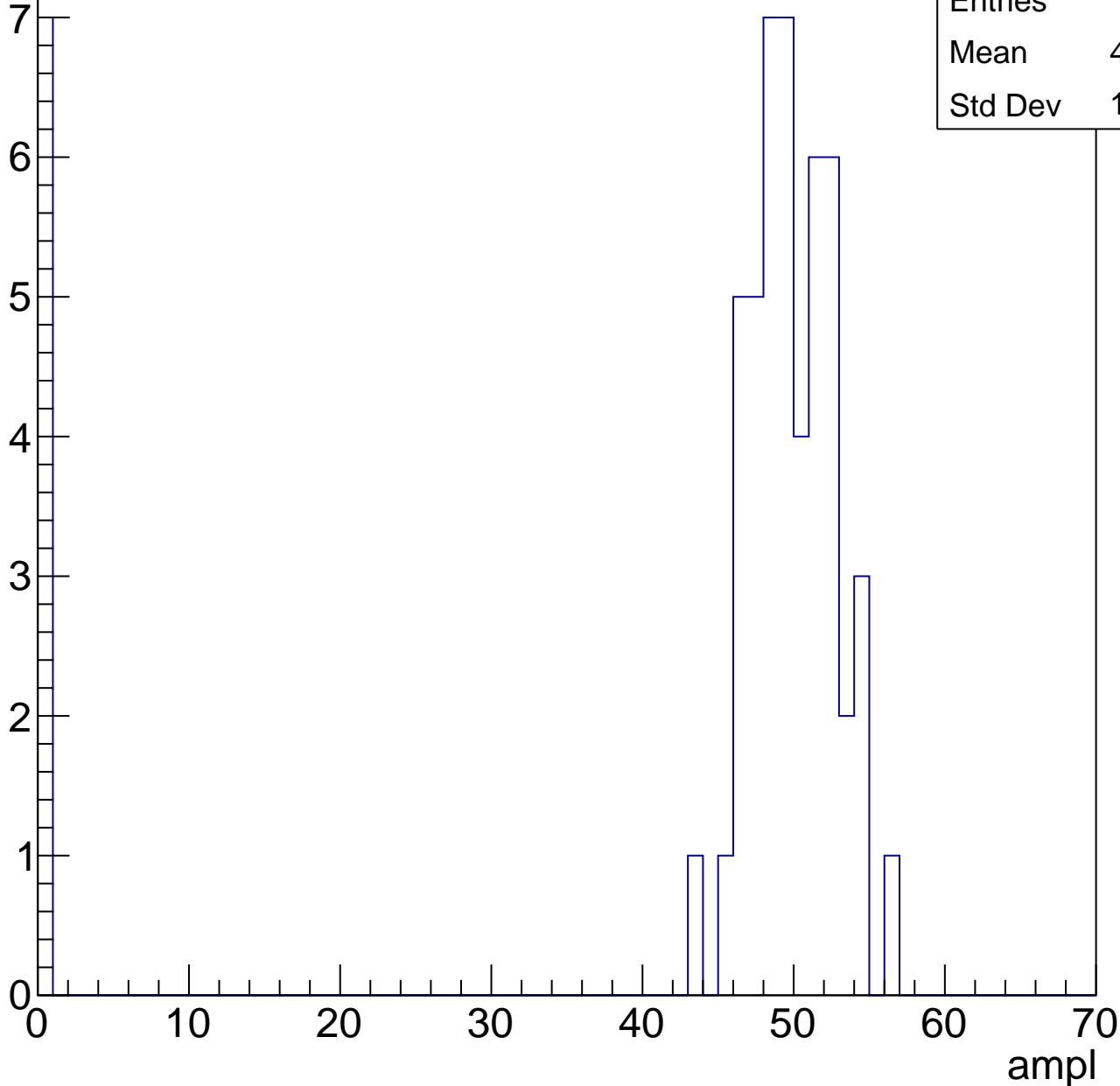


# B1L103S, U22-ch59, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	43.16
Std Dev	16.68

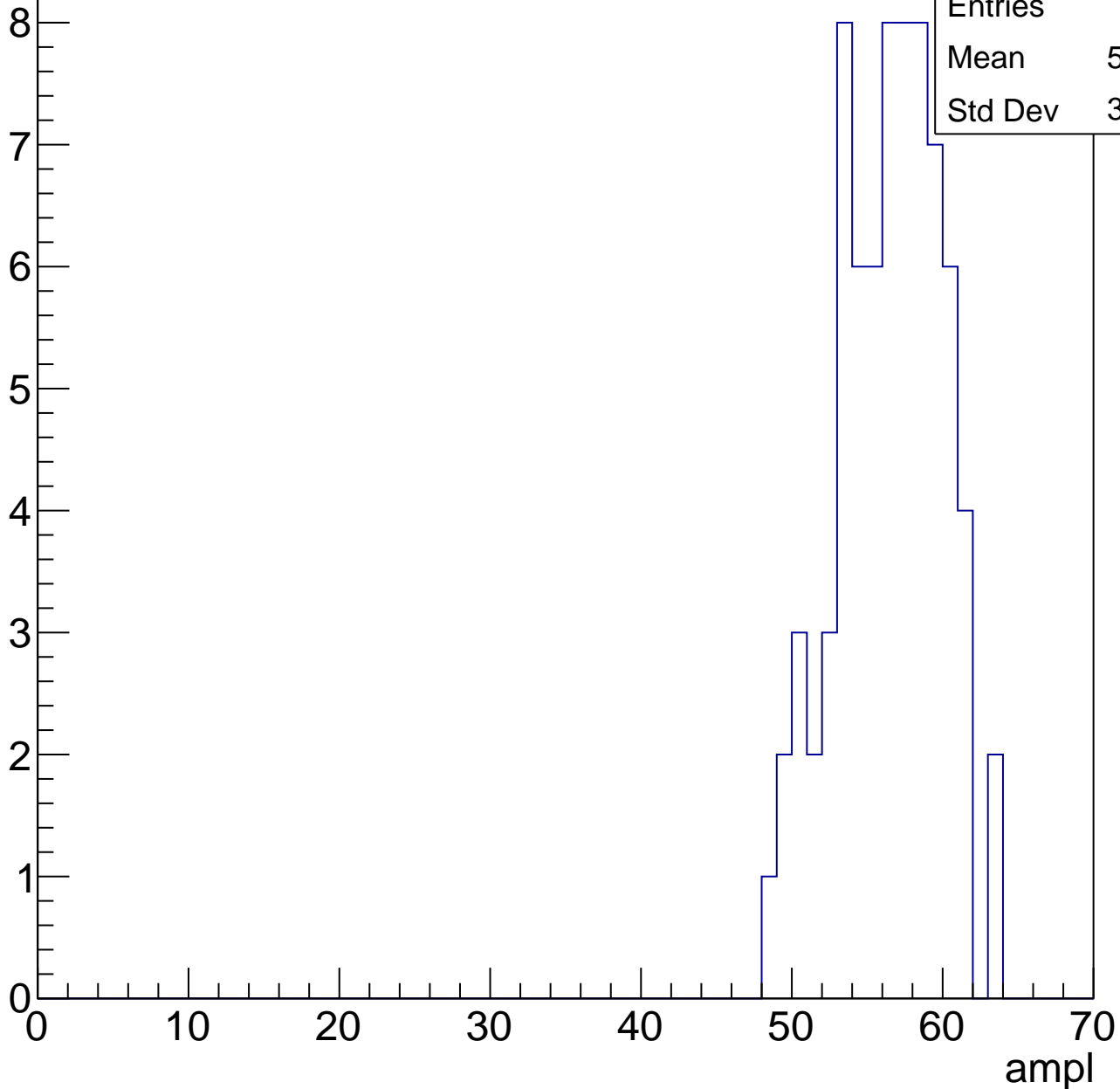


# B1L103S, U22-ch59, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	55.99
Std Dev	3.423

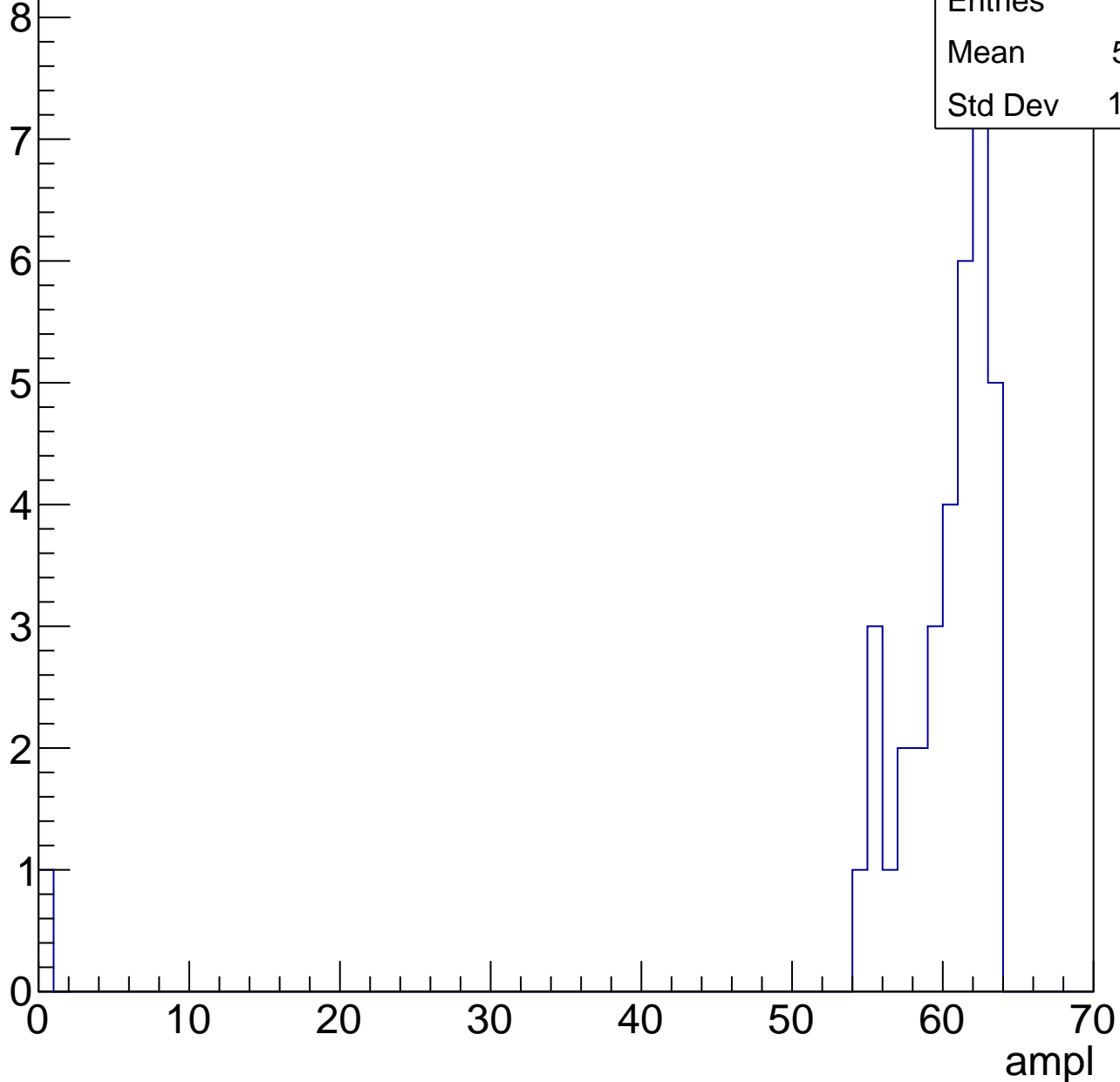


# B1L103S, U22-ch59, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	36
Mean	58.31
Std Dev	10.19



# B1L103S, U22-ch59, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



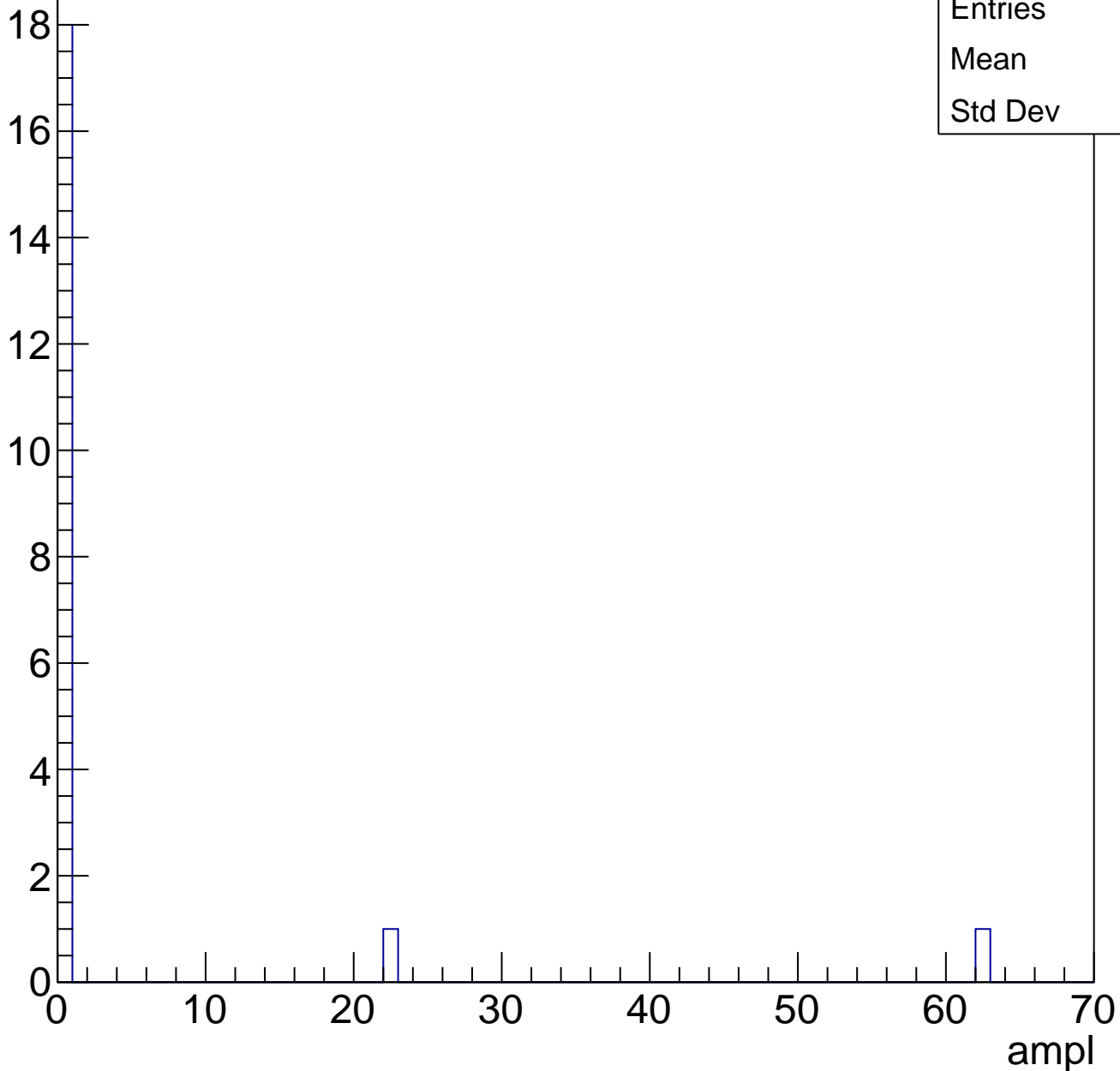


# B1L103S, U22-ch59, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	4.2
Std Dev	14.1

Entry



# B1L103S, U22-ch60, adc0

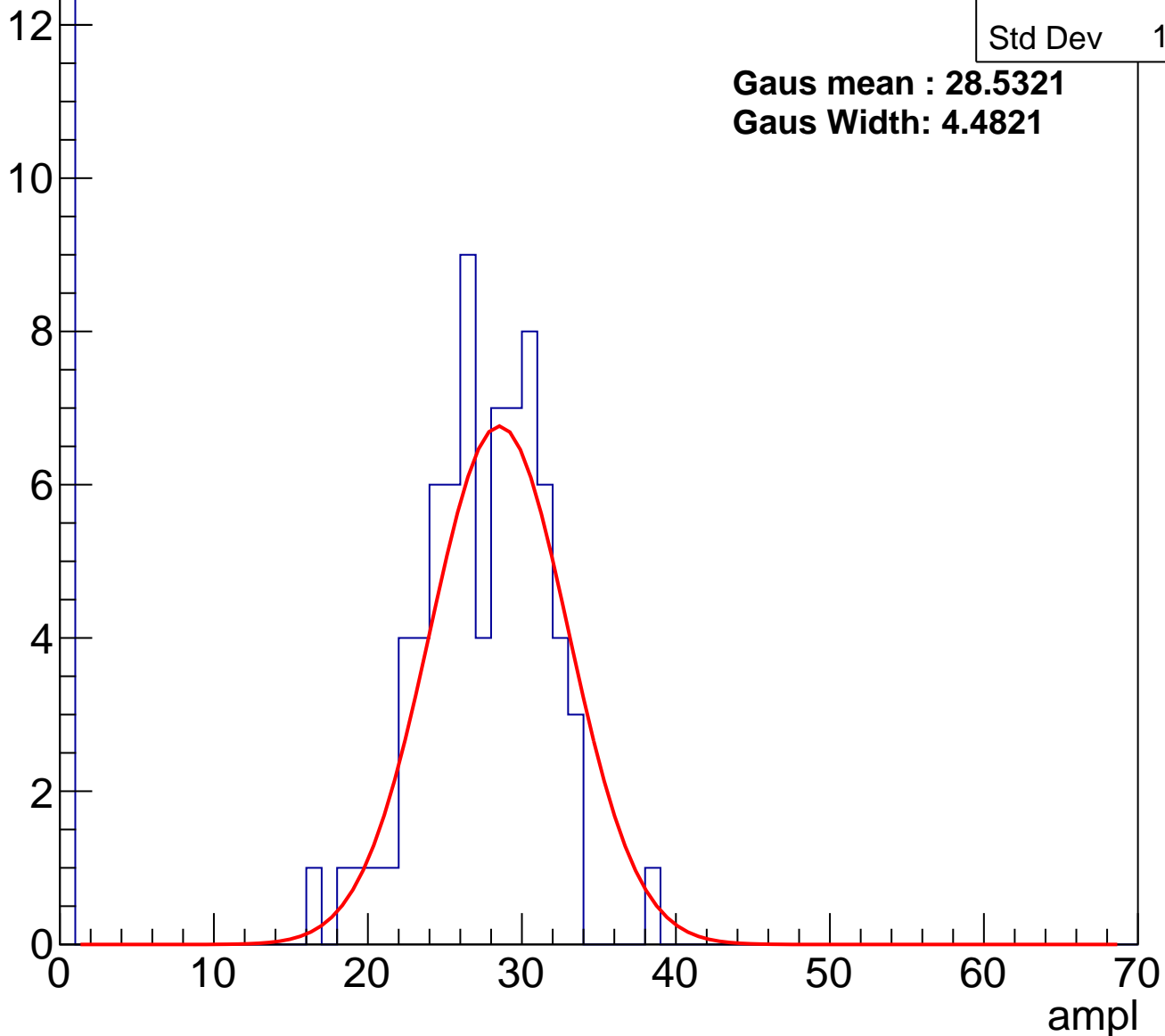
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	22.99
Std Dev	10.29

**Gaus mean : 28.5321**

**Gaus Width: 4.4821**

Entry

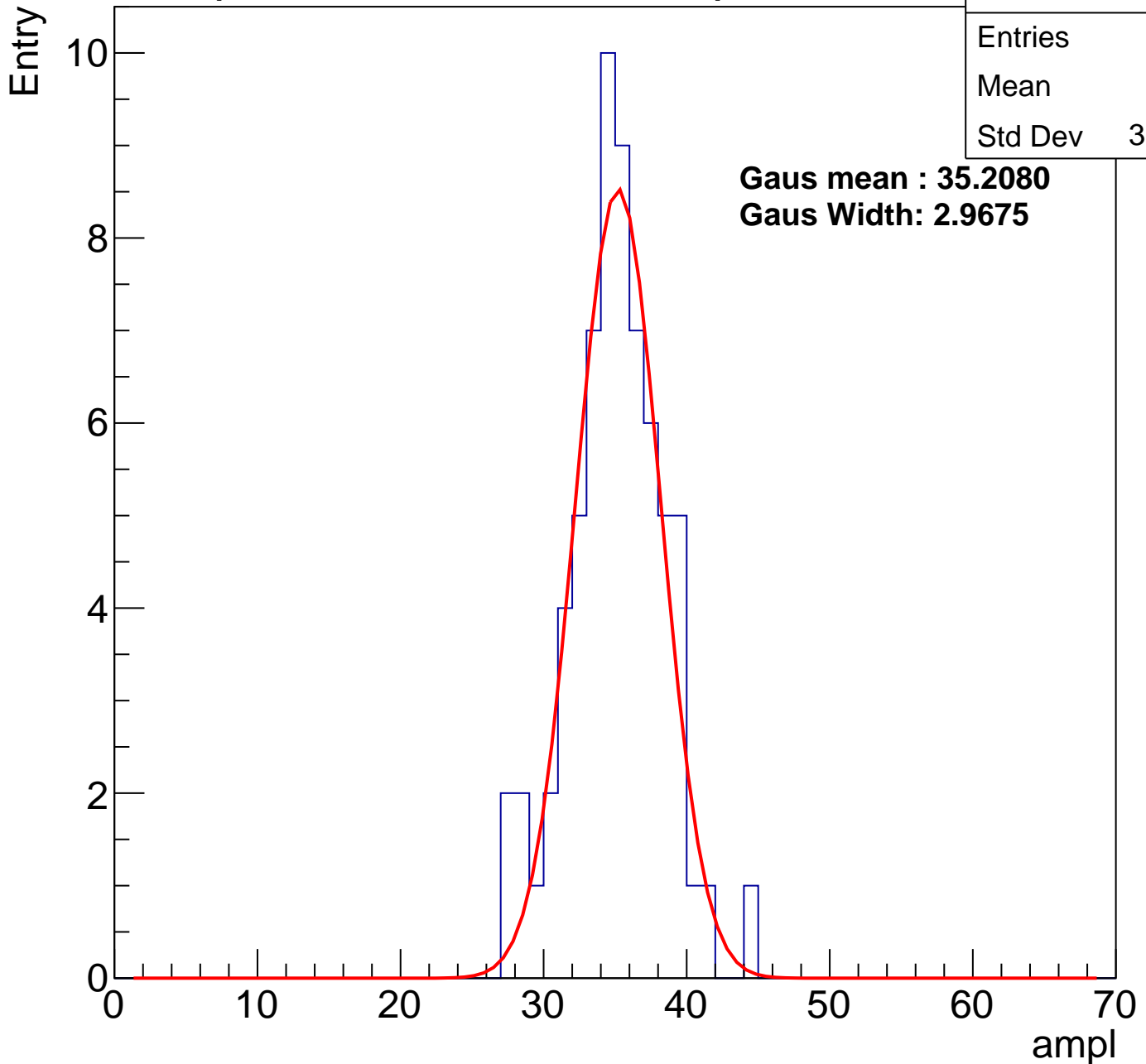


# B1L103S, U22-ch60, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	68
Mean	34.6
Std Dev	3.313

**Gaus mean : 35.2080**  
**Gaus Width: 2.9675**



# B1L103S, U22-ch60, adc2

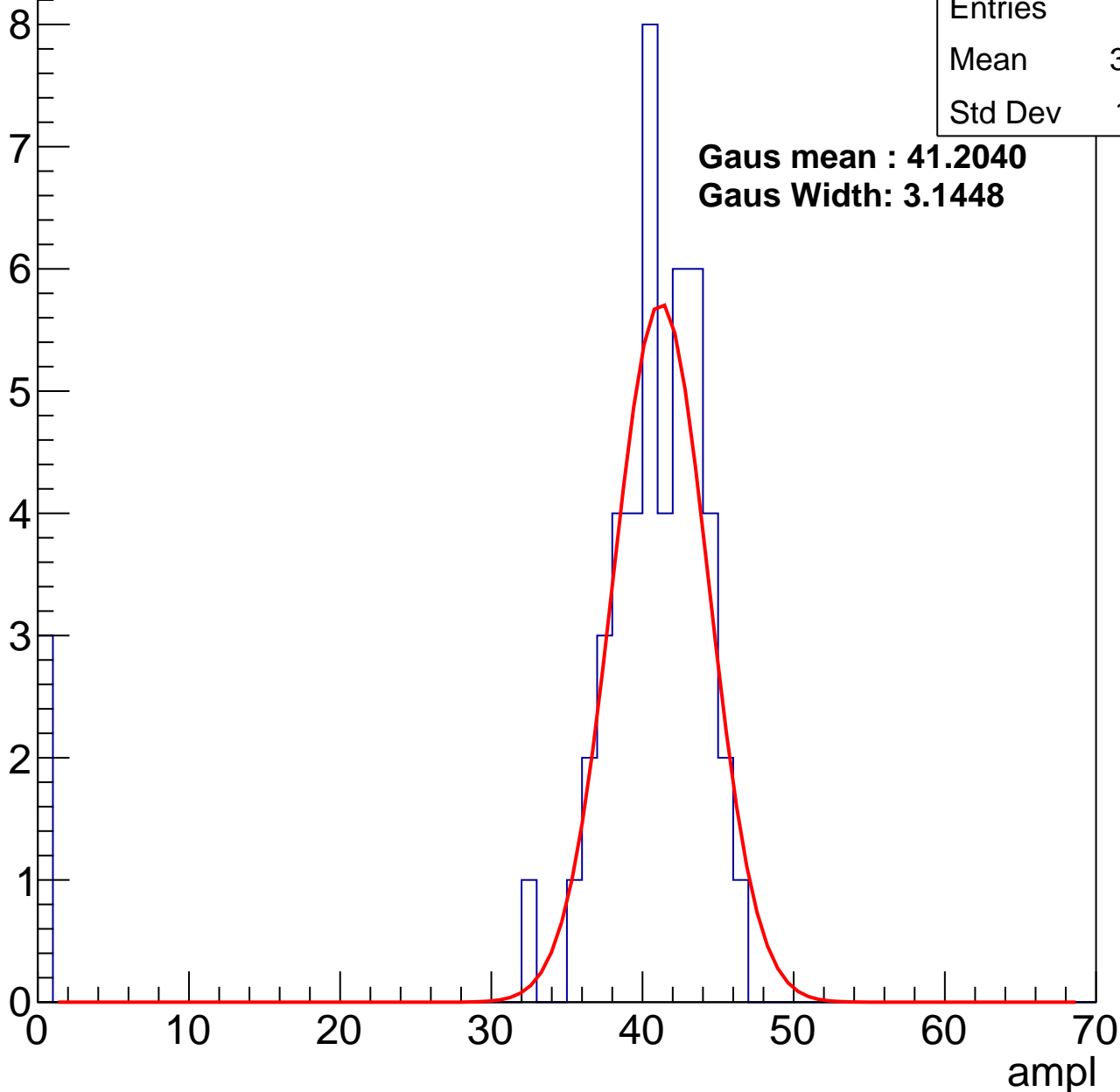
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	38.04
Std Dev	10.11

**Gaus mean : 41.2040**

**Gaus Width: 3.1448**



# B1L103S, U22-ch60, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	39.96
Std Dev	16.11

Entry

10

8

6

4

2

0

0

10

20

30

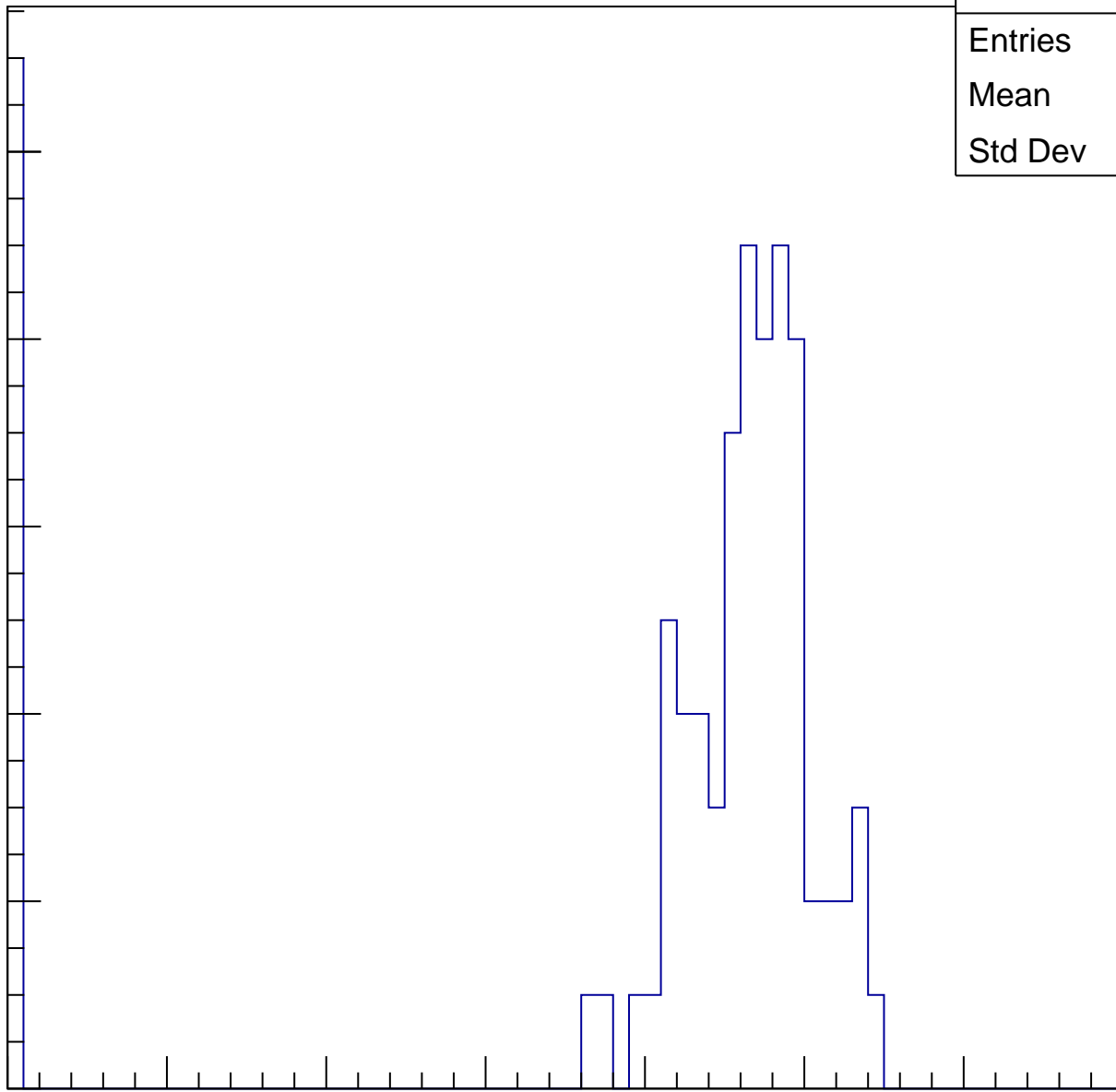
40

50

60

70

ampl

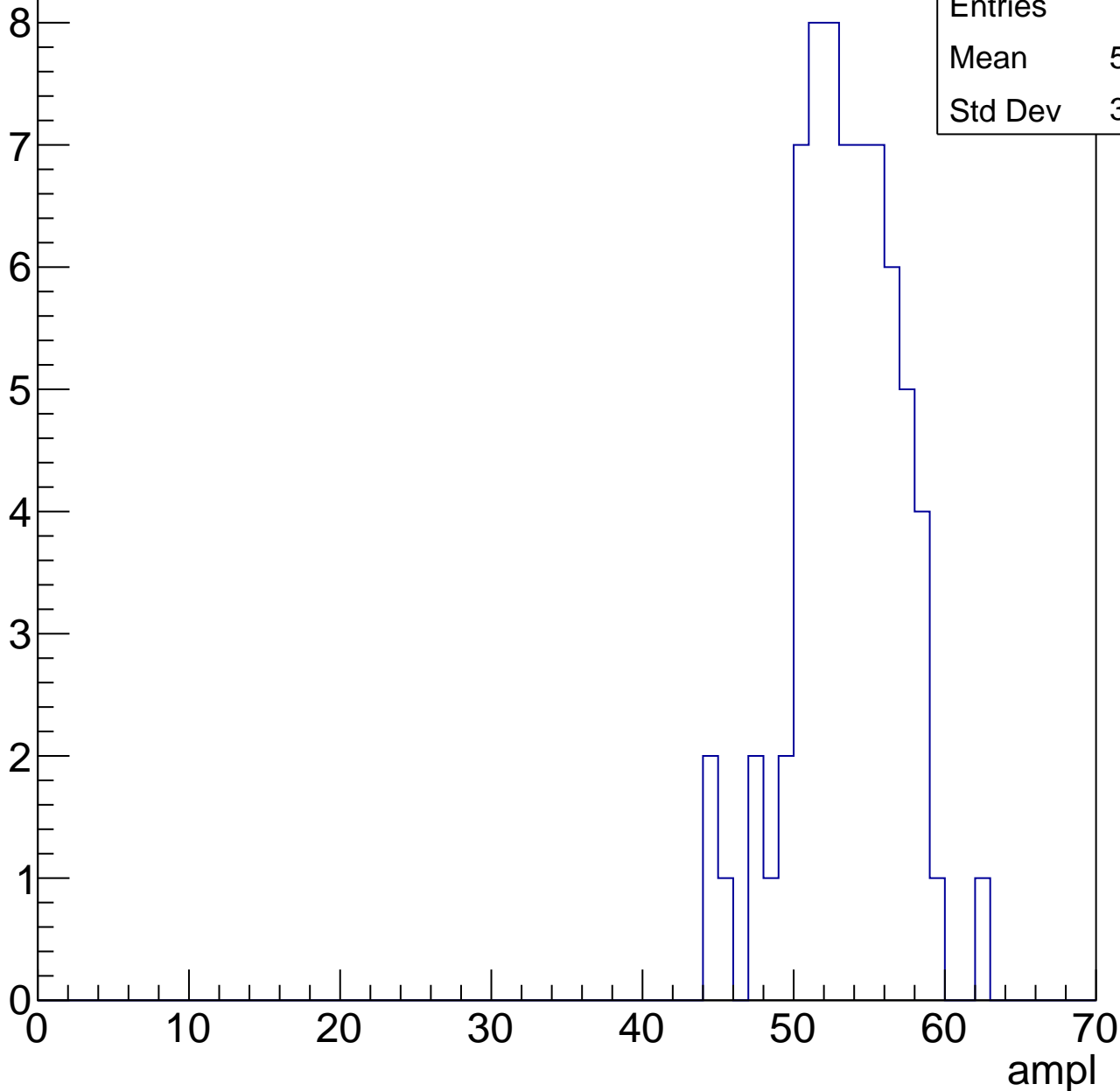


# B1L103S, U22-ch60, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	52.97
Std Dev	3.497

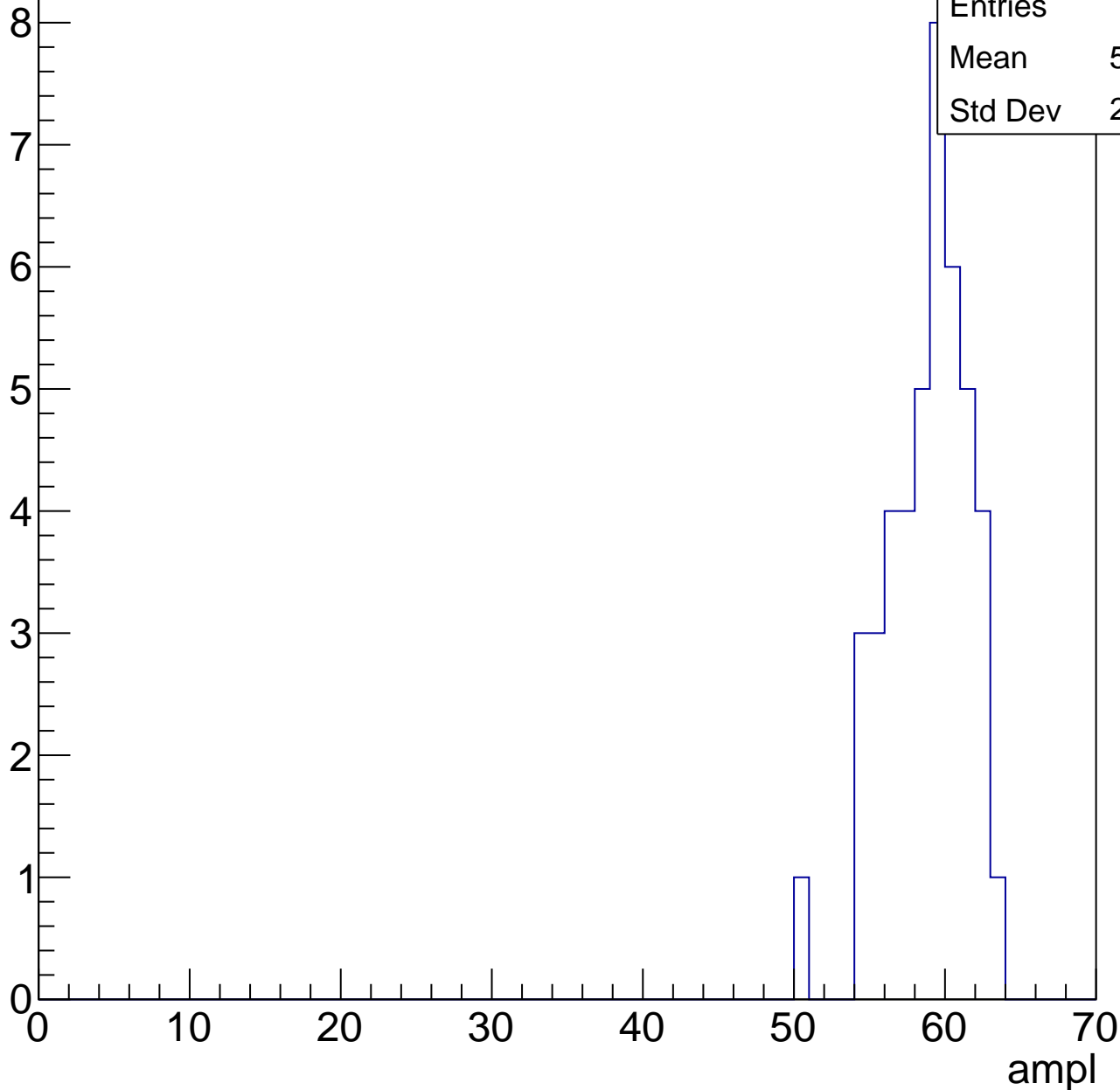


# B1L103S, U22-ch60, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

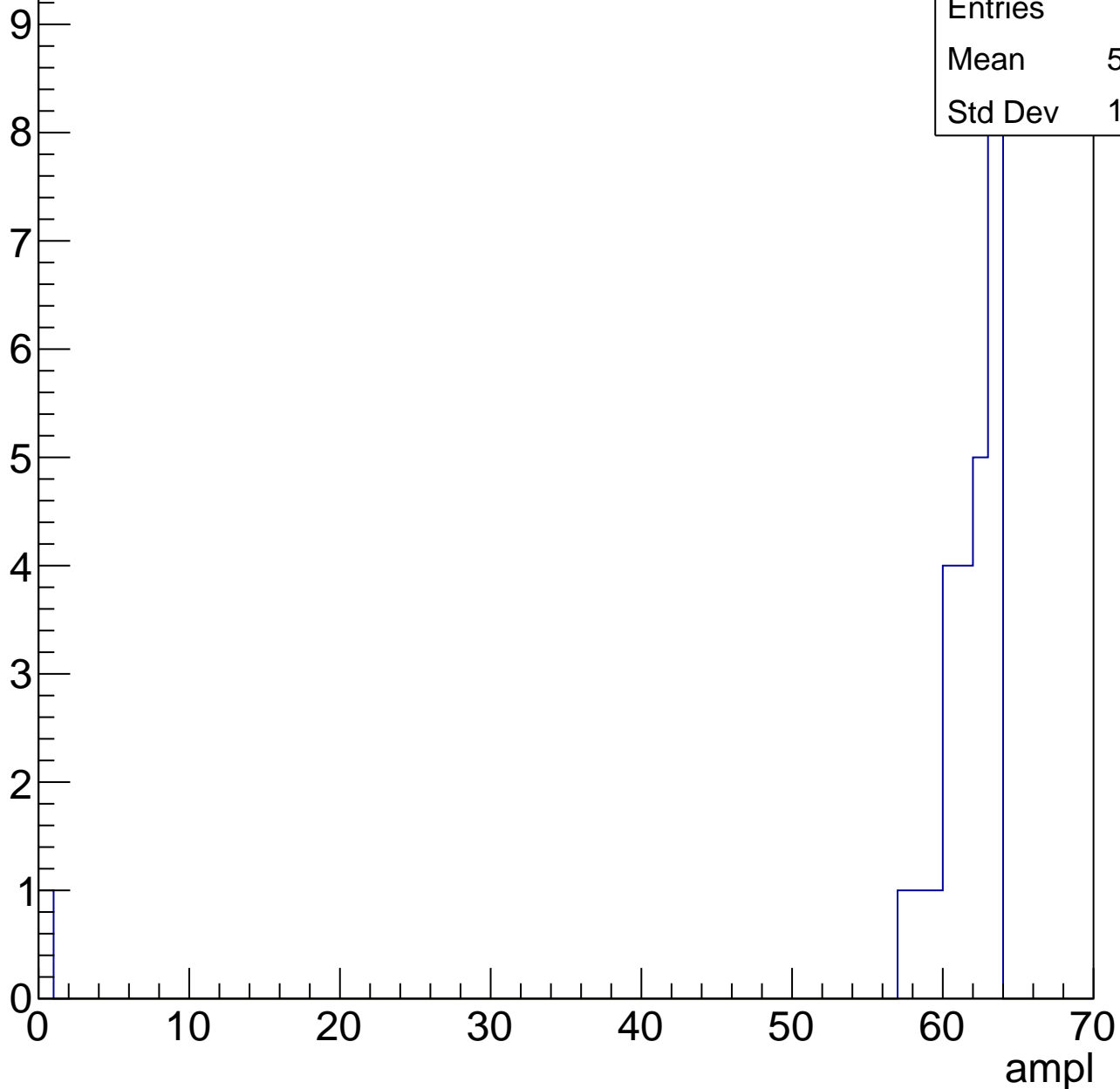
Entries	44
Mean	58.34
Std Dev	2.696



# B1L103S, U22-ch60, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch60, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



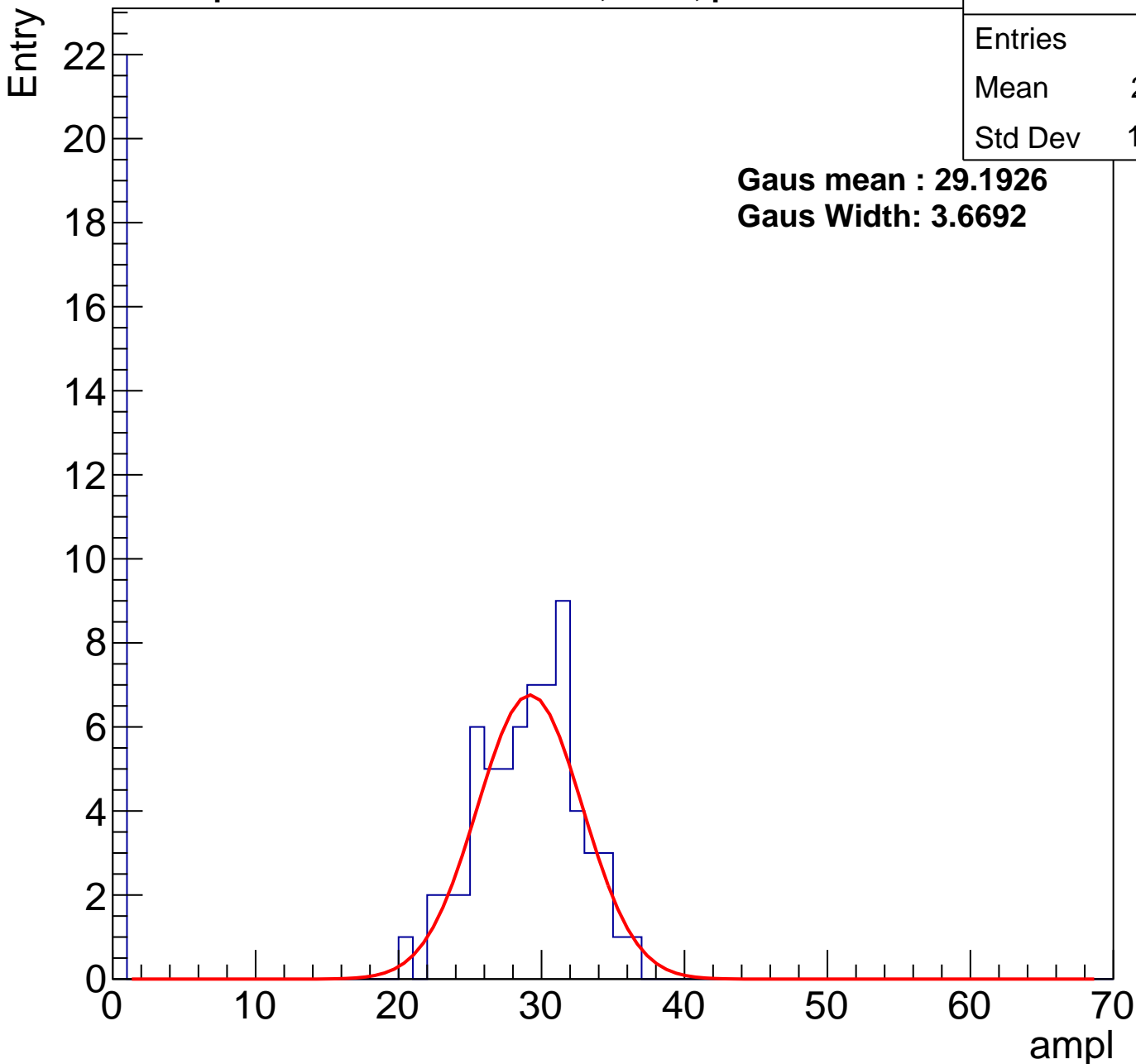
# B1L103S, U22-ch61, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	21.31
Std Dev	12.84

**Gaus mean : 29.1926**

**Gaus Width: 3.6692**



# B1L103S, U22-ch61, adc1

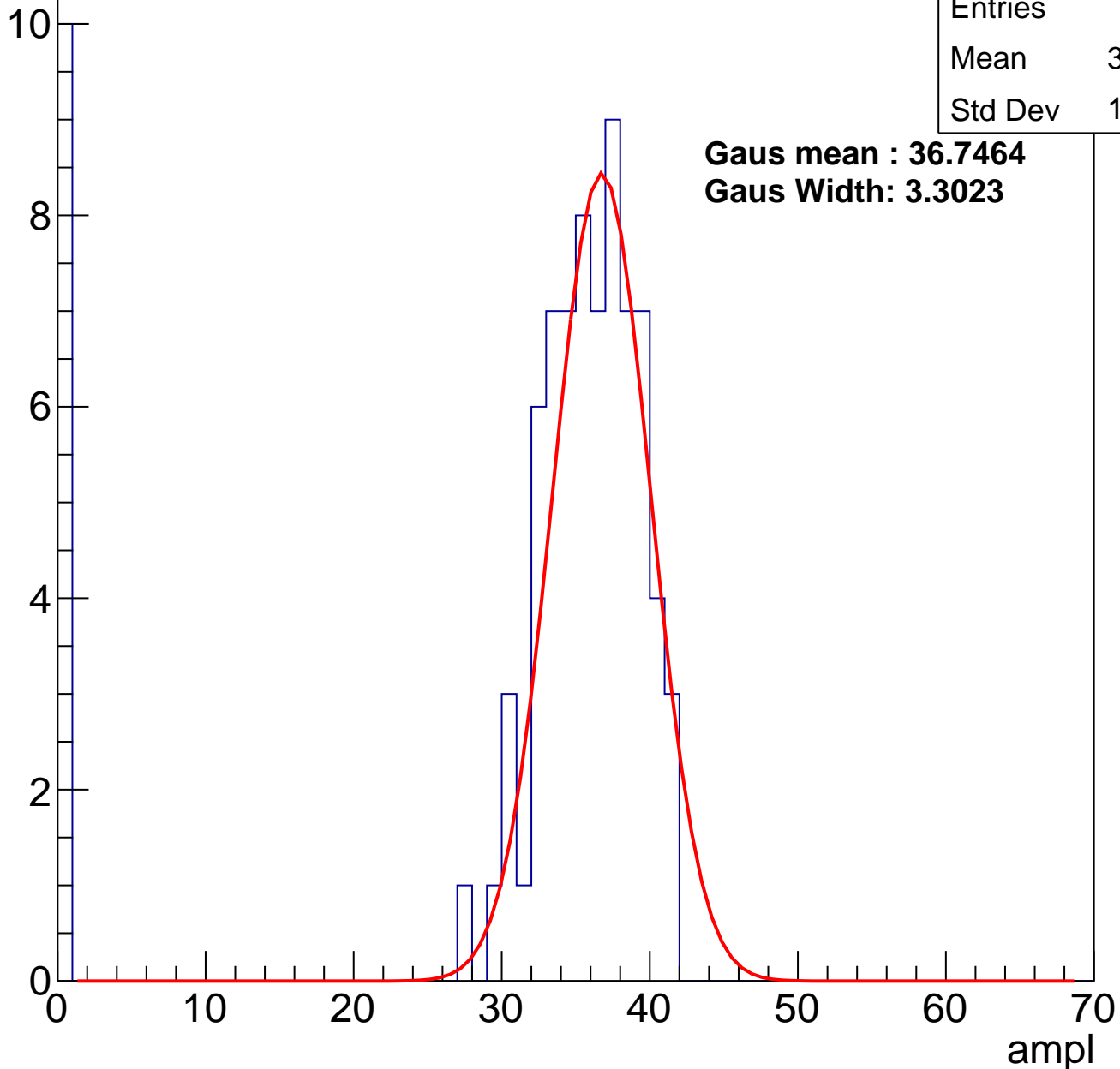
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	31.17
Std Dev	12.05

**Gaus mean : 36.7464**

**Gaus Width: 3.3023**

Entry



# B1L103S, U22-ch61, adc2

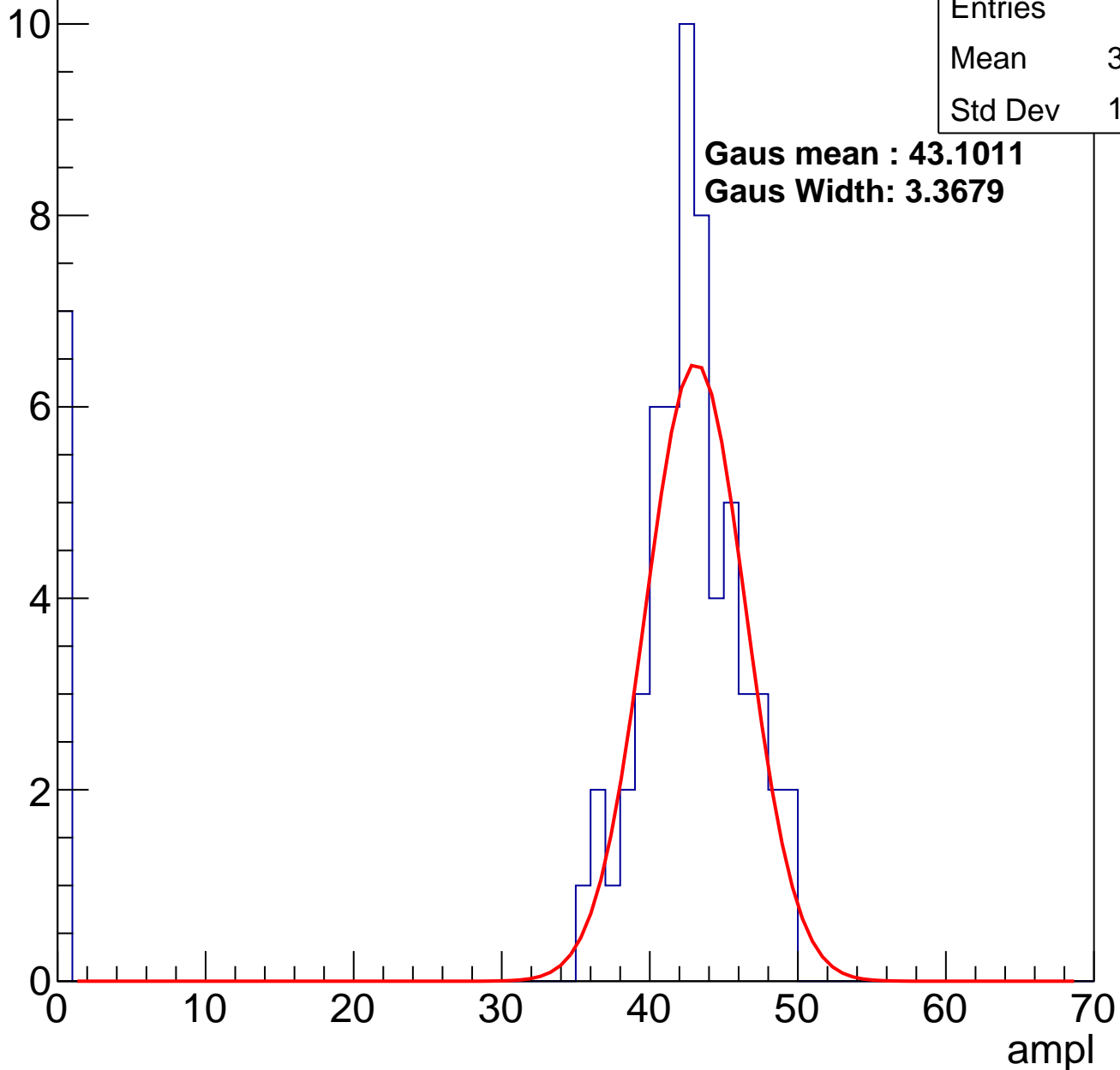
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	65
Mean	37.86
Std Dev	13.49

**Gaus mean : 43.1011**

**Gaus Width: 3.3679**

Entry

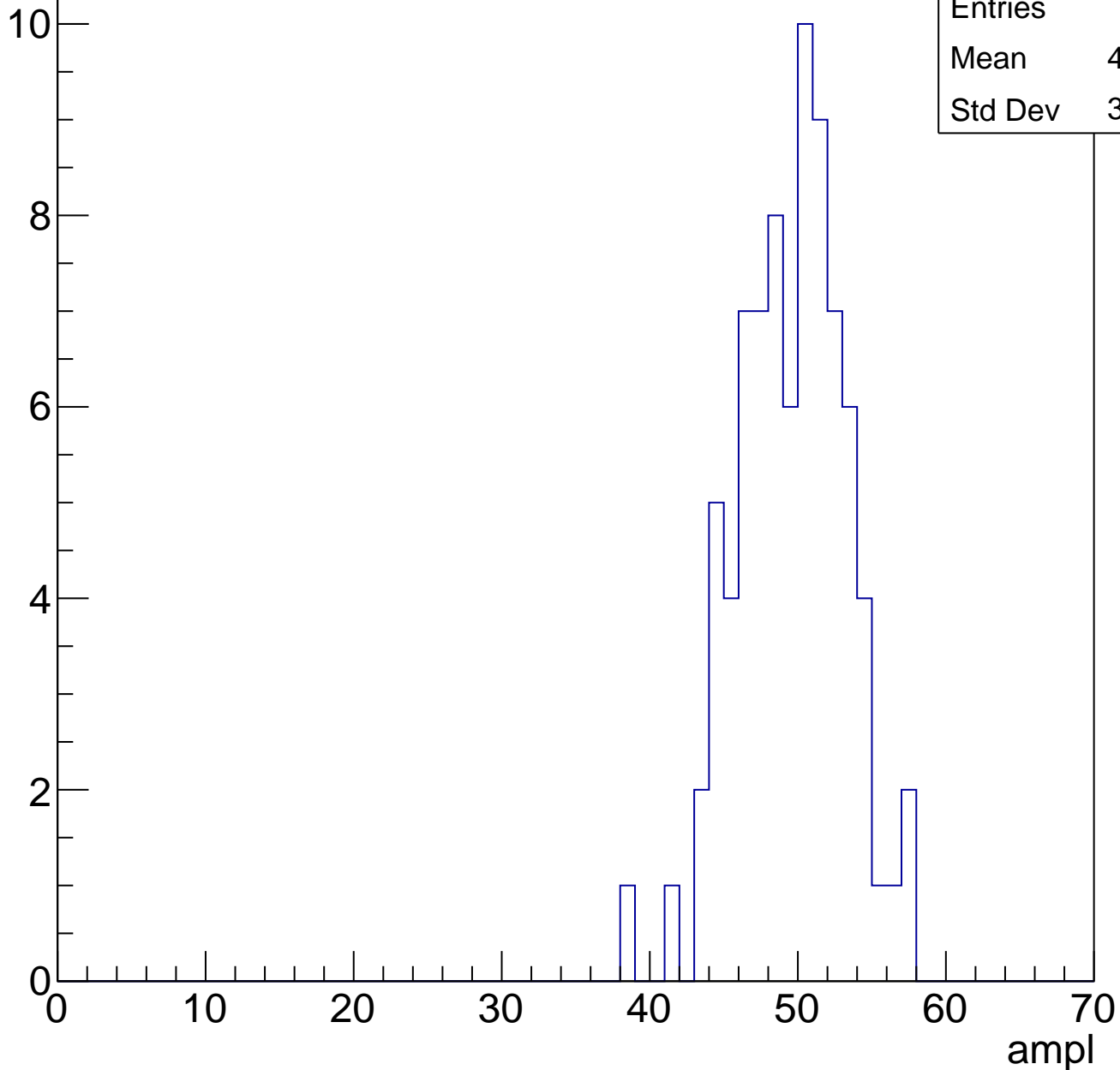


# B1L103S, U22-ch61, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	49.09
Std Dev	3.622

Entry

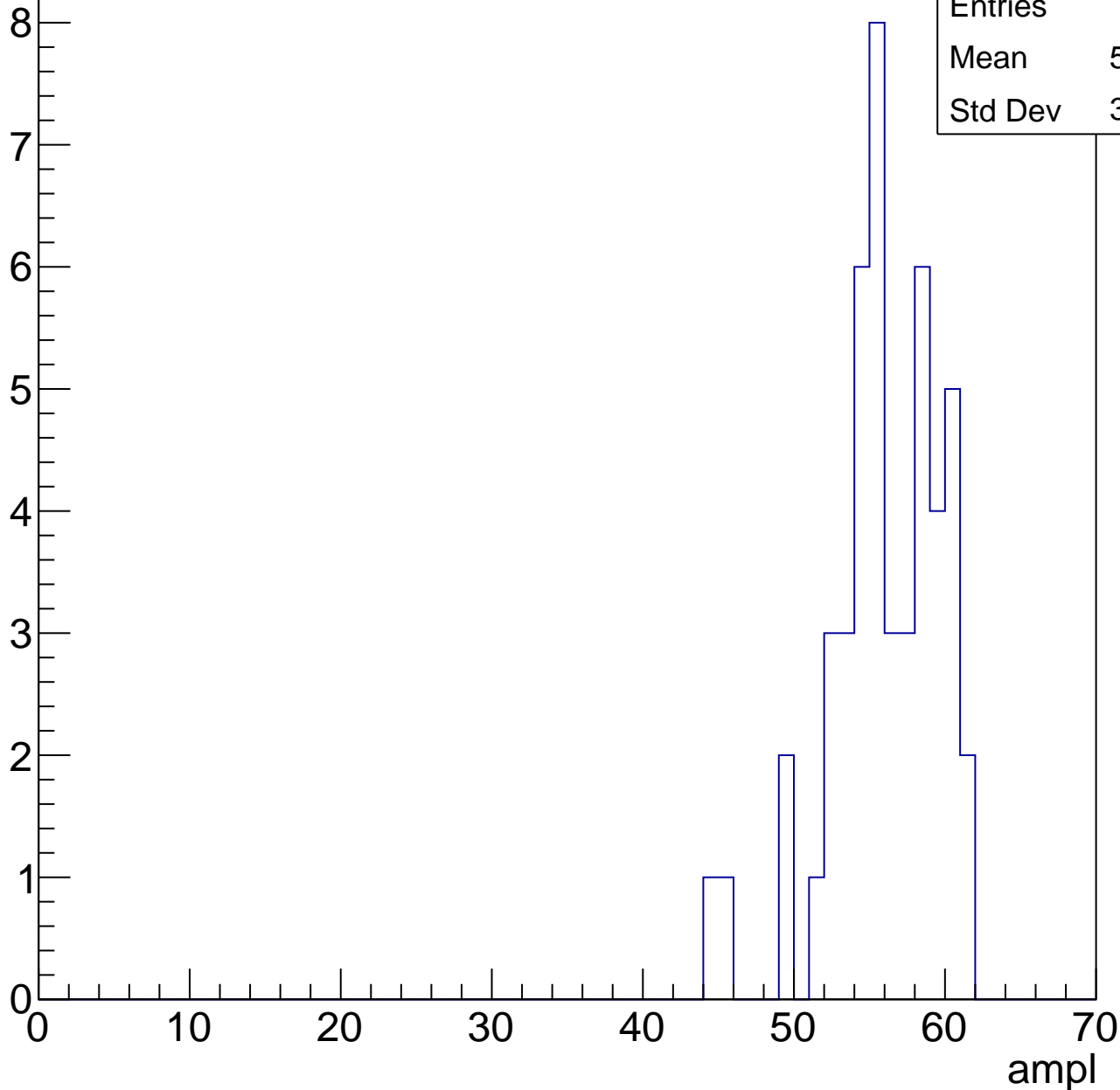


# B1L103S, U22-ch61, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	55.46
Std Dev	3.747

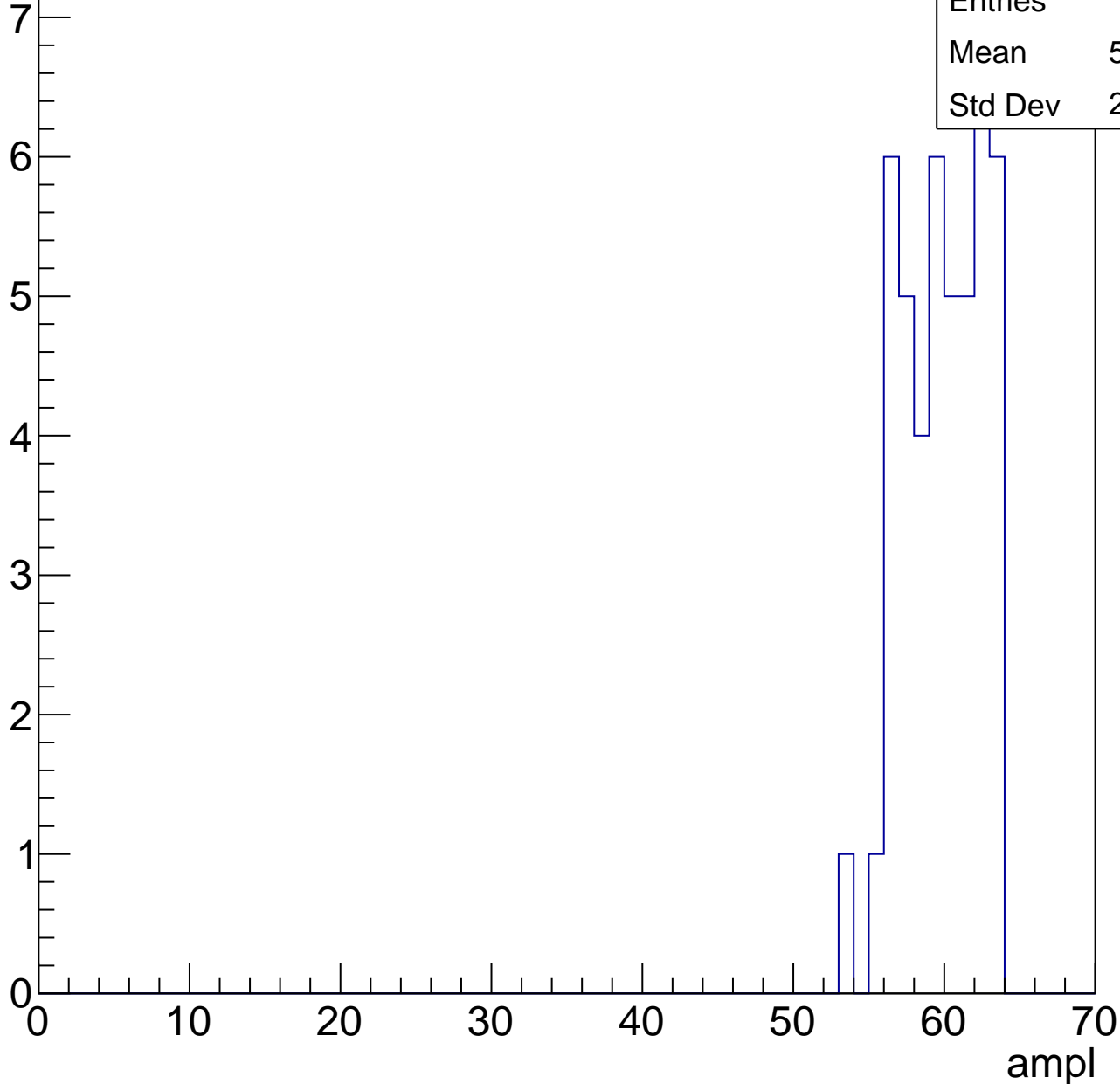


# B1L103S, U22-ch61, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

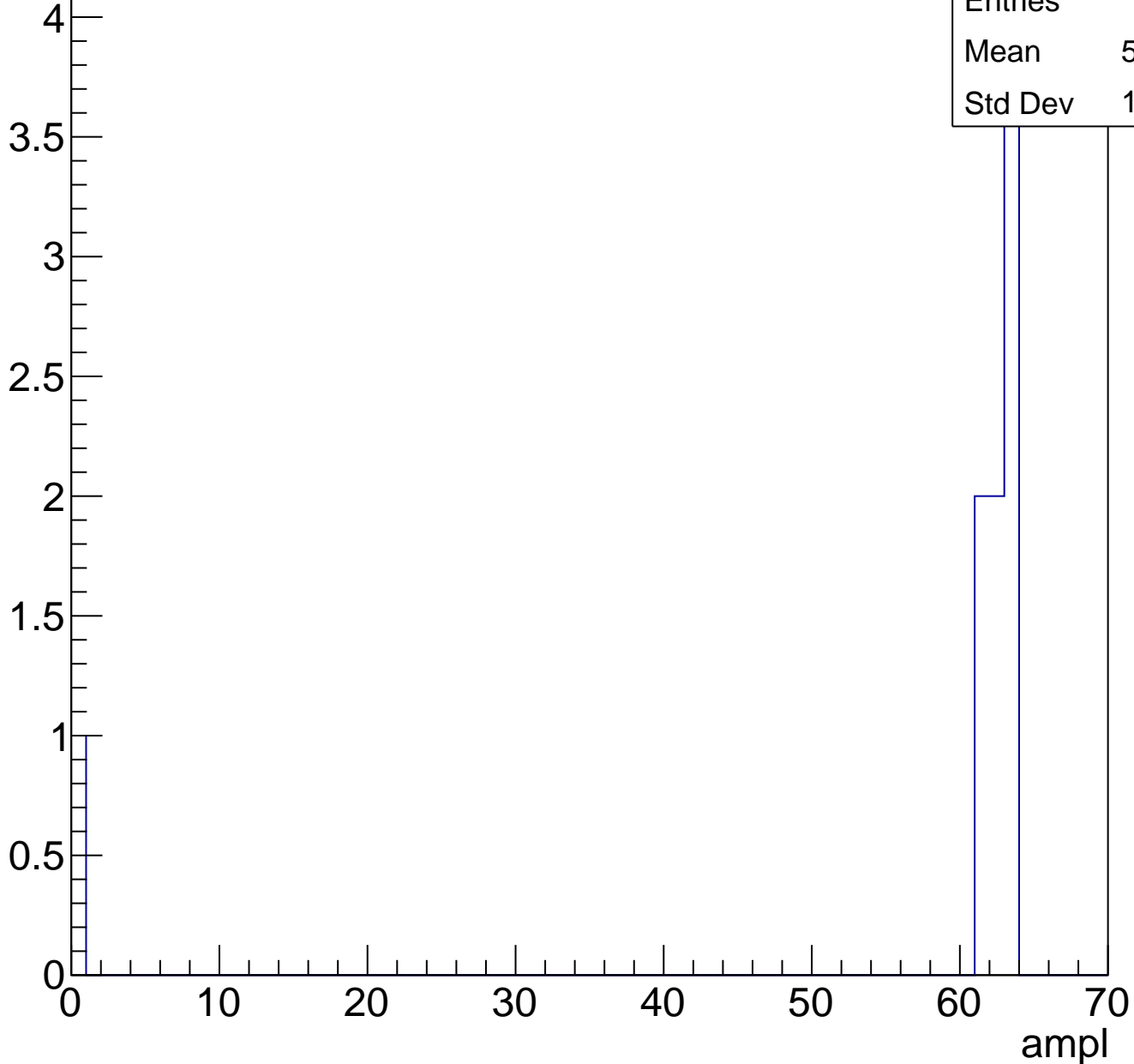
Entries	46
Mean	59.39
Std Dev	2.583



# B1L103S, U22-ch61, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



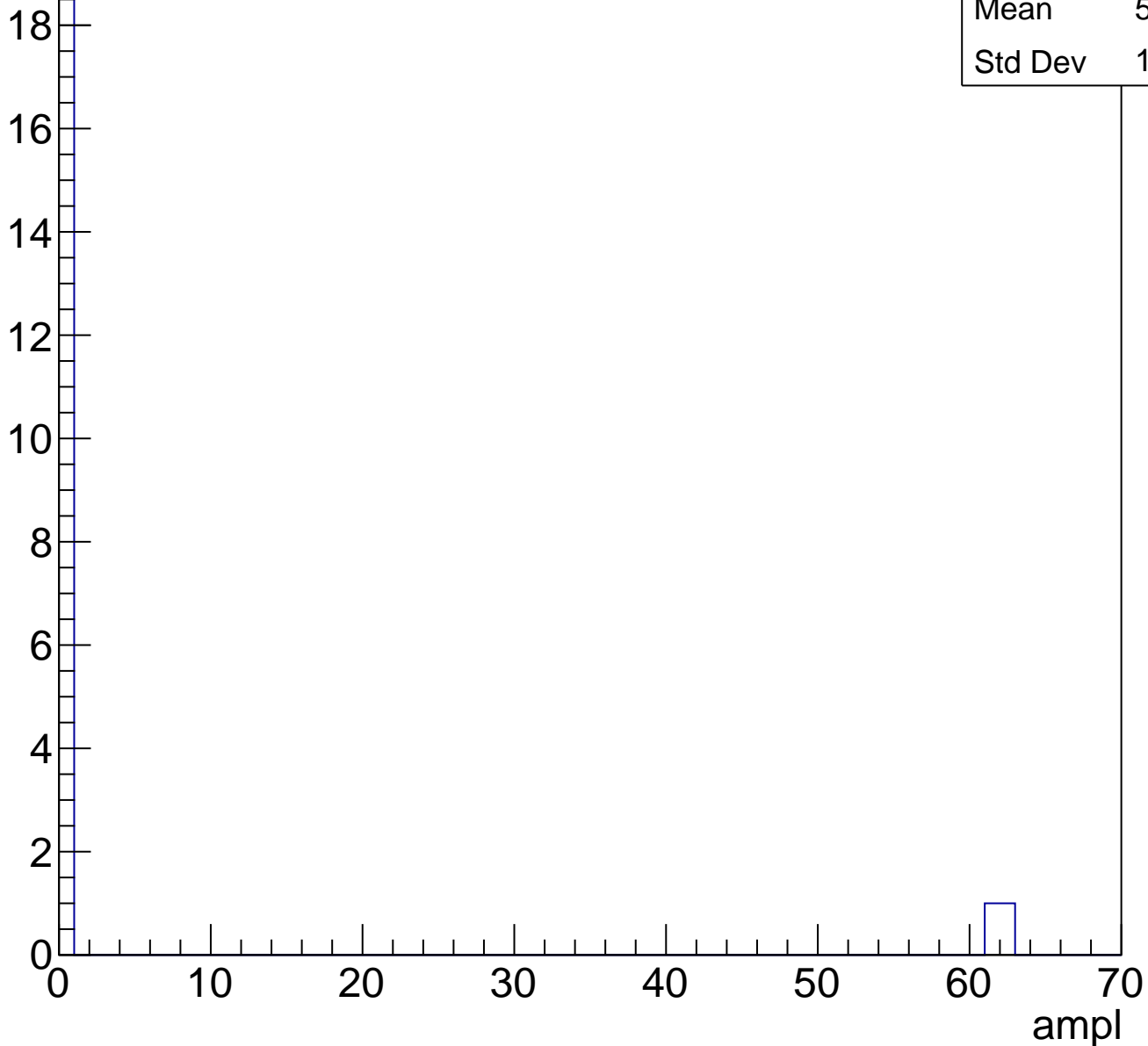


# B1L103S, U22-ch61, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.857
Std Dev	18.05

Entry



# B1L103S, U22-ch62, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	89
Mean	26.2
Std Dev	7.869

**Gaus mean : 29.0530**

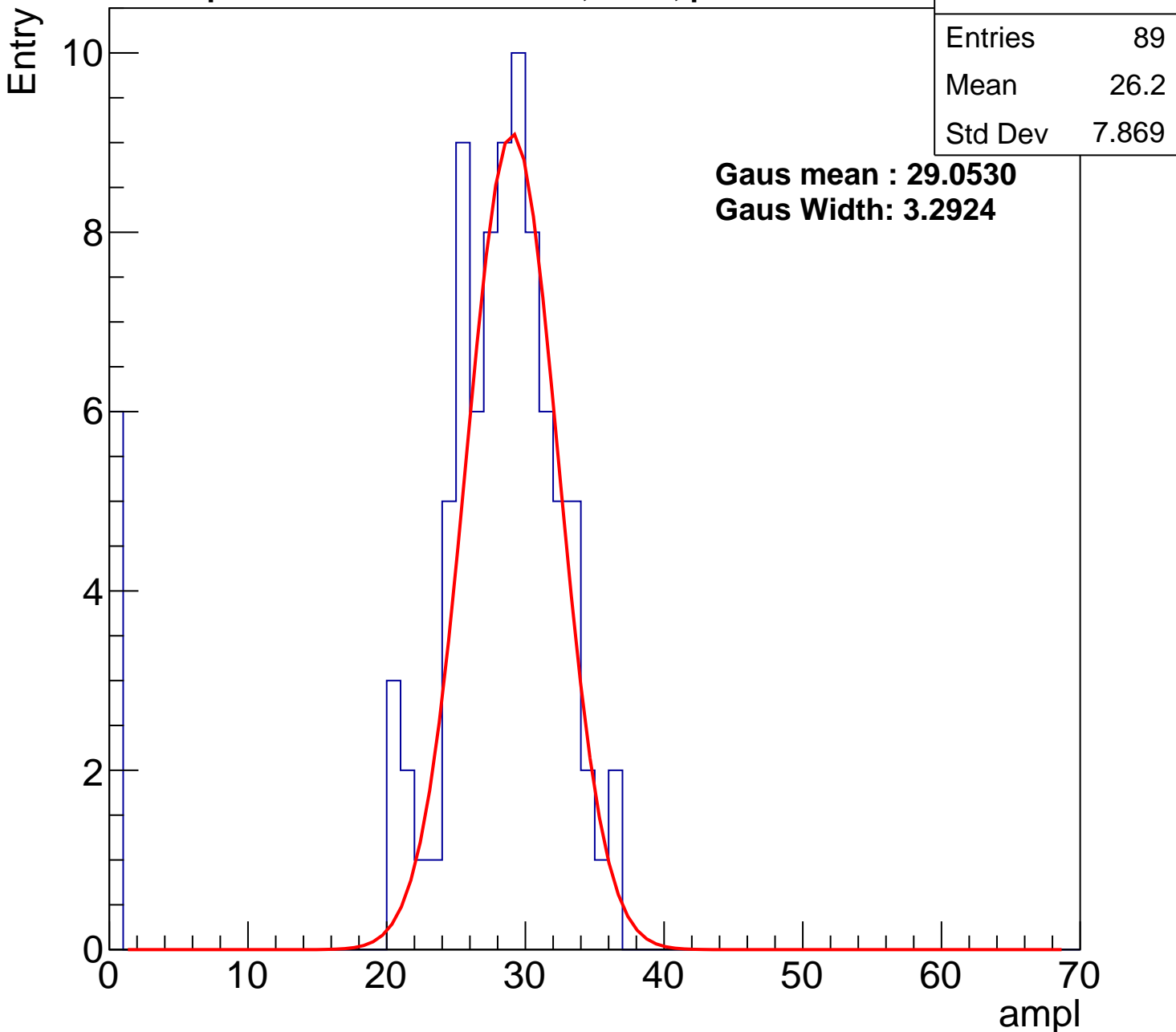
**Gaus Width: 3.2924**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch62, adc1

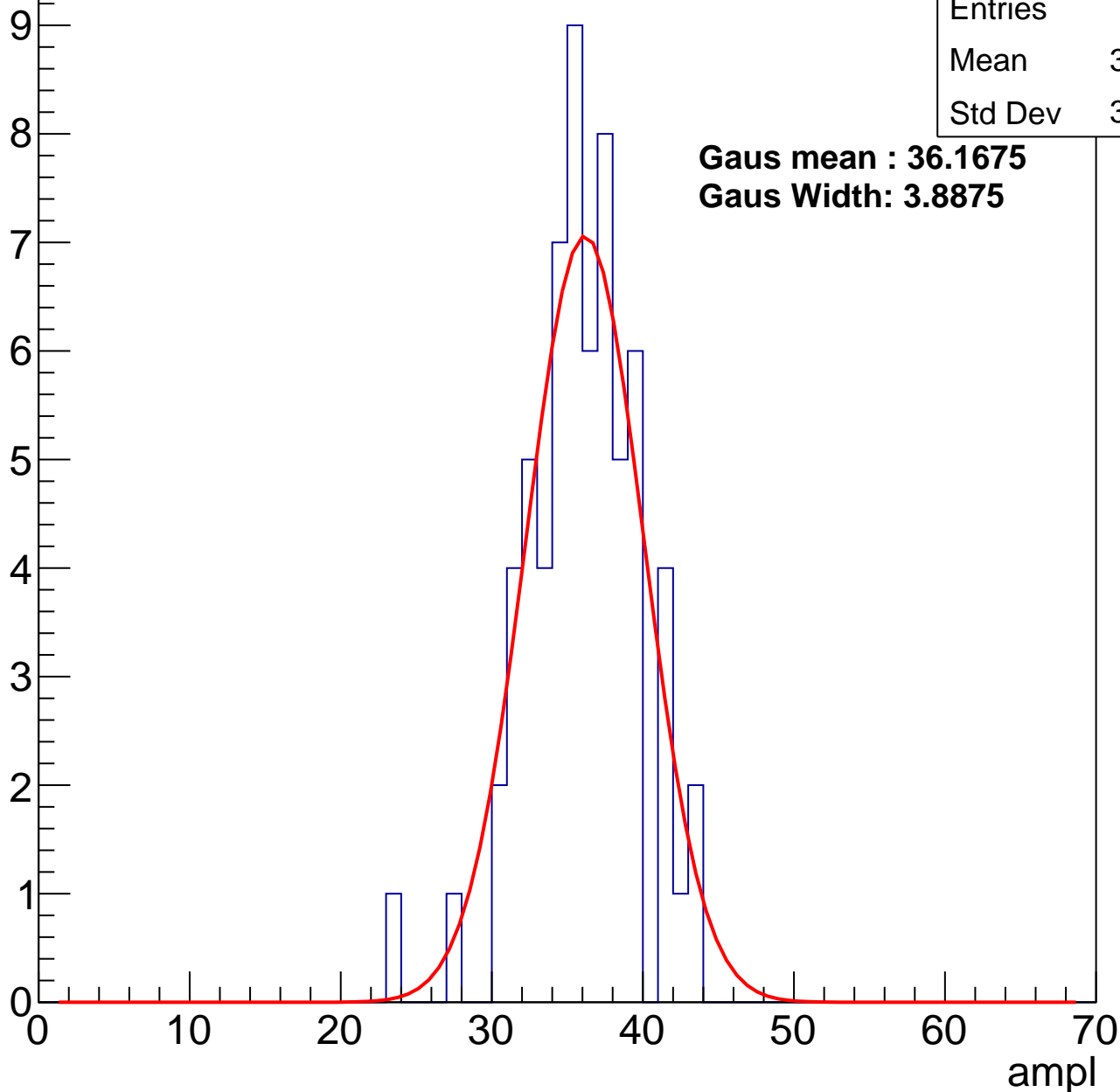
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	35.49
Std Dev	3.667

**Gaus mean : 36.1675**

**Gaus Width: 3.8875**



# B1L103S, U22-ch62, adc2

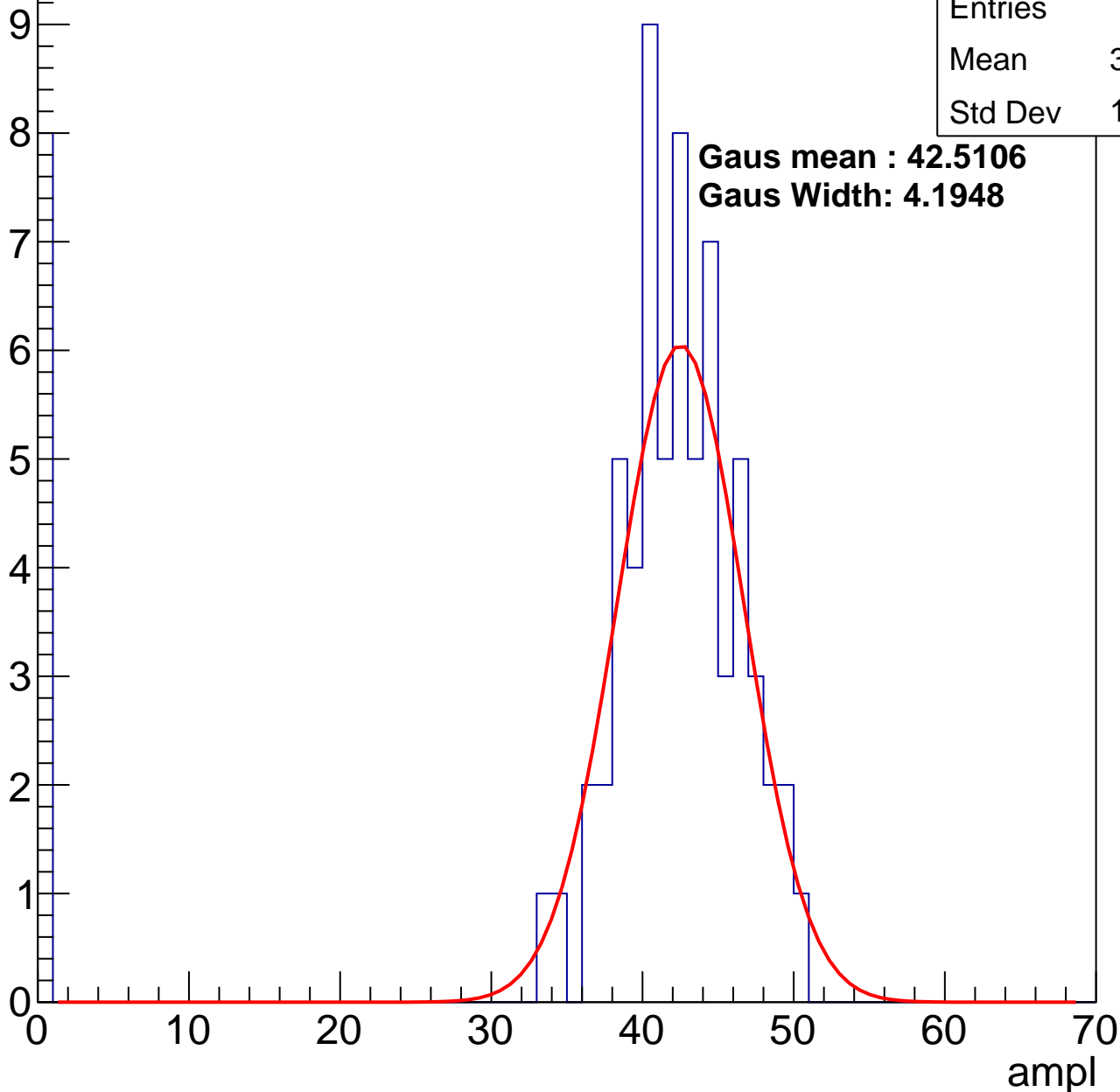
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	37.44
Std Dev	13.58

**Gaus mean : 42.5106**

**Gaus Width: 4.1948**

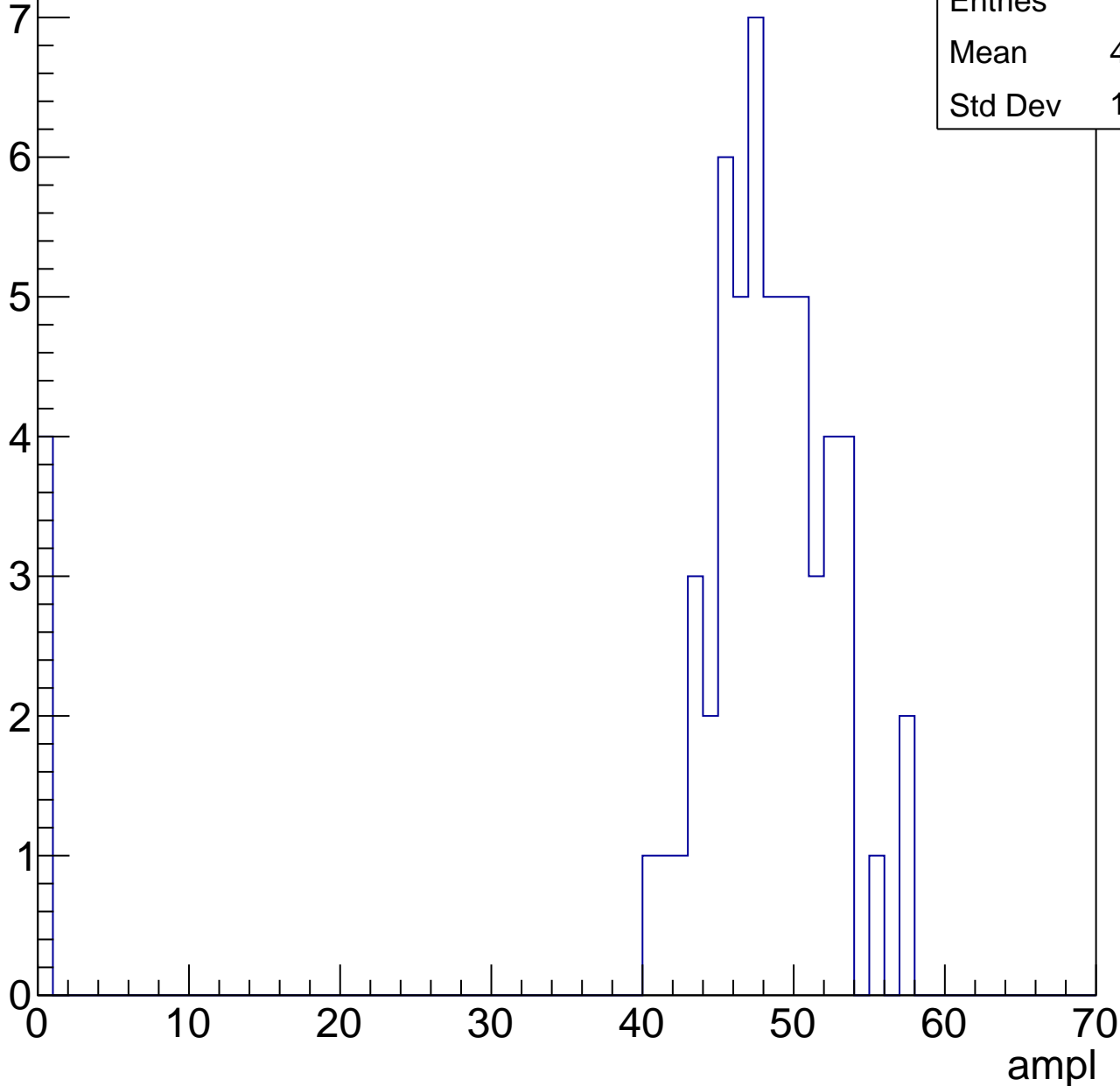


# B1L103S, U22-ch62, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	44.85
Std Dev	12.62

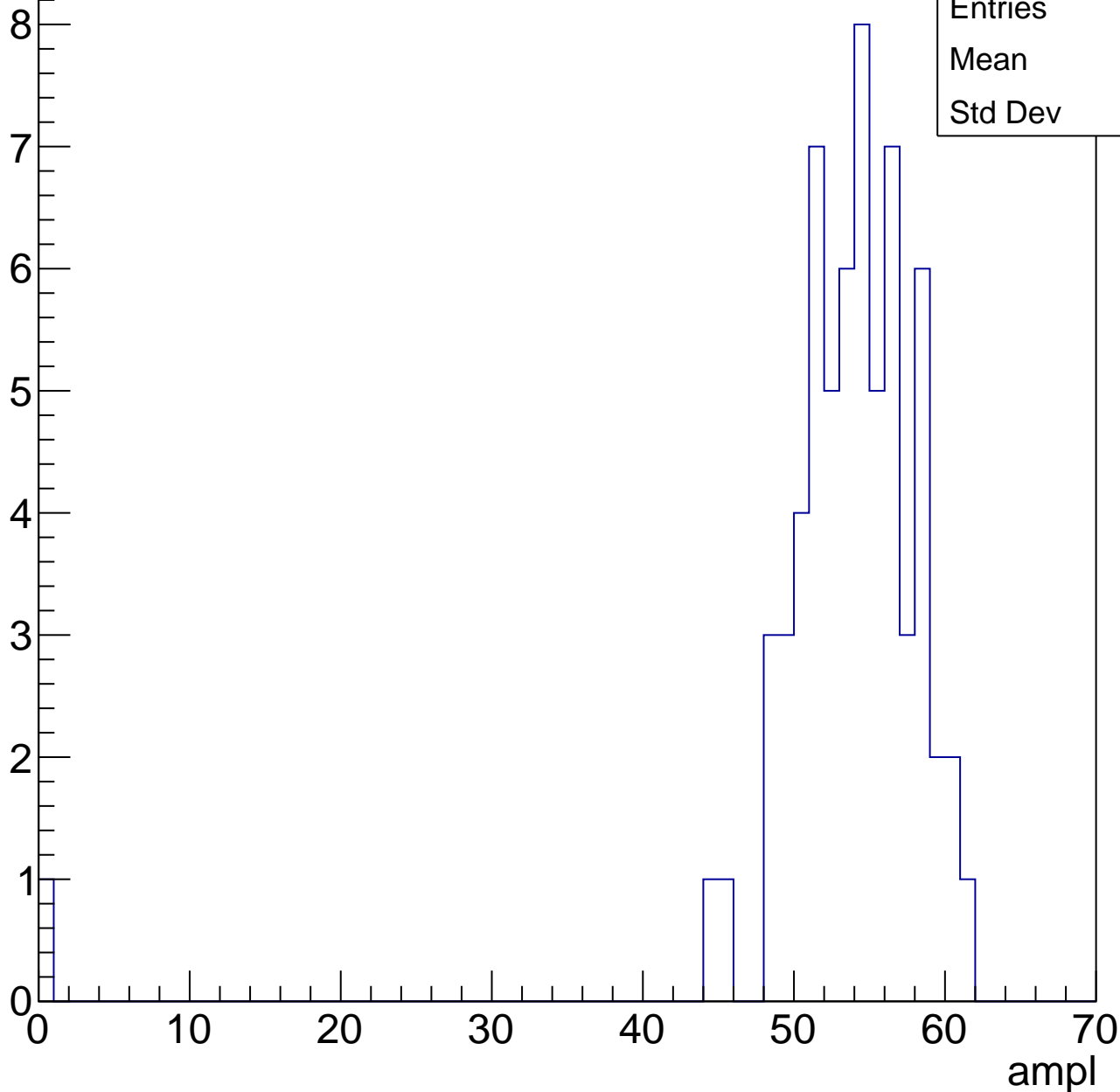


# B1L103S, U22-ch62, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	52.8
Std Dev	7.51



# B1L103S, U22-ch62, adc5

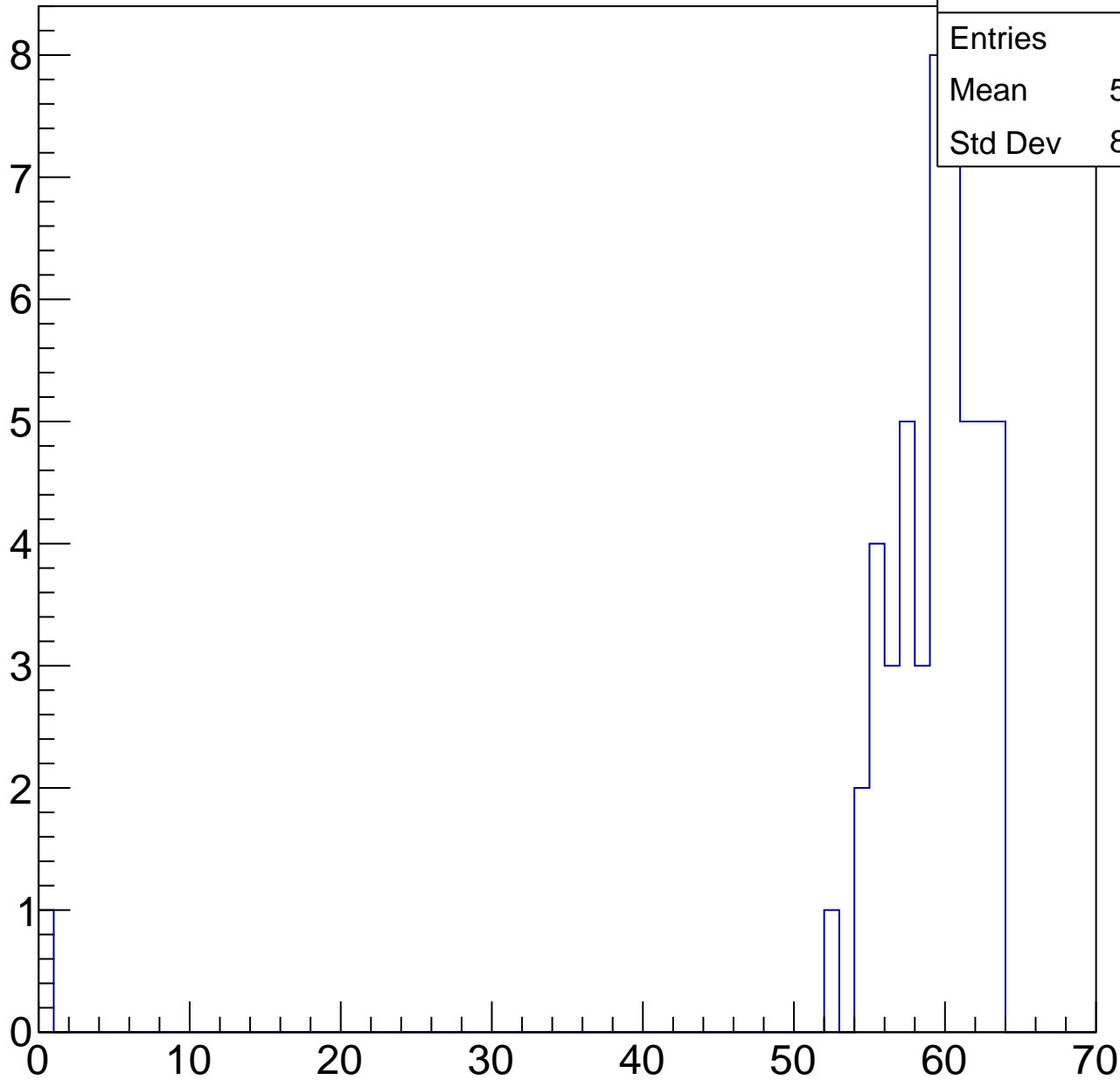
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	57.78
Std Dev	8.686

ampl

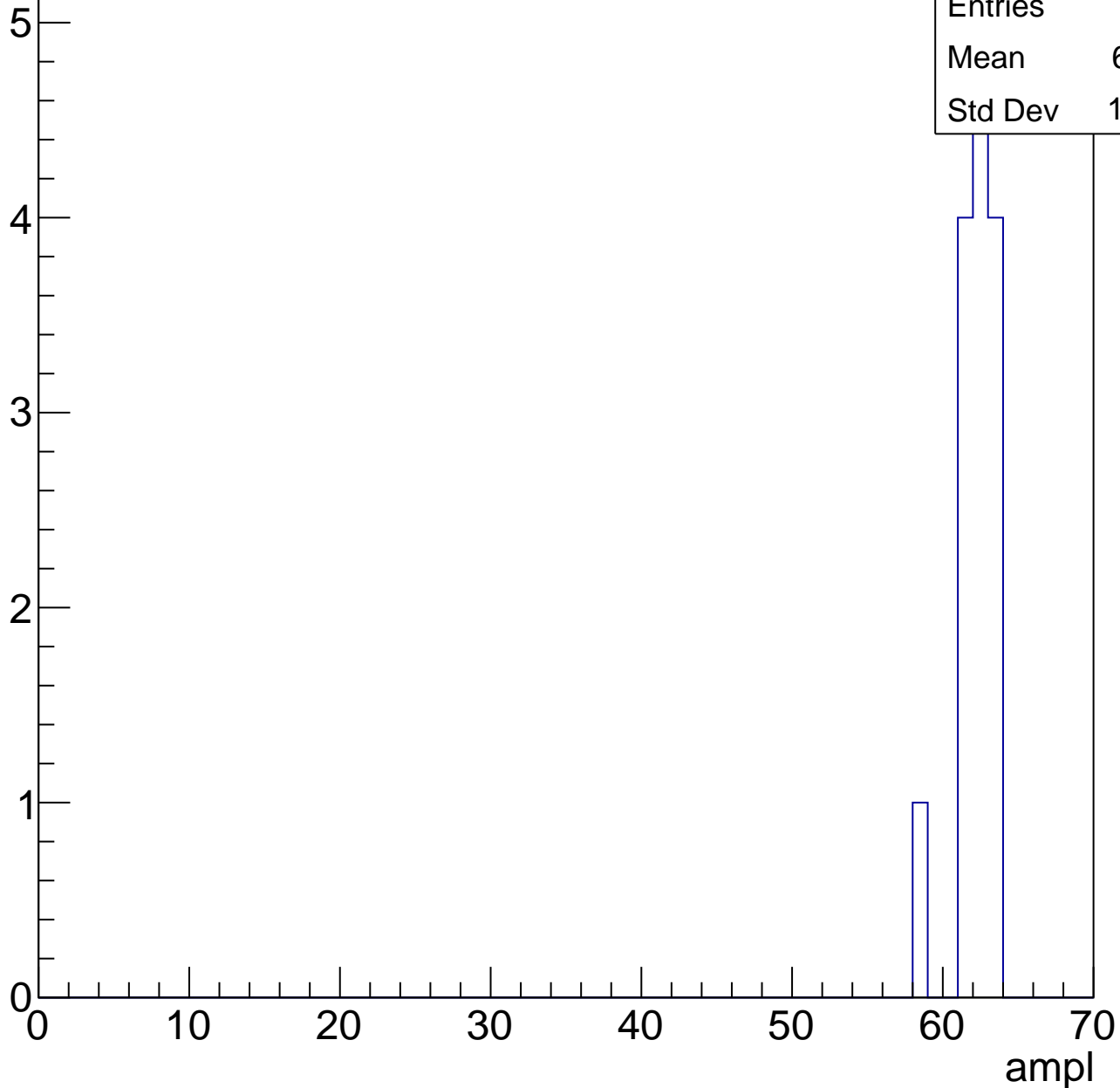


# B1L103S, U22-ch62, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	14
Mean	61.71
Std Dev	1.278



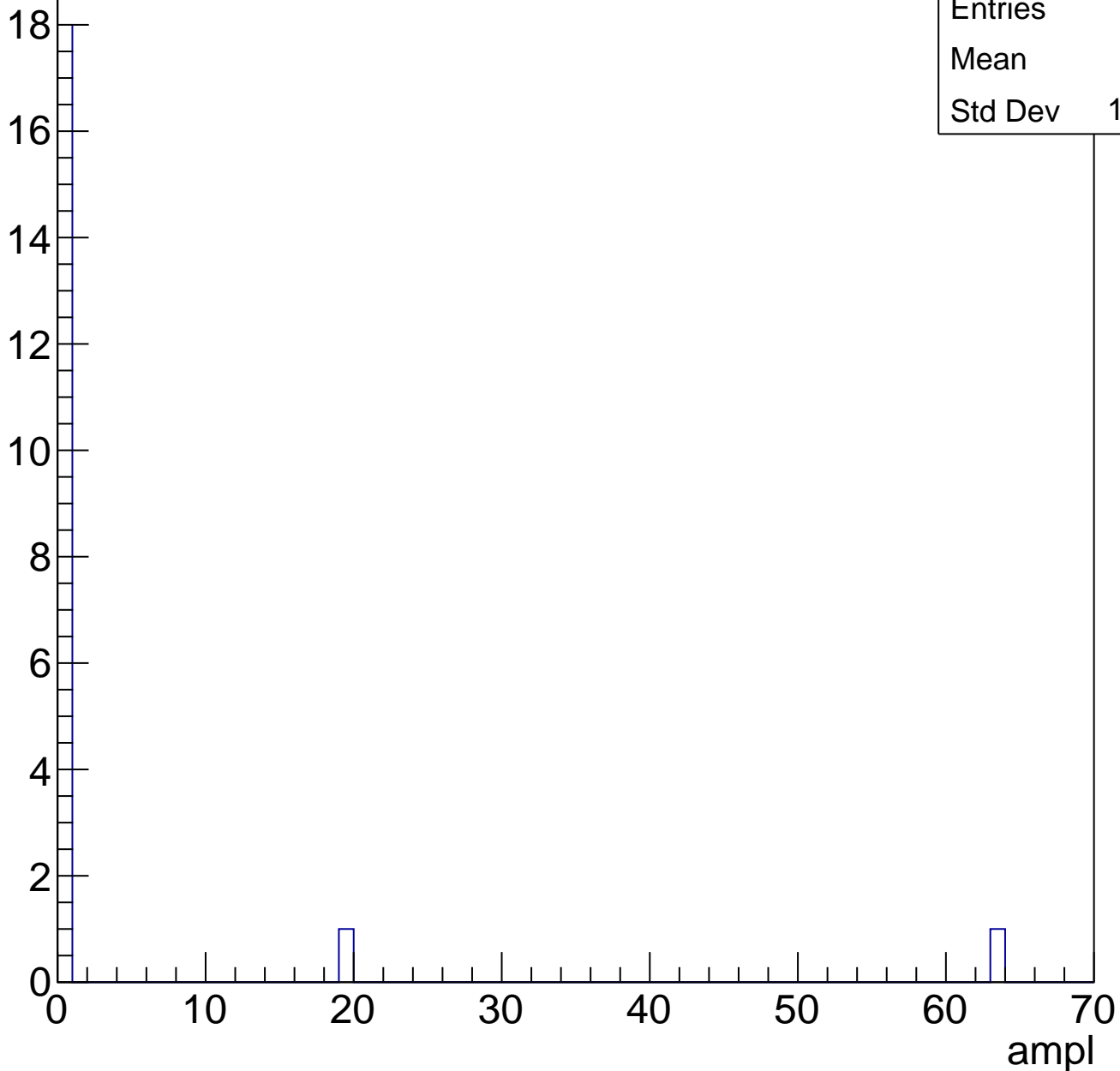


# B1L103S, U22-ch62, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	4.1
Std Dev	14.13

Entry



# B1L103S, U22-ch63, adc0

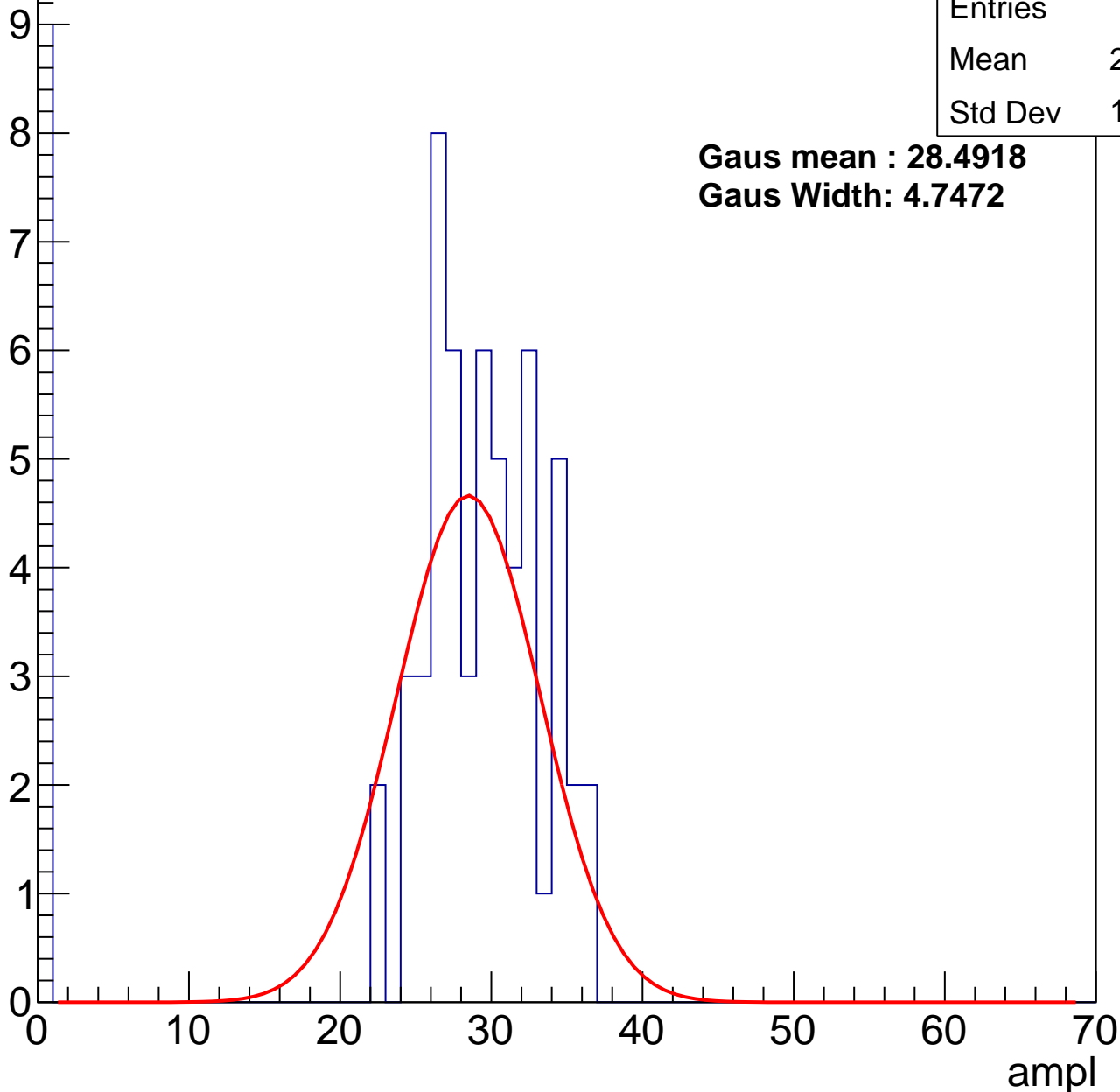
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	25.08
Std Dev	10.58

**Gaus mean : 28.4918**

**Gaus Width: 4.7472**



# B1L103S, U22-ch63, adc1

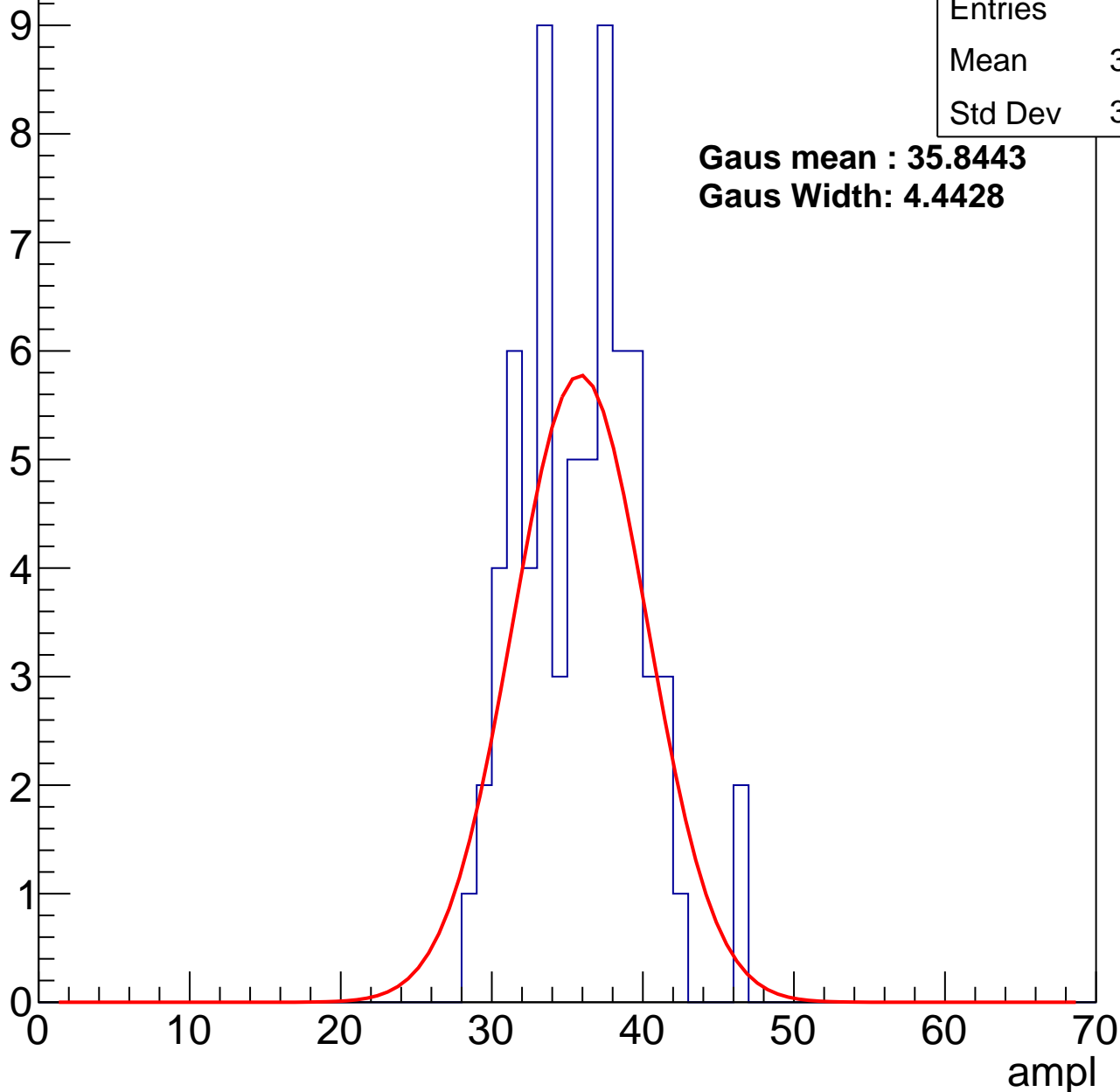
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	35.45
Std Dev	3.888

**Gaus mean : 35.8443**

**Gaus Width: 4.4428**



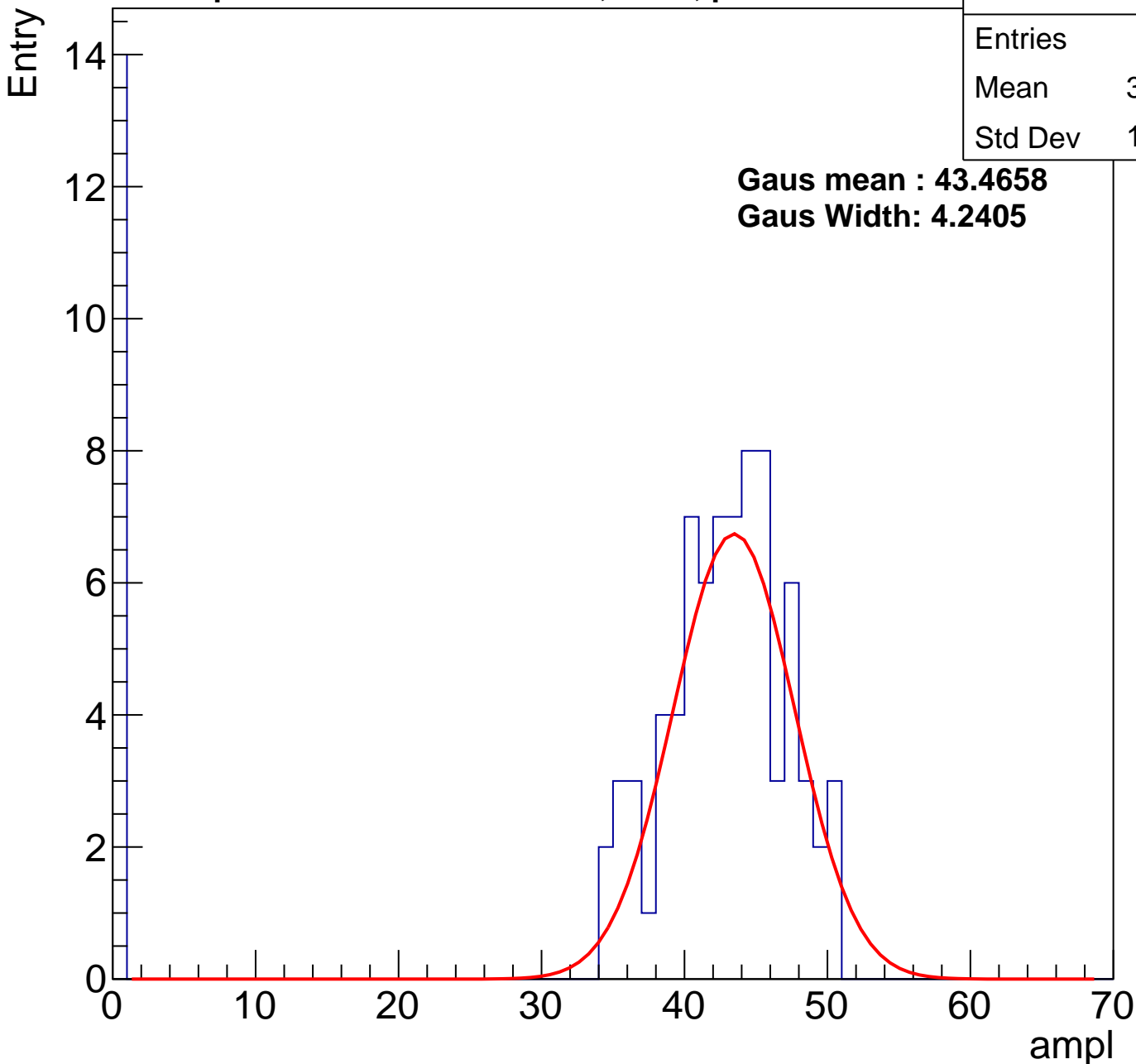
# B1L103S, U22-ch63, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	35.95
Std Dev	15.76

**Gaus mean : 43.4658**

**Gaus Width: 4.2405**

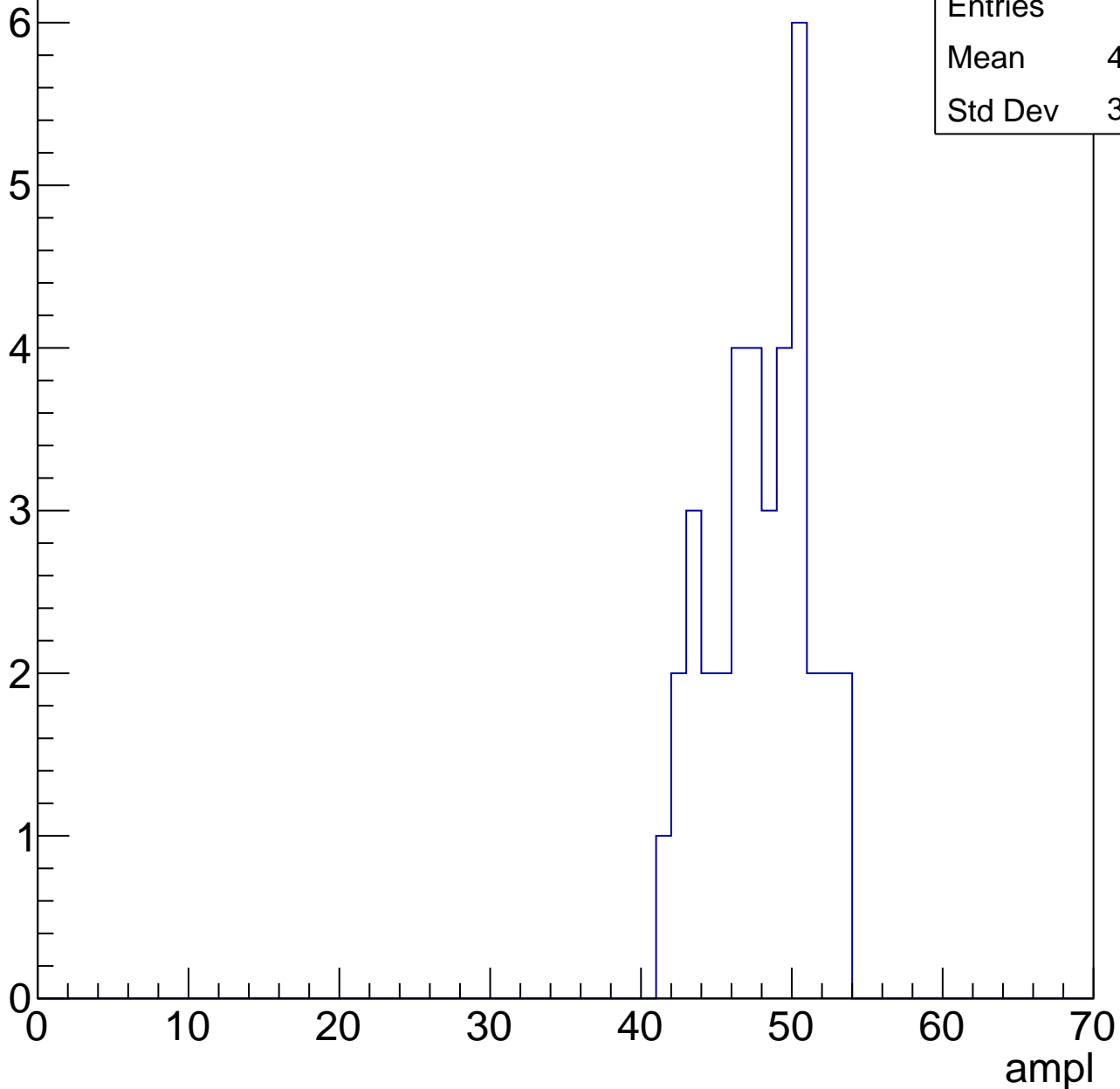


# B1L103S, U22-ch63, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	37
Mean	47.46
Std Dev	3.218



# B1L103S, U22-ch63, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	54.41
Std Dev	3.509

Entry

10

8

6

4

2

0

0

10

20

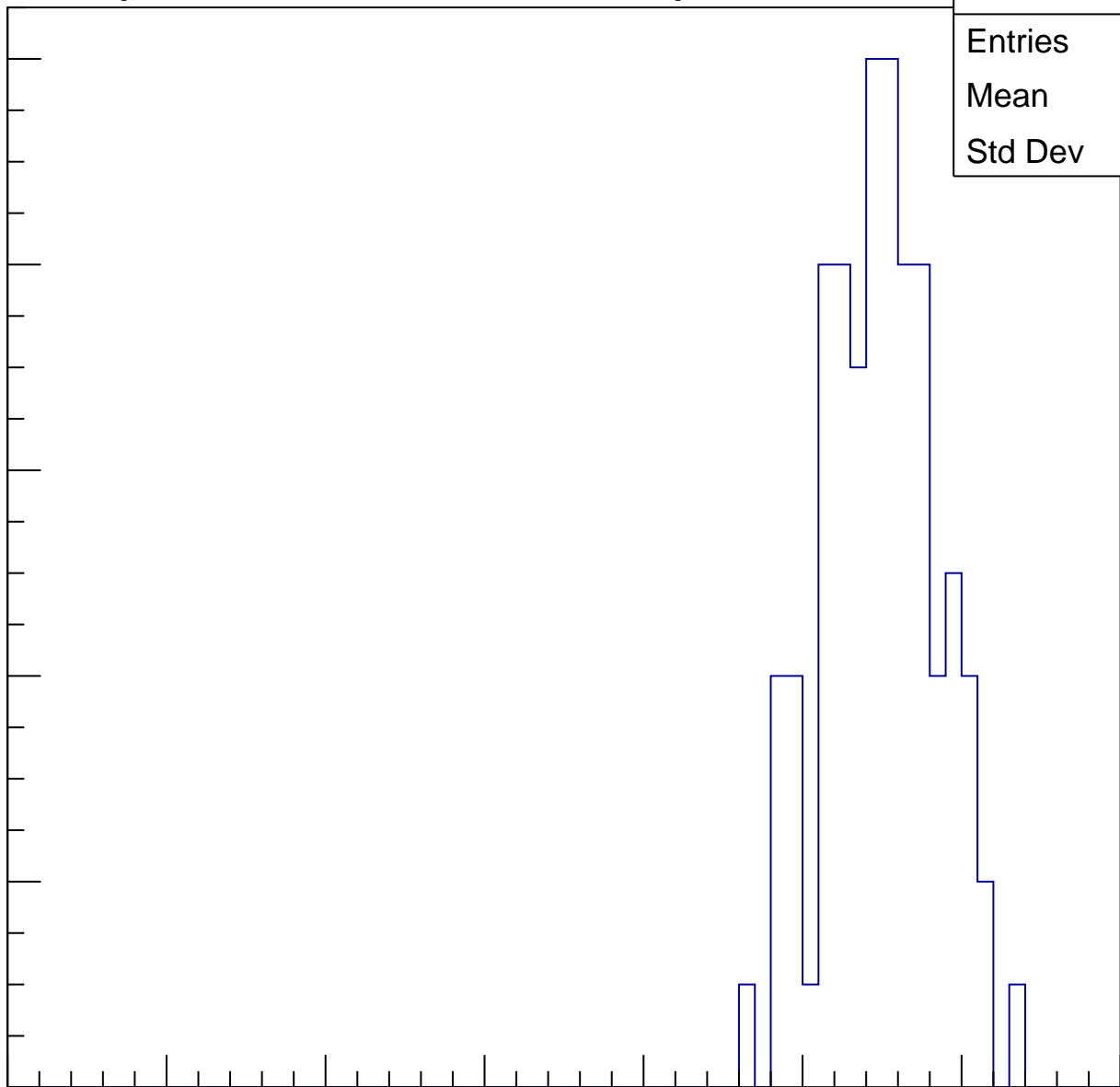
30

40

50

60

ampl

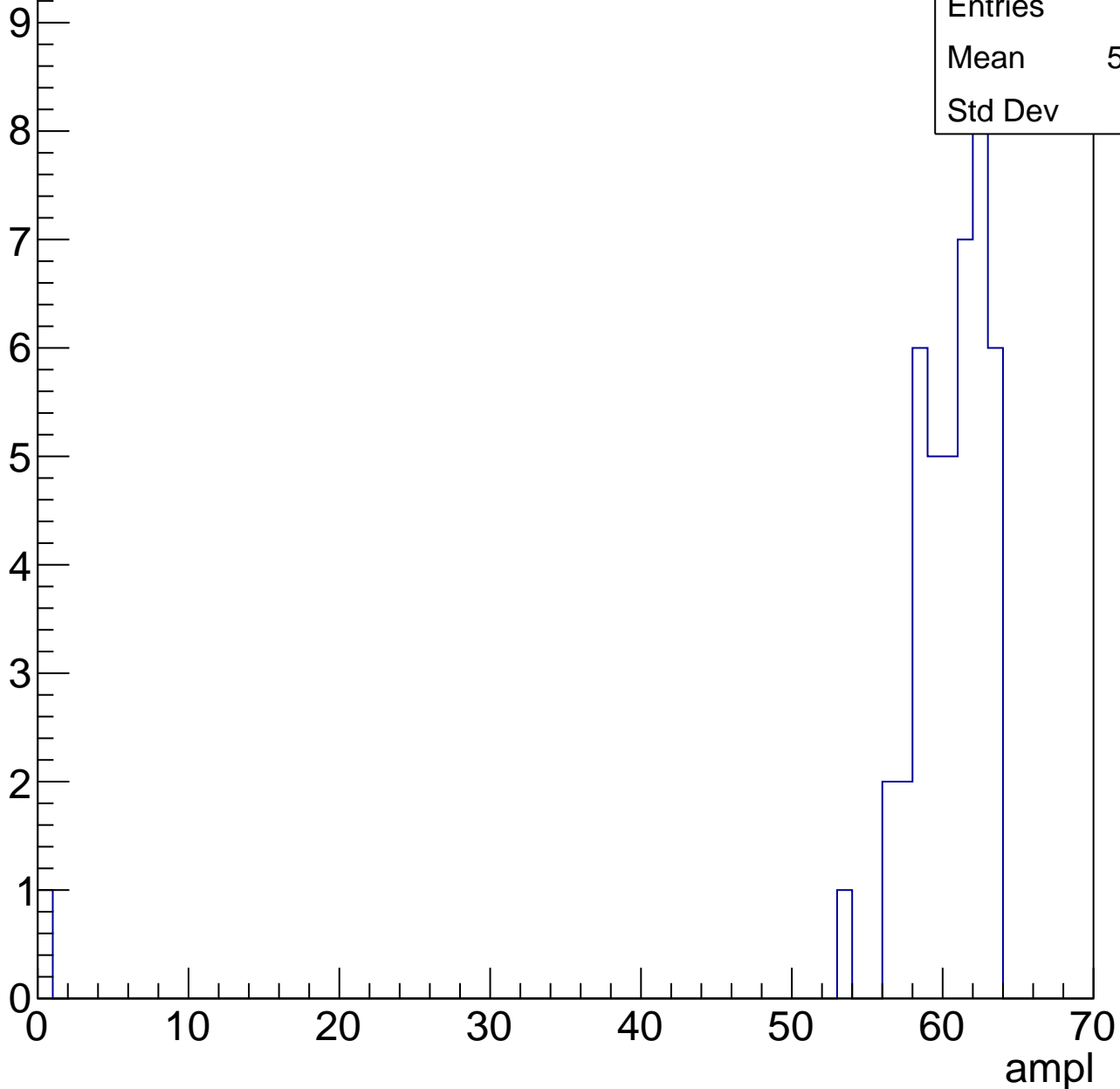


# B1L103S, U22-ch63, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	44
Mean	58.75
Std Dev	9.24



# B1L103S, U22-ch63, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



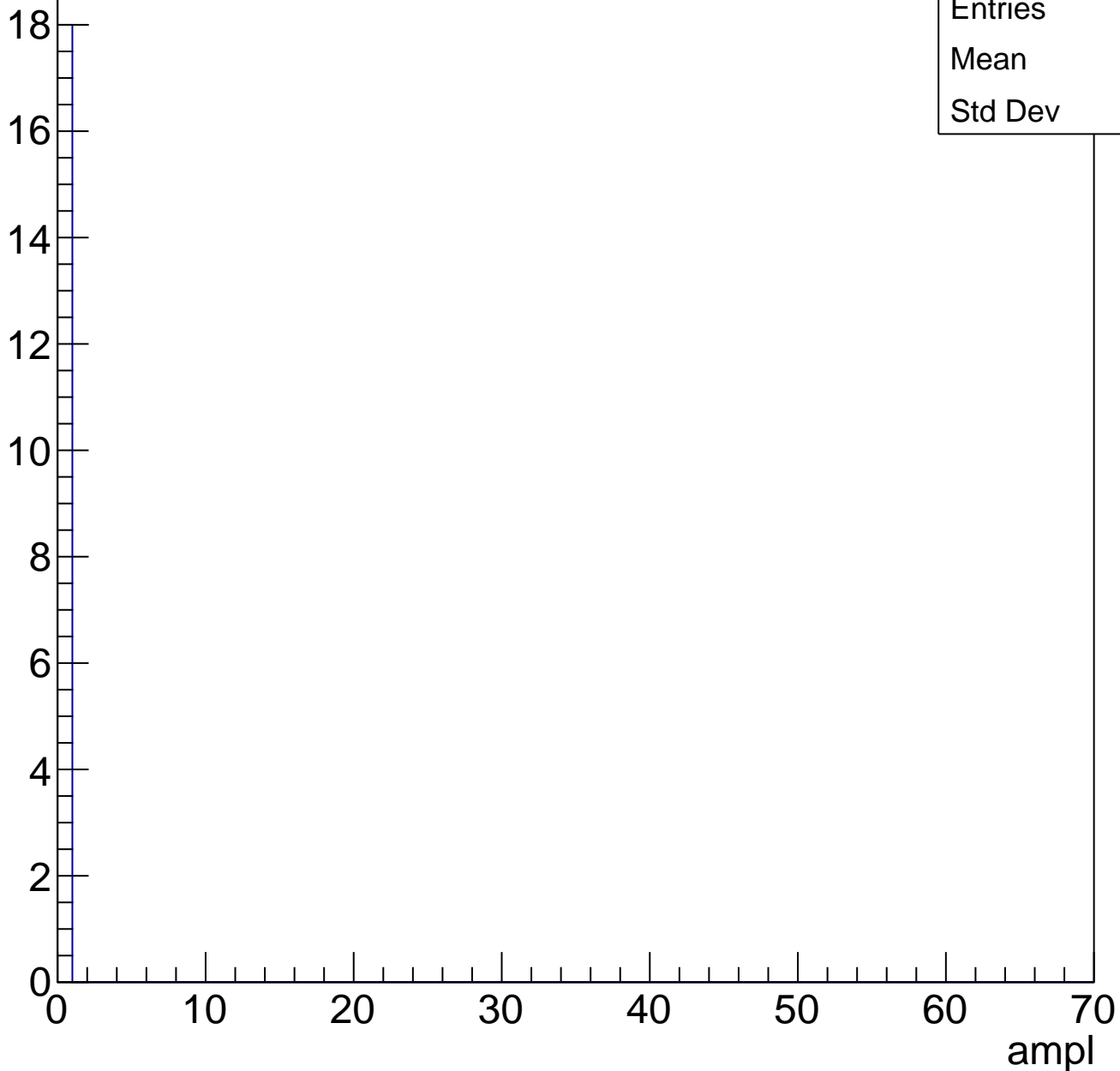


# B1L103S, U22-ch63, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	18
Mean	0
Std Dev	0

Entry



# B1L103S, U22-ch64, adc0

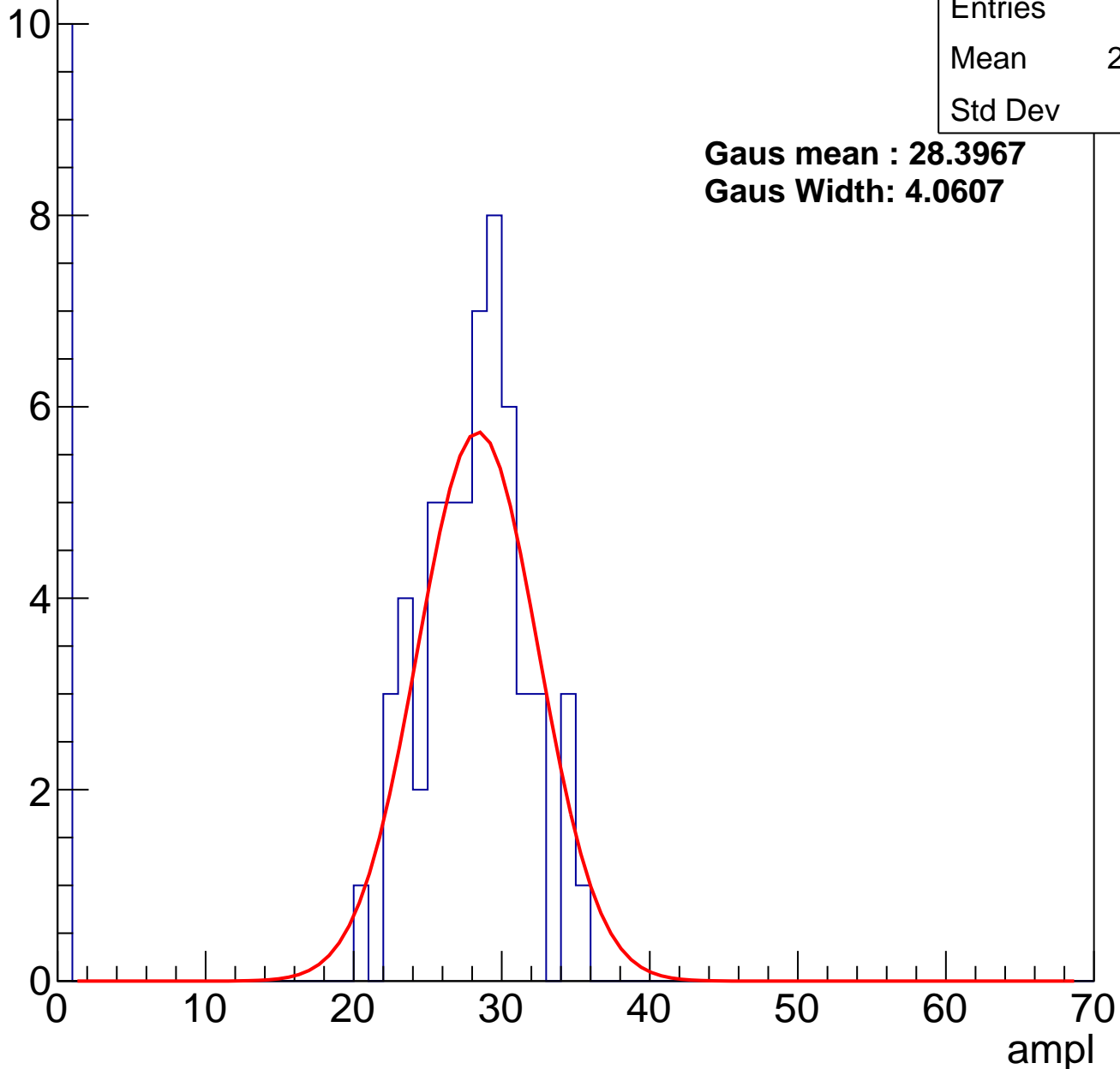
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	66
Mean	23.48
Std Dev	10.4

**Gaus mean : 28.3967**

**Gaus Width: 4.0607**

Entry



# B1L103S, U22-ch64, adc1

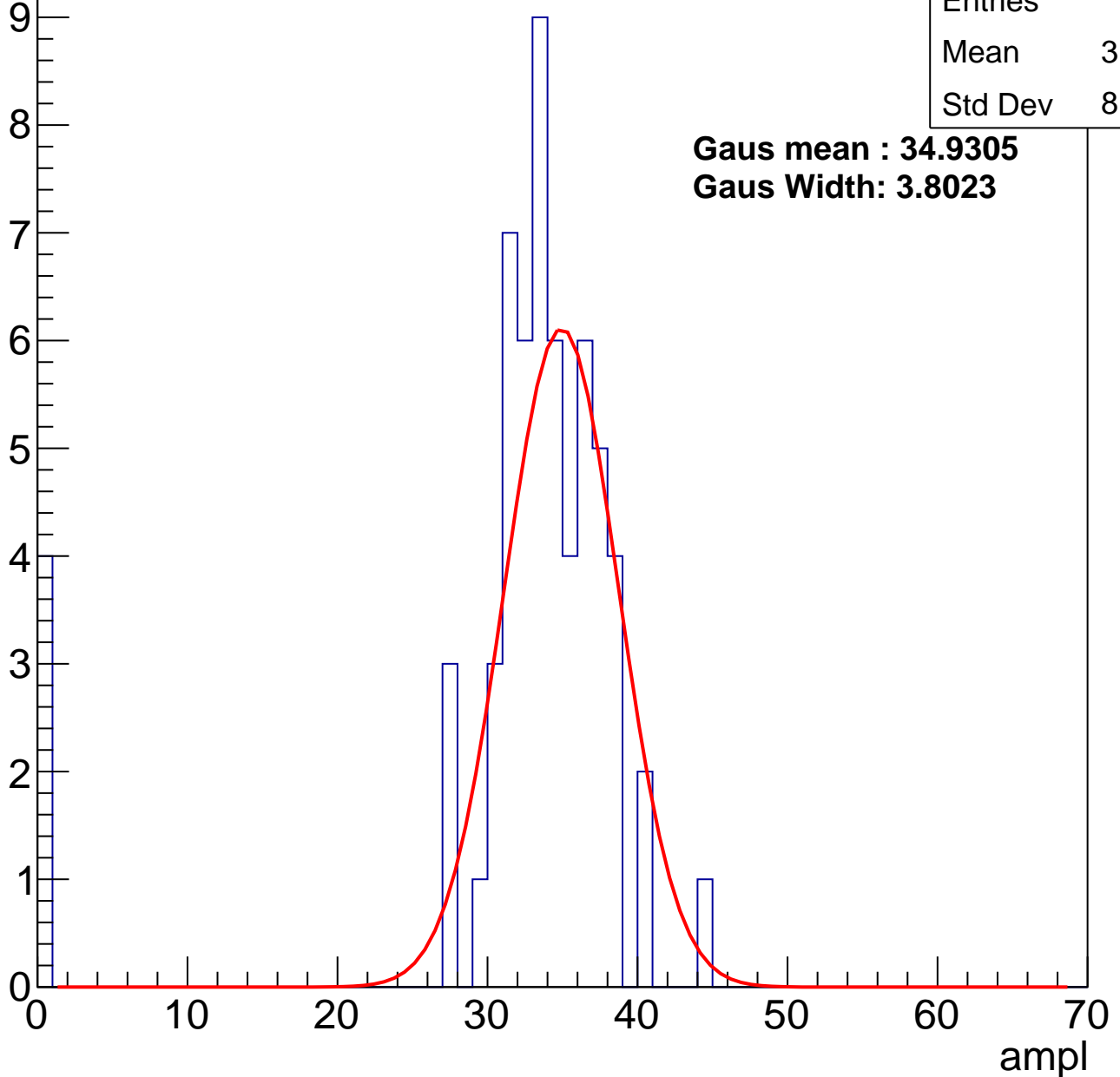
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	31.59
Std Dev	8.958

**Gaus mean : 34.9305**

**Gaus Width: 3.8023**



# B1L103S, U22-ch64, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	98
Mean	35.39
Std Dev	14.27

**Gaus mean : 41.0207**

**Gaus Width: 3.8662**

Entry

12

10

8

6

4

2

0

0

10

20

30

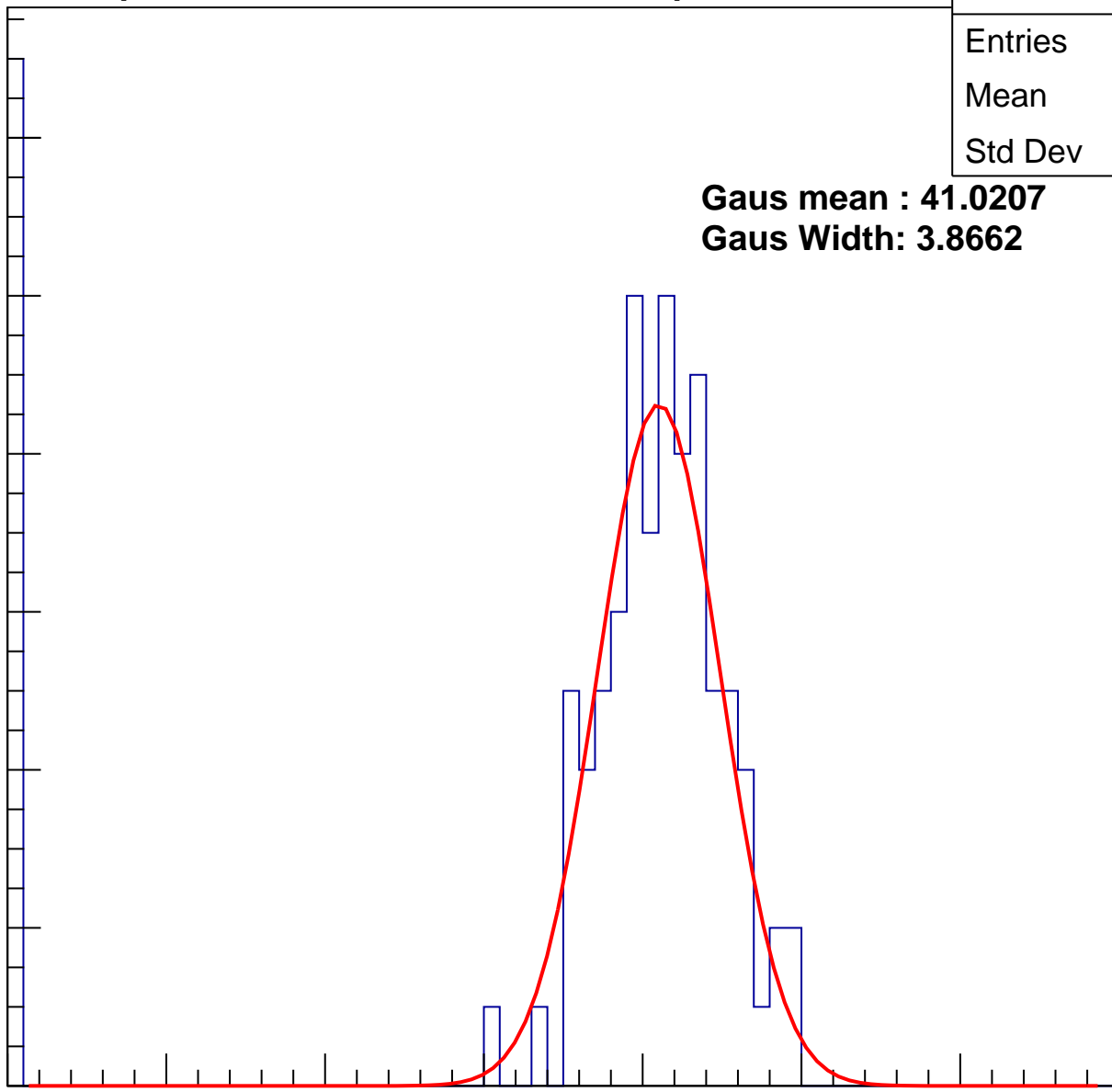
40

50

60

70

ampl

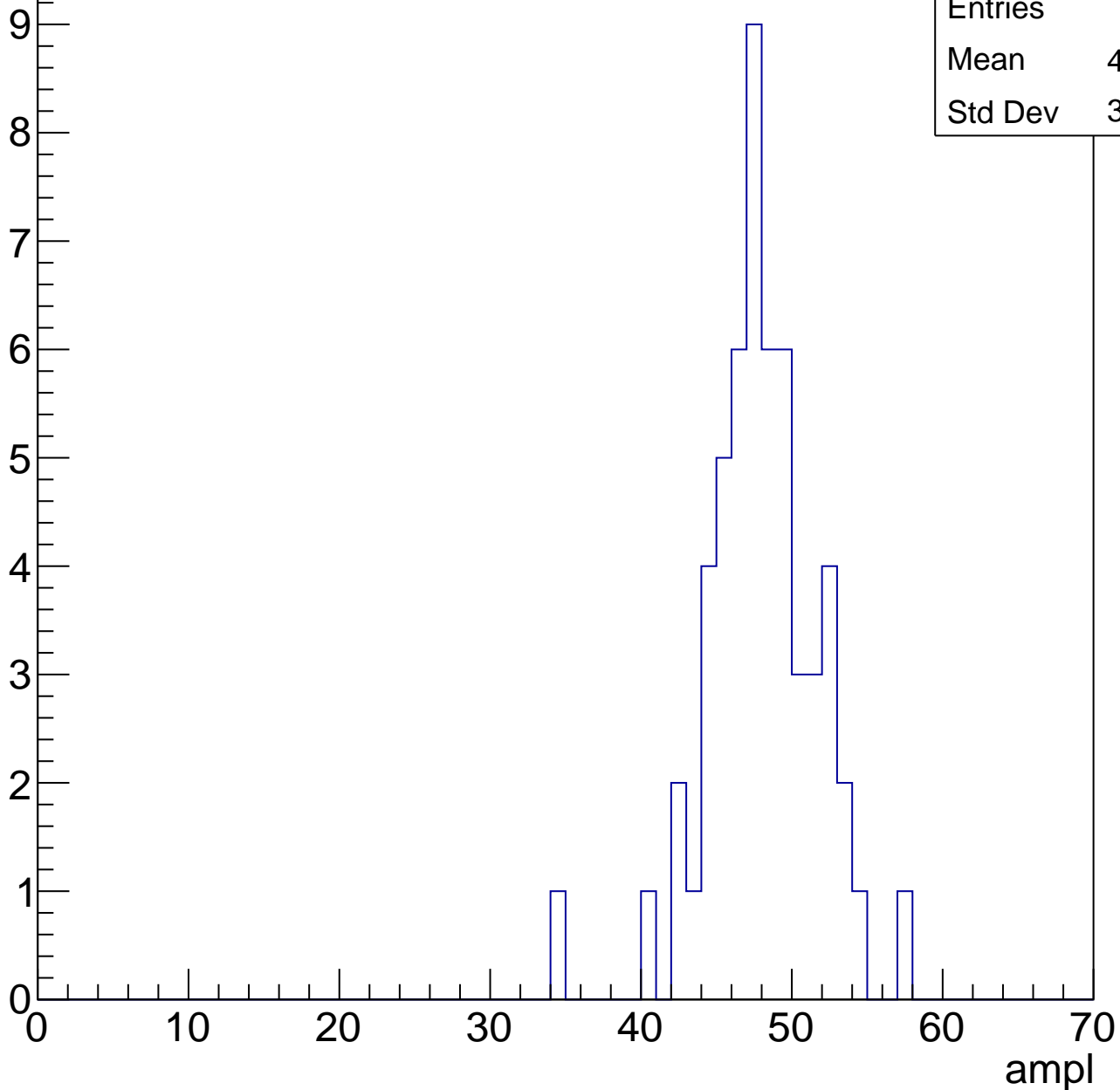


# B1L103S, U22-ch64, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	47.47
Std Dev	3.717

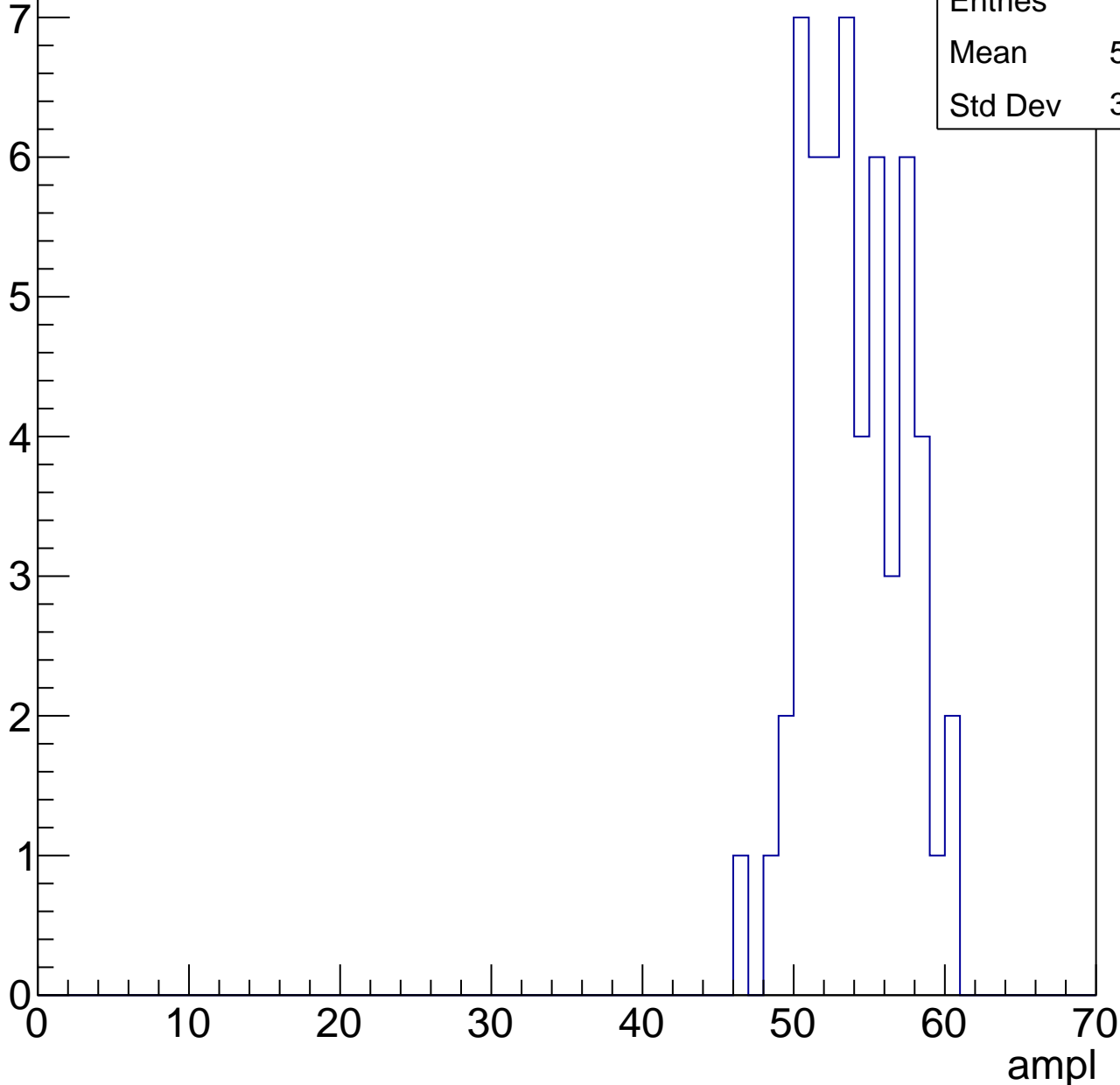


# B1L103S, U22-ch64, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	53.54
Std Dev	3.185

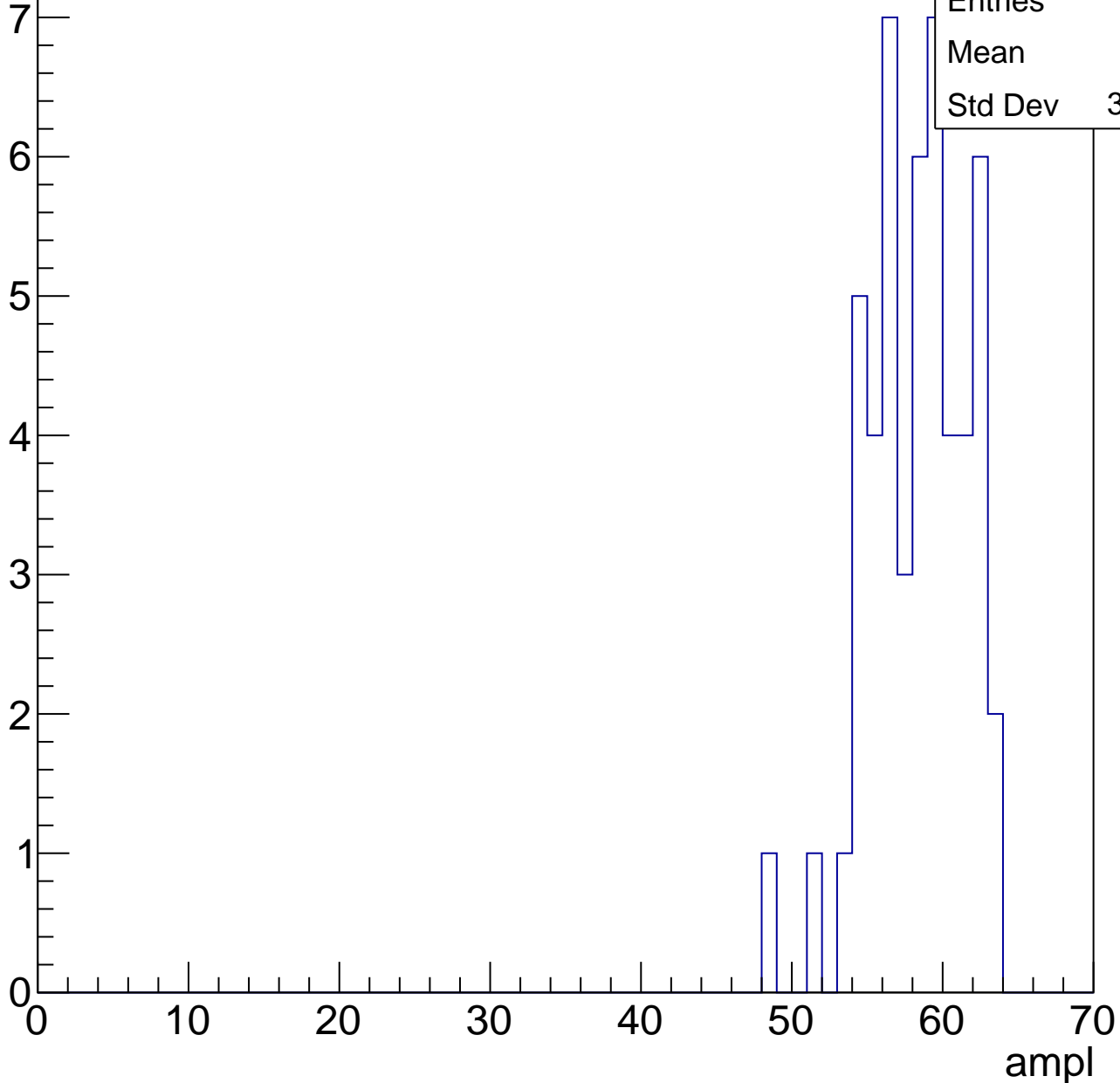


# B1L103S, U22-ch64, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.8
Std Dev	3.206

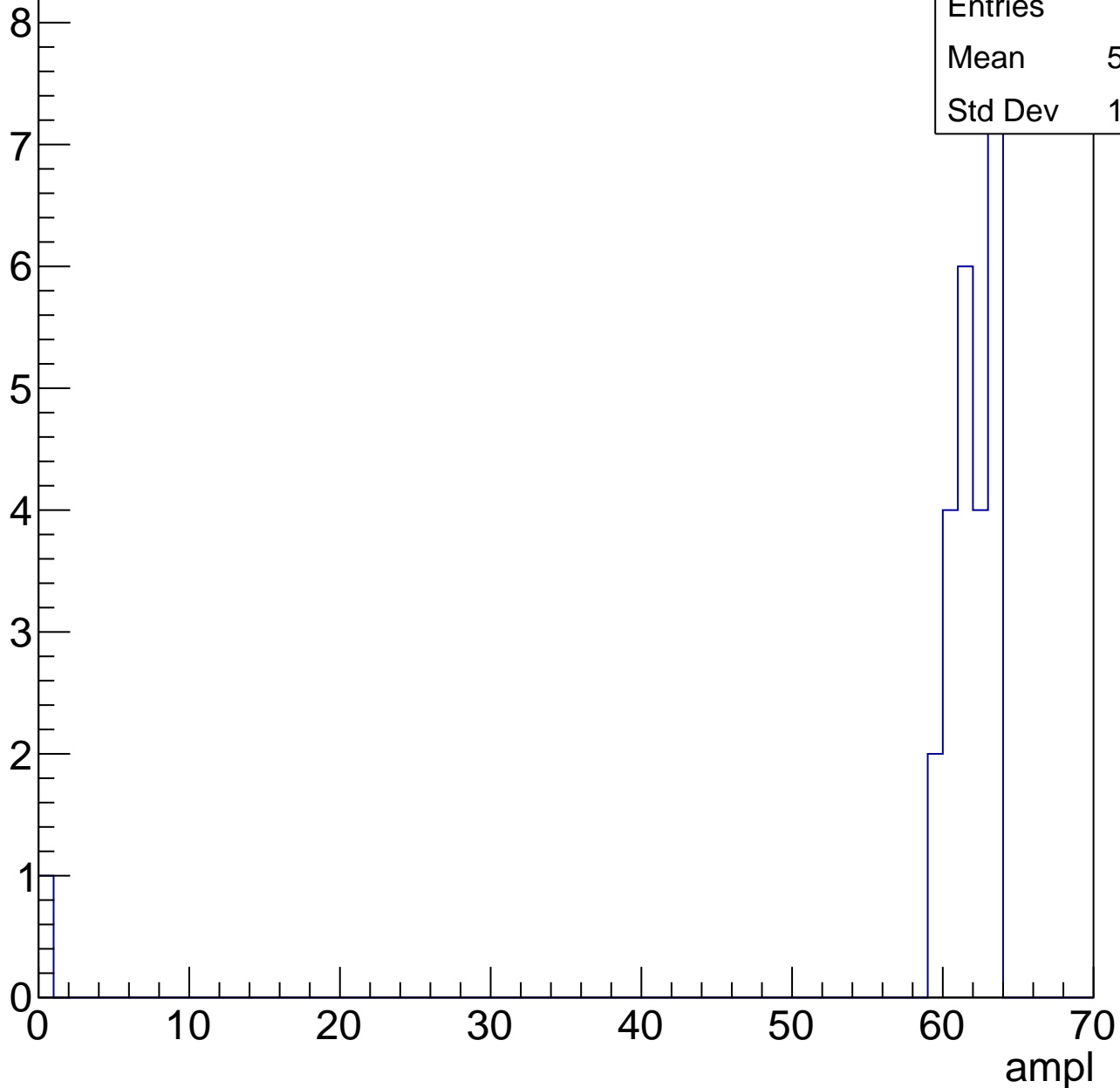


# B1L103S, U22-ch64, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	25
Mean	59.04
Std Dev	12.12





# B1L103S, U22-ch64, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch65, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	103
Mean	17.04
Std Dev	13.3

**Gaus mean : 27.5009**

**Gaus Width: 3.8050**

Entry

35

30

25

20

15

10

5

0

0

10

20

30

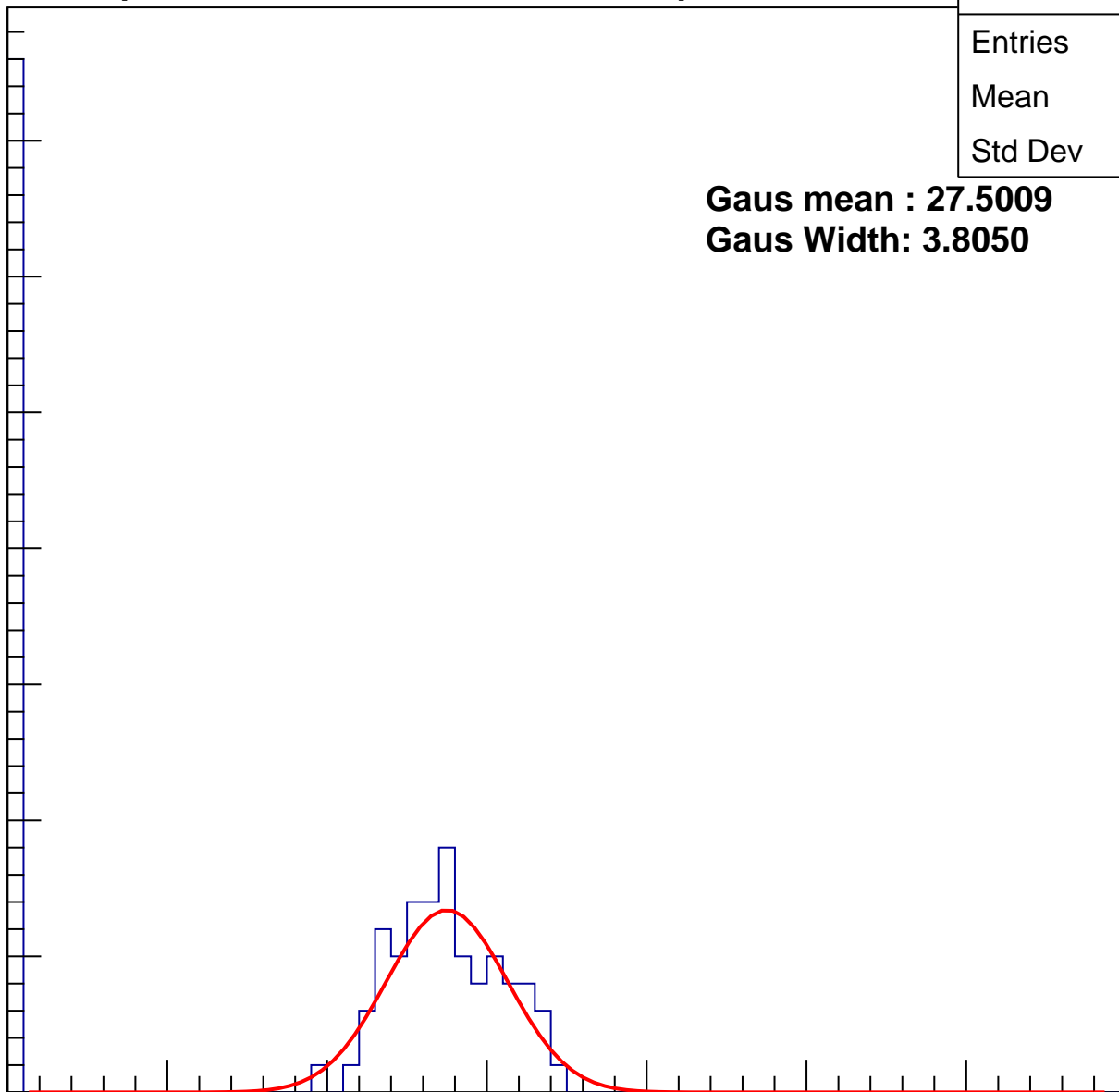
40

50

60

70

ampl

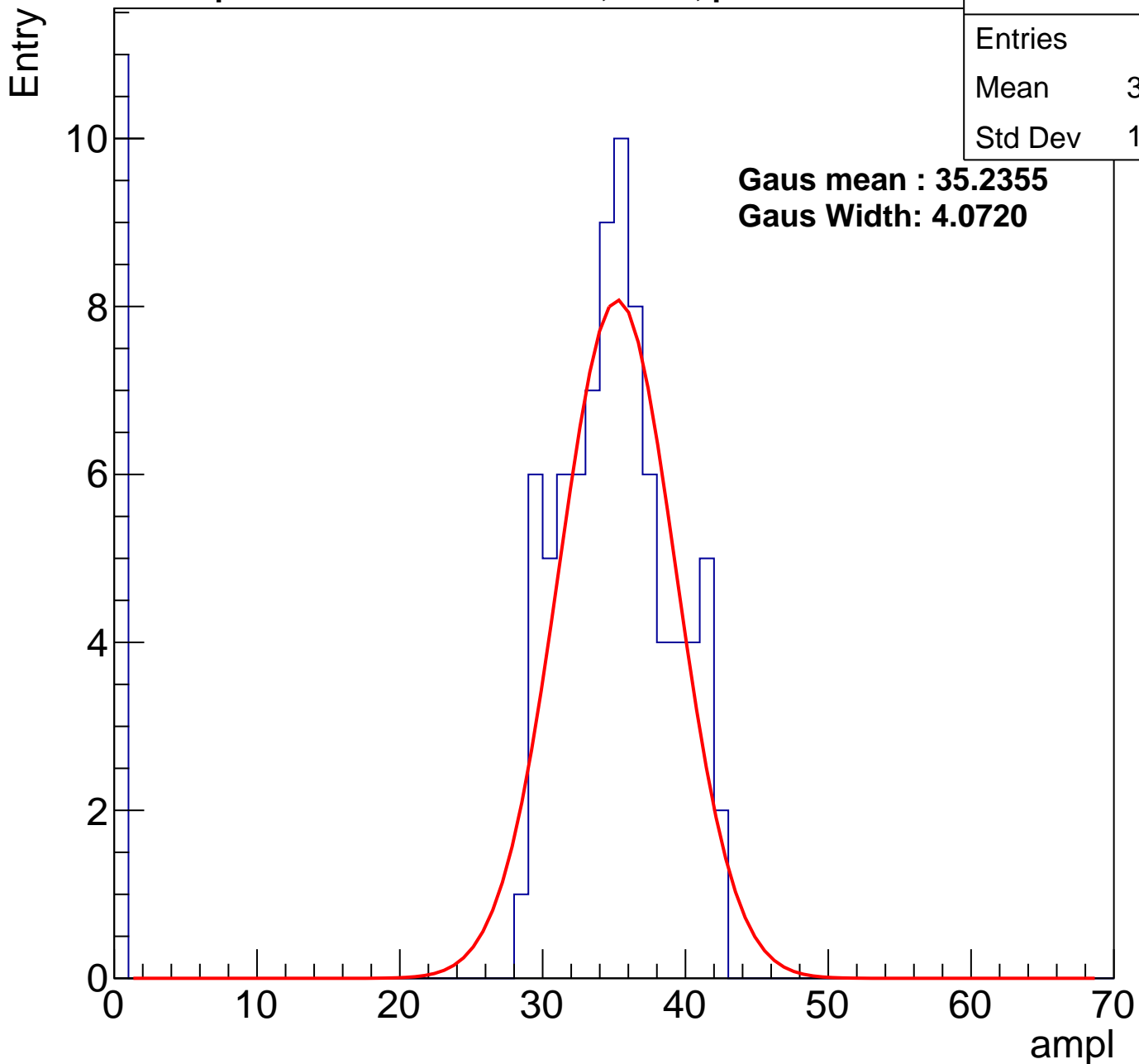


# B1L103S, U22-ch65, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	94
Mean	30.68
Std Dev	11.67

**Gaus mean : 35.2355**  
**Gaus Width: 4.0720**



# B1L103S, U22-ch65, adc2

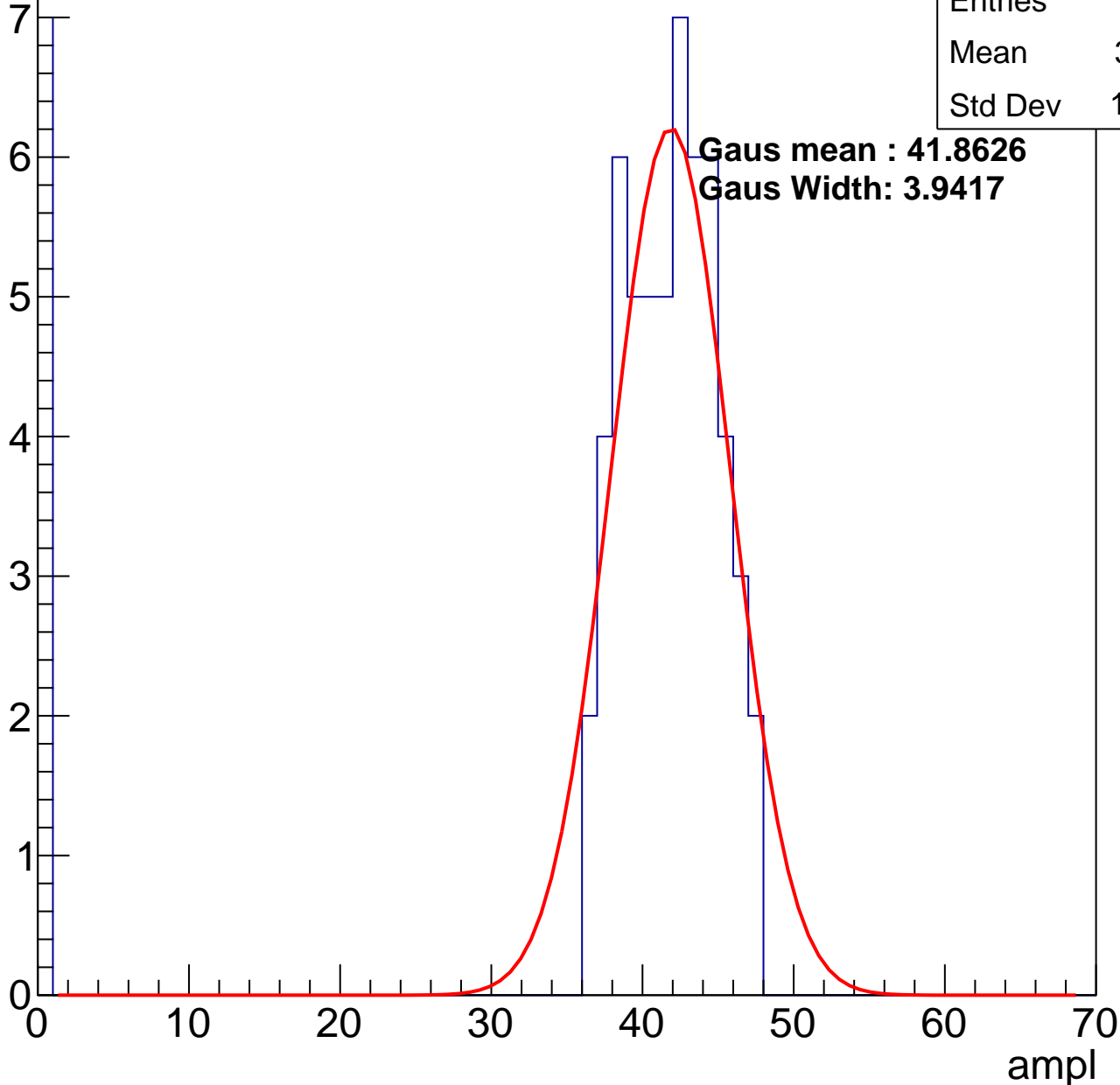
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	36.71
Std Dev	13.39

**Gaus mean : 41.8626**

**Gaus Width: 3.9417**

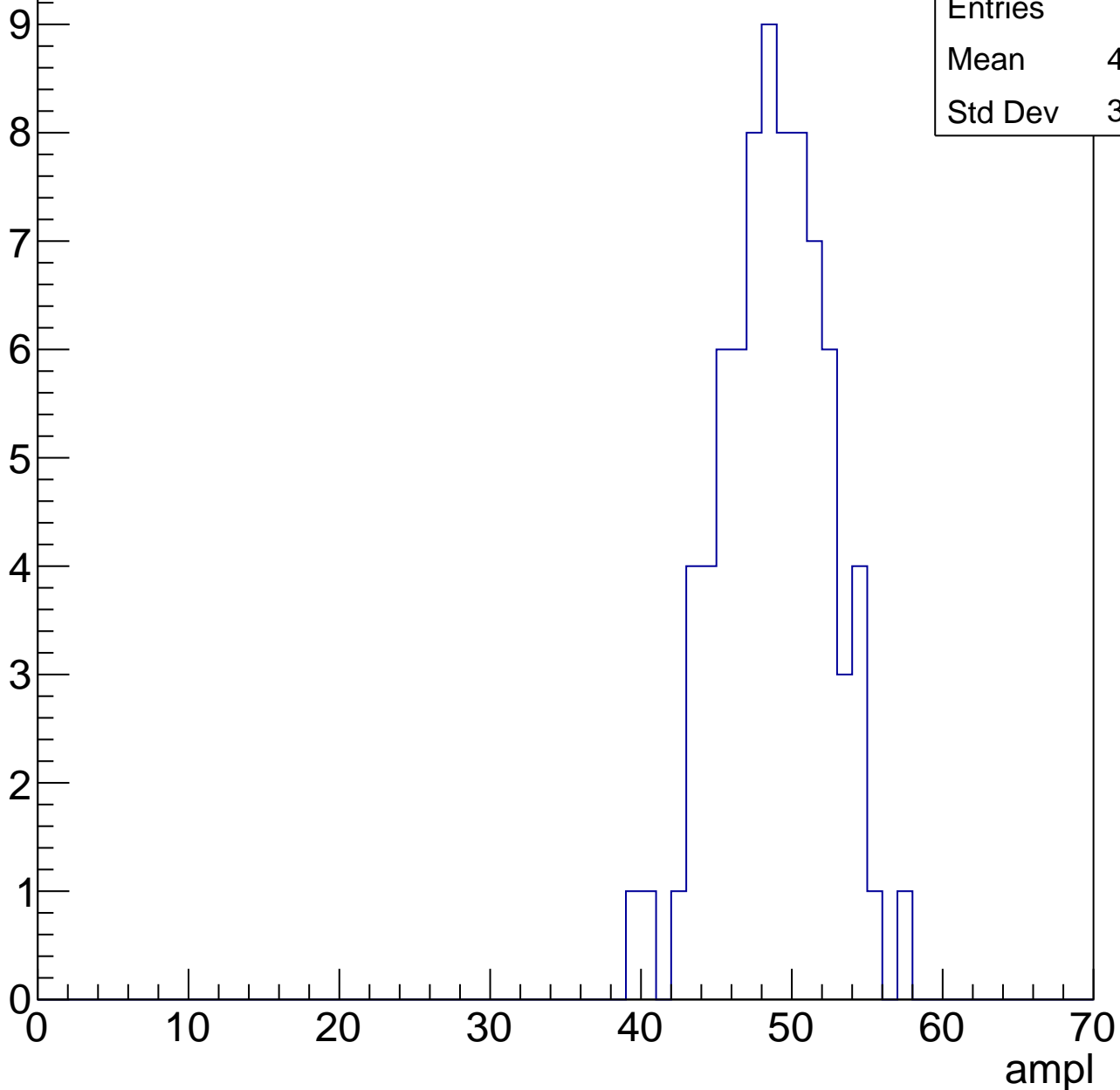


# B1L103S, U22-ch65, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	48.35
Std Dev	3.526

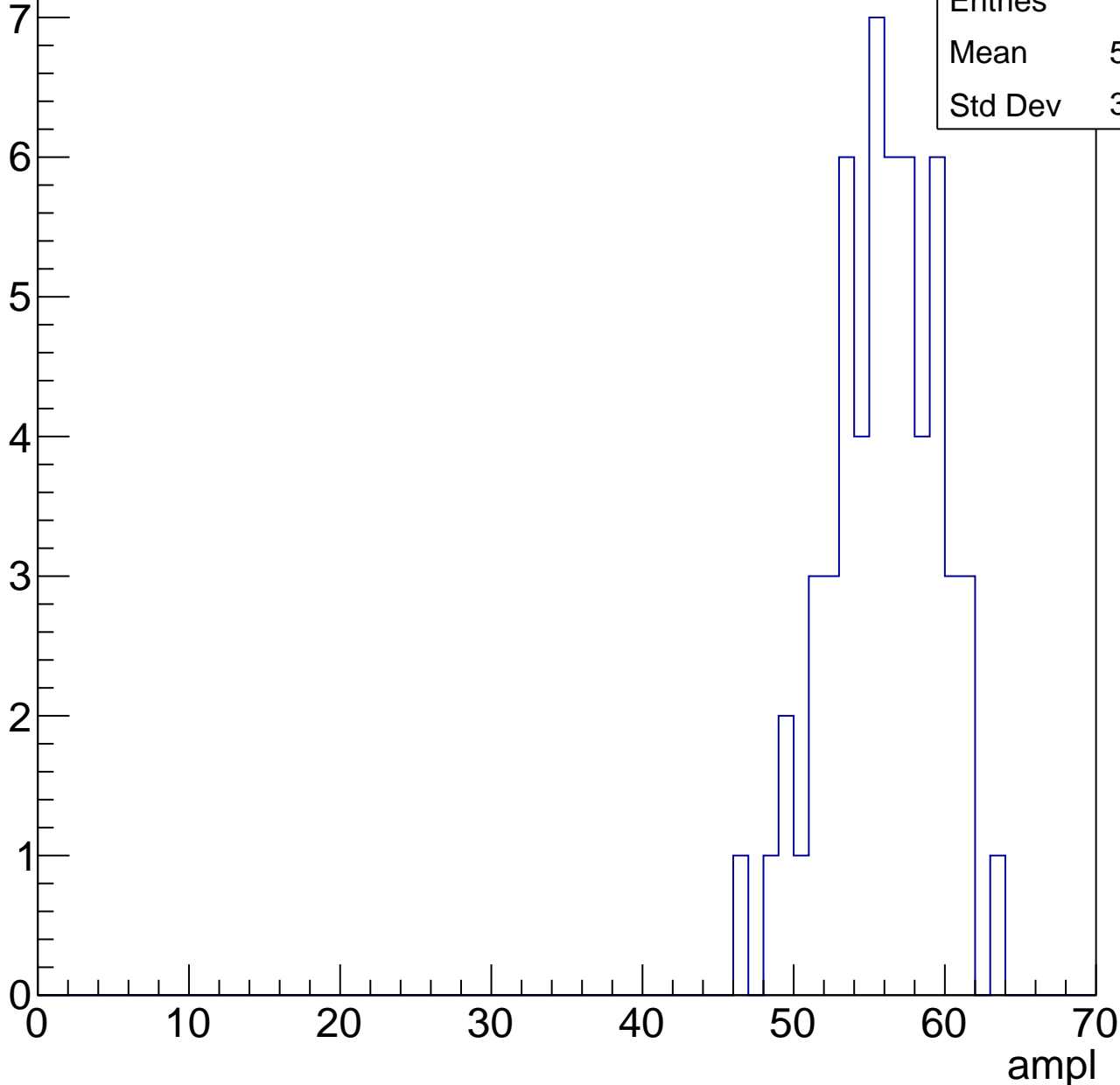


# B1L103S, U22-ch65, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	55.44
Std Dev	3.574

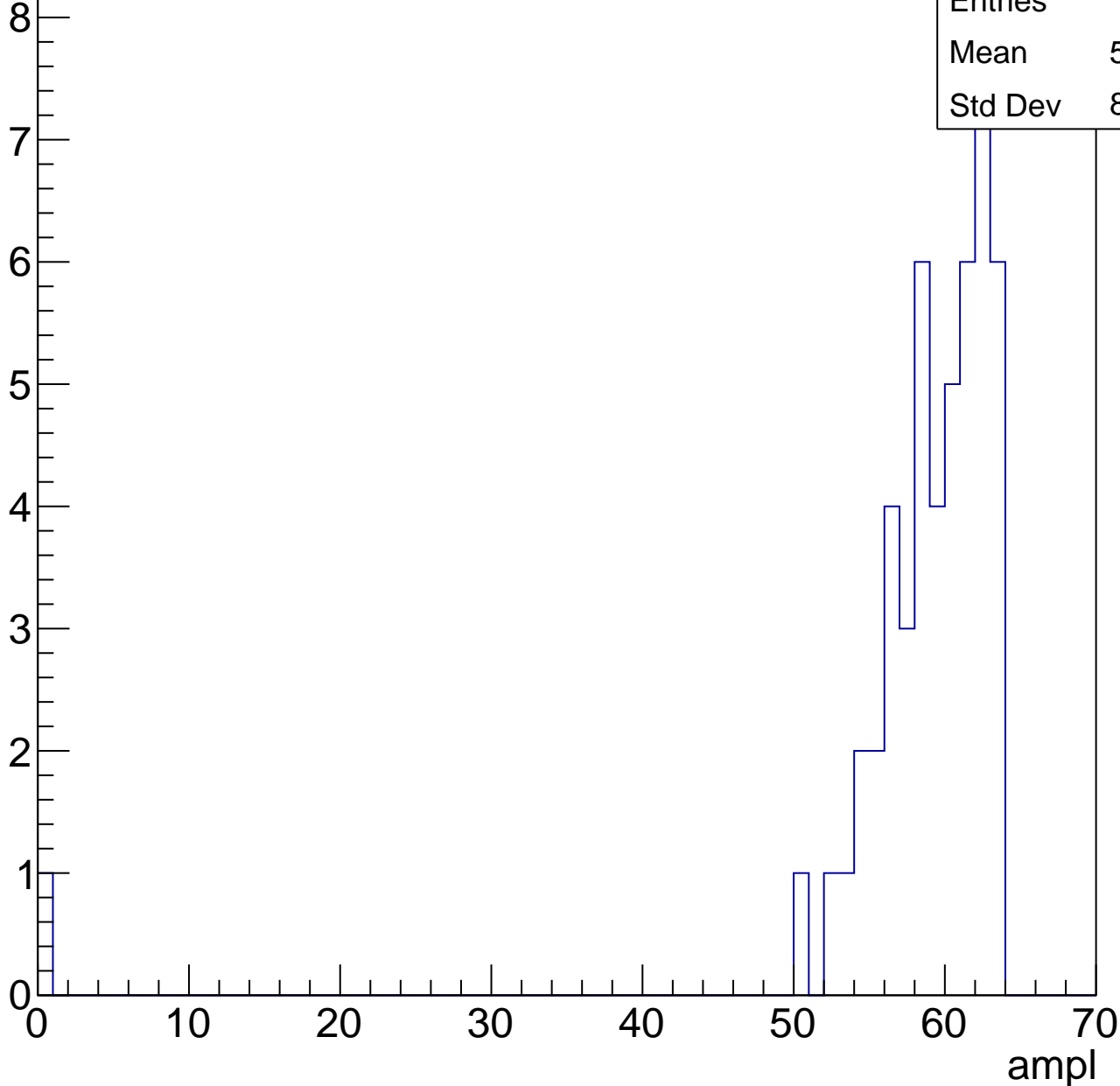


# B1L103S, U22-ch65, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

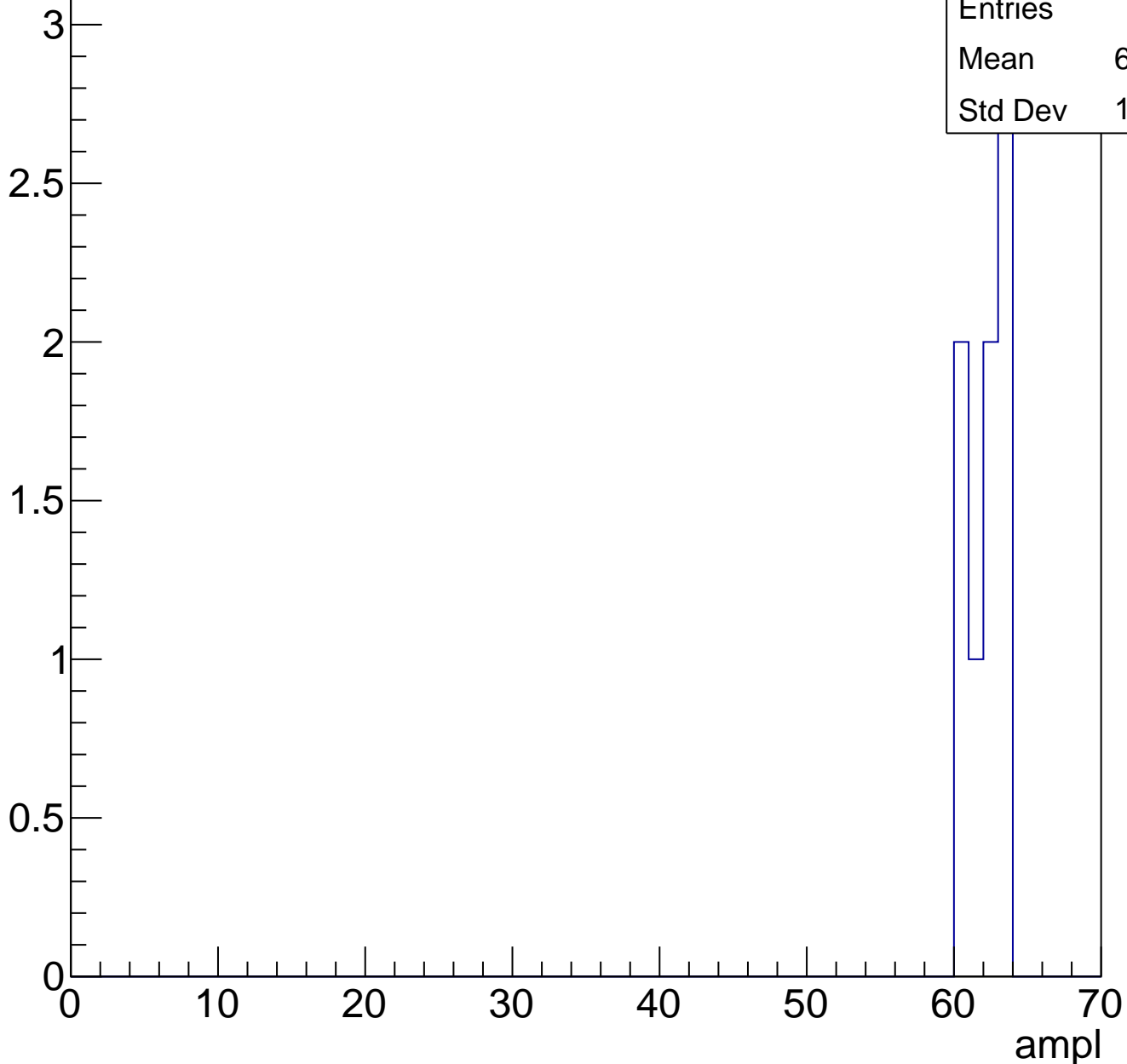
Entries	50
Mean	57.84
Std Dev	8.846



# B1L103S, U22-ch65, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch65, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch66, adc0

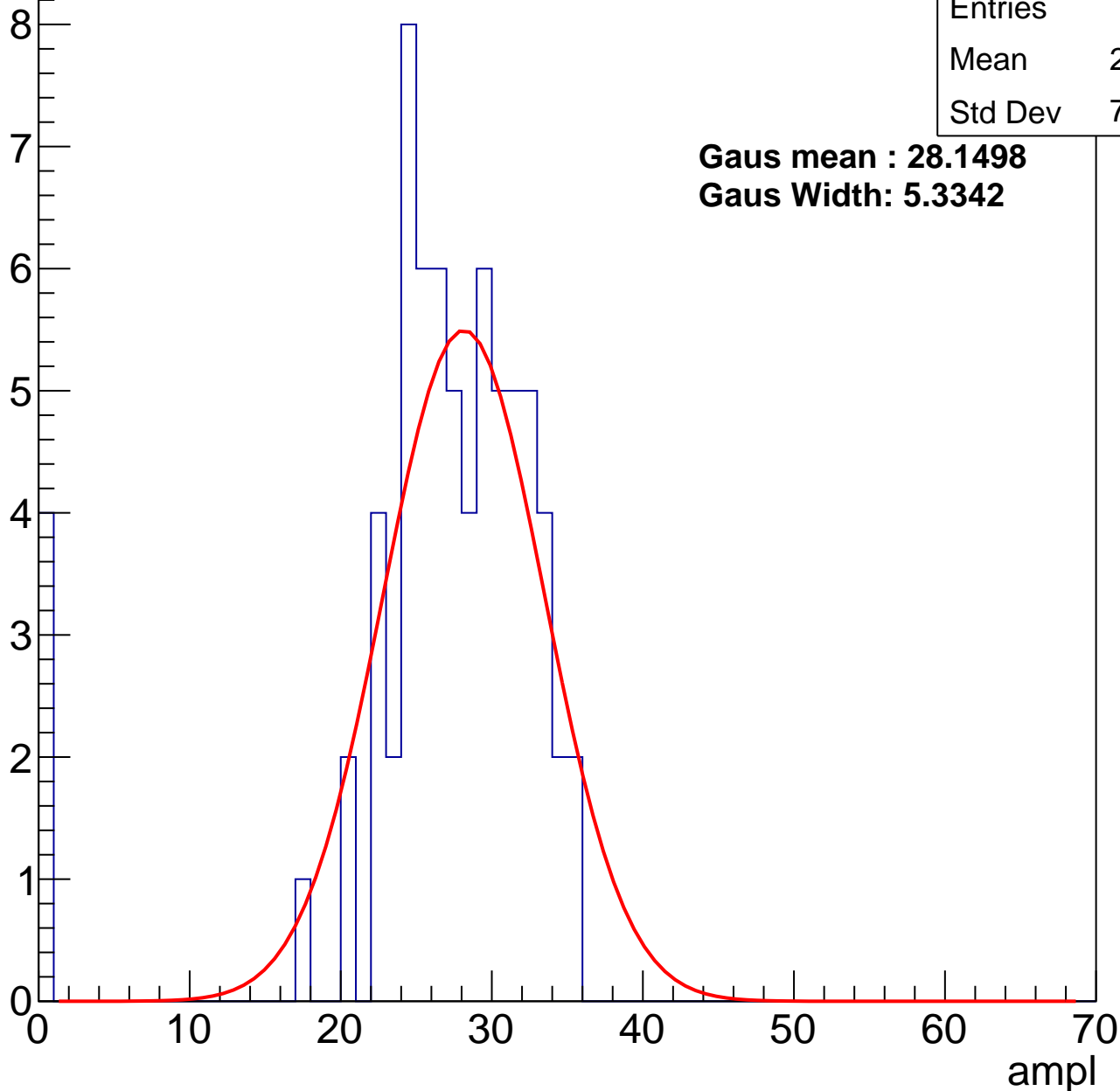
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	25.99
Std Dev	7.433

**Gaus mean : 28.1498**

**Gaus Width: 5.3342**



# B1L103S, U22-ch66, adc1

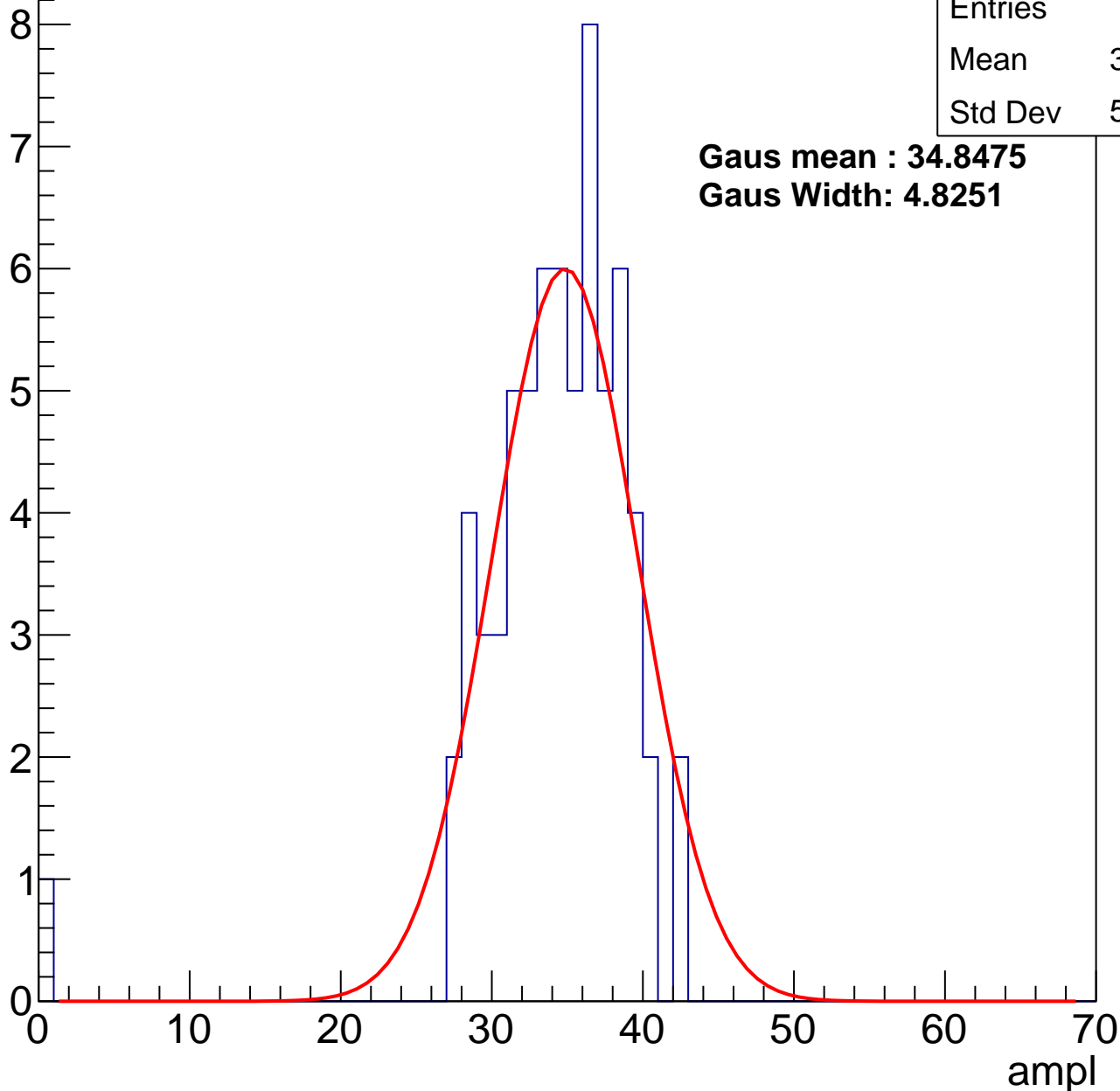
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	33.67
Std Dev	5.549

**Gaus mean : 34.8475**

**Gaus Width: 4.8251**



# B1L103S, U22-ch66, adc2

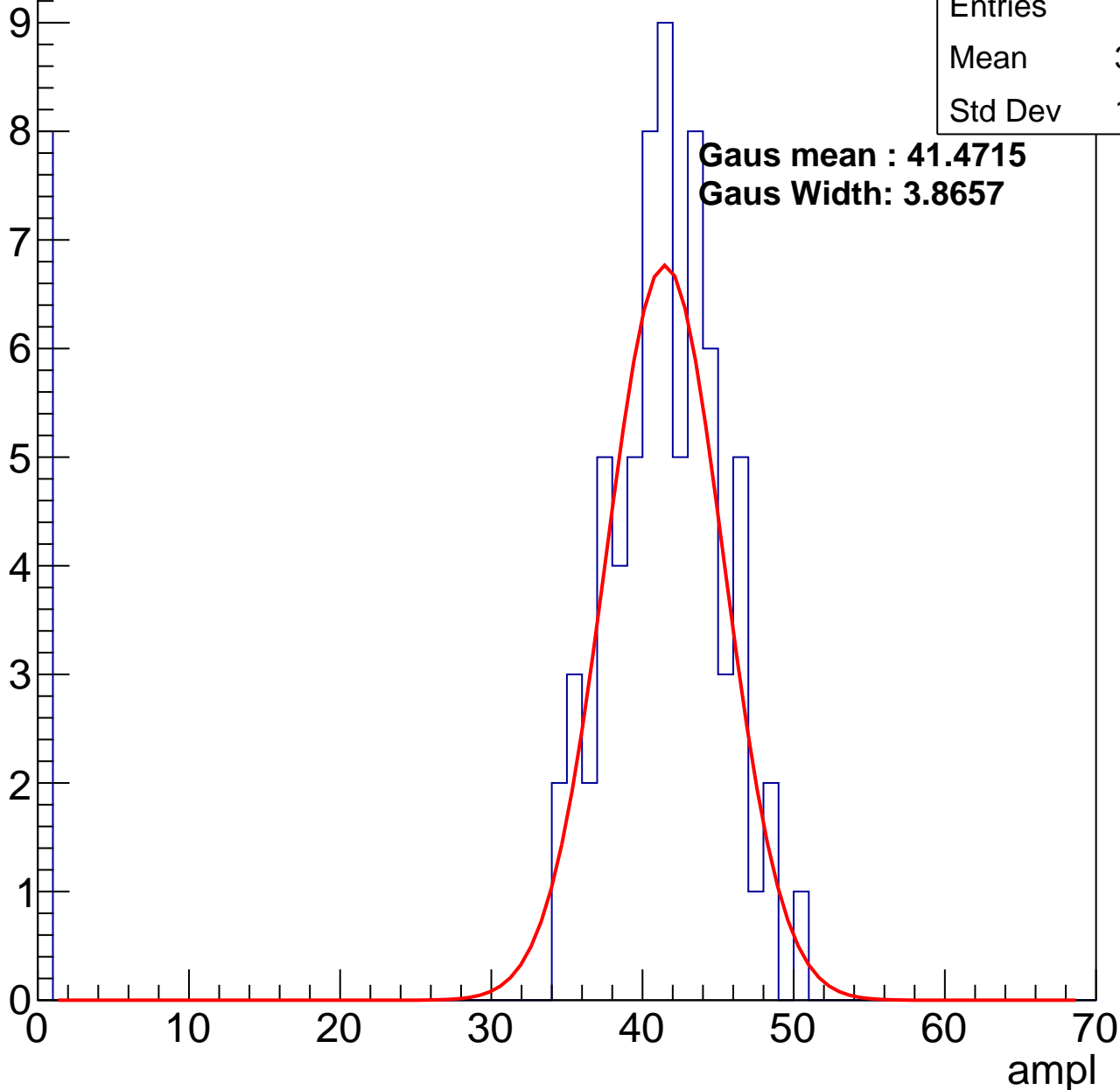
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	77
Mean	36.91
Std Dev	13.01

**Gaus mean : 41.4715**

**Gaus Width: 3.8657**

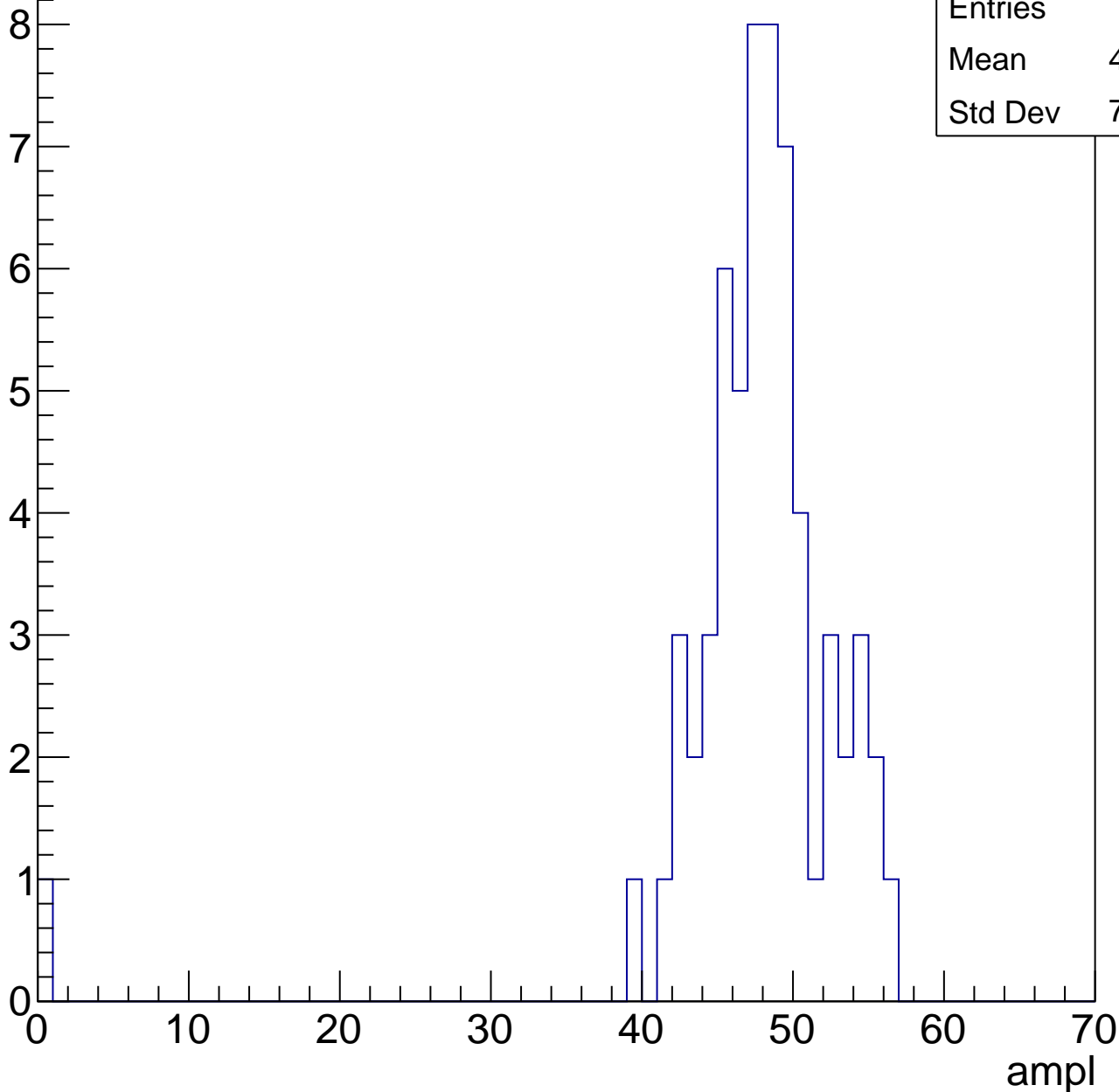


# B1L103S, U22-ch66, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.02
Std Dev	7.086

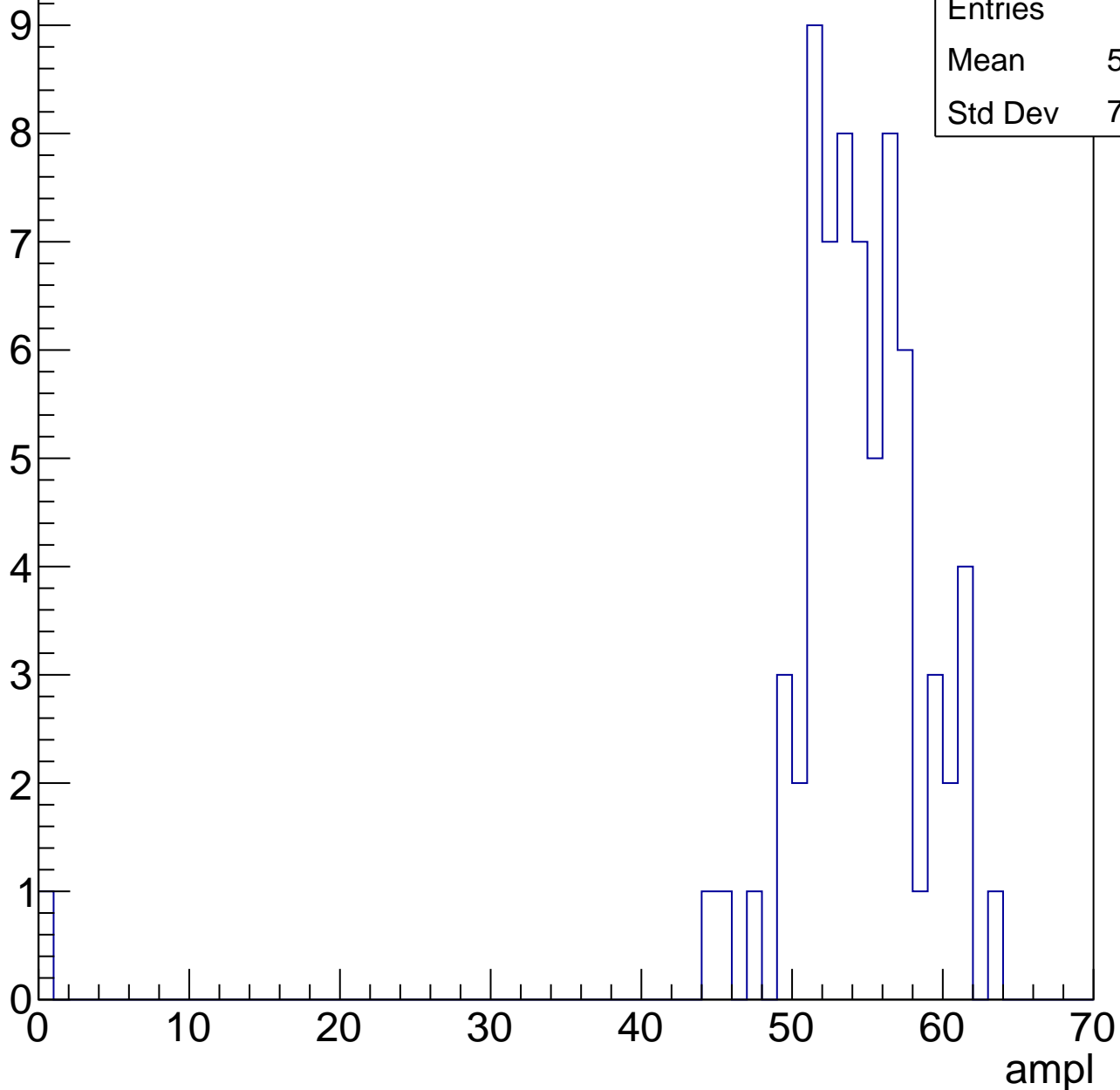


# B1L103S, U22-ch66, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

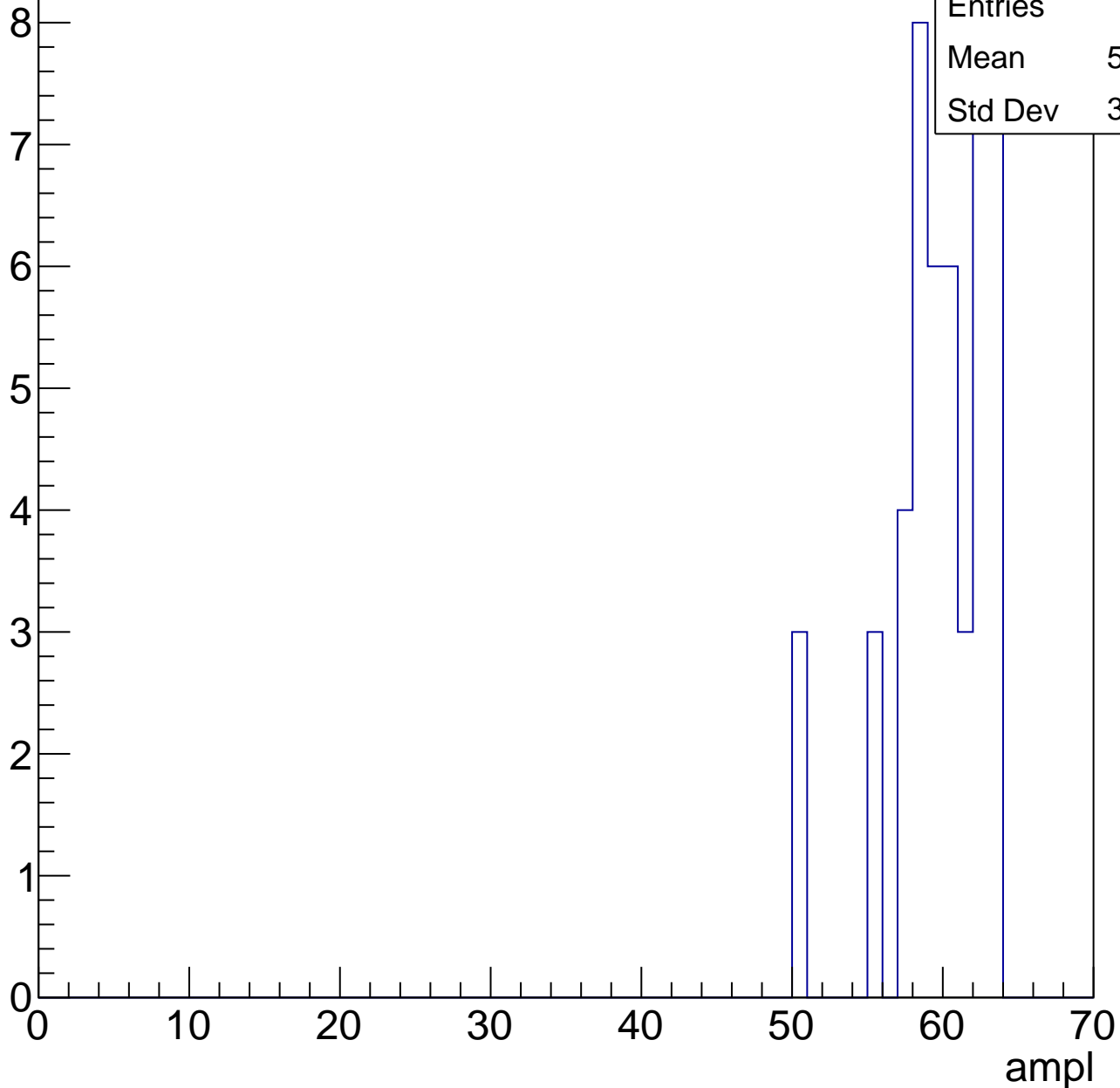
Entries	70
Mean	53.36
Std Dev	7.437



# B1L103S, U22-ch66, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch66, adc6

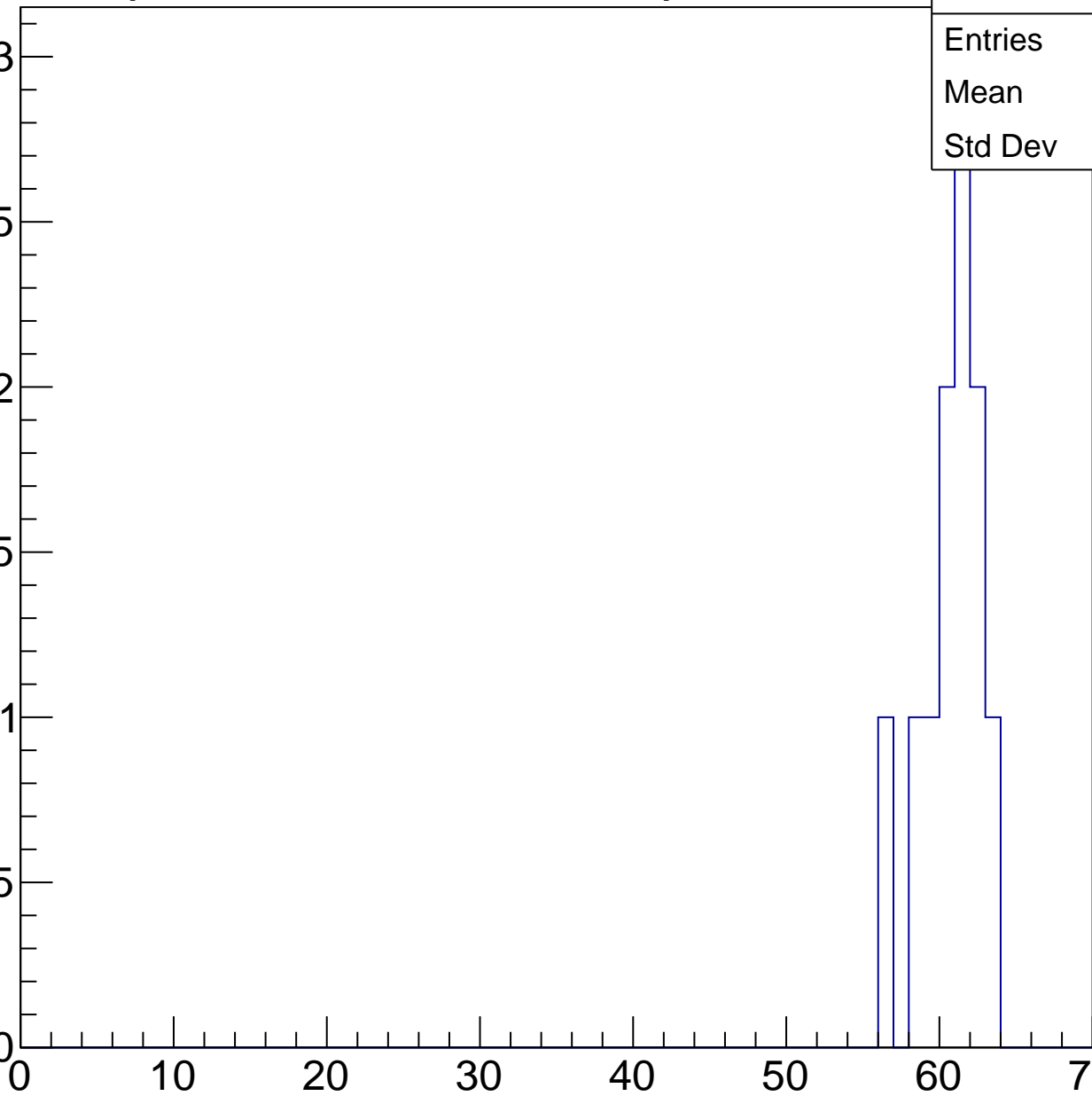
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	11
Mean	60.27
Std Dev	1.911

ampl





# B1L103S, U22-ch66, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

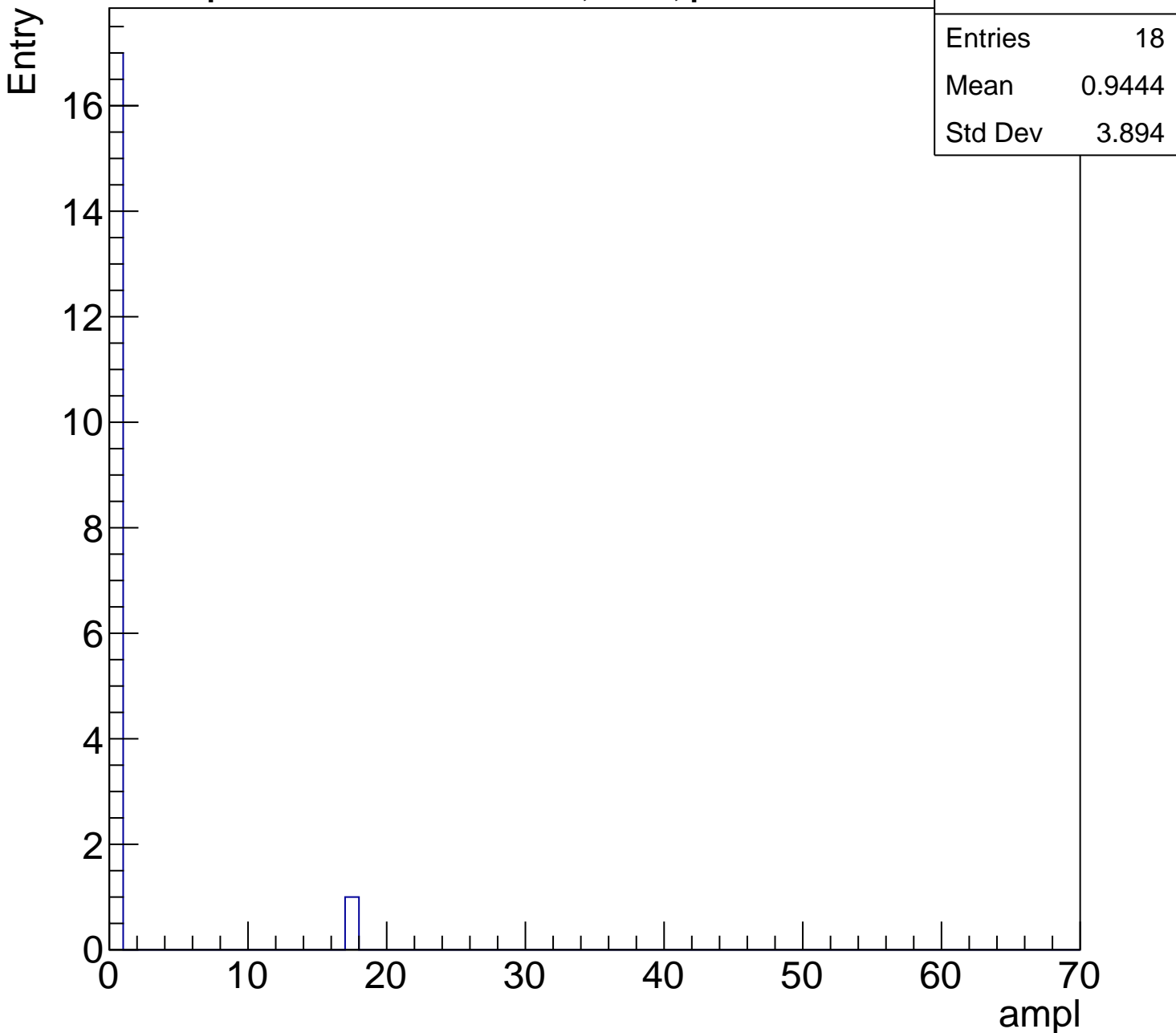
Entries	18
Mean	0.9444
Std Dev	3.894

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch67, adc0

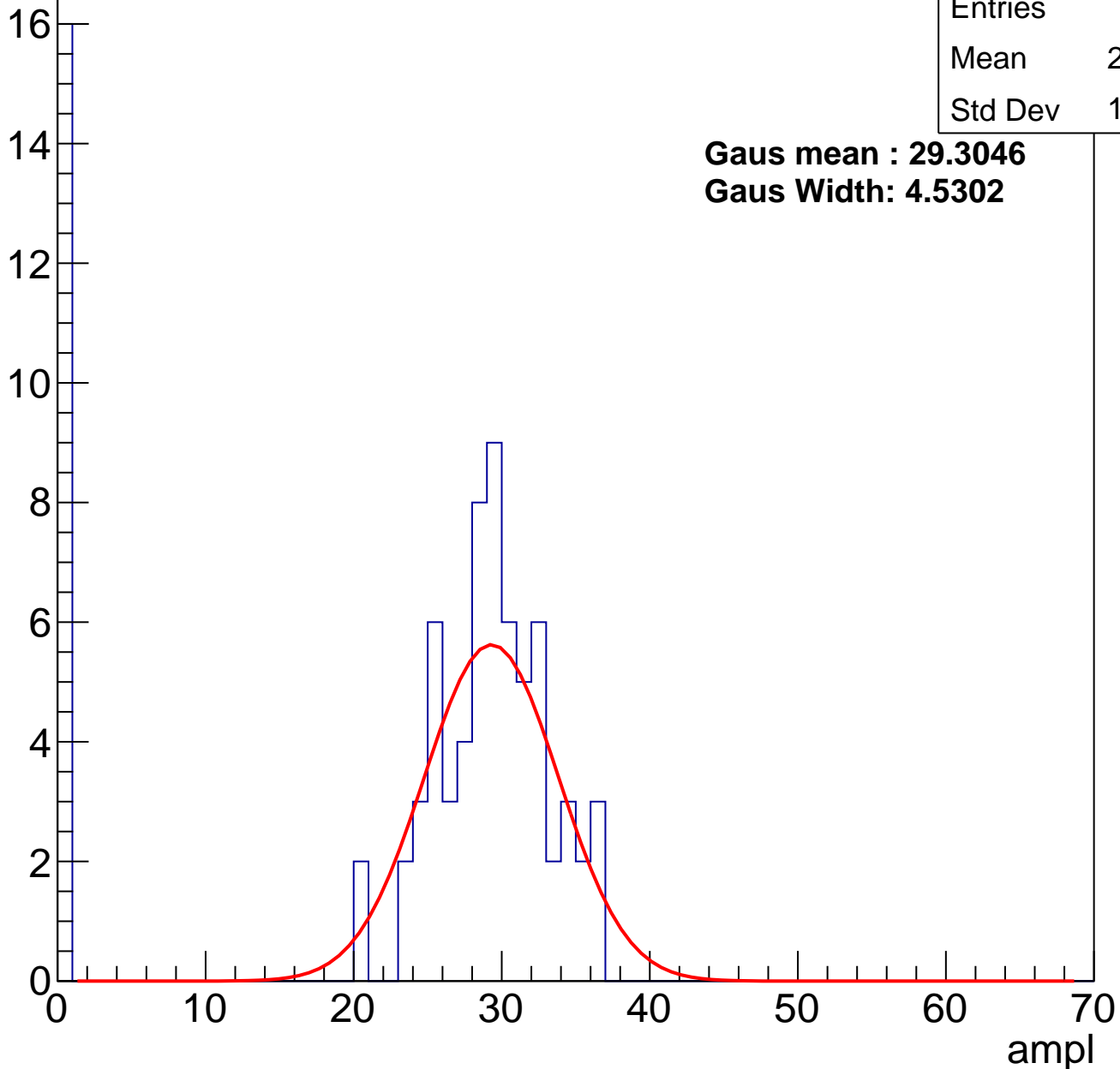
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	23.15
Std Dev	12.03

**Gaus mean : 29.3046**

**Gaus Width: 4.5302**

Entry



# B1L103S, U22-ch67, adc1

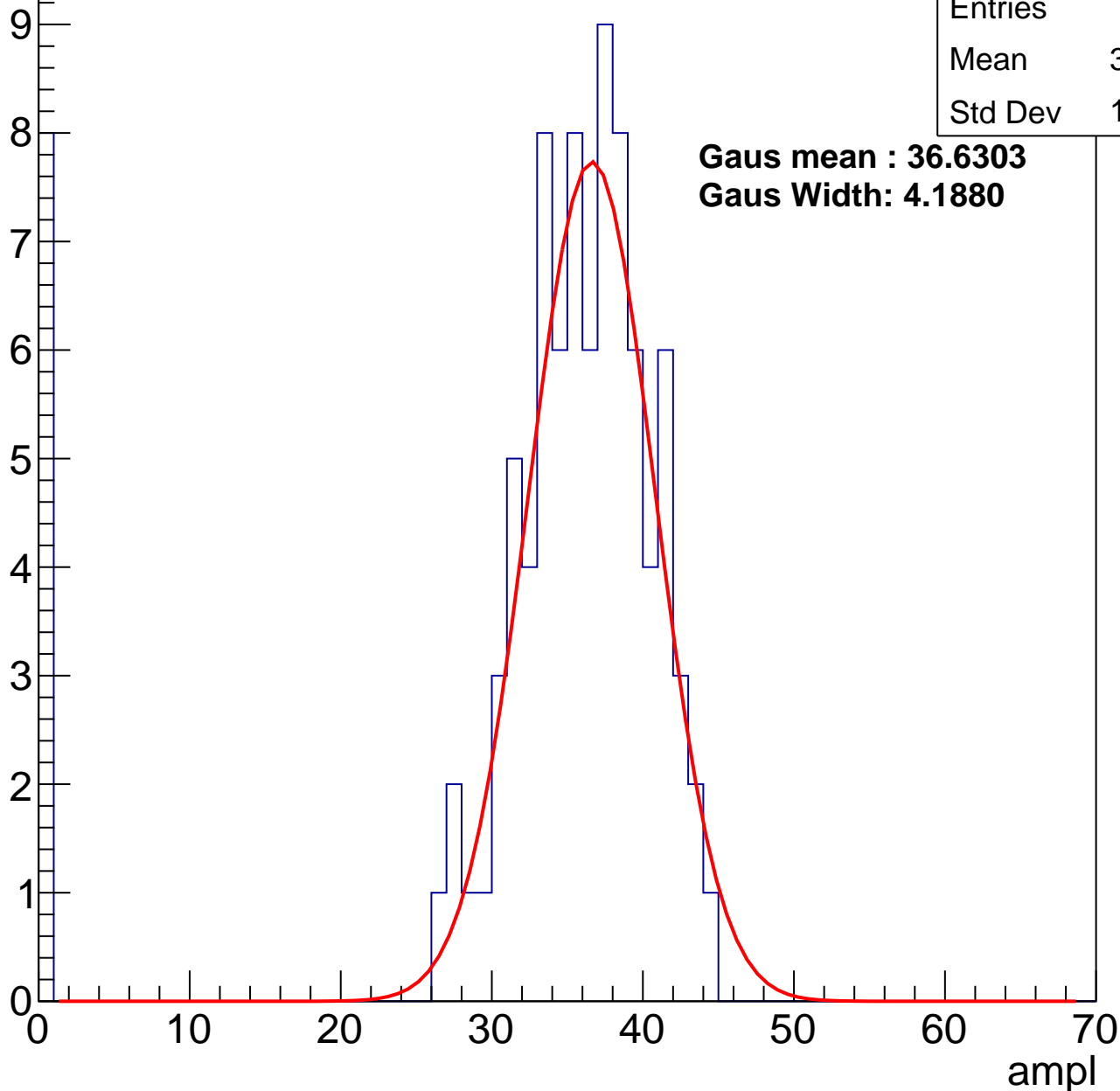
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	92
Mean	32.68
Std Dev	10.79

**Gaus mean : 36.6303**

**Gaus Width: 4.1880**



# B1L103S, U22-ch67, adc2

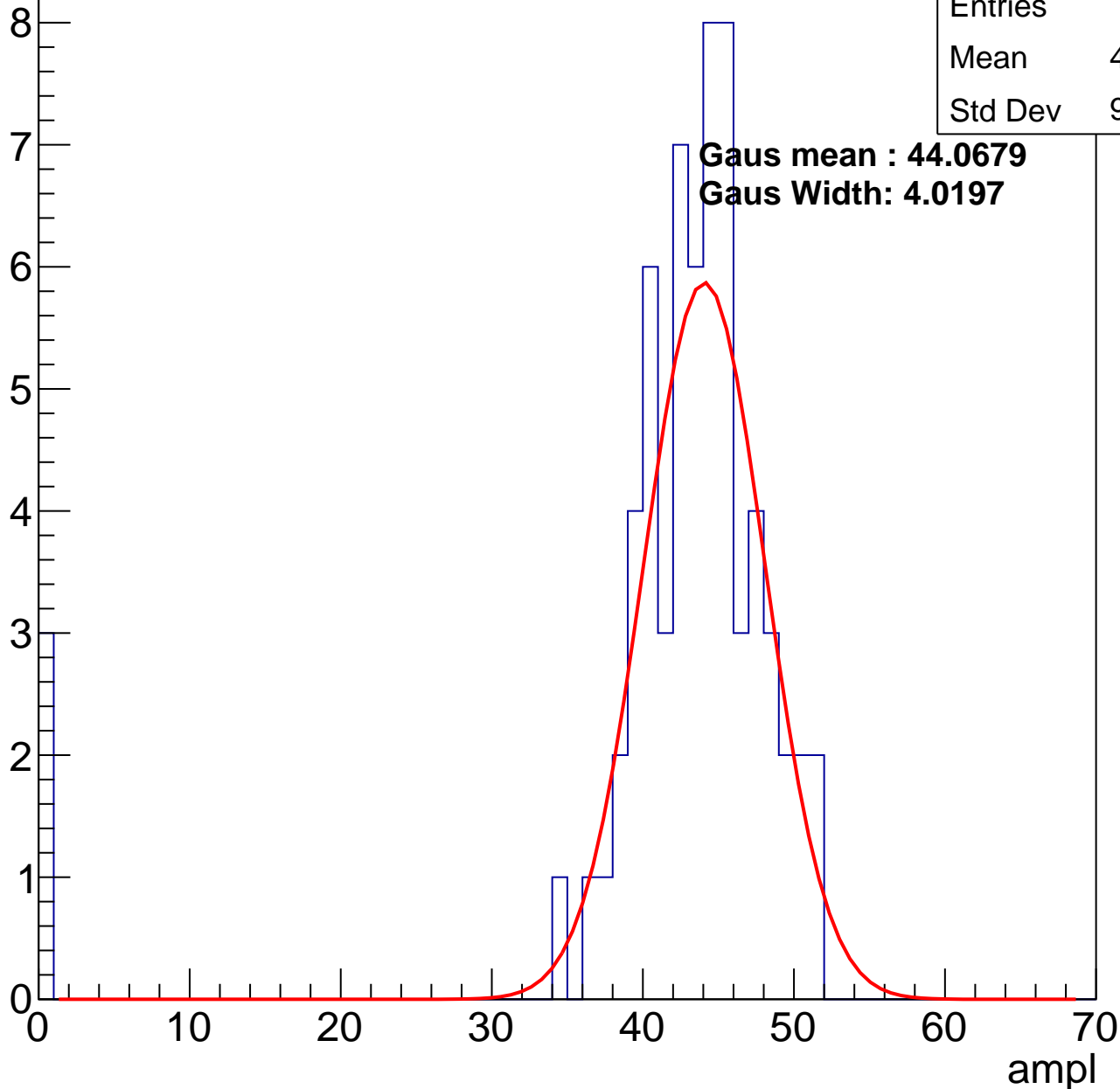
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	41.45
Std Dev	9.728

**Gaus mean : 44.0679**

**Gaus Width: 4.0197**

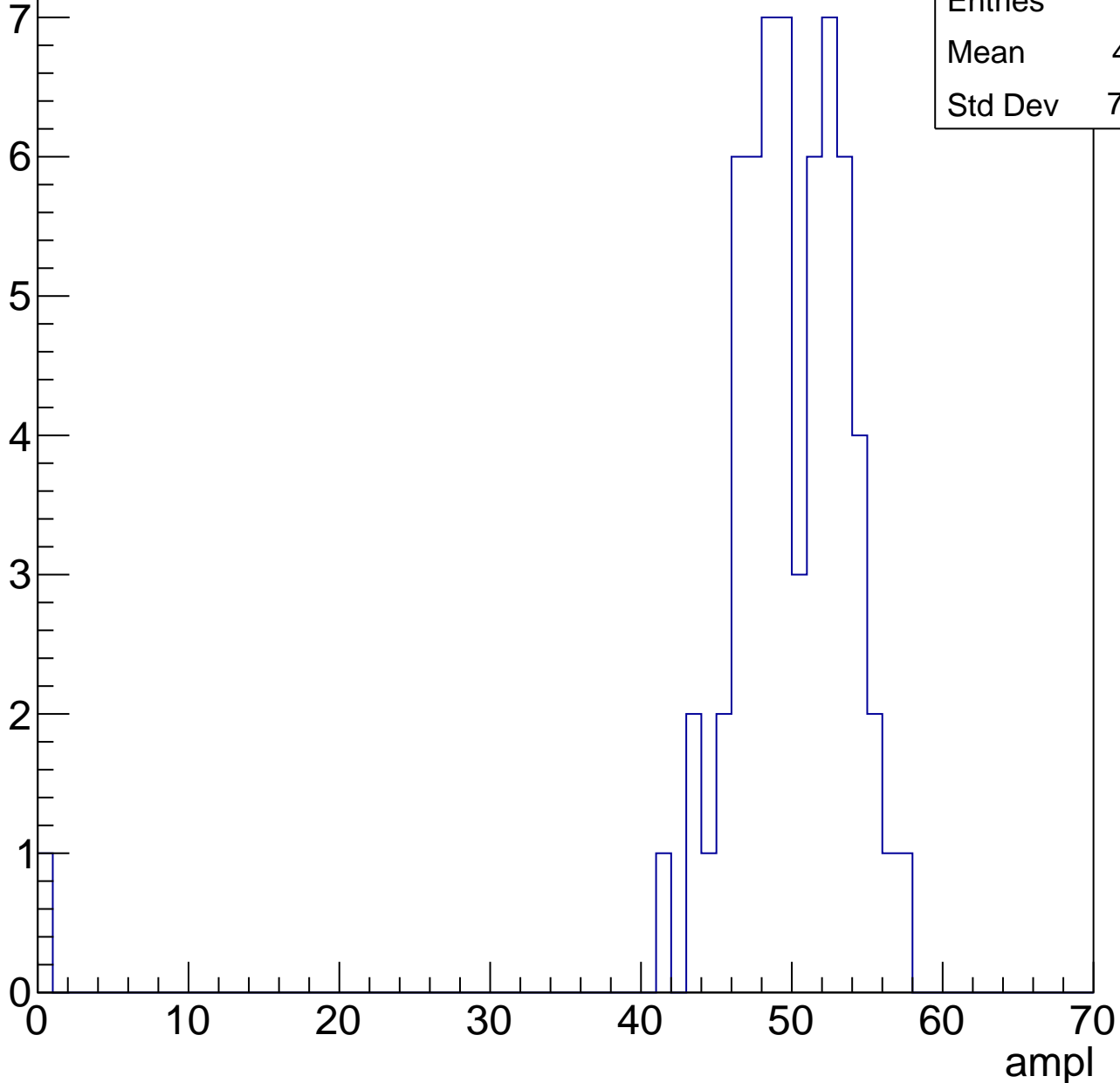


# B1L103S, U22-ch67, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	48.81
Std Dev	7.069

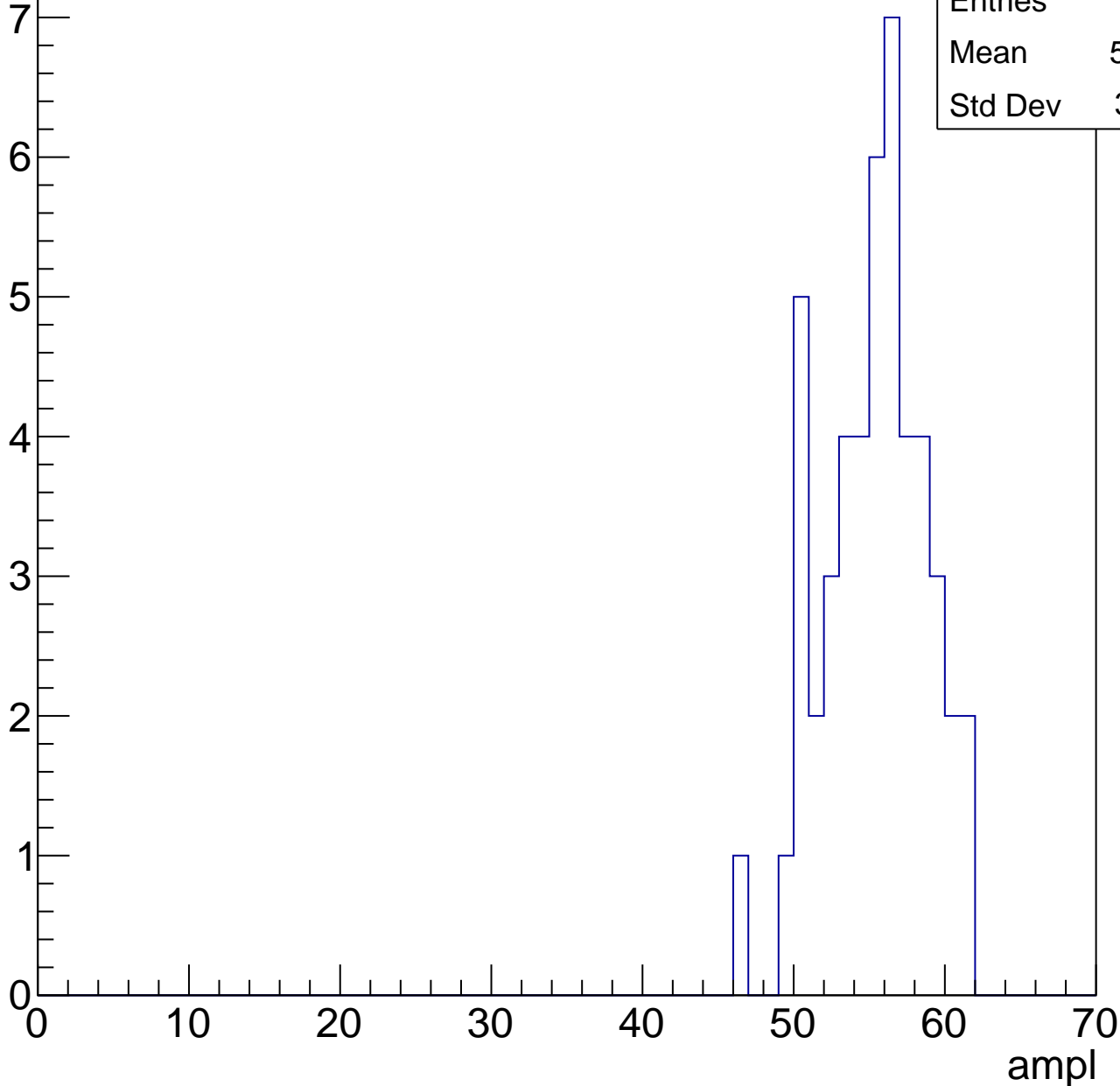


# B1L103S, U22-ch67, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	54.83
Std Dev	3.381



# B1L103S, U22-ch67, adc5

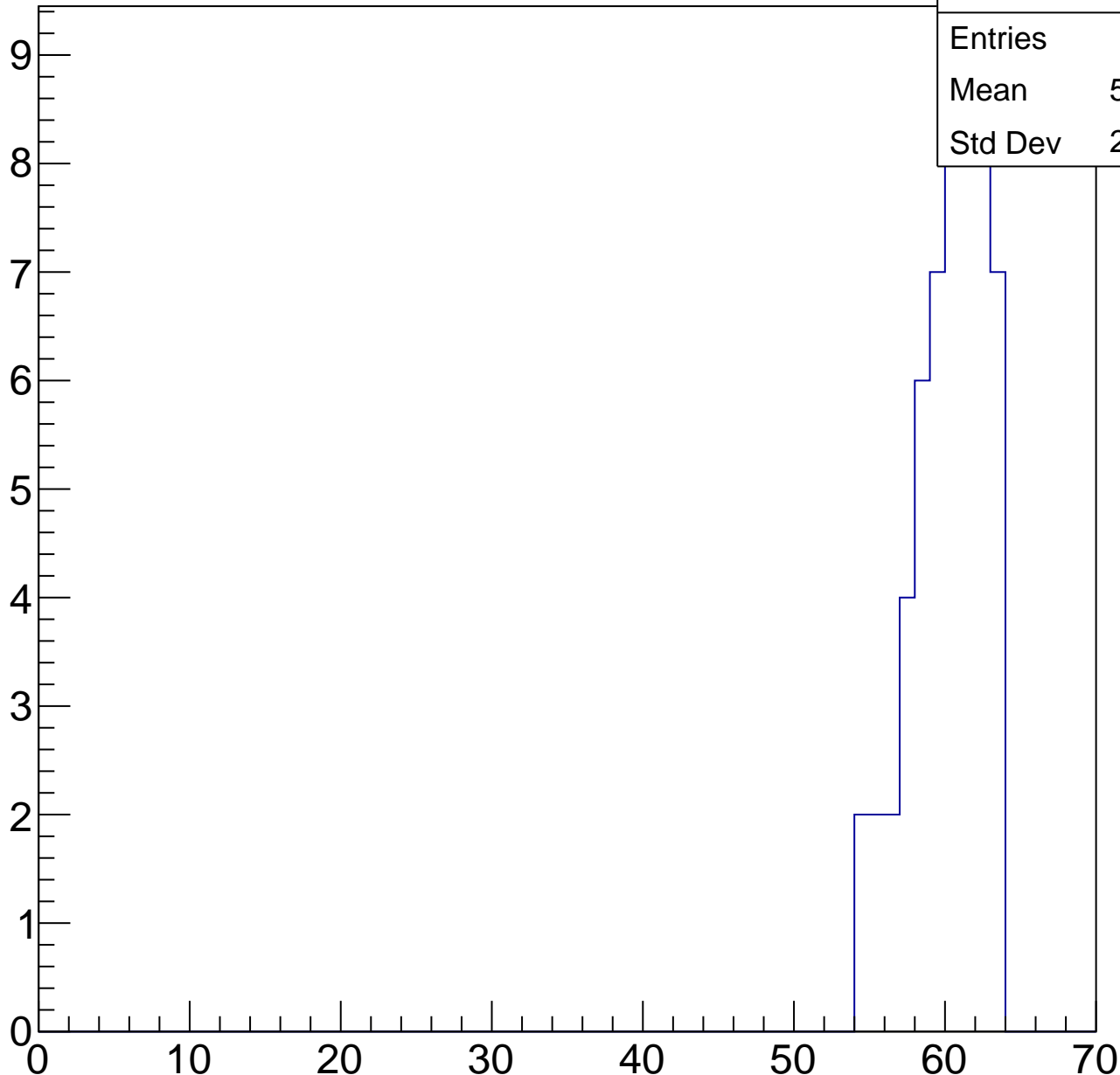
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	55
Mean	59.75
Std Dev	2.429

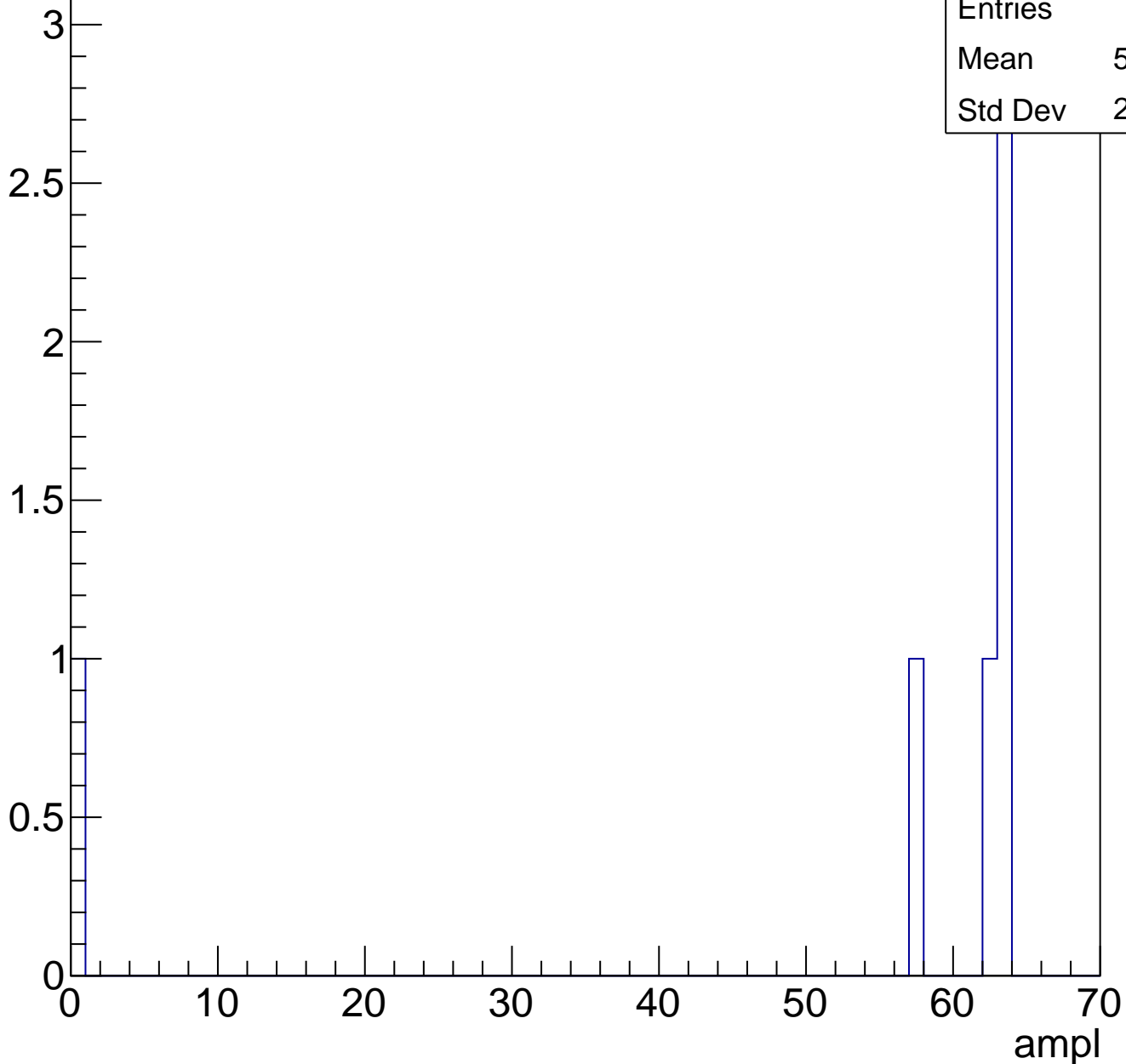
ampl



# B1L103S, U22-ch67, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

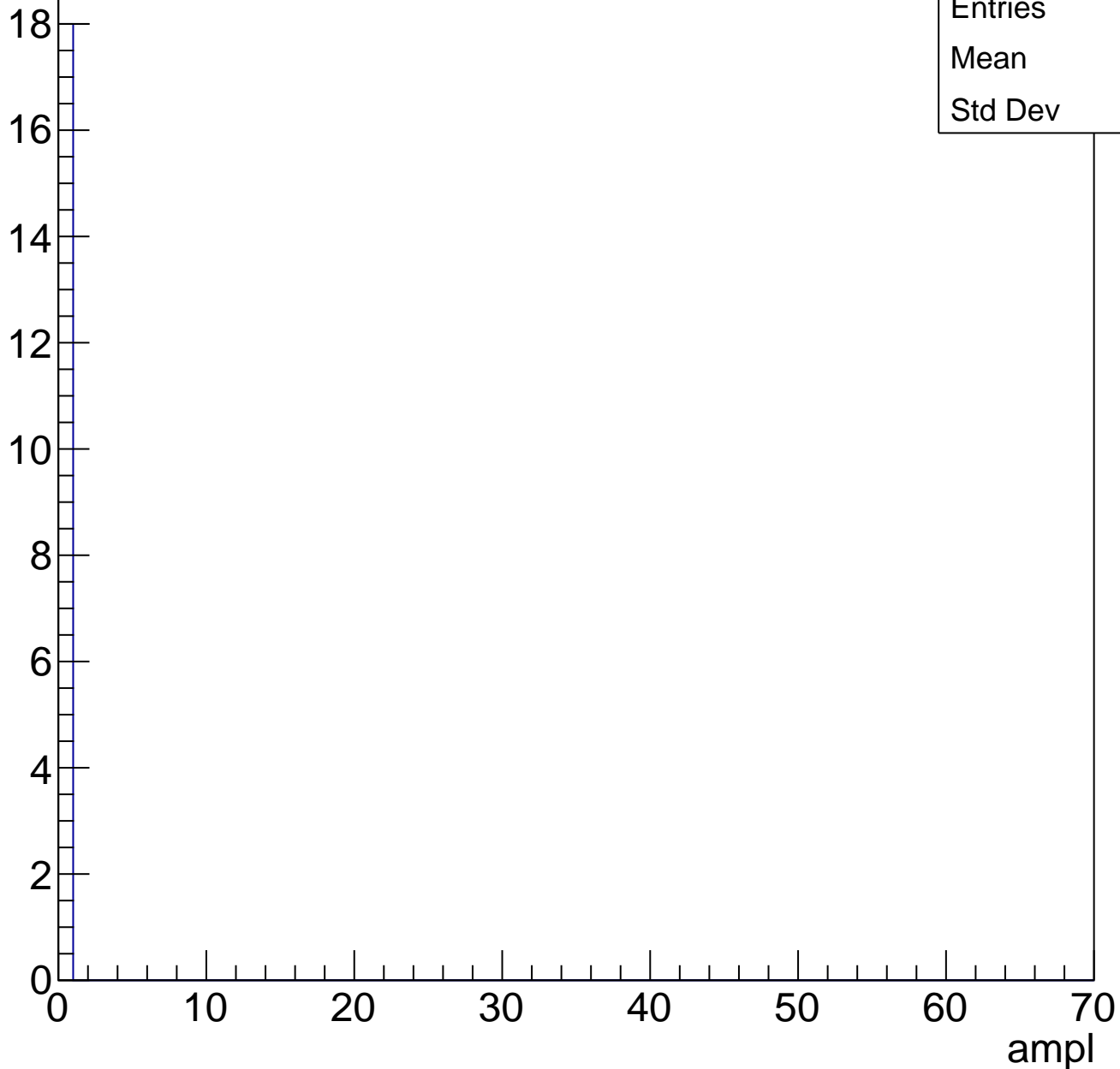




# B1L103S, U22-ch67, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	18
Mean	0
Std Dev	0

# B1L103S, U22-ch68, adc0

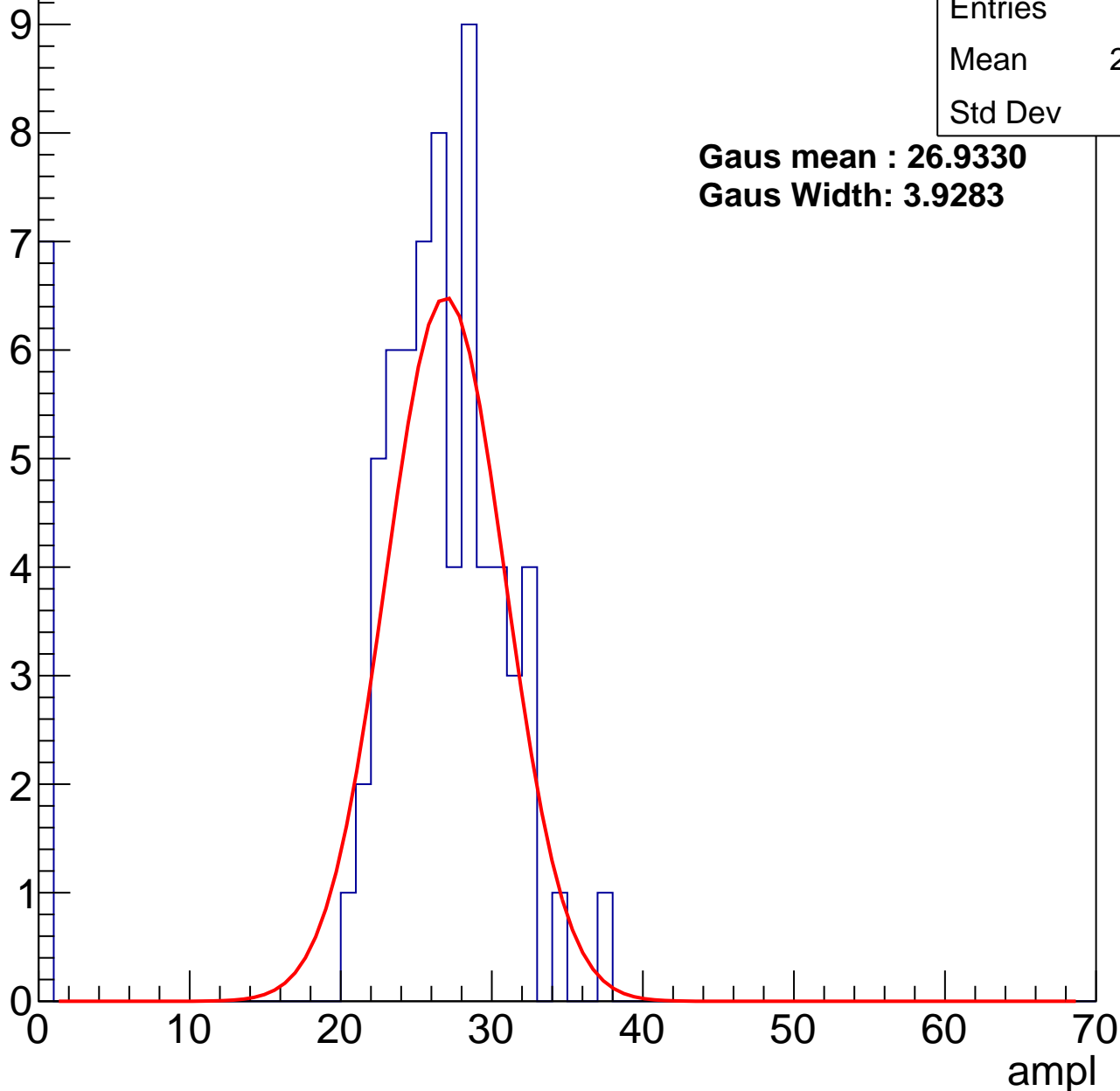
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	23.96
Std Dev	8.52

**Gaus mean : 26.9330**

**Gaus Width: 3.9283**



# B1L103S, U22-ch68, adc1

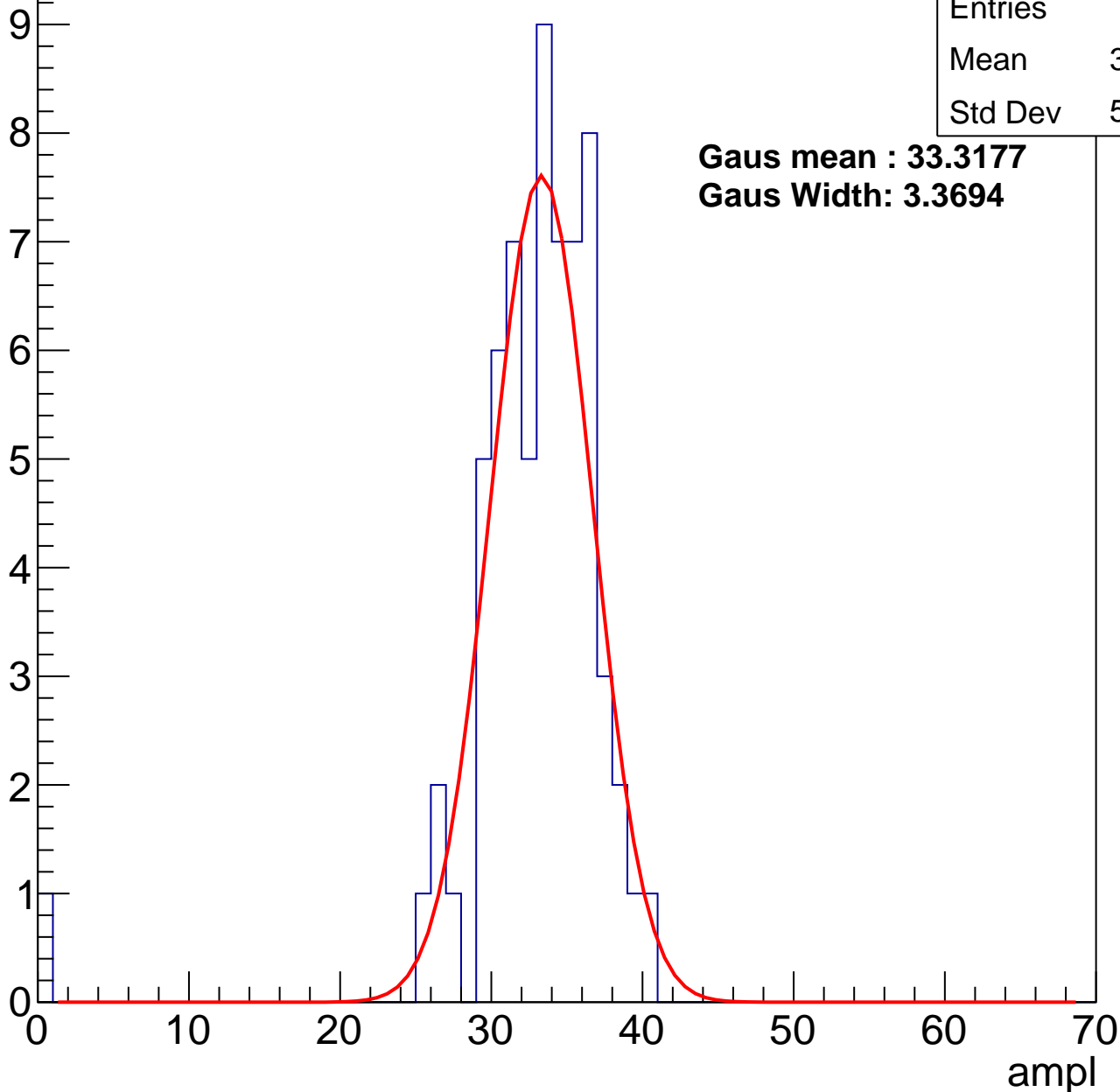
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	32.42
Std Dev	5.108

**Gaus mean : 33.3177**

**Gaus Width: 3.3694**



# B1L103S, U22-ch68, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	34.34
Std Dev	14.13

**Gaus mean : 40.4896**

**Gaus Width: 3.3294**

Entry

10

8

6

4

2

0

0

10

20

30

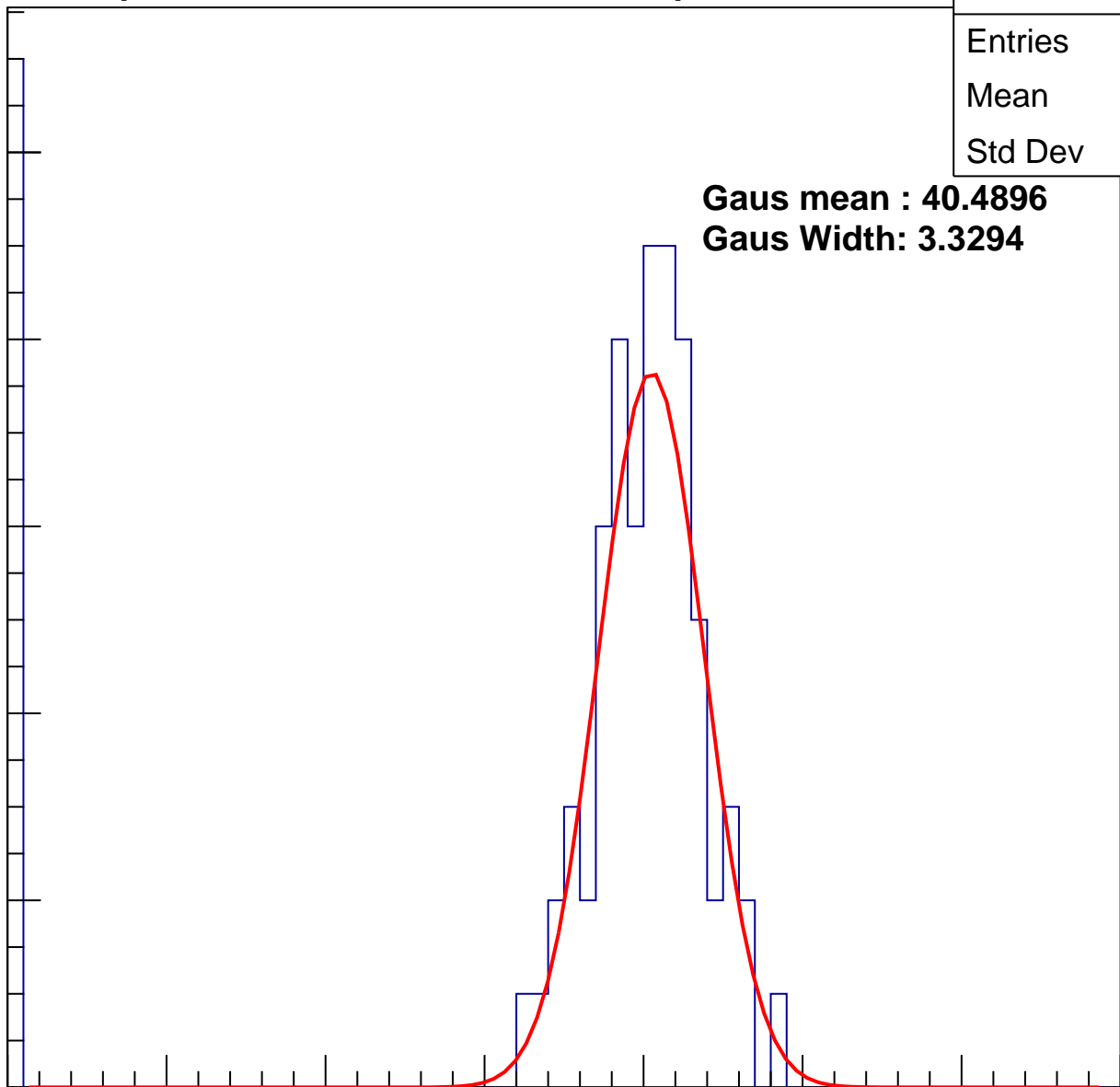
40

50

60

70

ampl

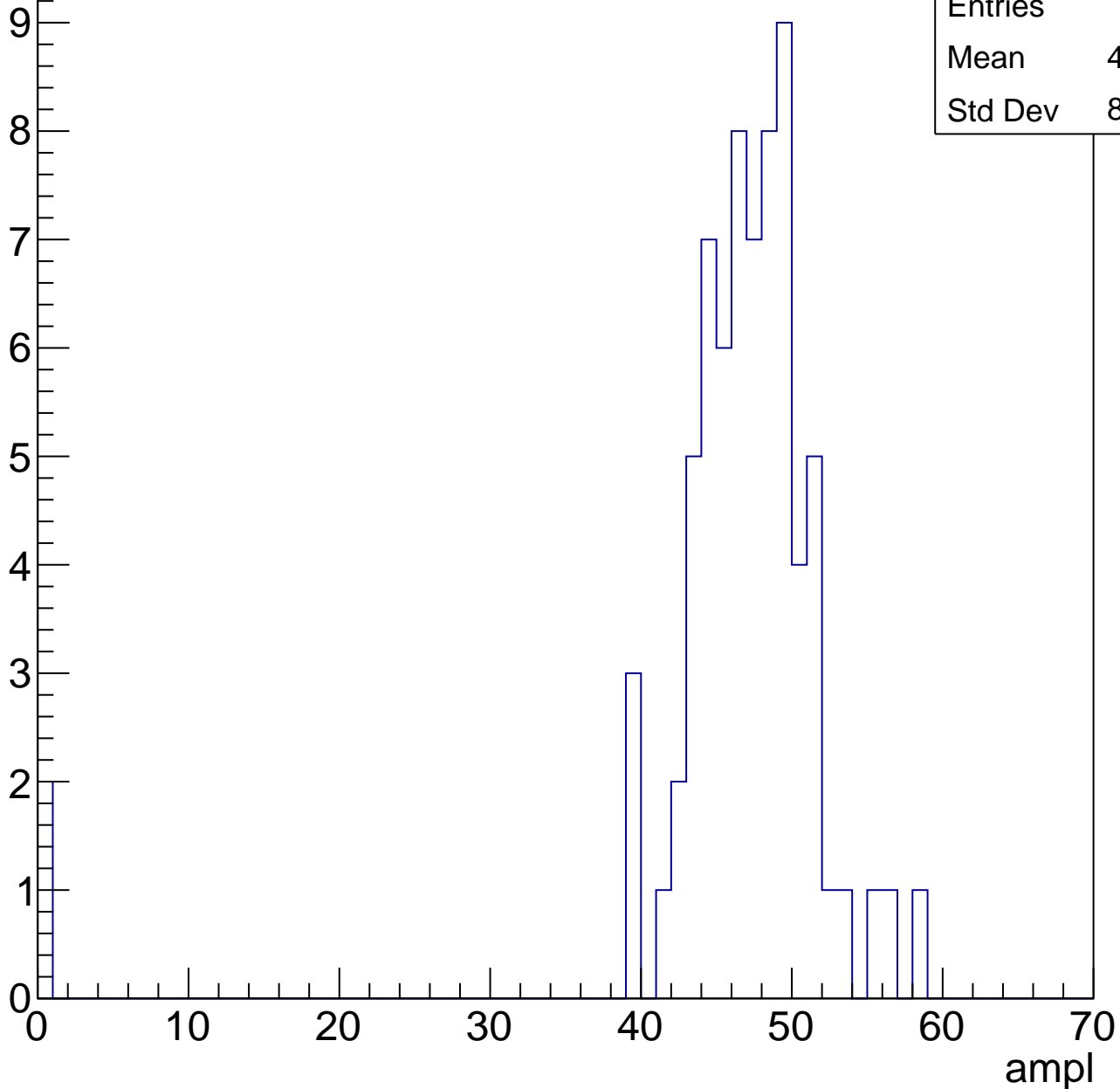


# B1L103S, U22-ch68, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	45.64
Std Dev	8.518

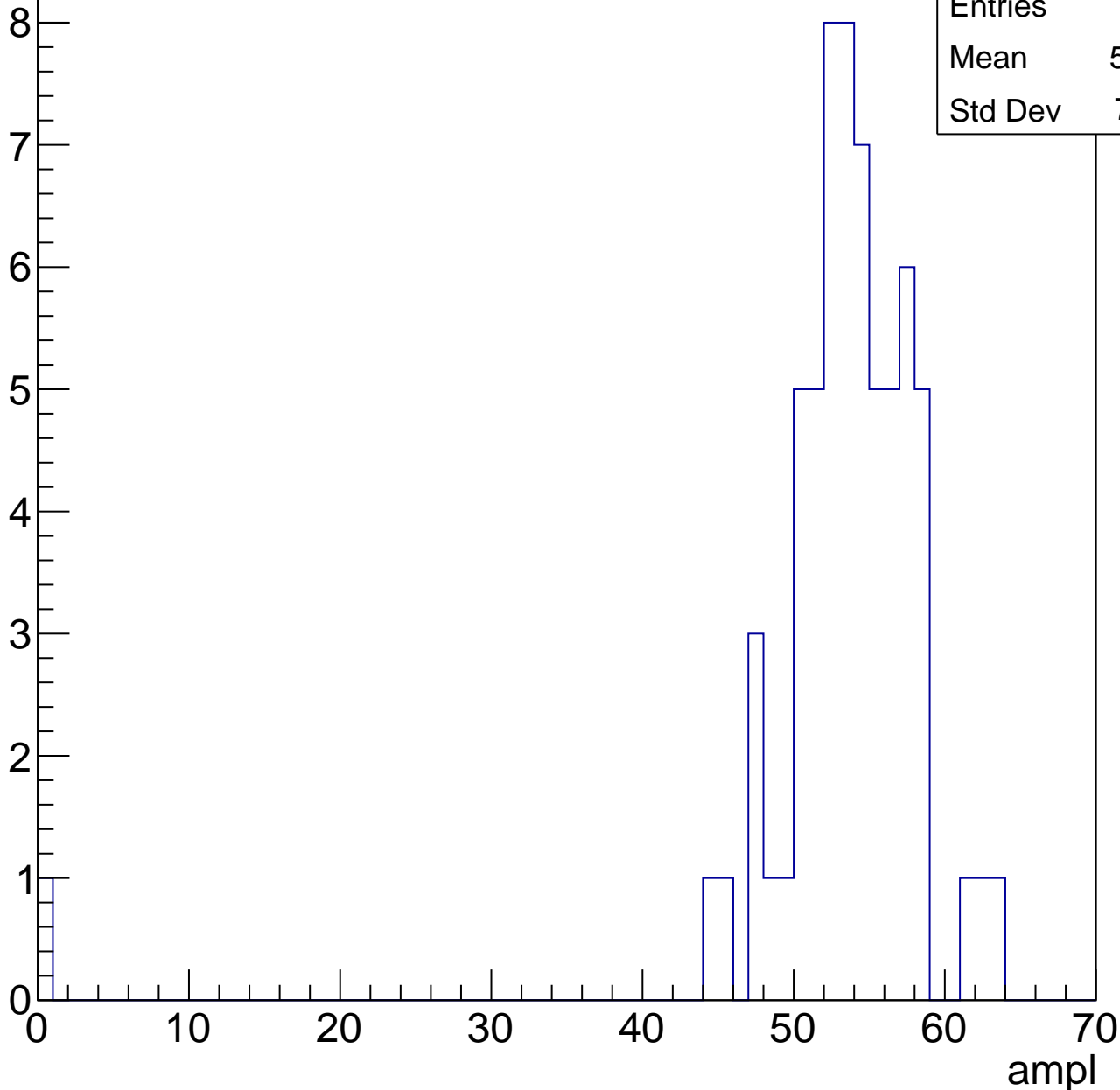


# B1L103S, U22-ch68, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

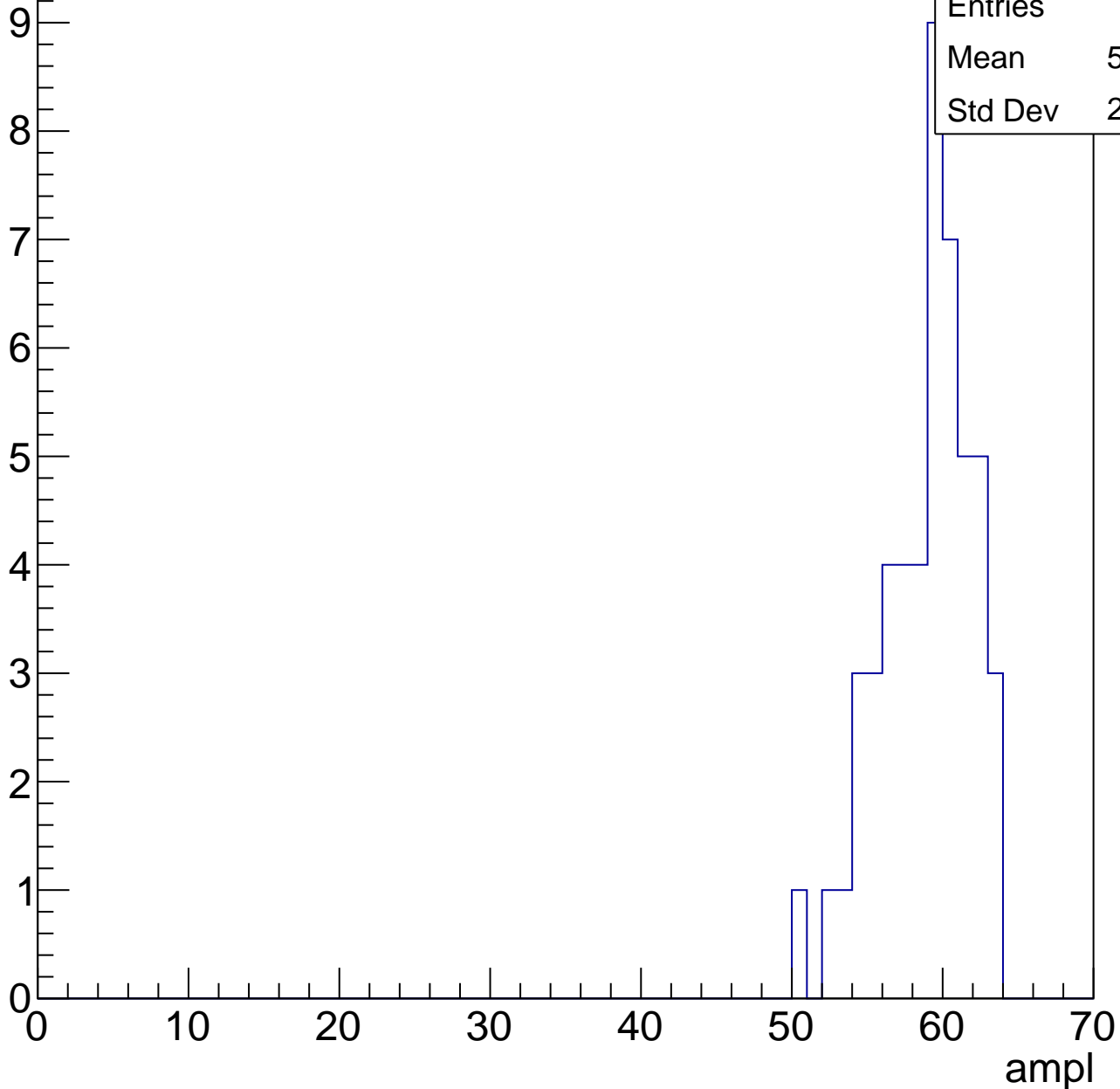
Entries	65
Mean	52.66
Std Dev	7.551



# B1L103S, U22-ch68, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



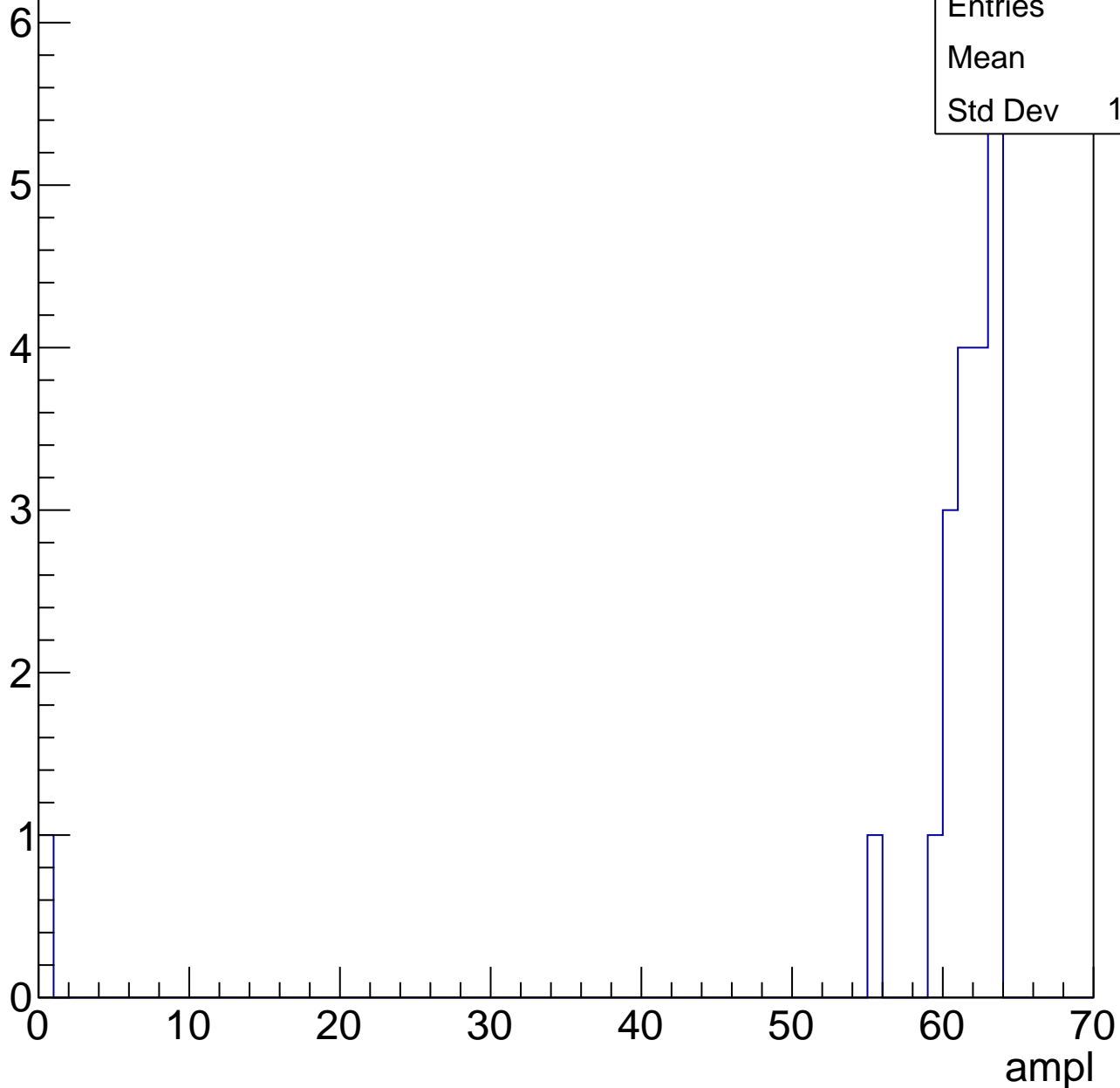
Entries	50
Mean	58.42
Std Dev	2.994

# B1L103S, U22-ch68, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	58.2
Std Dev	13.48





# B1L103S, U22-ch68, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.053
Std Dev	4.466

Entry



# B1L103S, U22-ch69, adc0

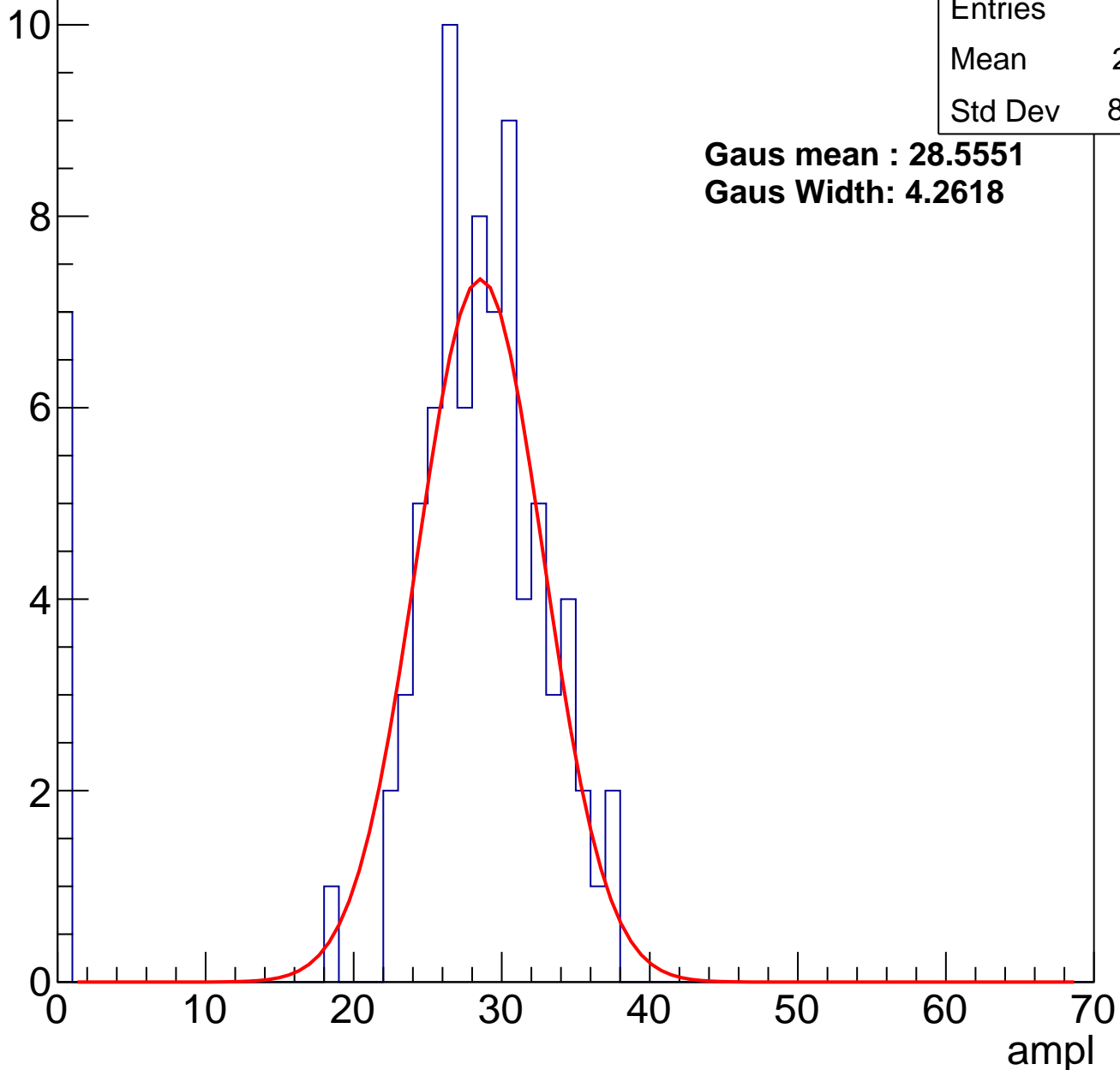
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	26.11
Std Dev	8.617

**Gaus mean : 28.5551**

**Gaus Width: 4.2618**

Entry



# B1L103S, U22-ch69, adc1

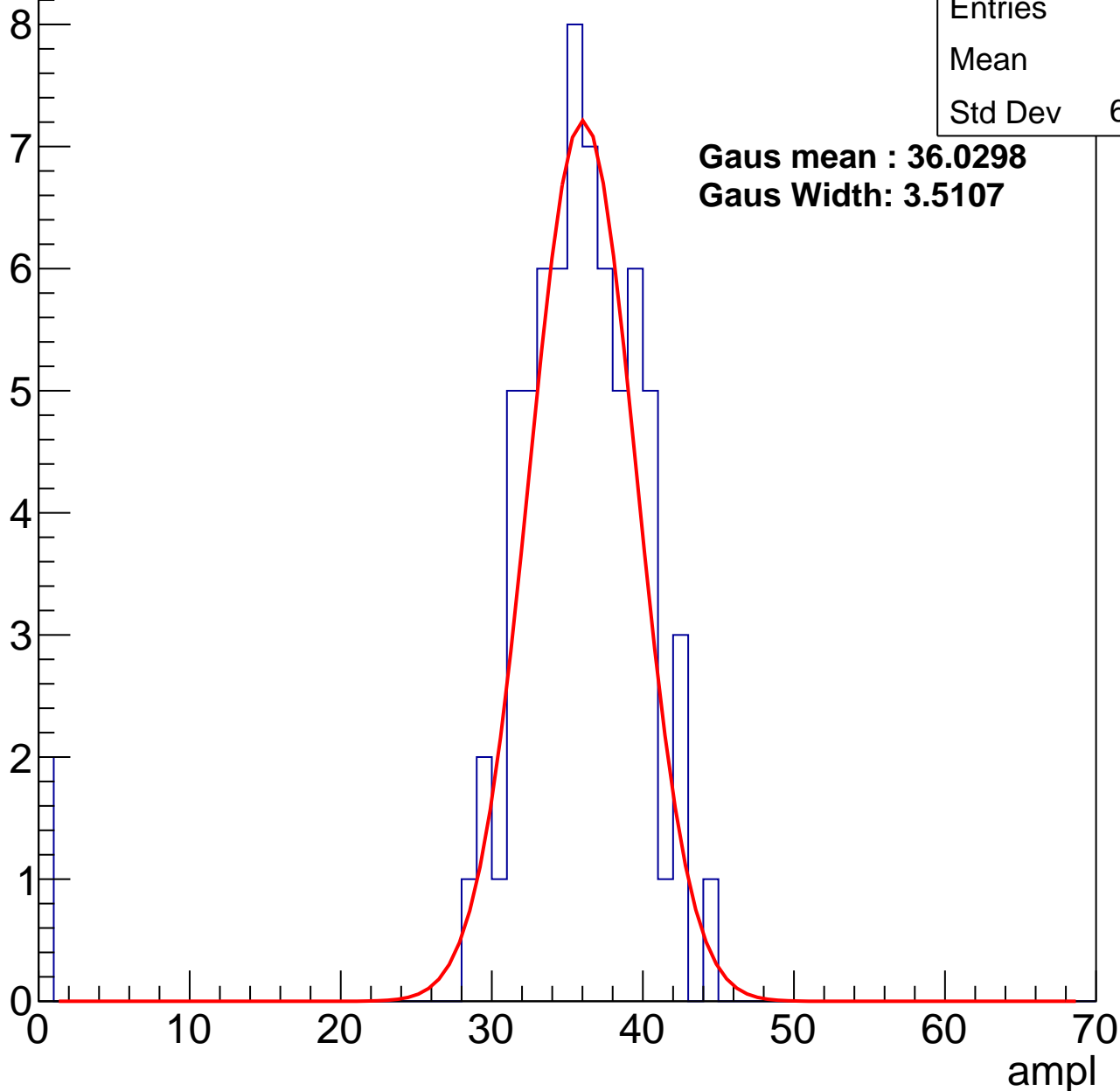
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	34.6
Std Dev	6.867

**Gaus mean : 36.0298**

**Gaus Width: 3.5107**



# B1L103S, U22-ch69, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	68
Mean	35.16
Std Dev	15.75

**Gaus mean : 42.4127**

**Gaus Width: 3.9702**

Entry

10

8

6

4

2

0

0

10

20

30

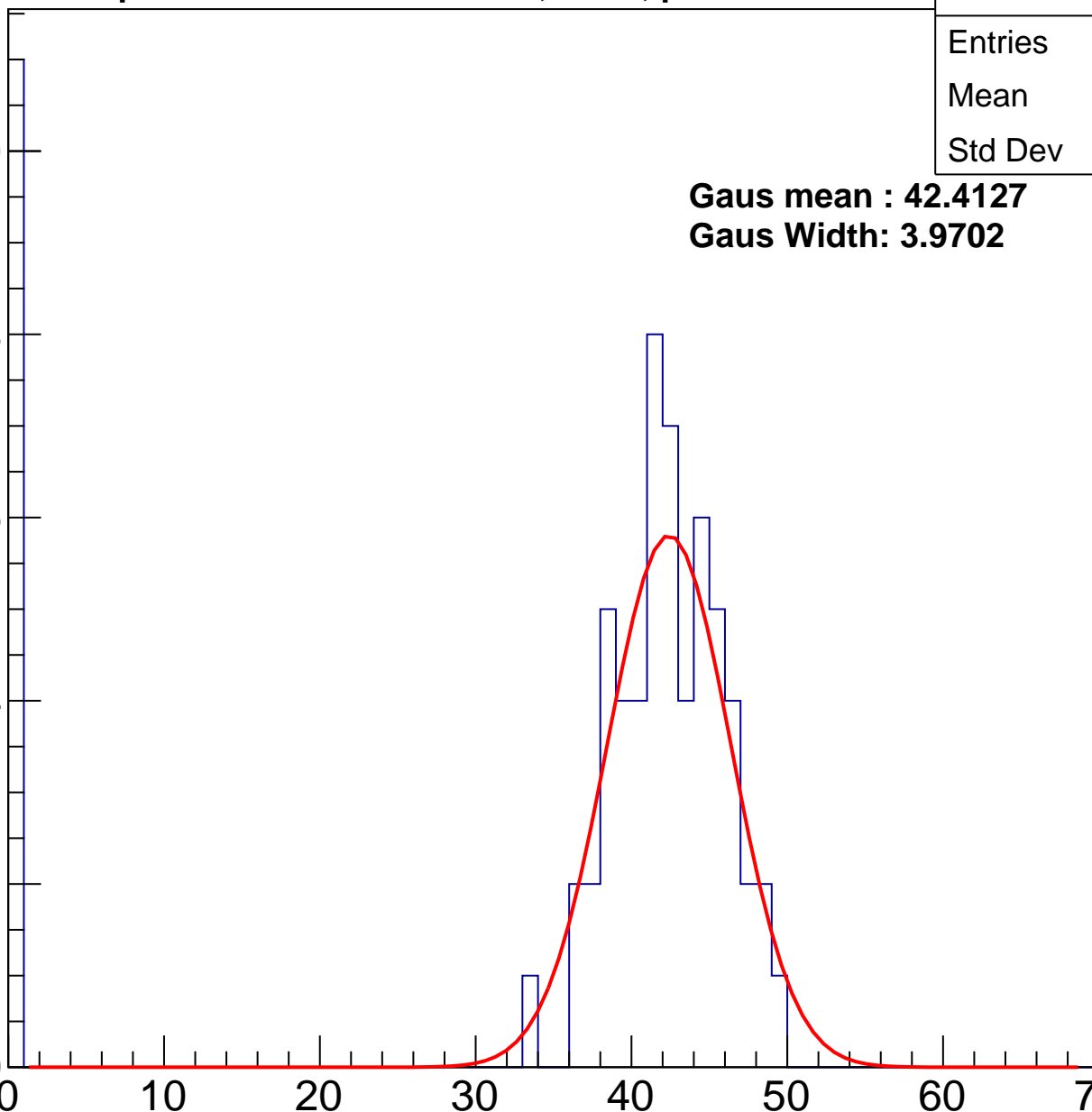
40

50

60

70

ampl

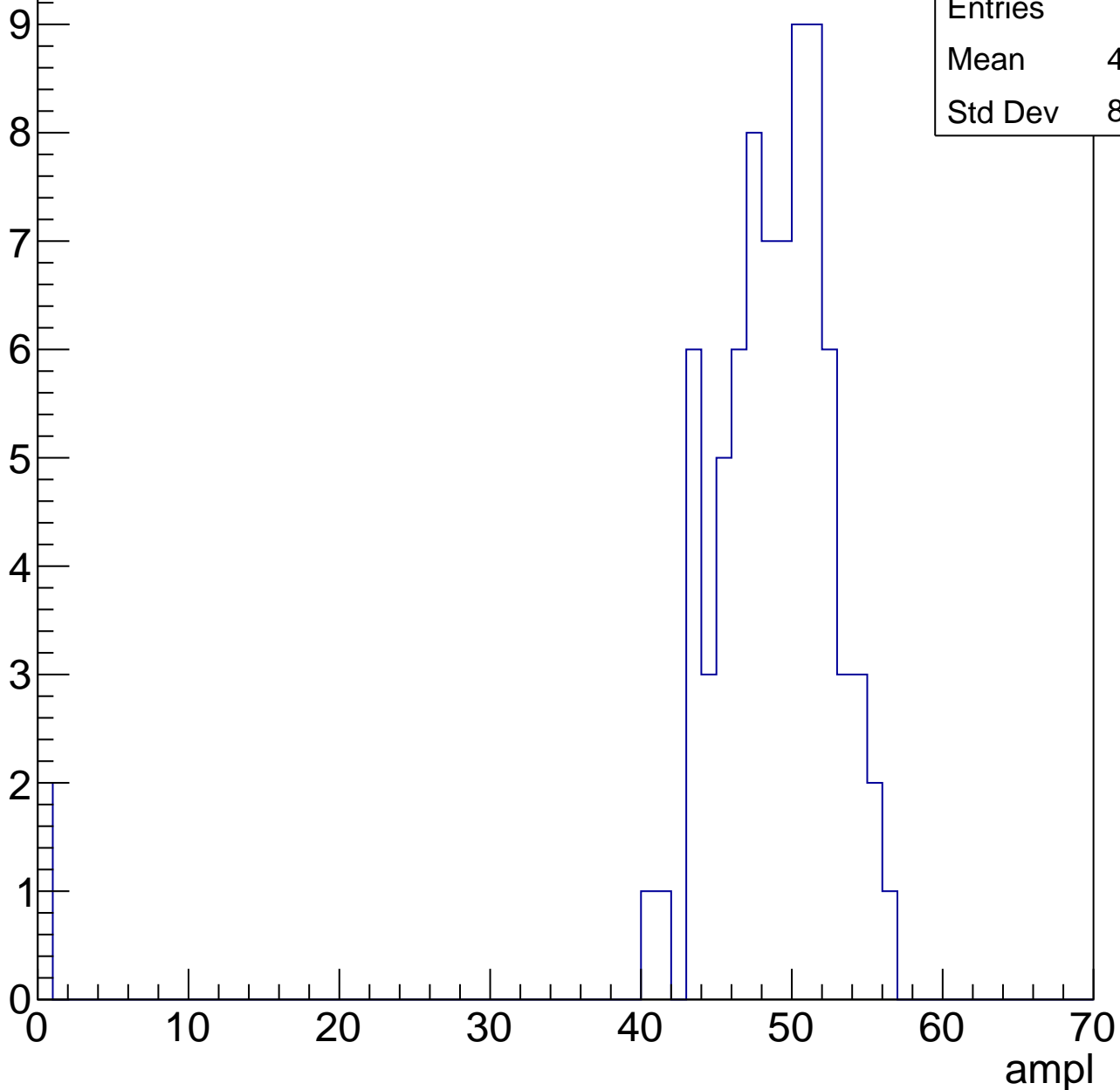


# B1L103S, U22-ch69, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	47.28
Std Dev	8.359

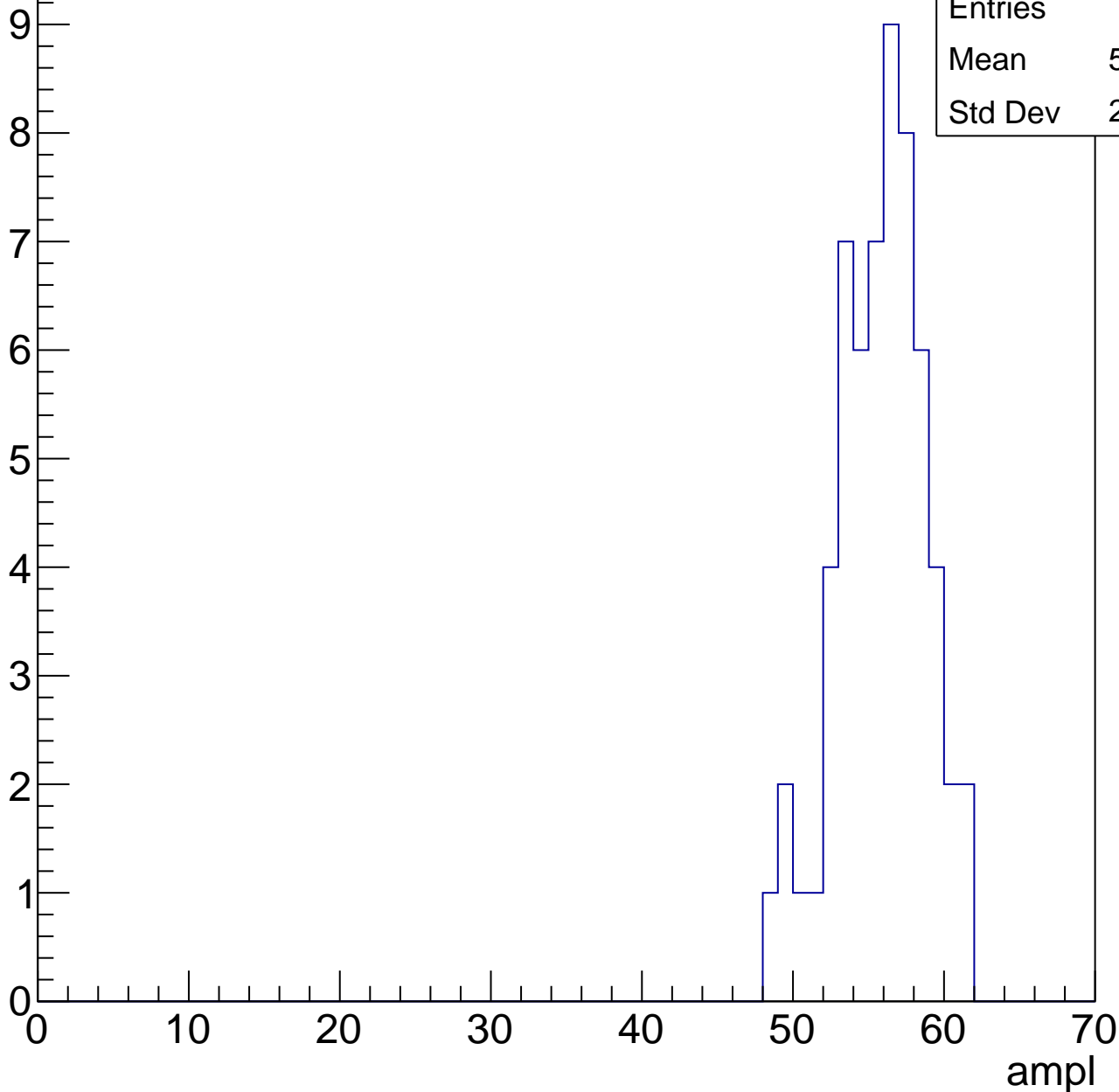


# B1L103S, U22-ch69, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	55.35
Std Dev	2.903

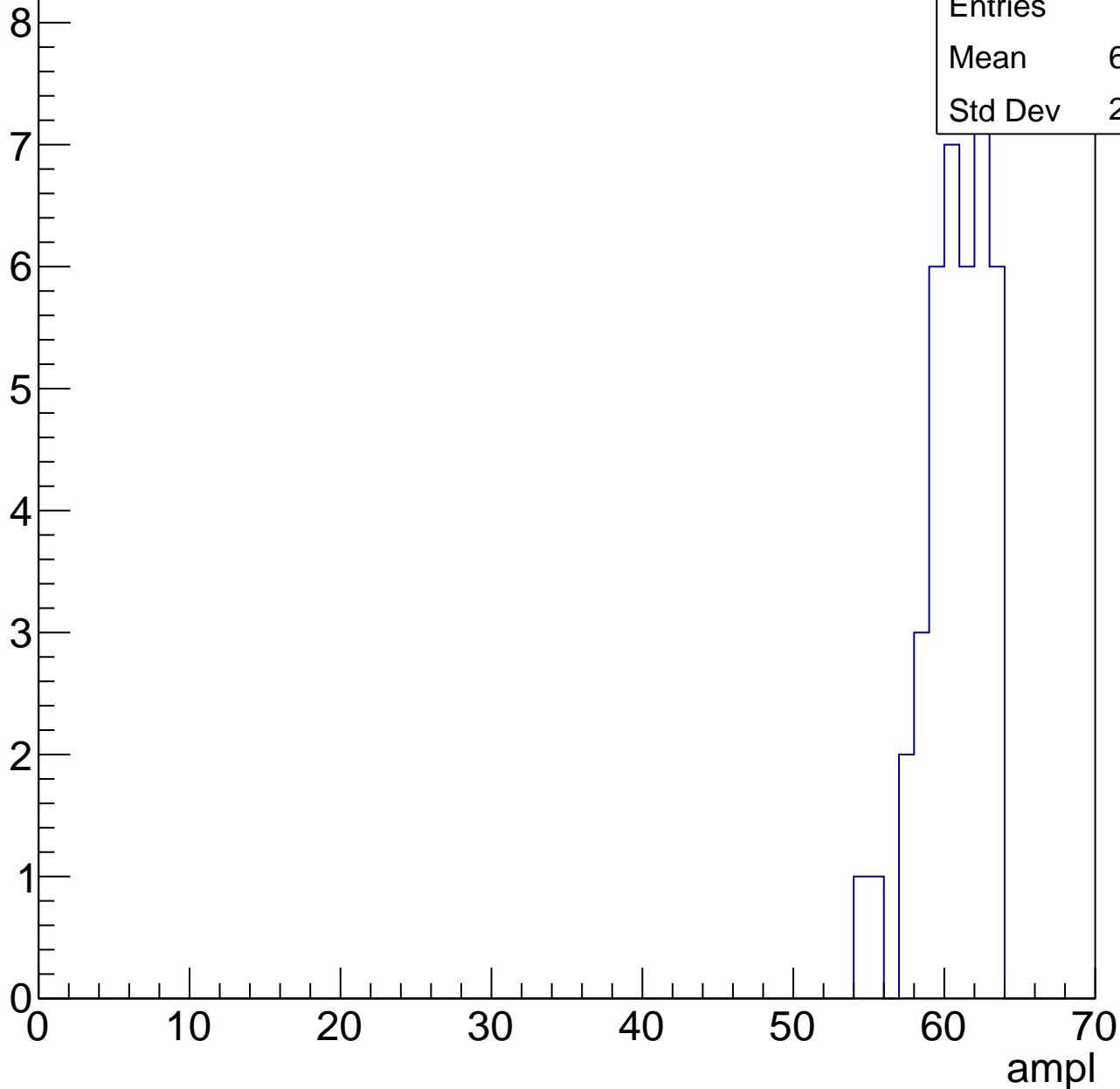


# B1L103S, U22-ch69, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

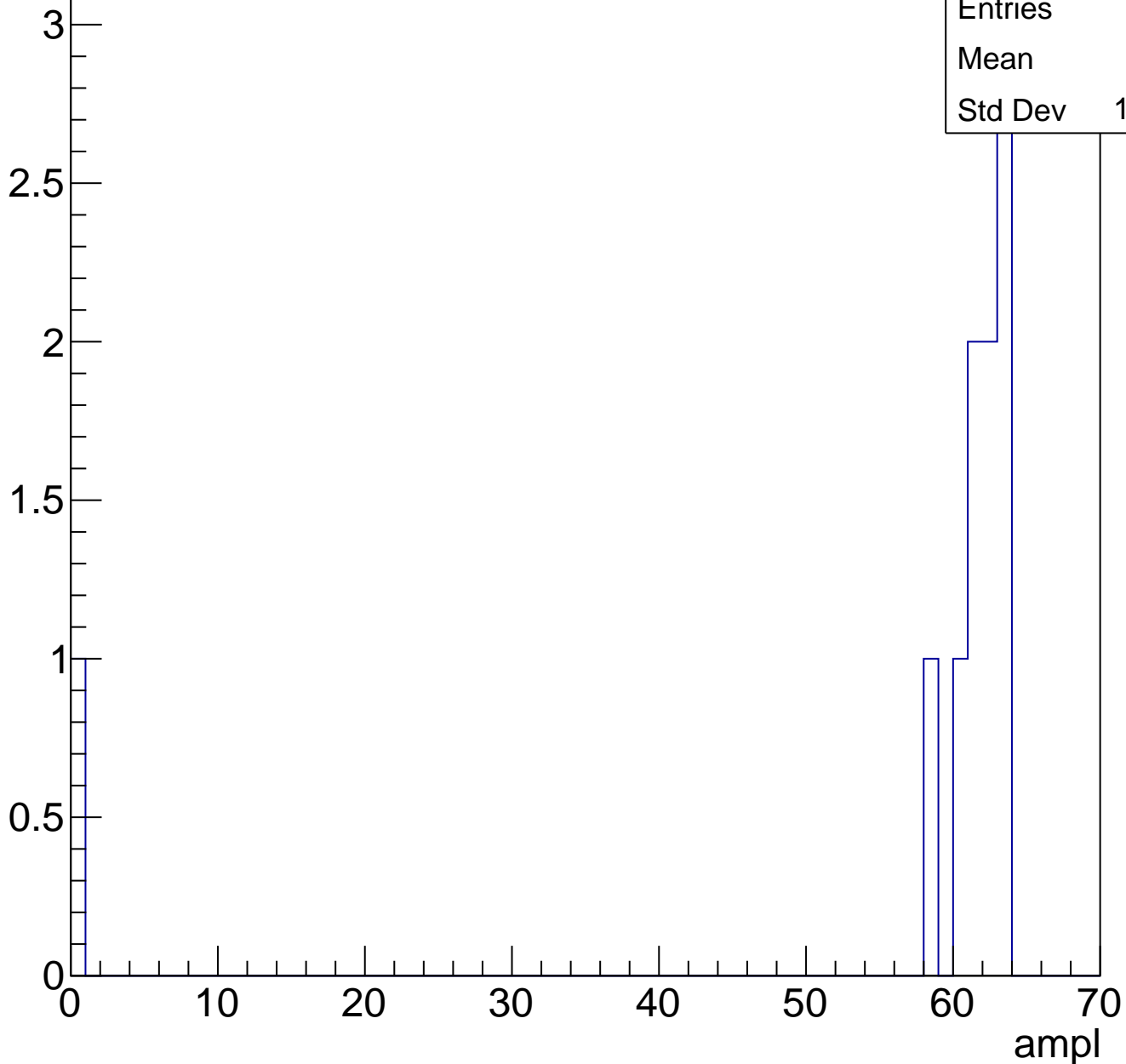
Entries	40
Mean	60.27
Std Dev	2.156



# B1L103S, U22-ch69, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch69, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



# B1L103S, U22-ch70, adc0

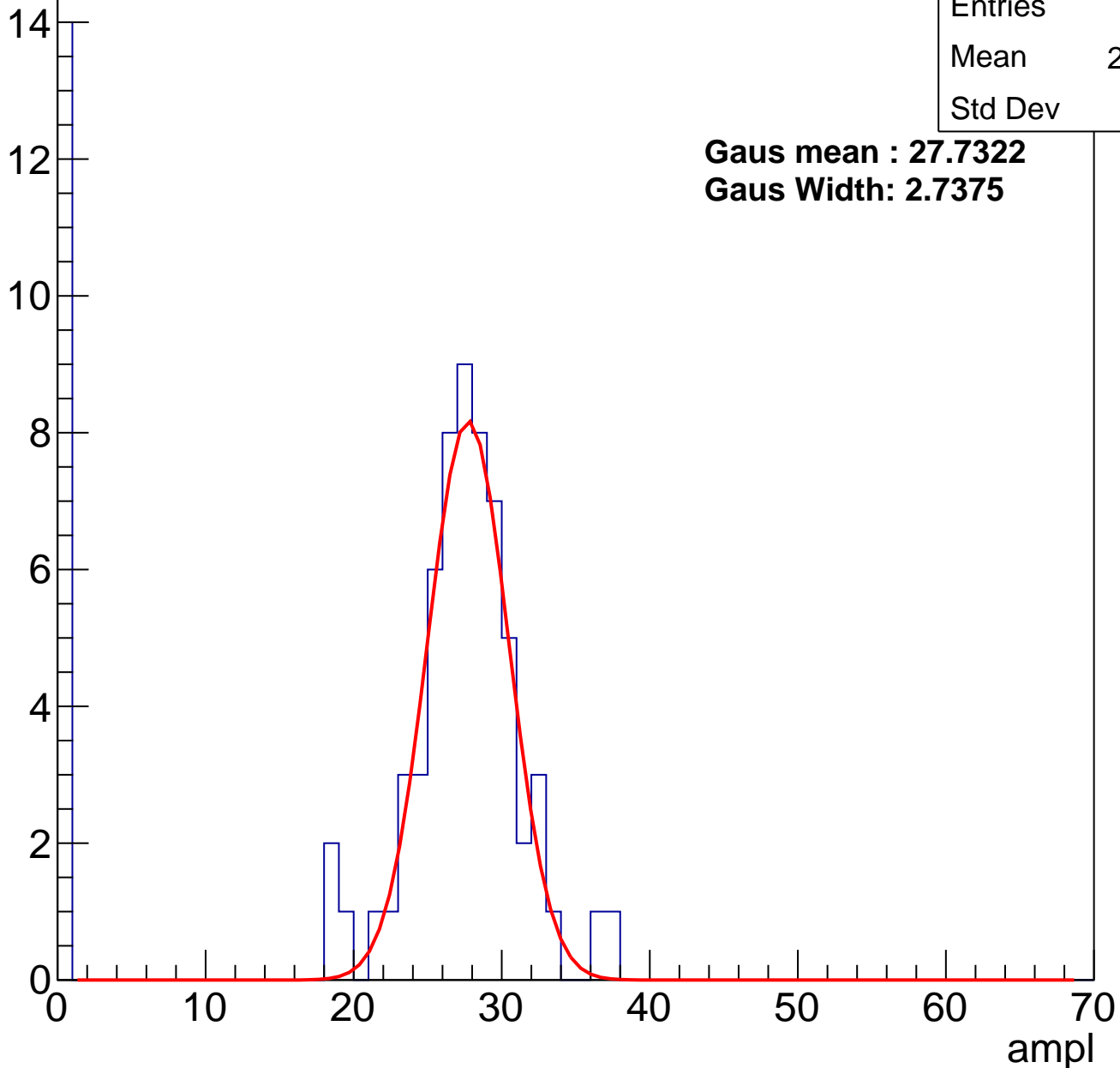
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	22.12
Std Dev	11

**Gaus mean : 27.7322**

**Gaus Width: 2.7375**

Entry



# B1L103S, U22-ch70, adc1

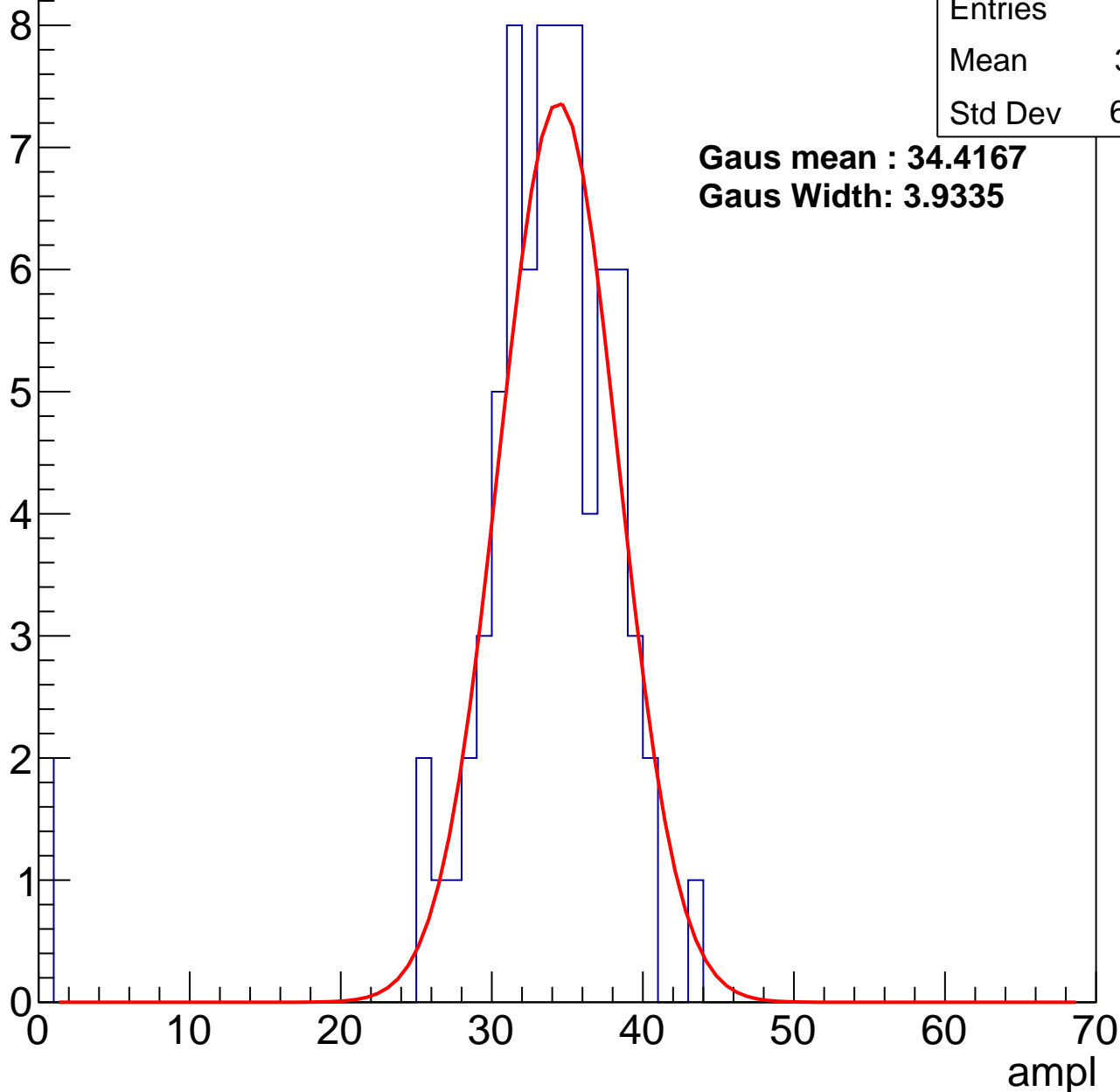
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	32.71
Std Dev	6.482

**Gaus mean : 34.4167**

**Gaus Width: 3.9335**



# B1L103S, U22-ch70, adc2

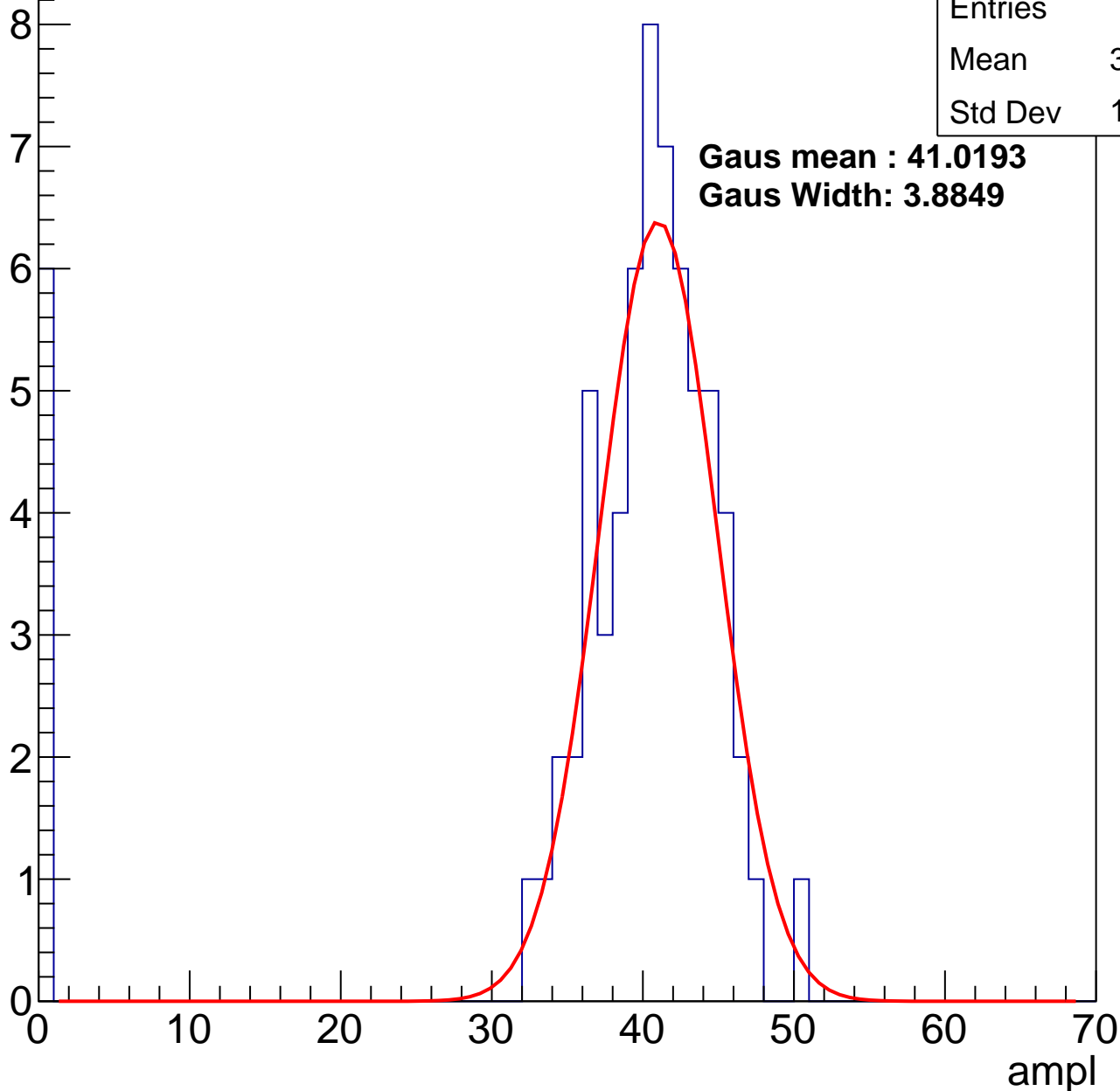
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	36.86
Std Dev	11.89

**Gaus mean : 41.0193**

**Gaus Width: 3.8849**

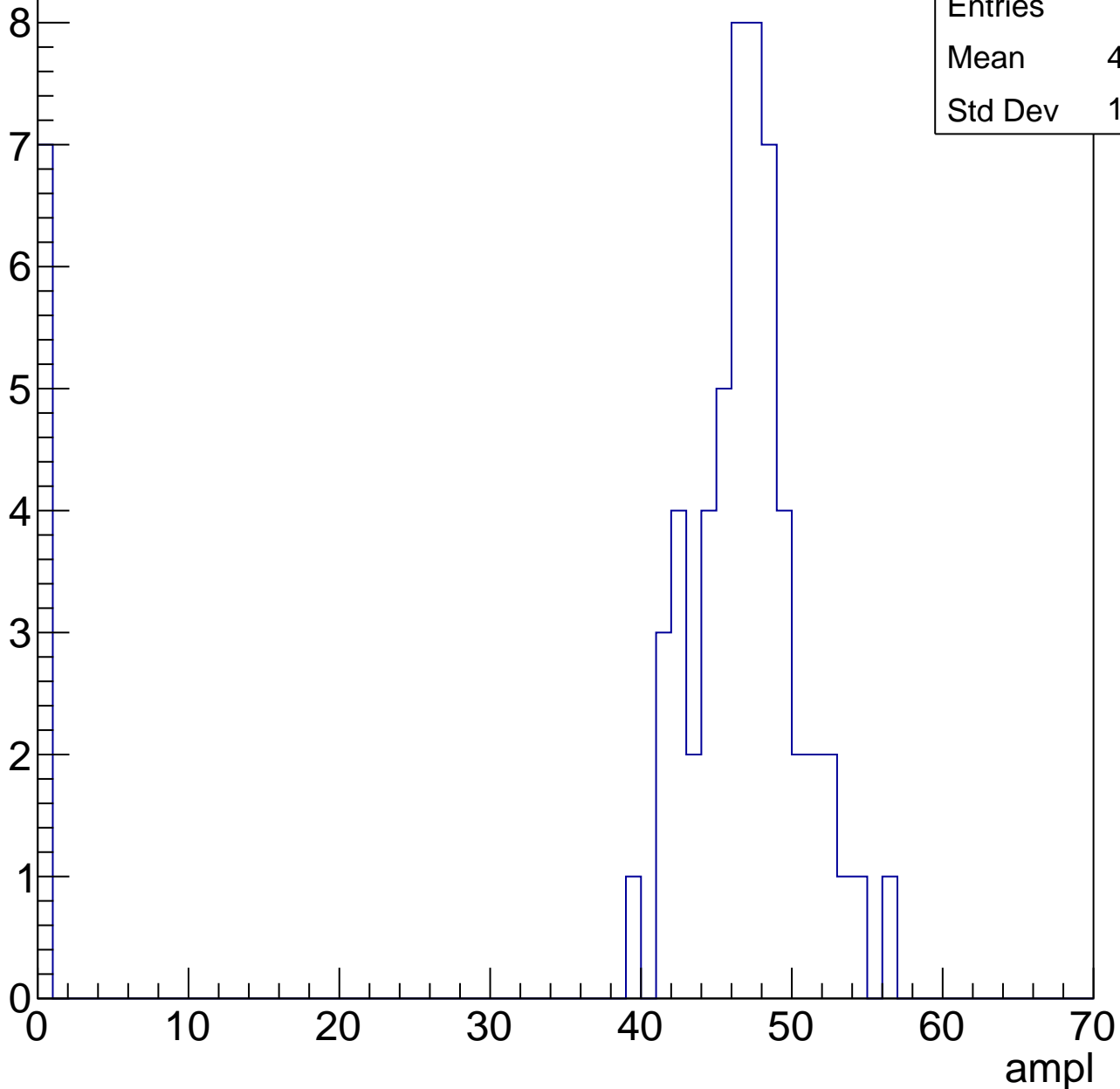


# B1L103S, U22-ch70, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	41.32
Std Dev	15.09

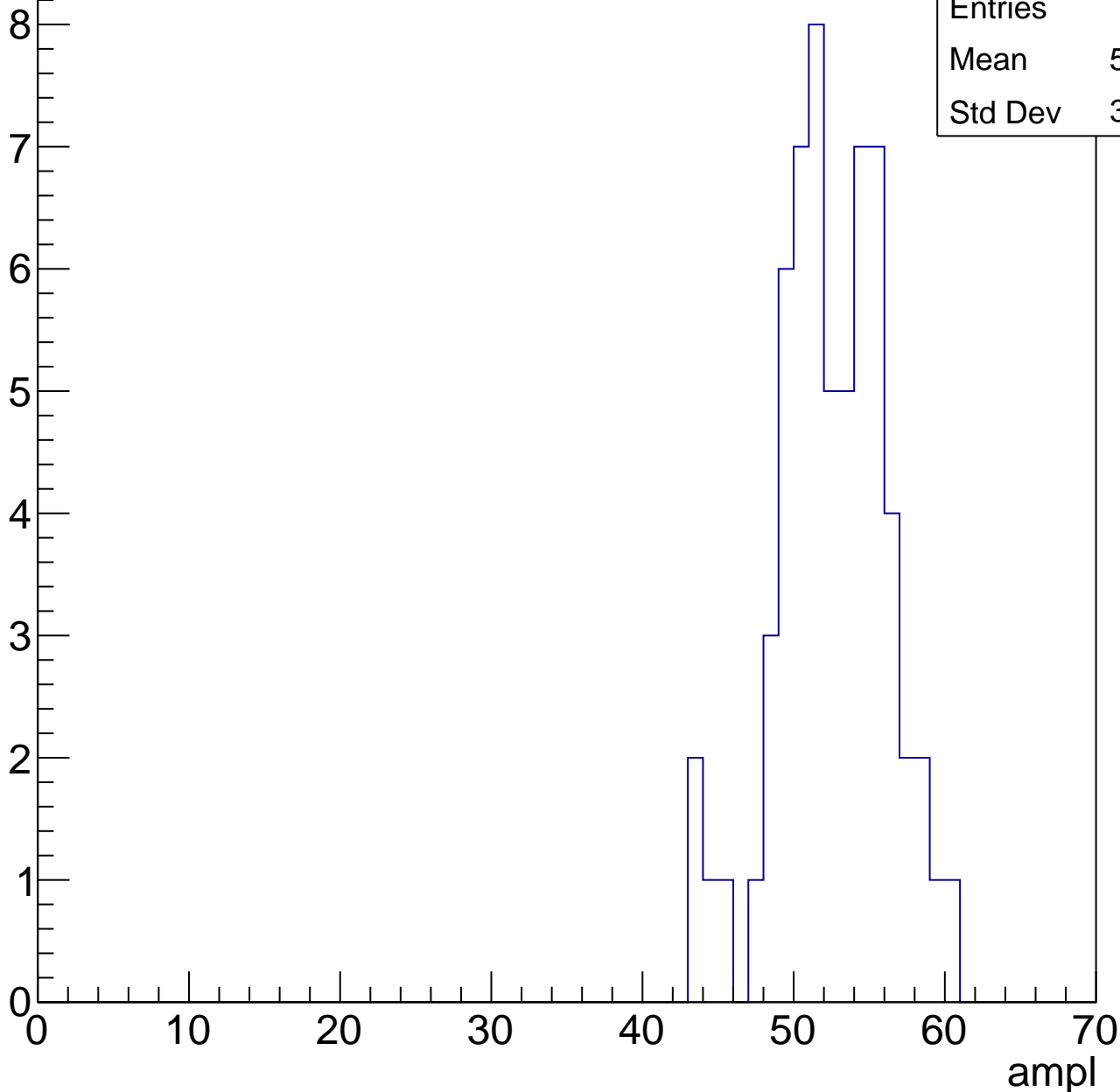


# B1L103S, U22-ch70, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	52.05
Std Dev	3.636

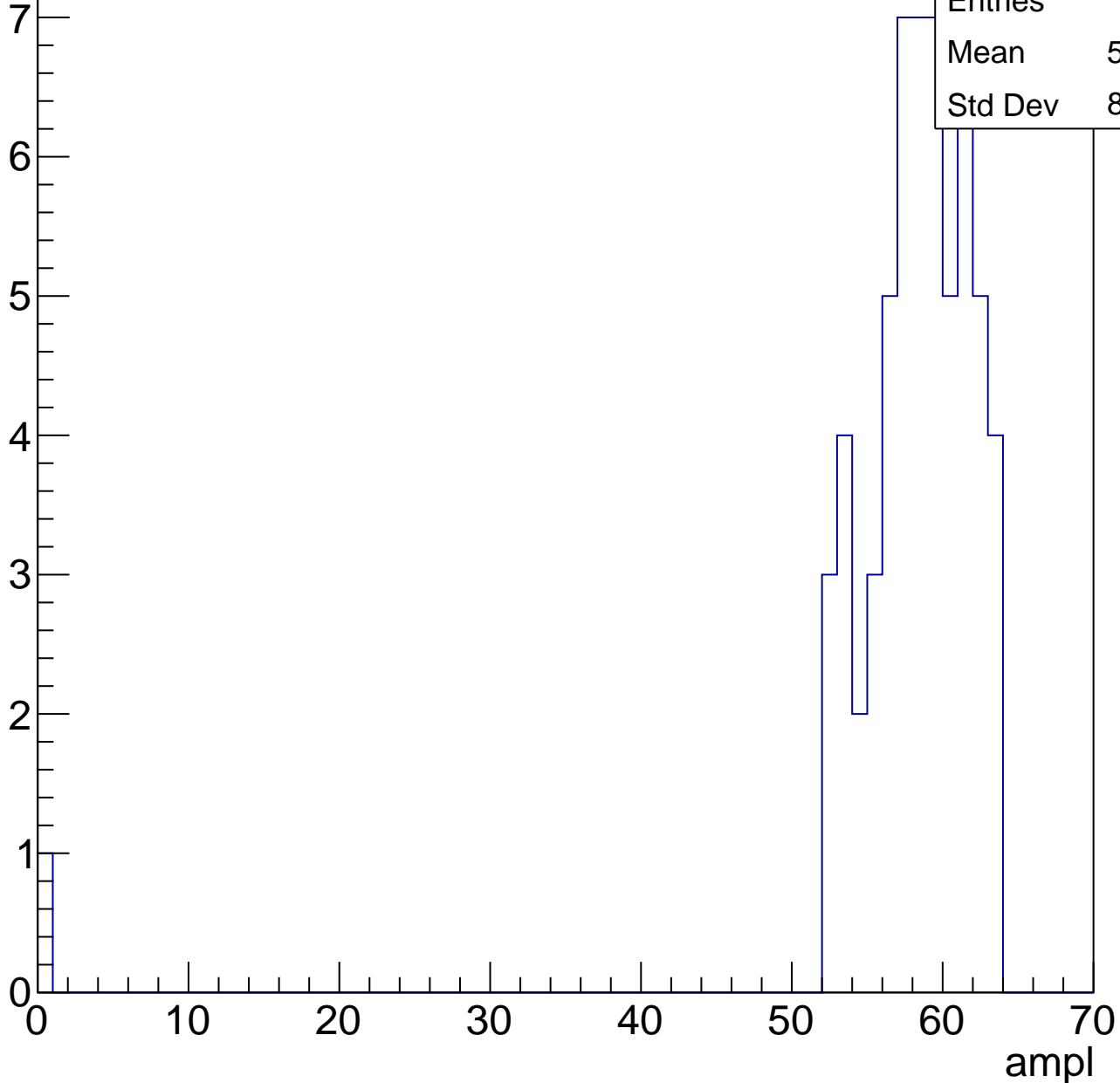


# B1L103S, U22-ch70, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	57.13
Std Dev	8.045

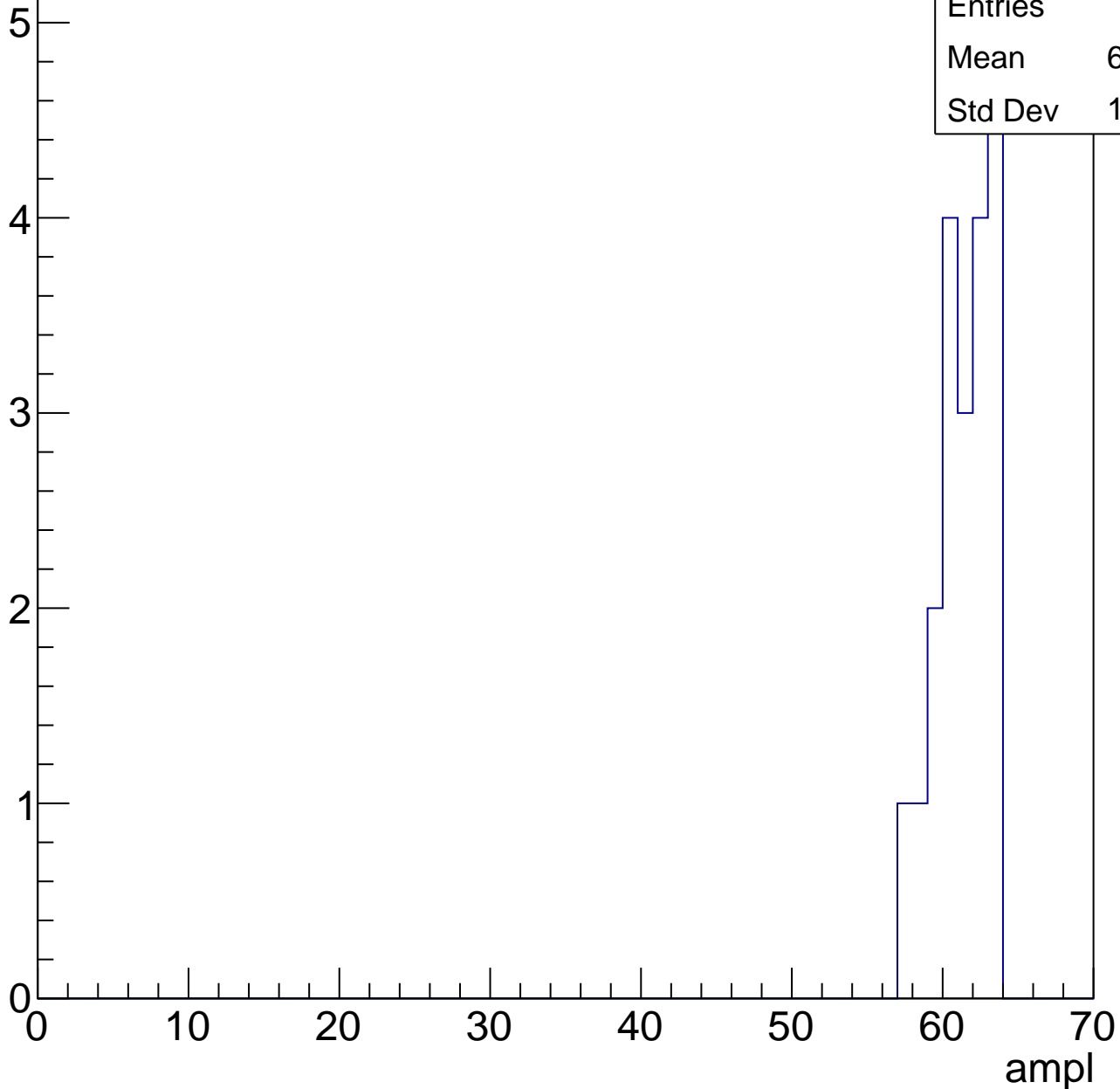


# B1L103S, U22-ch70, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	60.95
Std Dev	1.746



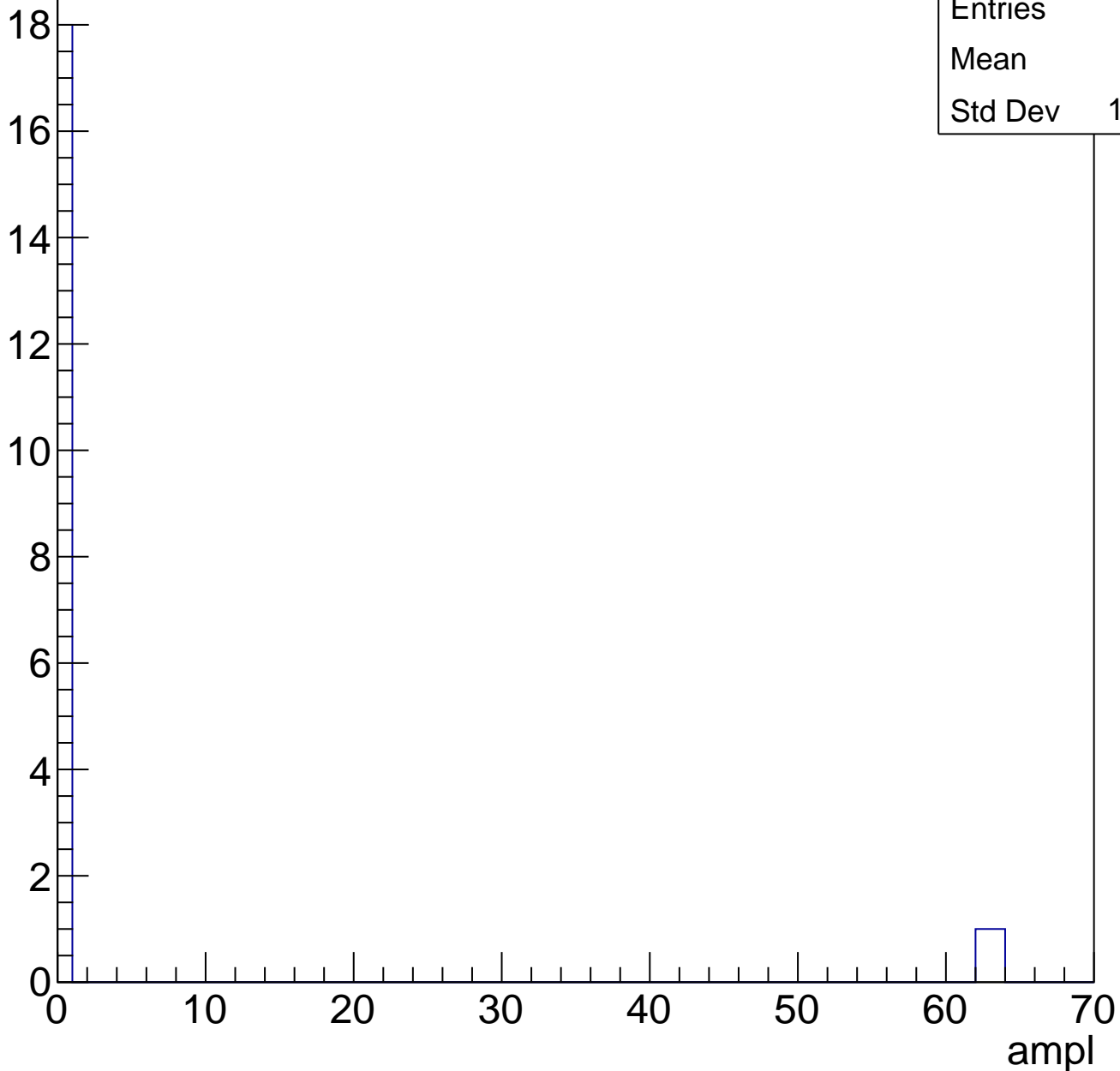


# B1L103S, U22-ch70, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	6.25
Std Dev	18.75

Entry



# B1L103S, U22-ch71, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	88
Mean	23.49
Std Dev	11.94

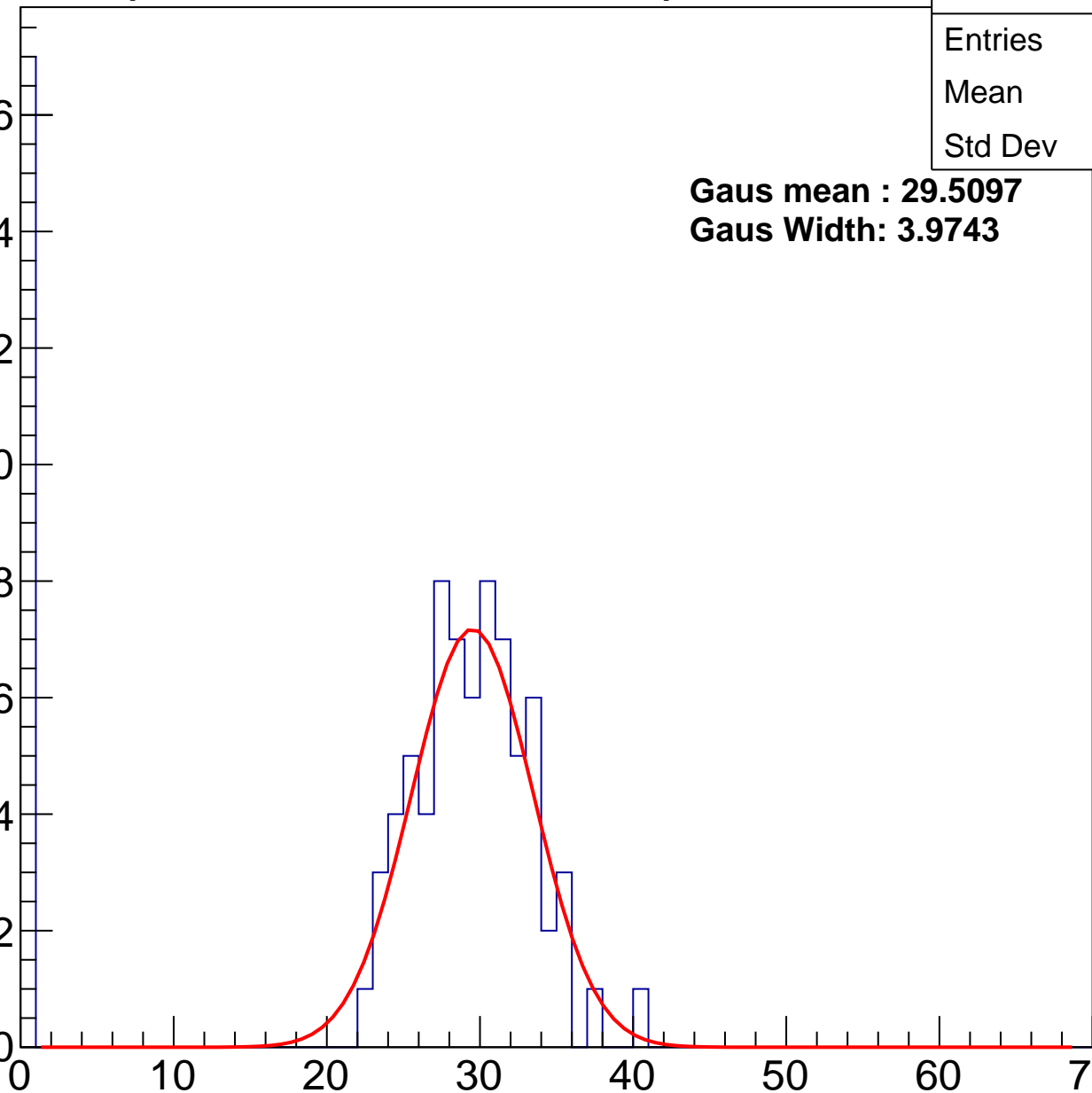
**Gaus mean : 29.5097**

**Gaus Width: 3.9743**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch71, adc1

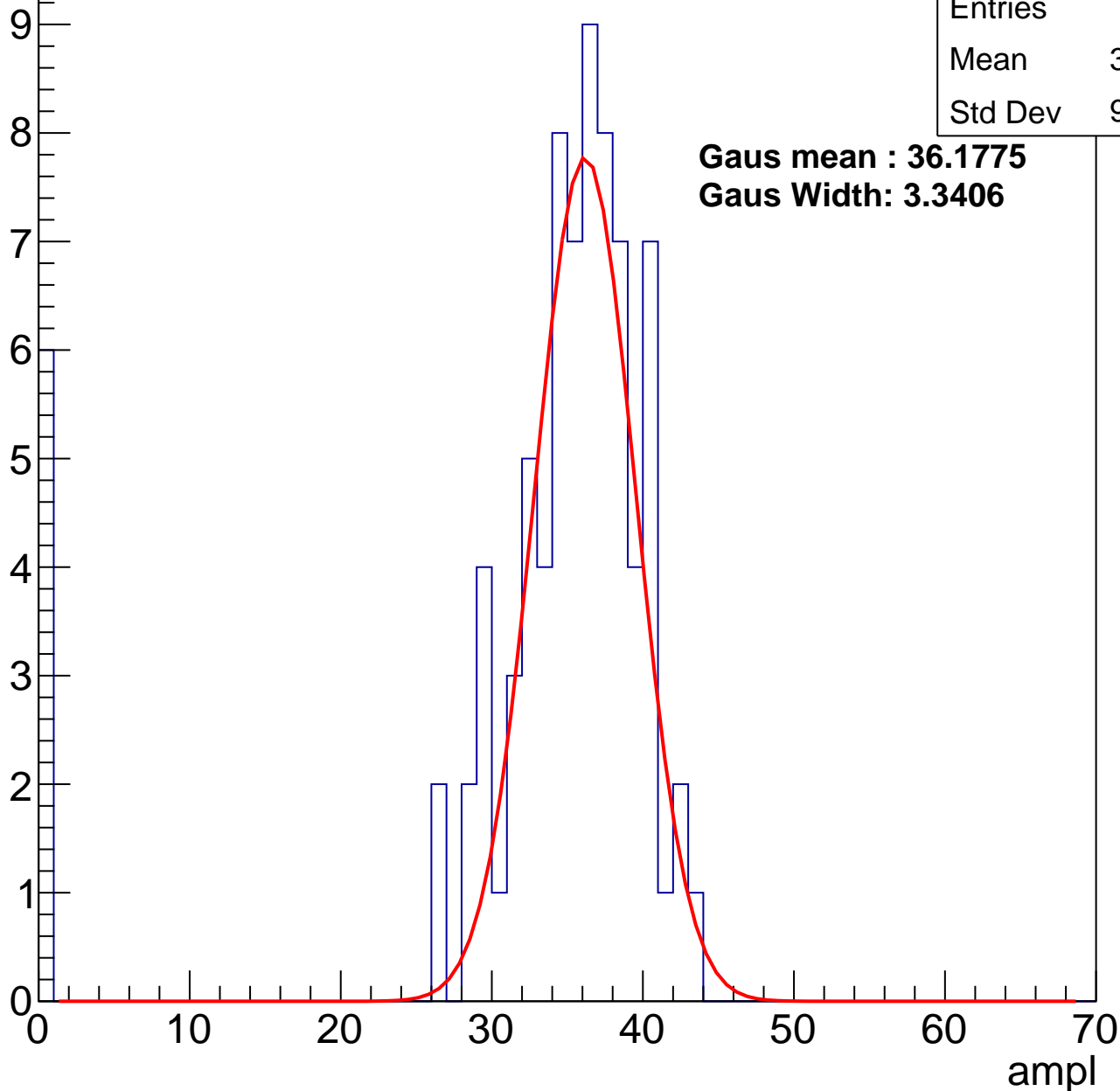
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	32.67
Std Dev	9.936

**Gaus mean : 36.1775**

**Gaus Width: 3.3406**



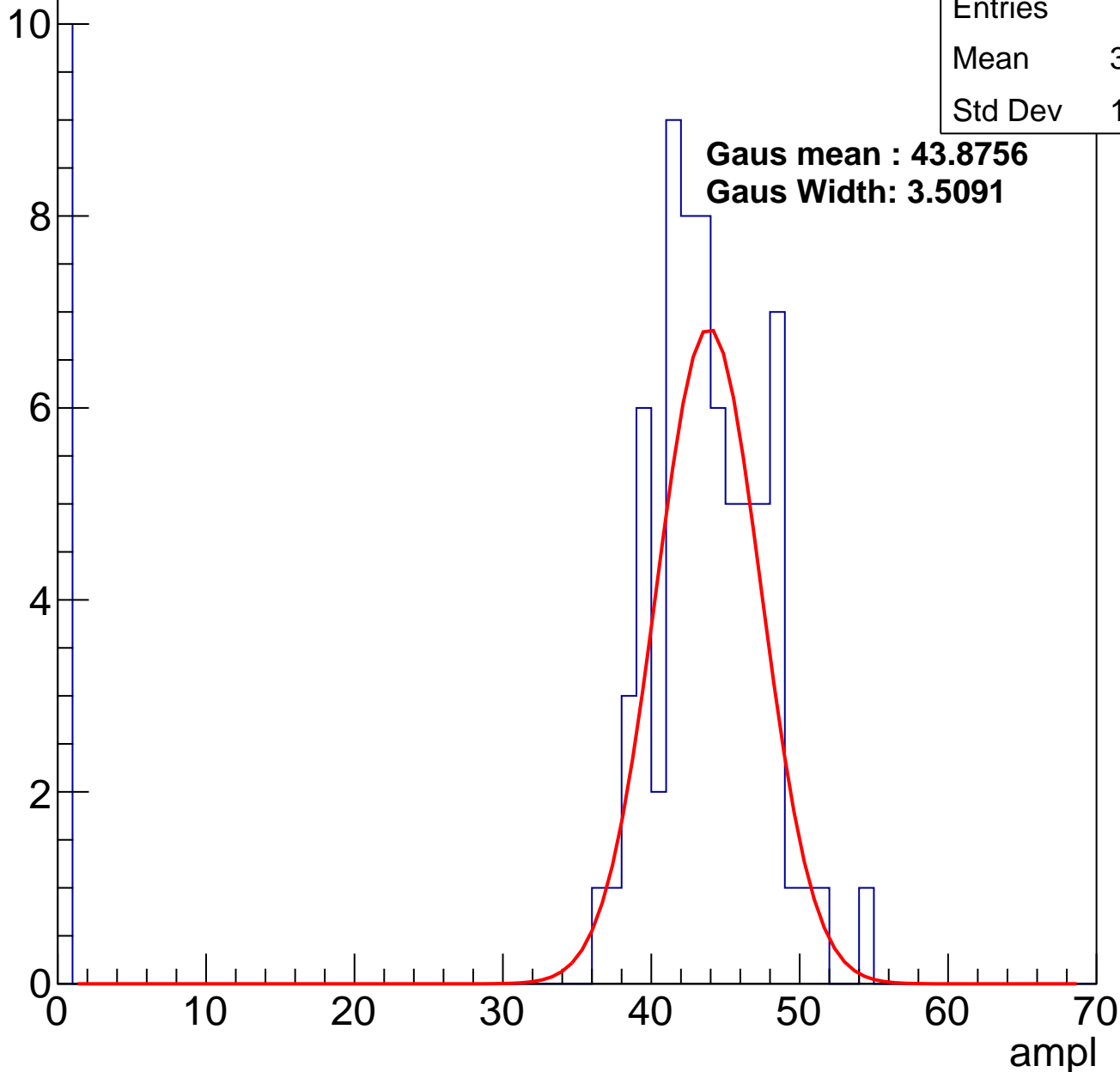
# B1L103S, U22-ch71, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	38.05
Std Dev	14.77

**Gaus mean : 43.8756**  
**Gaus Width: 3.5091**

Entry

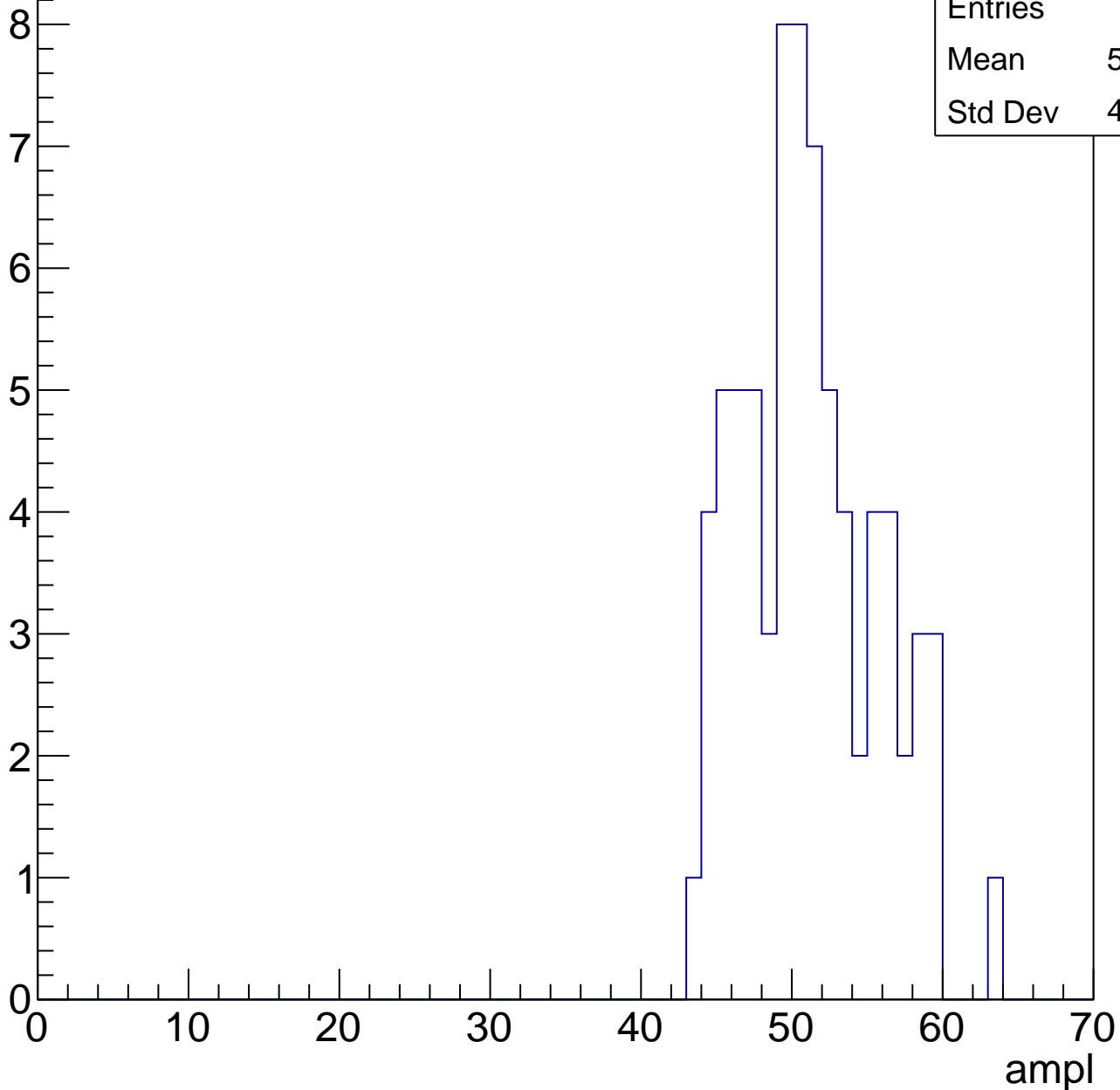


# B1L103S, U22-ch71, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	50.73
Std Dev	4.449

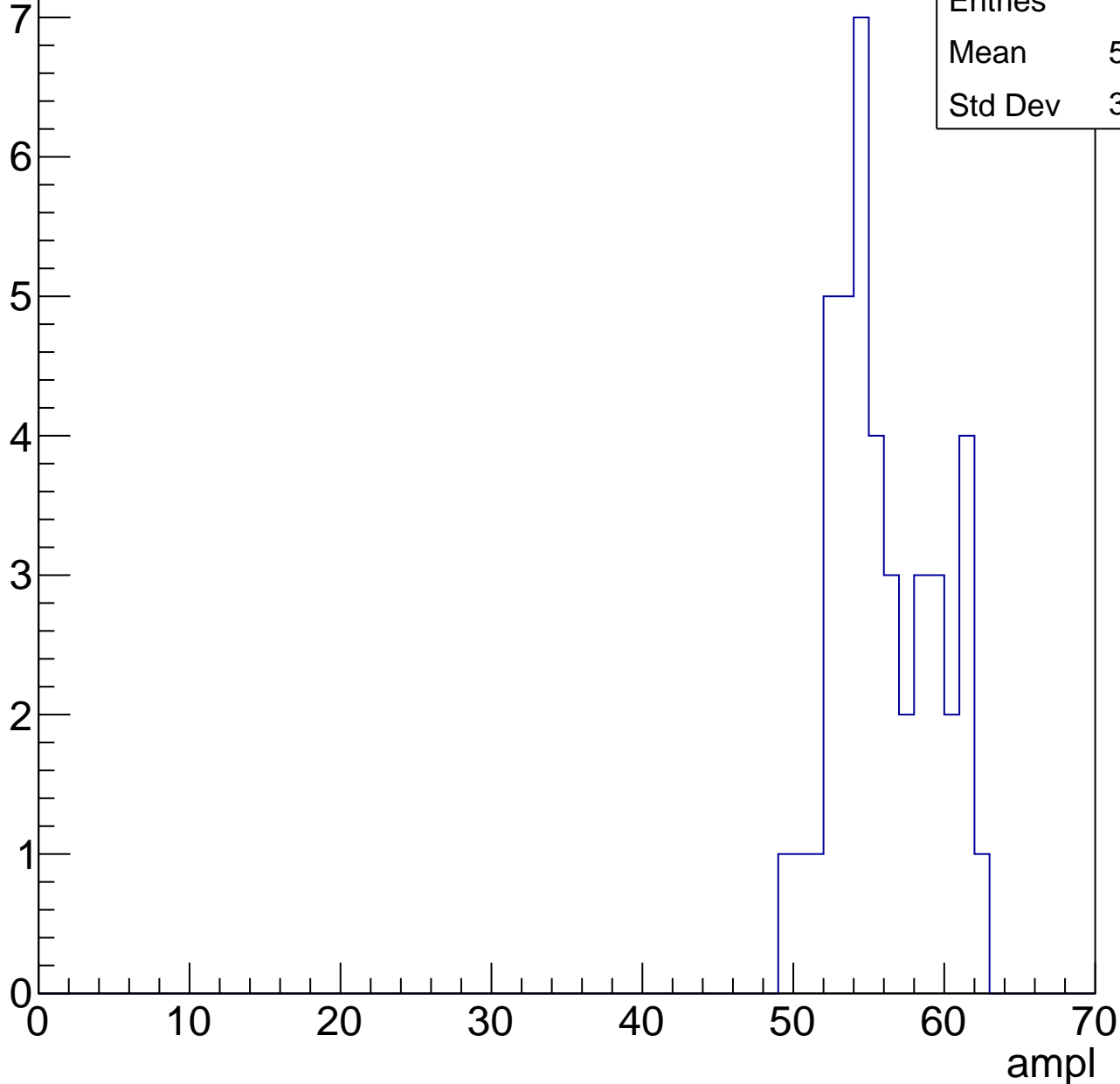


# B1L103S, U22-ch71, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	42
Mean	55.52
Std Dev	3.333

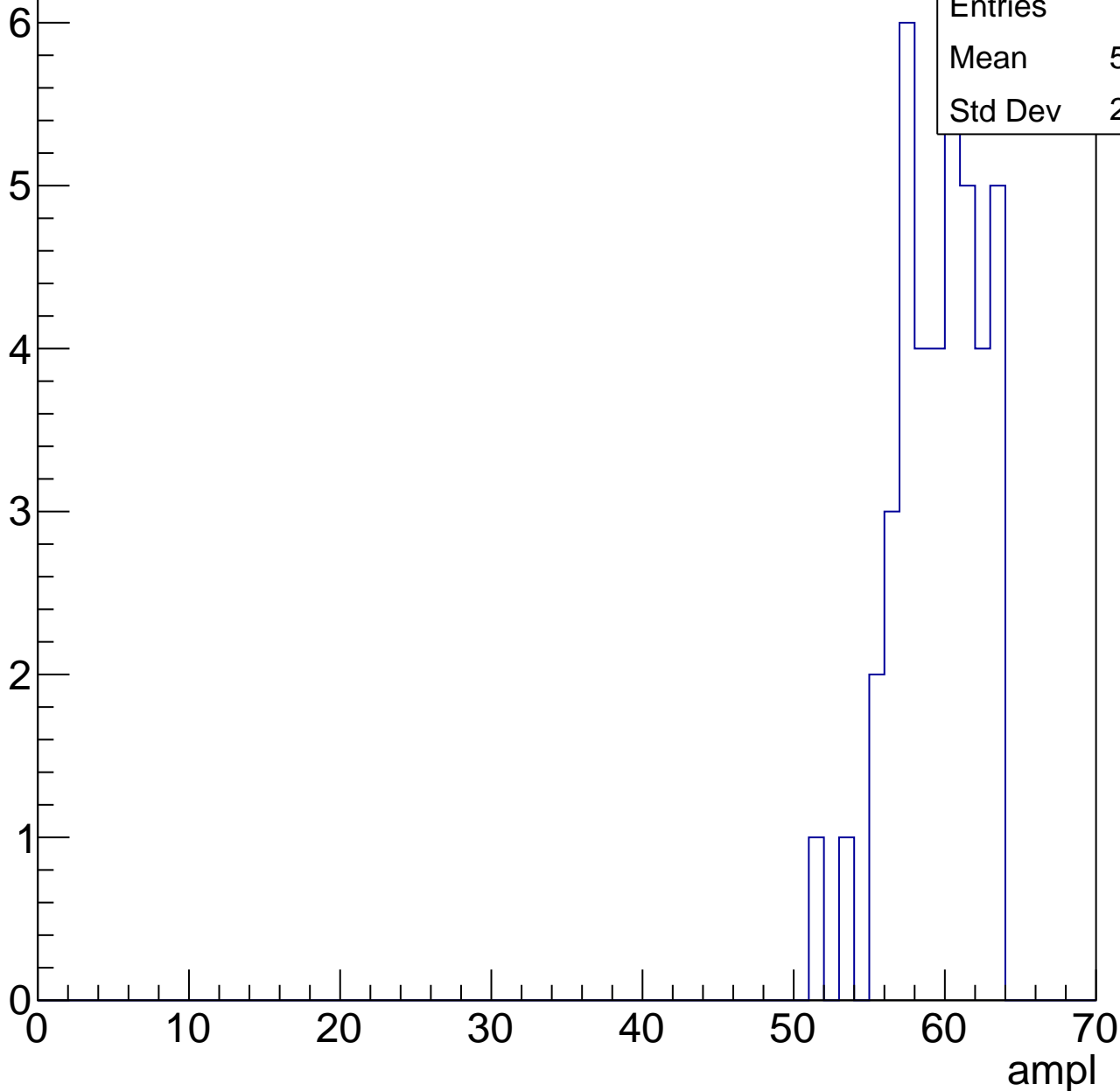


# B1L103S, U22-ch71, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	41
Mean	59.02
Std Dev	2.833

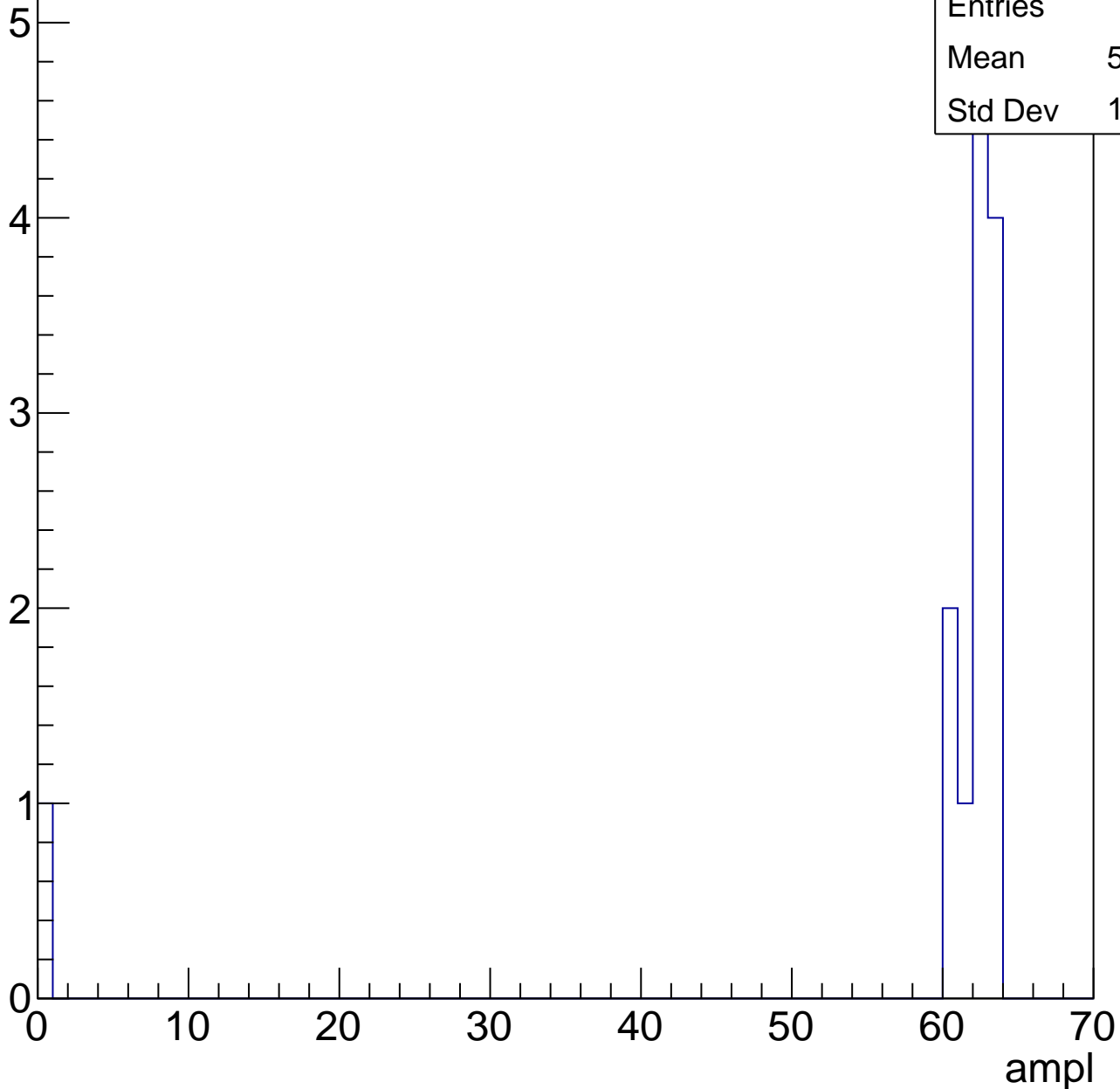


# B1L103S, U22-ch71, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	13
Mean	57.15
Std Dev	16.53





# B1L103S, U22-ch71, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch72, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	22.55
Std Dev	10.94

**Gaus mean : 27.5955**

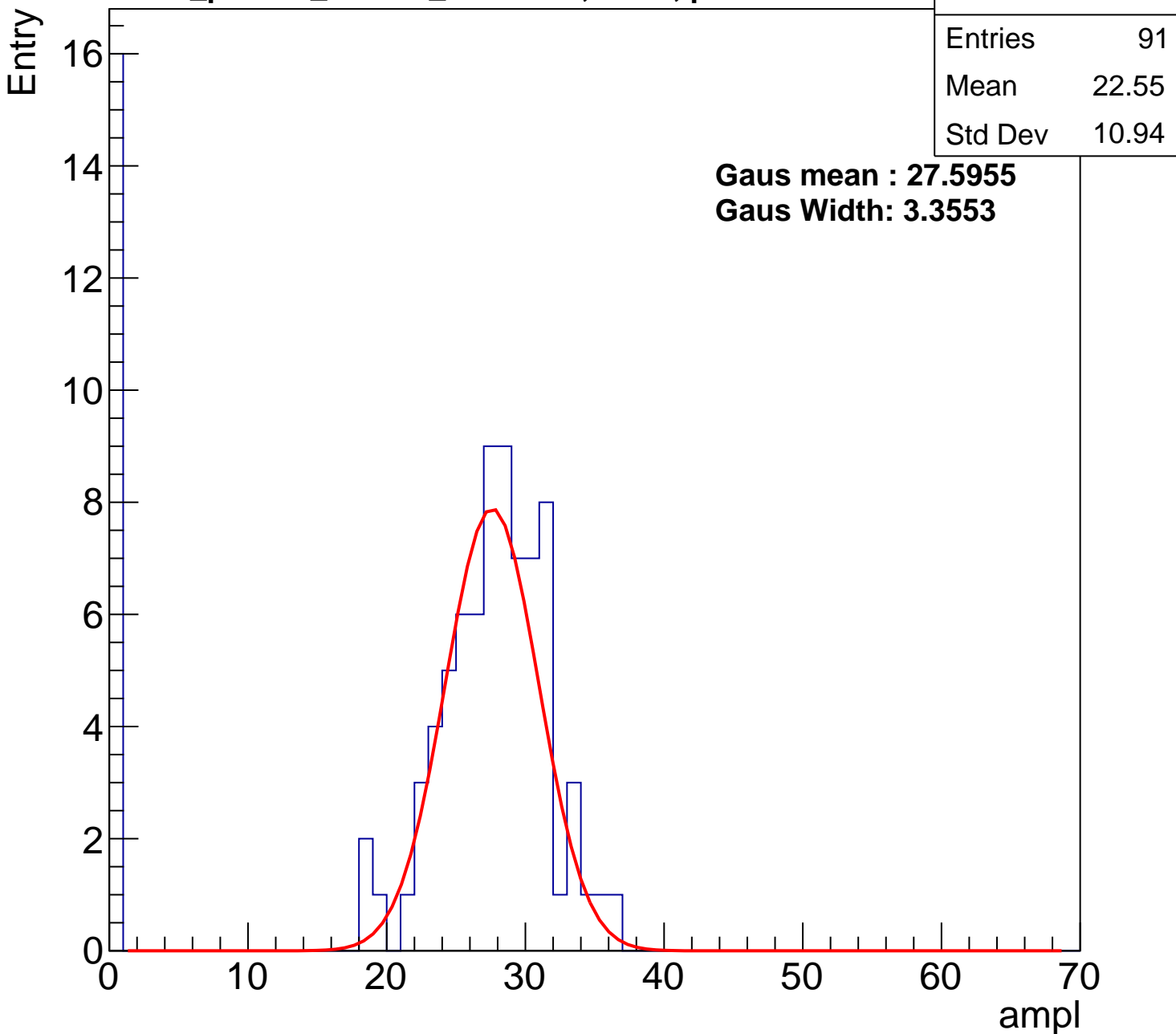
**Gaus Width: 3.3553**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch72, adc1

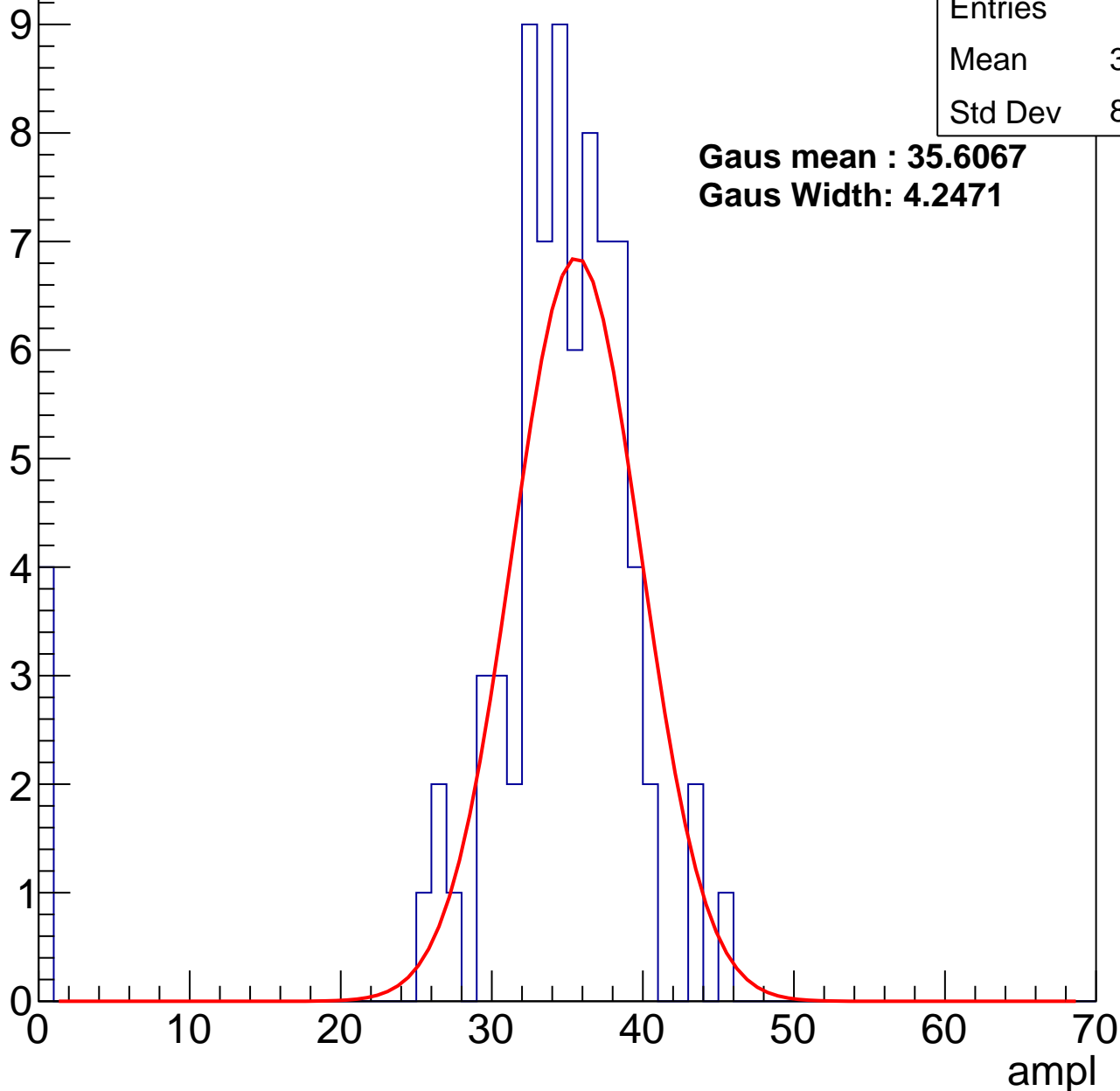
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	32.79
Std Dev	8.489

**Gaus mean : 35.6067**

**Gaus Width: 4.2471**



# B1L103S, U22-ch72, adc2

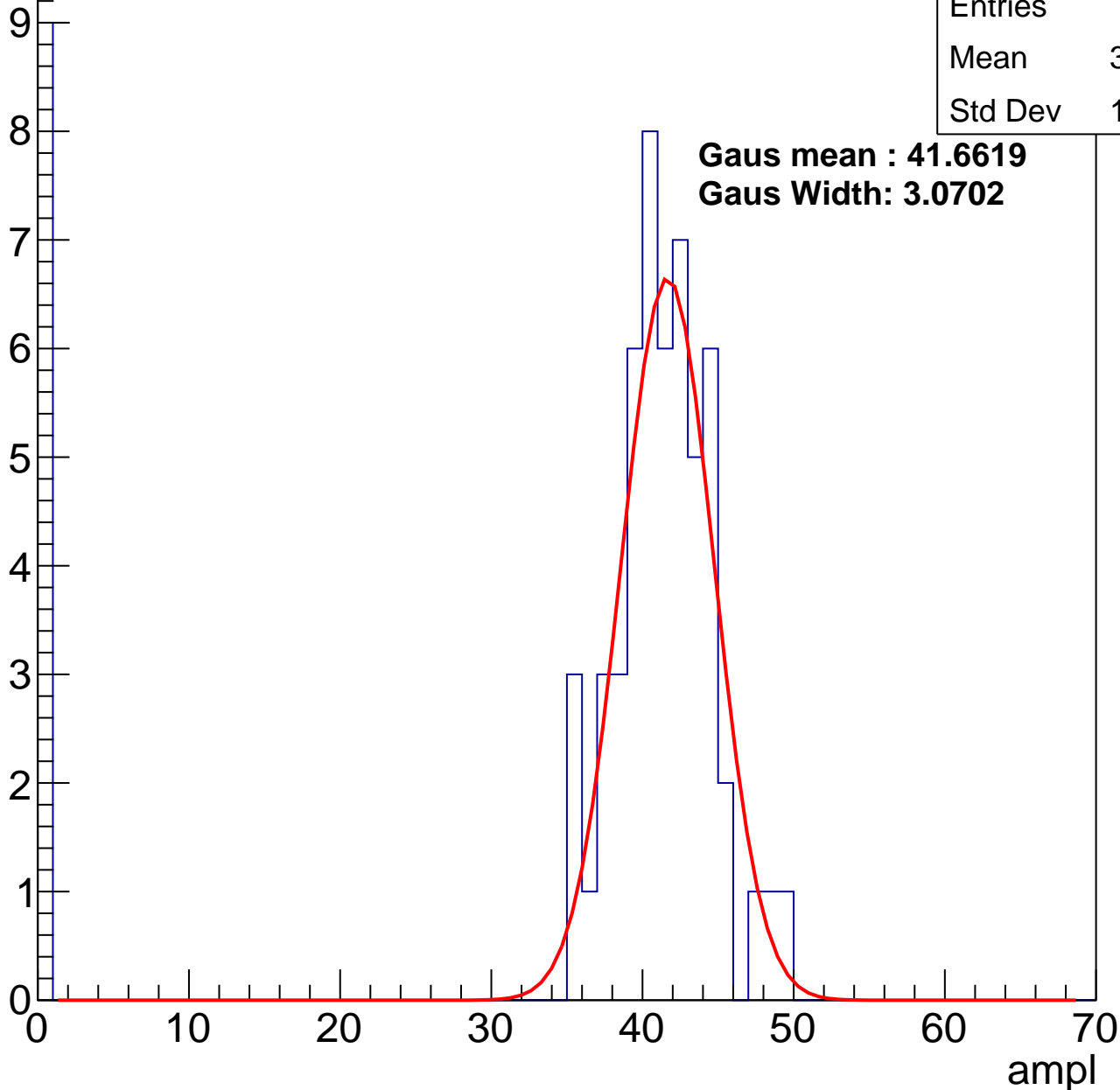
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	35.05
Std Dev	14.72

**Gaus mean : 41.6619**

**Gaus Width: 3.0702**



# B1L103S, U22-ch72, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

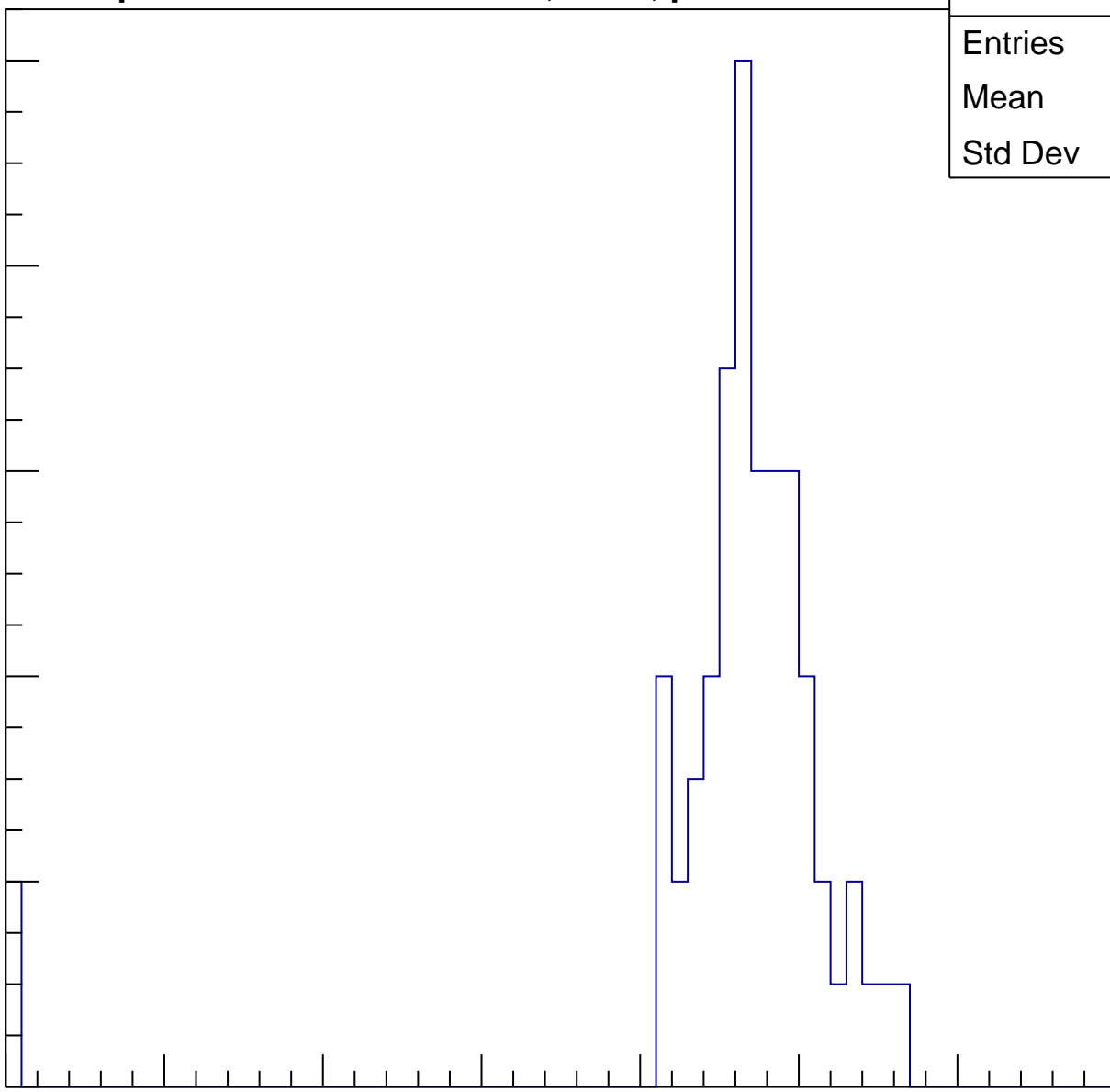
Entries	62
Mean	45.44
Std Dev	8.954

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

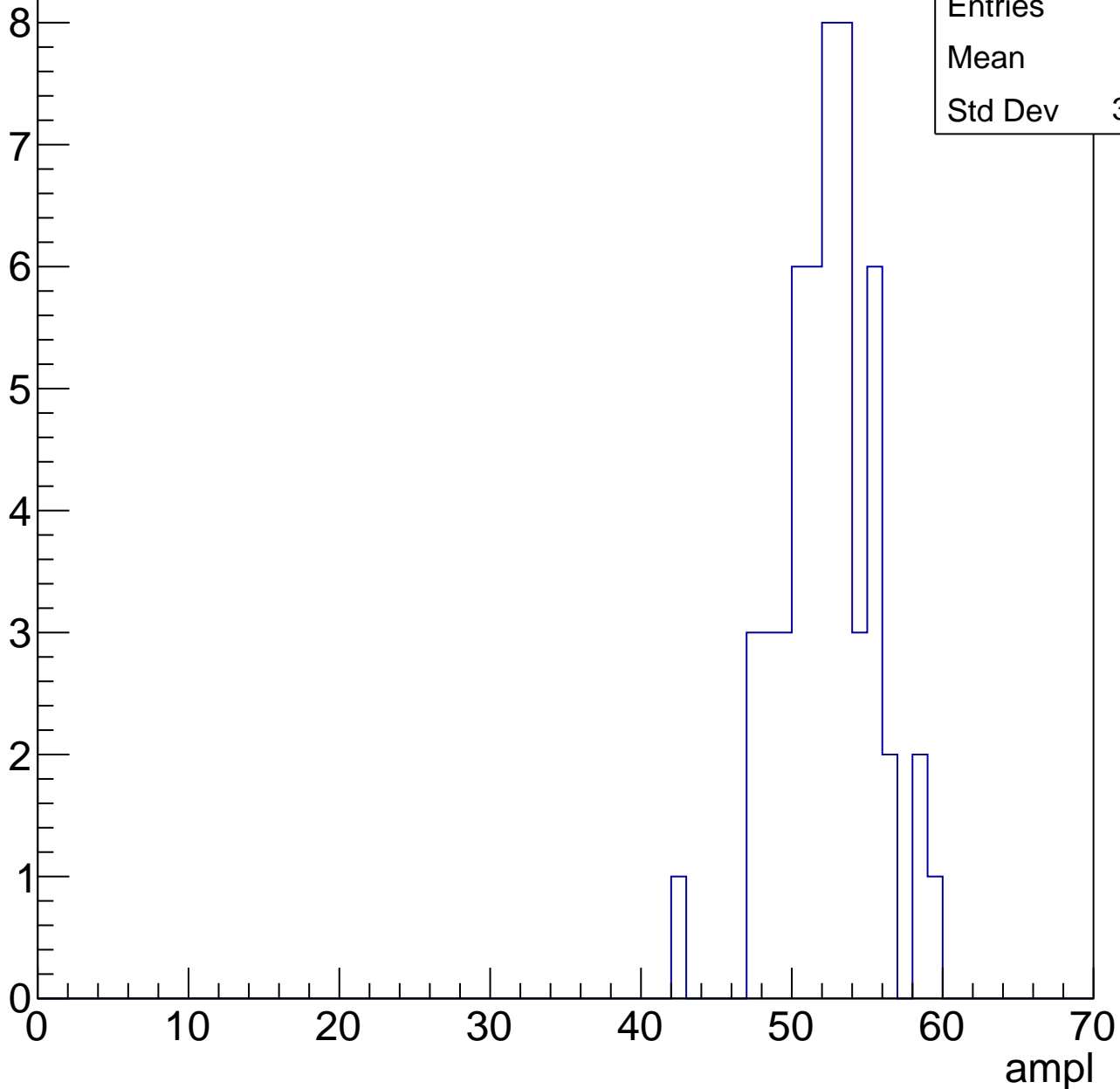


# B1L103S, U22-ch72, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

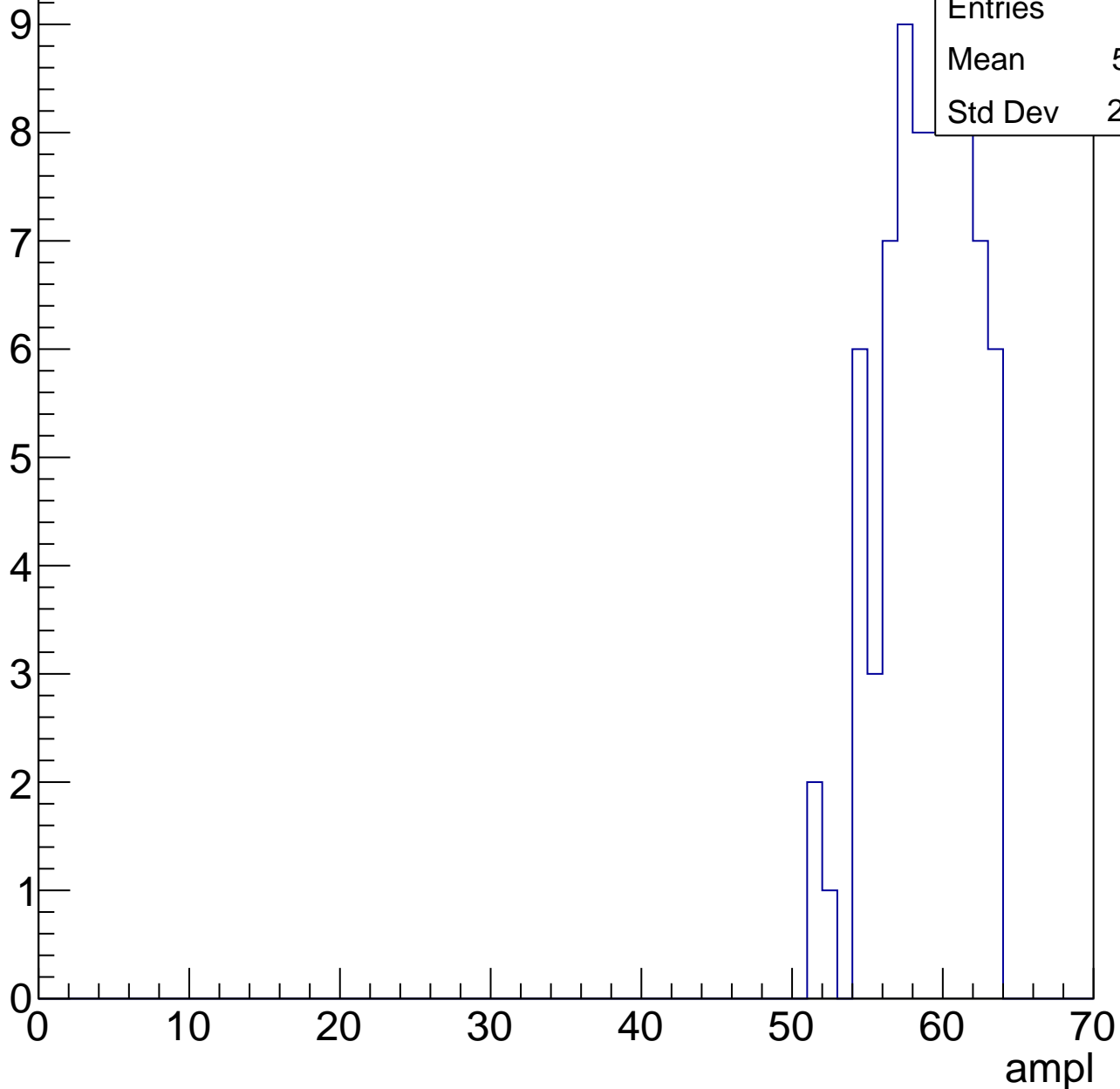
Entries	52
Mean	51.9
Std Dev	3.121



# B1L103S, U22-ch72, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

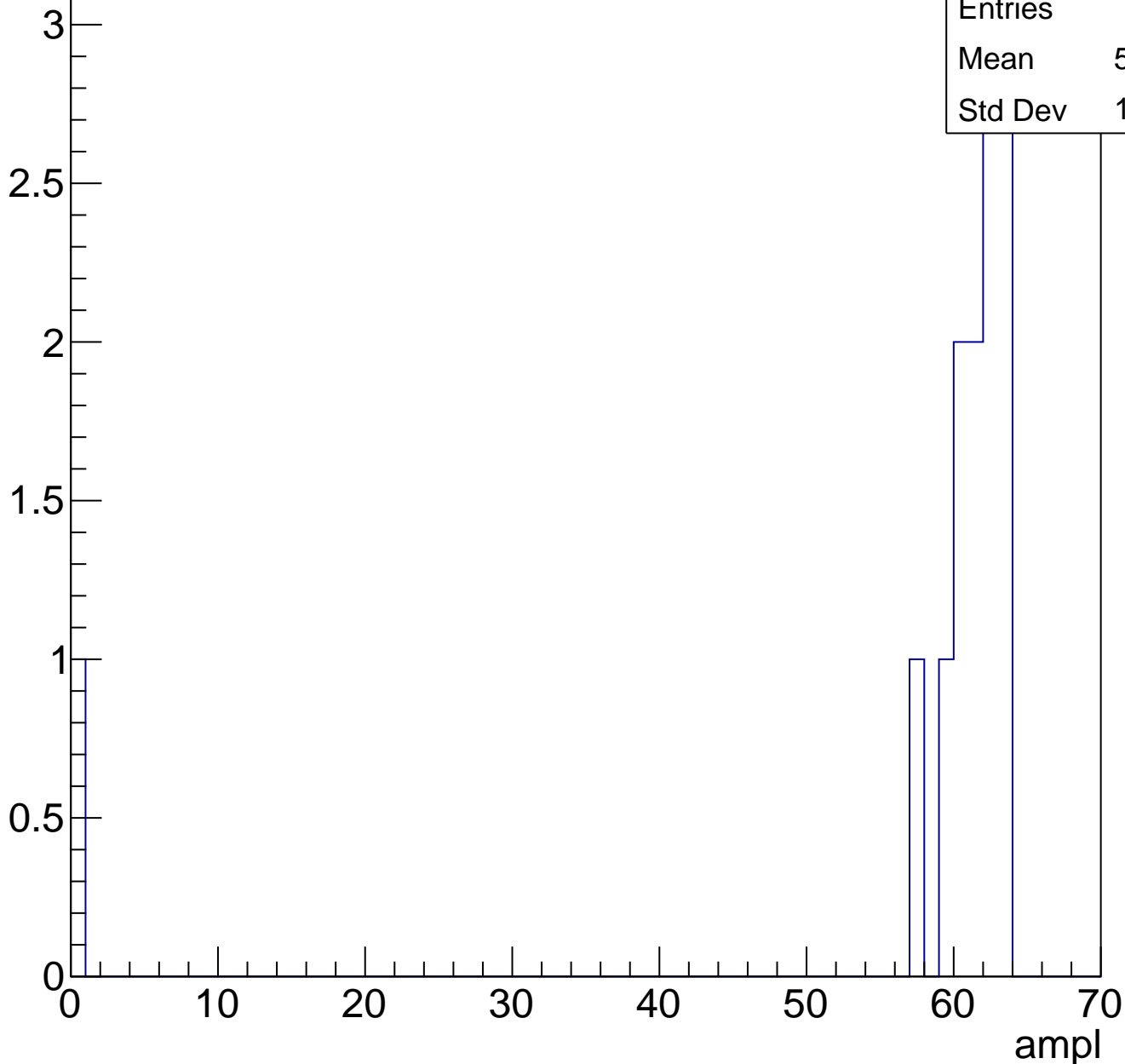
Entry



# B1L103S, U22-ch72, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



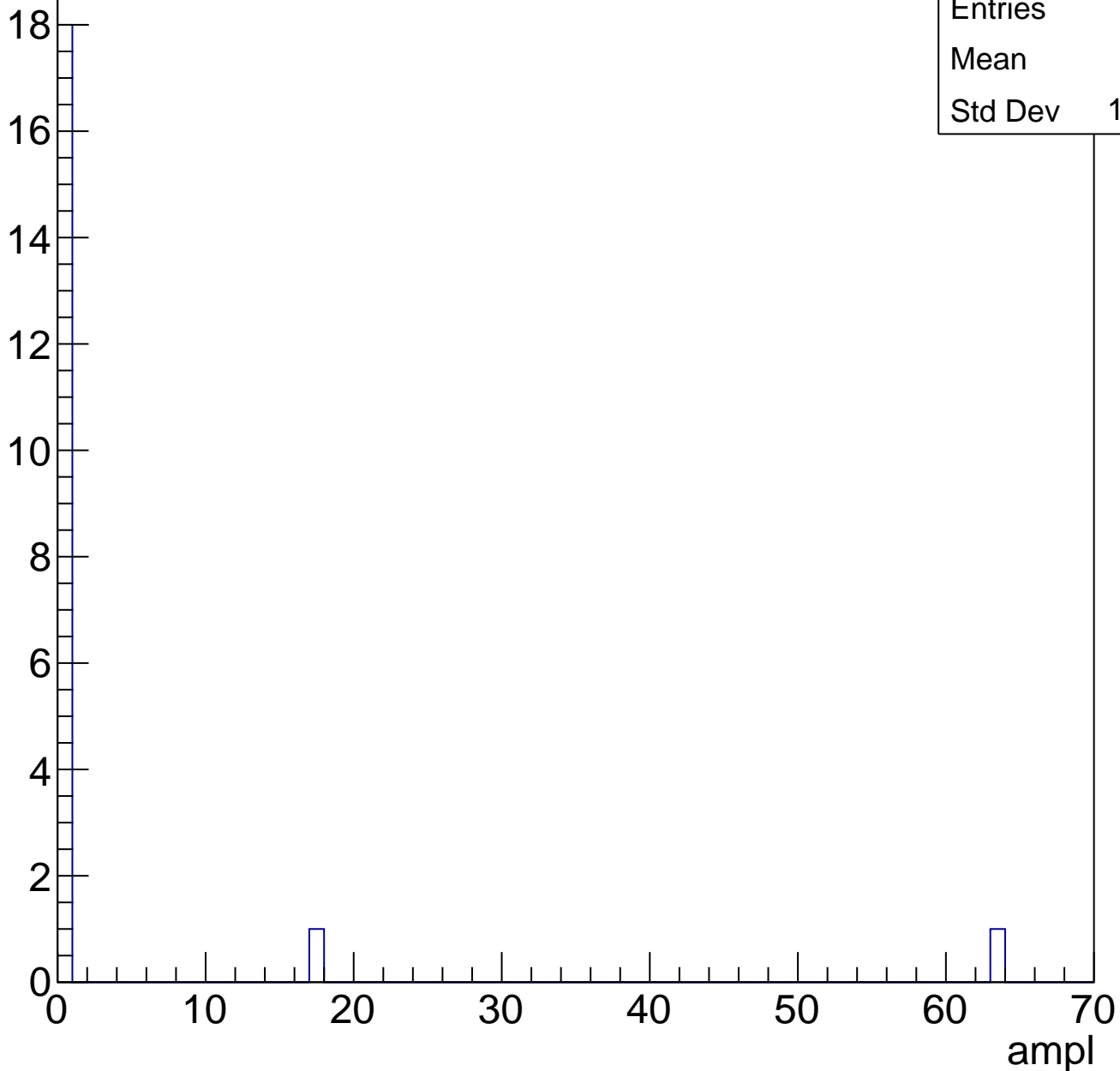


# B1L103S, U22-ch72, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	4
Std Dev	14.03

Entry



# B1L103S, U22-ch73, adc0

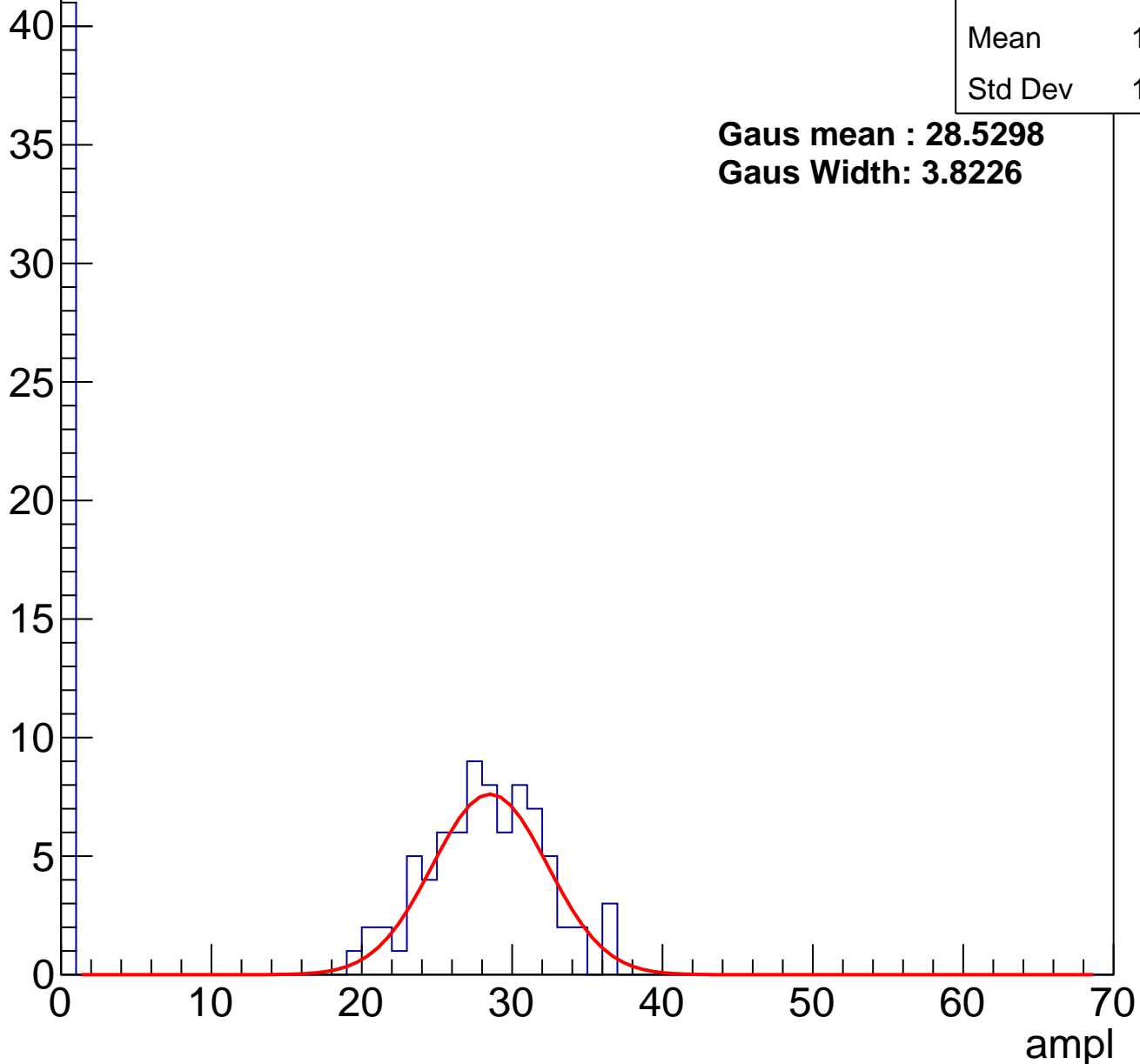
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	118
Mean	18.14
Std Dev	13.58

**Gaus mean : 28.5298**

**Gaus Width: 3.8226**

Entry



# B1L103S, U22-ch73, adc1

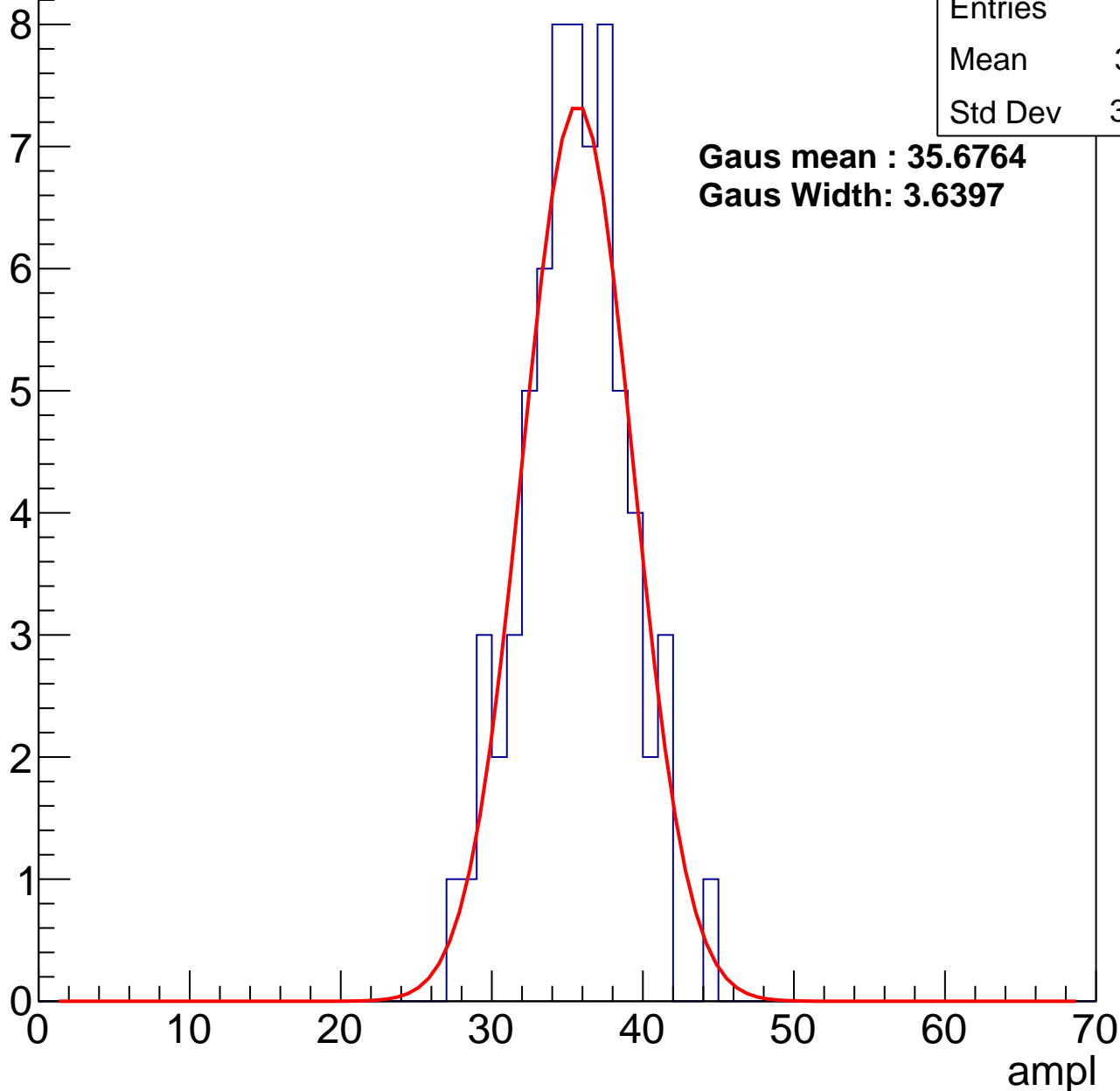
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.01
Std Dev	3.419

**Gaus mean : 35.6764**

**Gaus Width: 3.6397**



# B1L103S, U22-ch73, adc2

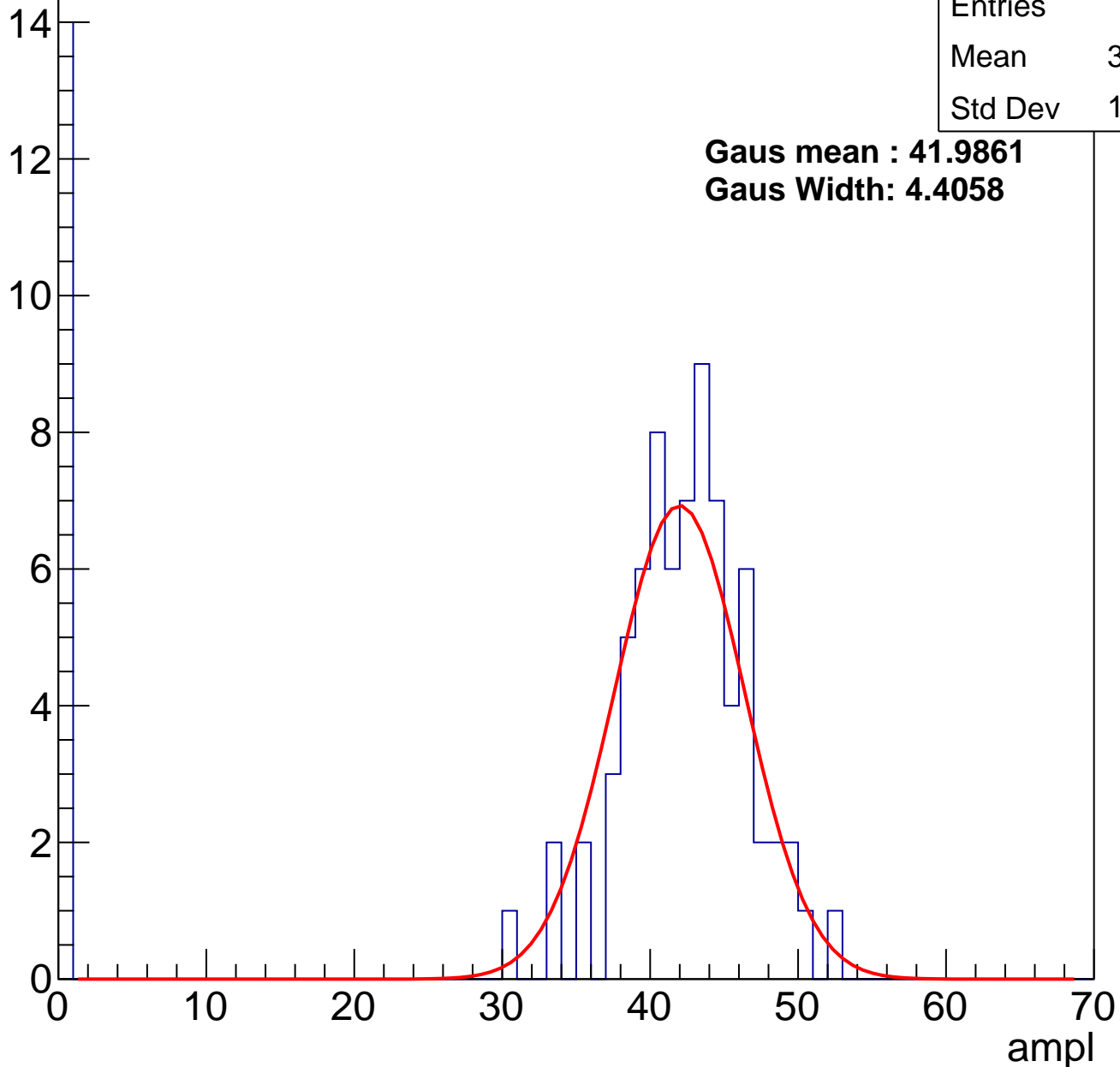
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	88
Mean	35.25
Std Dev	15.77

**Gaus mean : 41.9861**

**Gaus Width: 4.4058**

Entry

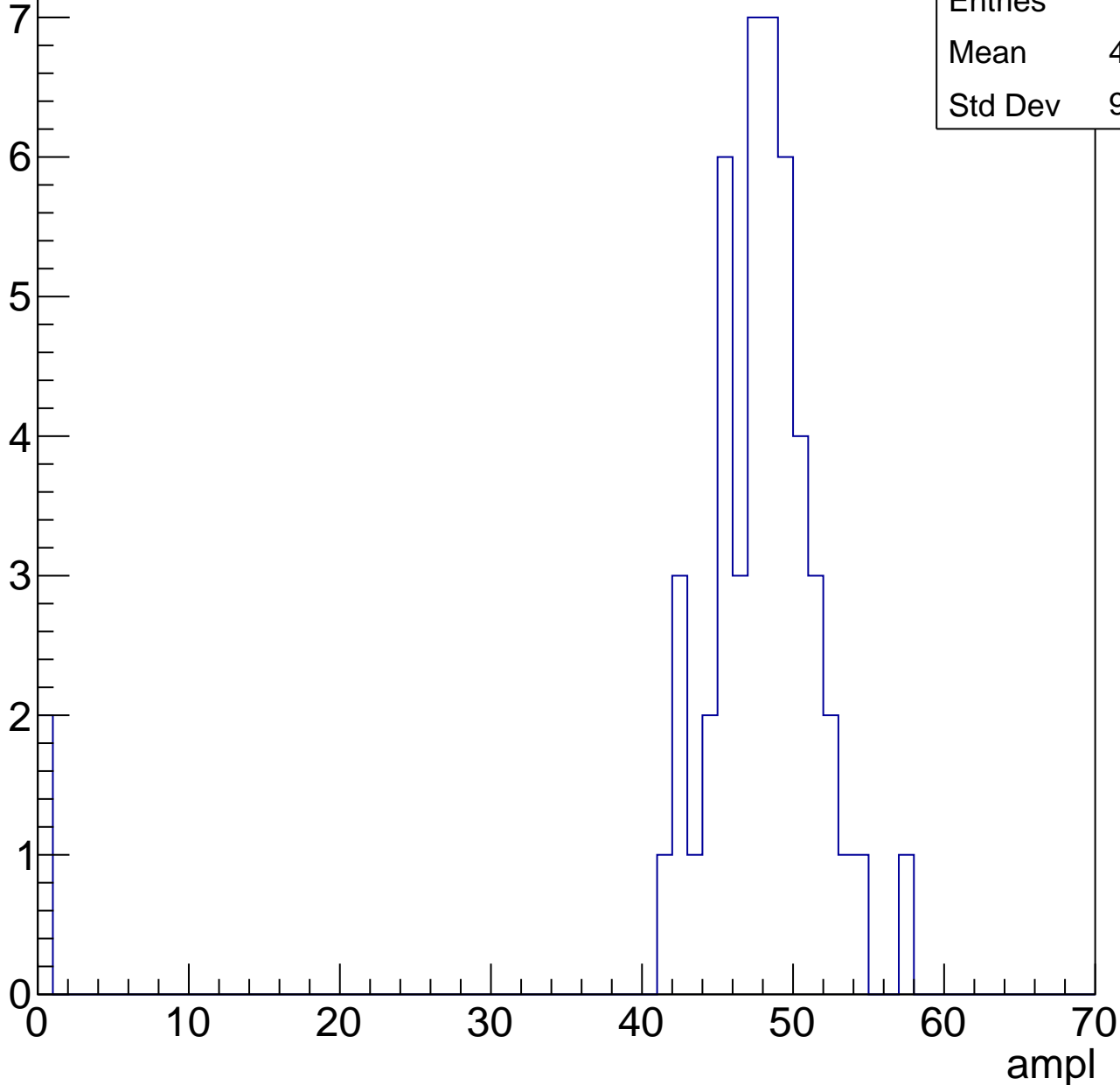


# B1L103S, U22-ch73, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	45.72
Std Dev	9.857

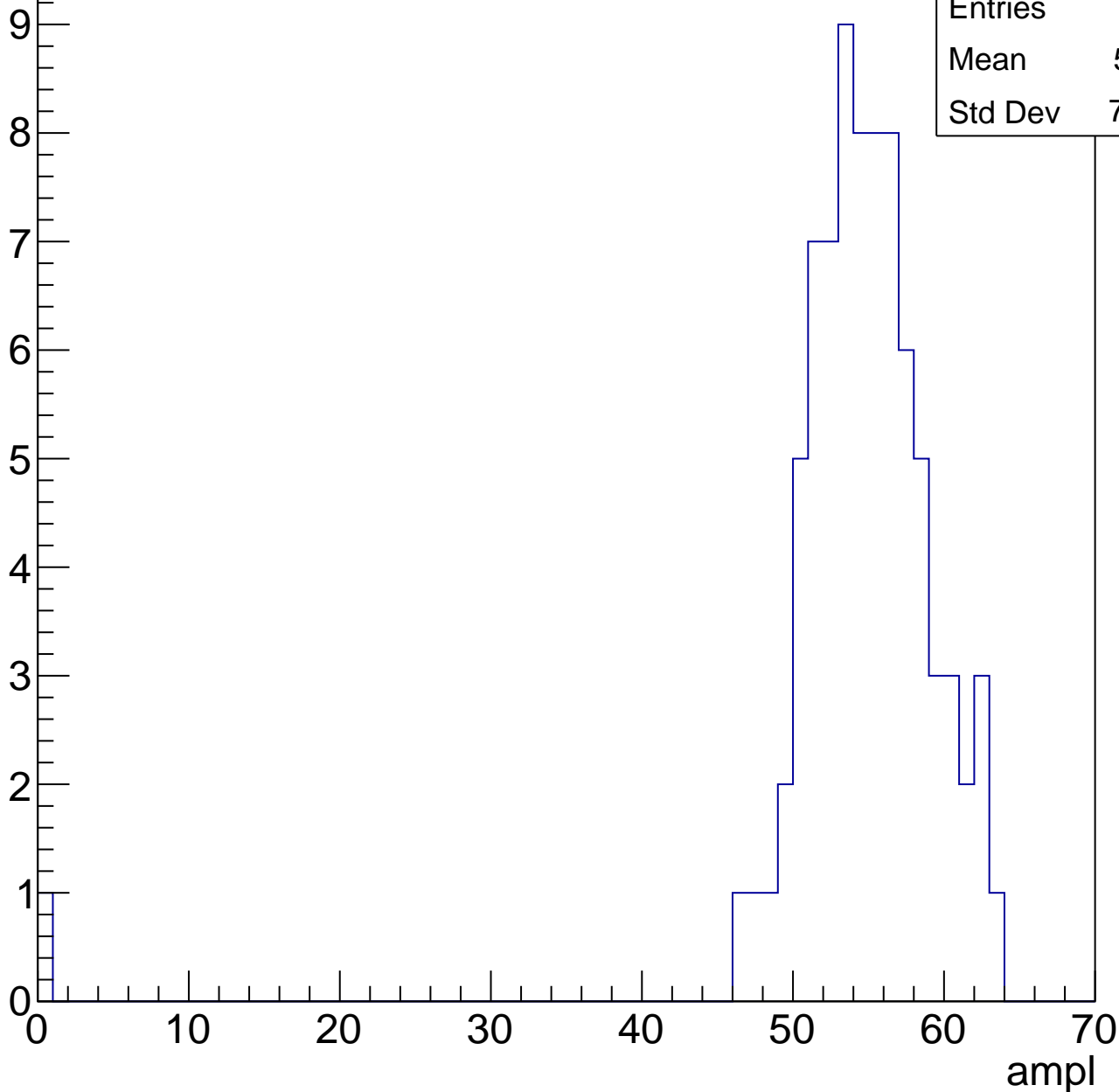


# B1L103S, U22-ch73, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

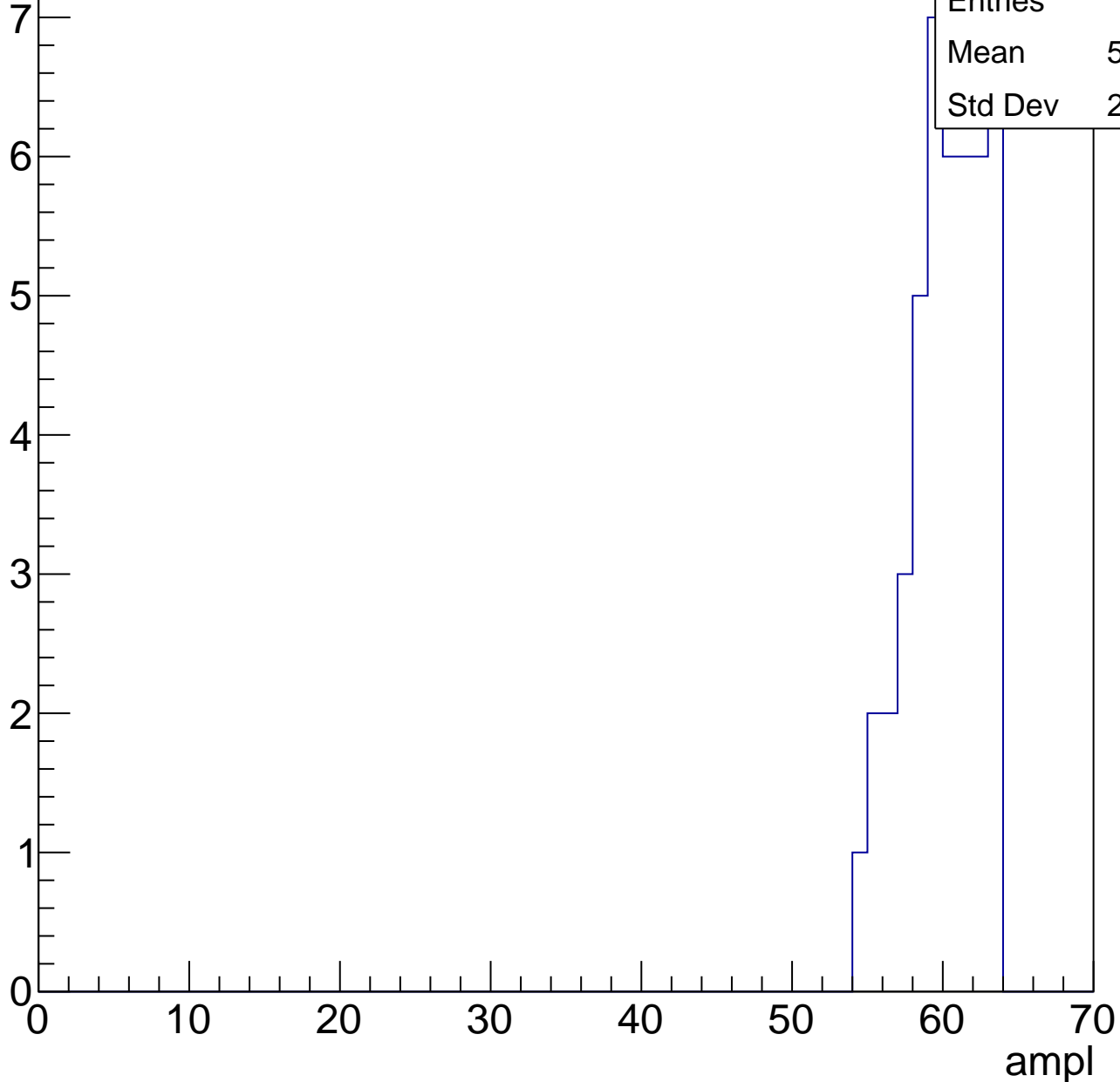
Entries	81
Mean	53.91
Std Dev	7.047



# B1L103S, U22-ch73, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

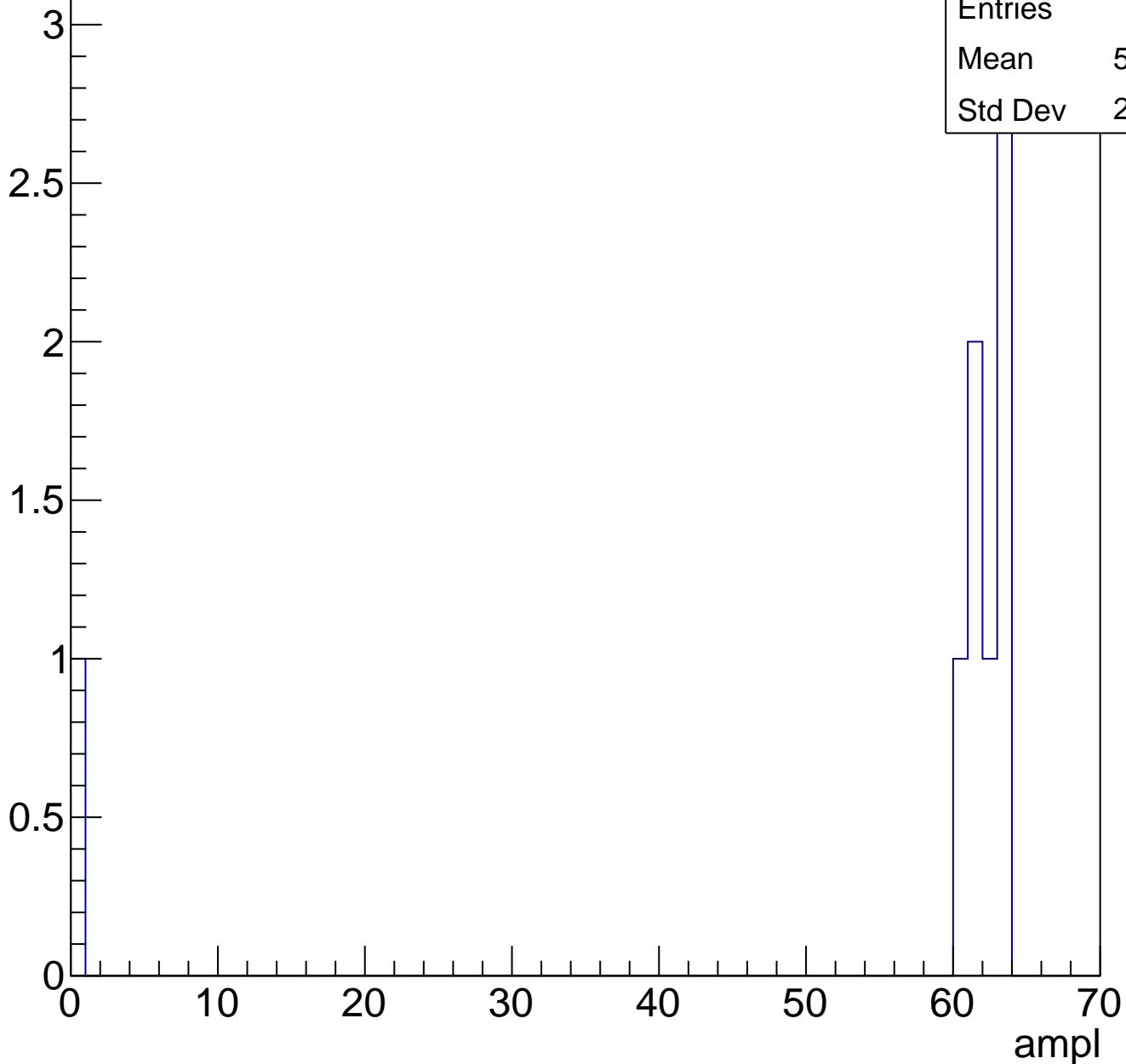


Entries	45
Mean	59.76
Std Dev	2.414

# B1L103S, U22-ch73, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



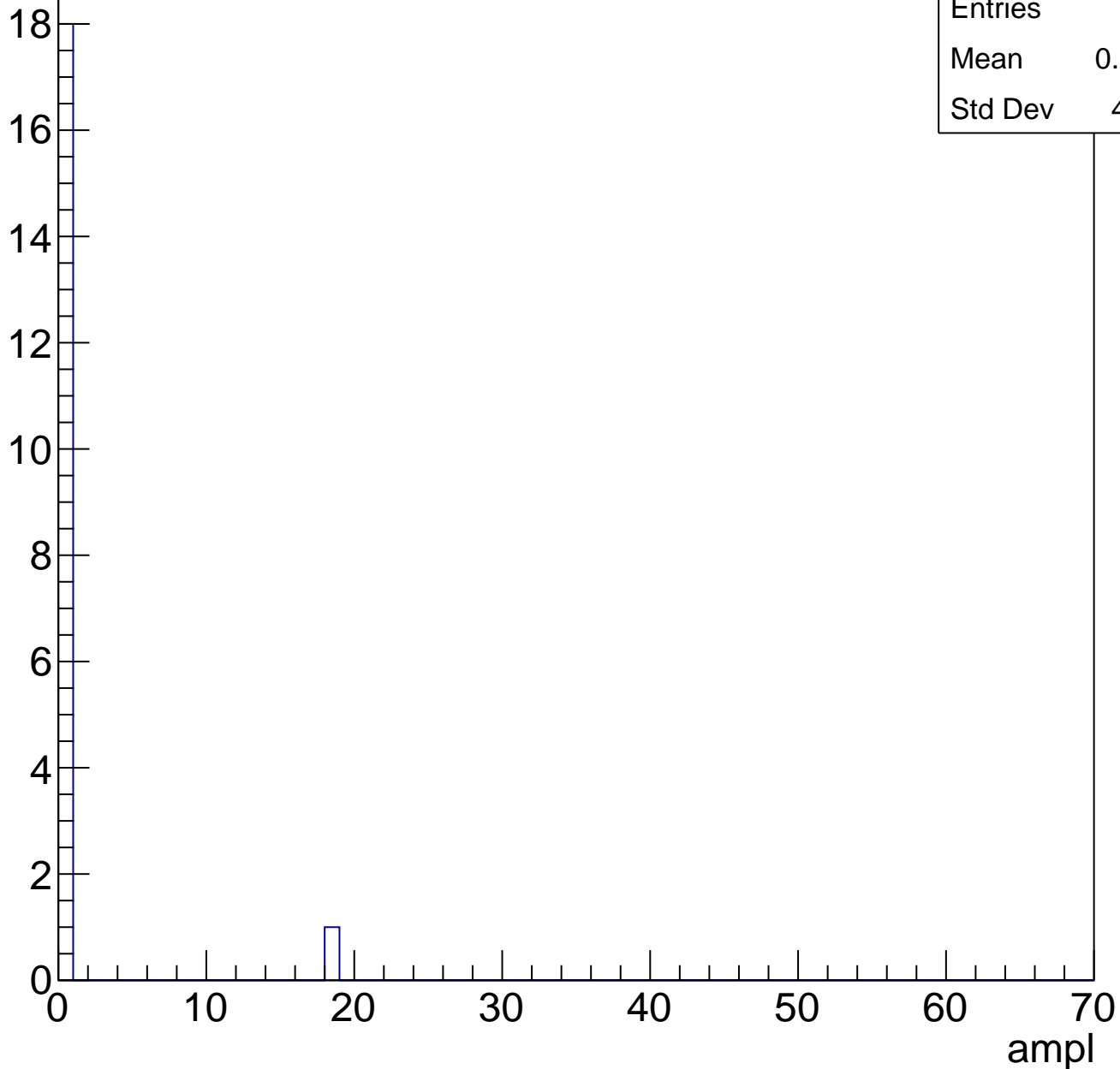


# B1L103S, U22-ch73, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	0.9474
Std Dev	4.019

Entry



# B1L103S, U22-ch74, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	105
Mean	18.94
Std Dev	12.67

**Gaus mean : 27.1654**

**Gaus Width: 3.7516**

Entry

30

25

20

15

10

5

0

0

10

20

30

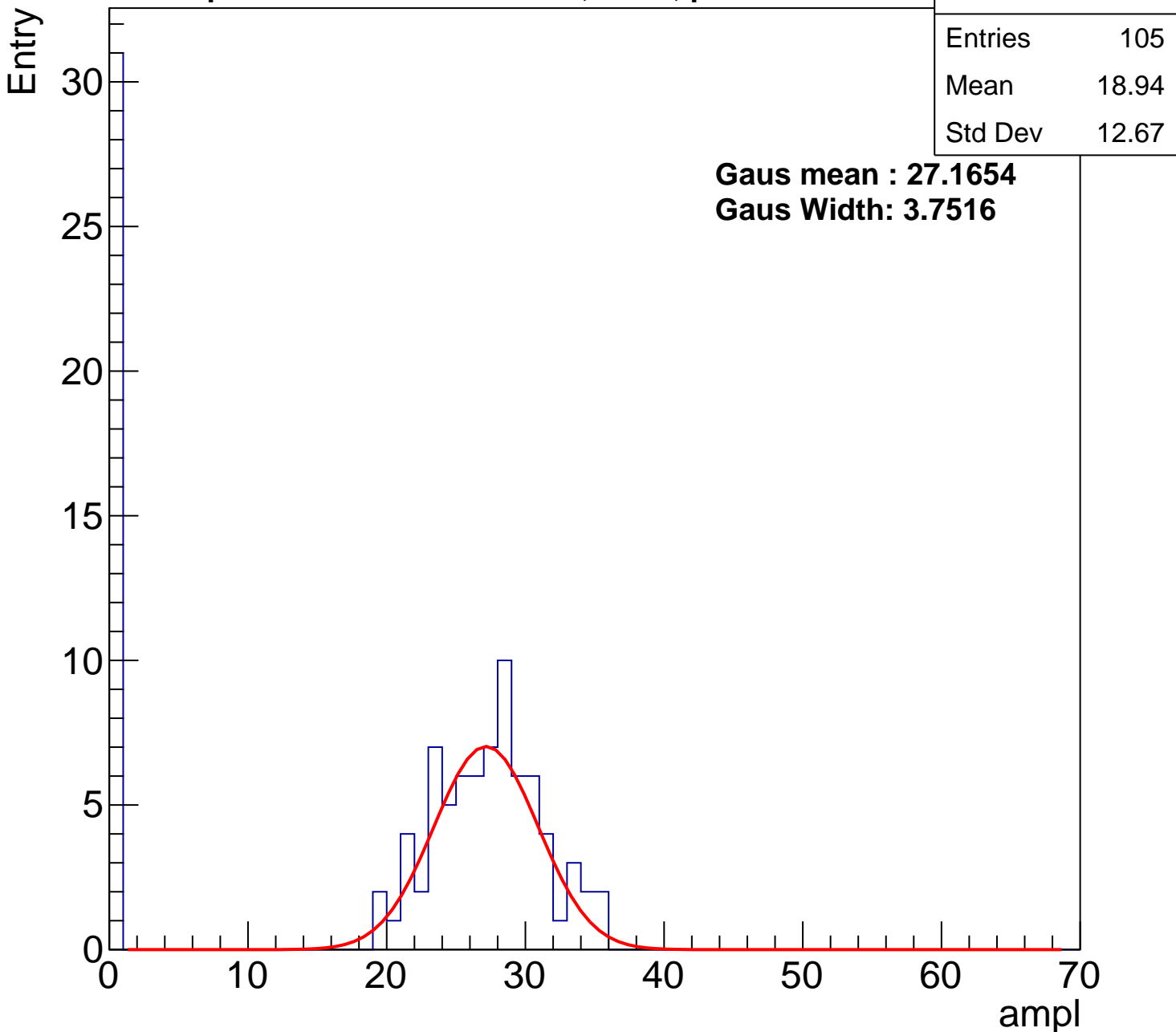
40

50

60

70

ampl



# B1L103S, U22-ch74, adc1

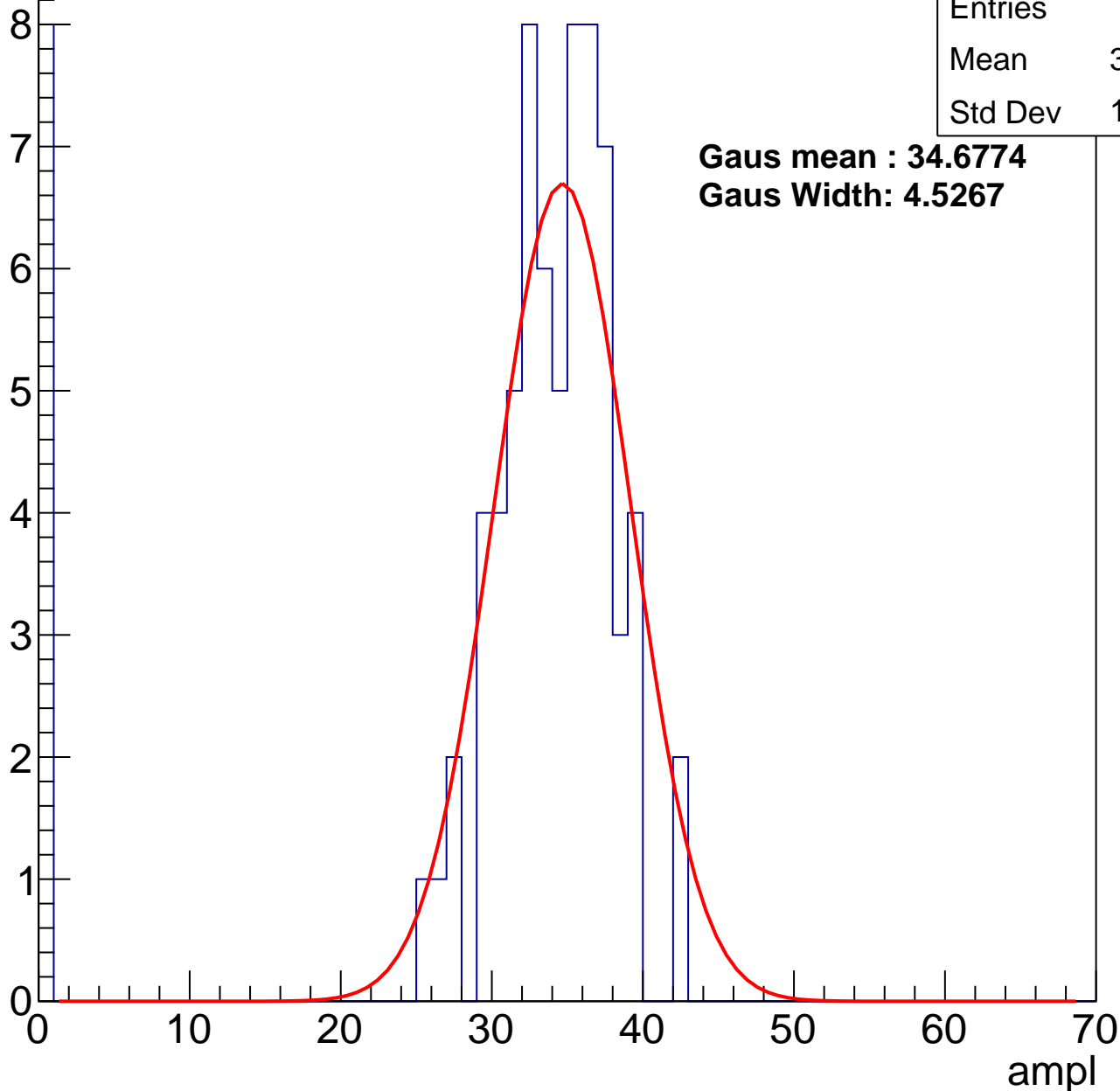
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	30.28
Std Dev	10.92

**Gaus mean : 34.6774**

**Gaus Width: 4.5267**



# B1L103S, U22-ch74, adc2

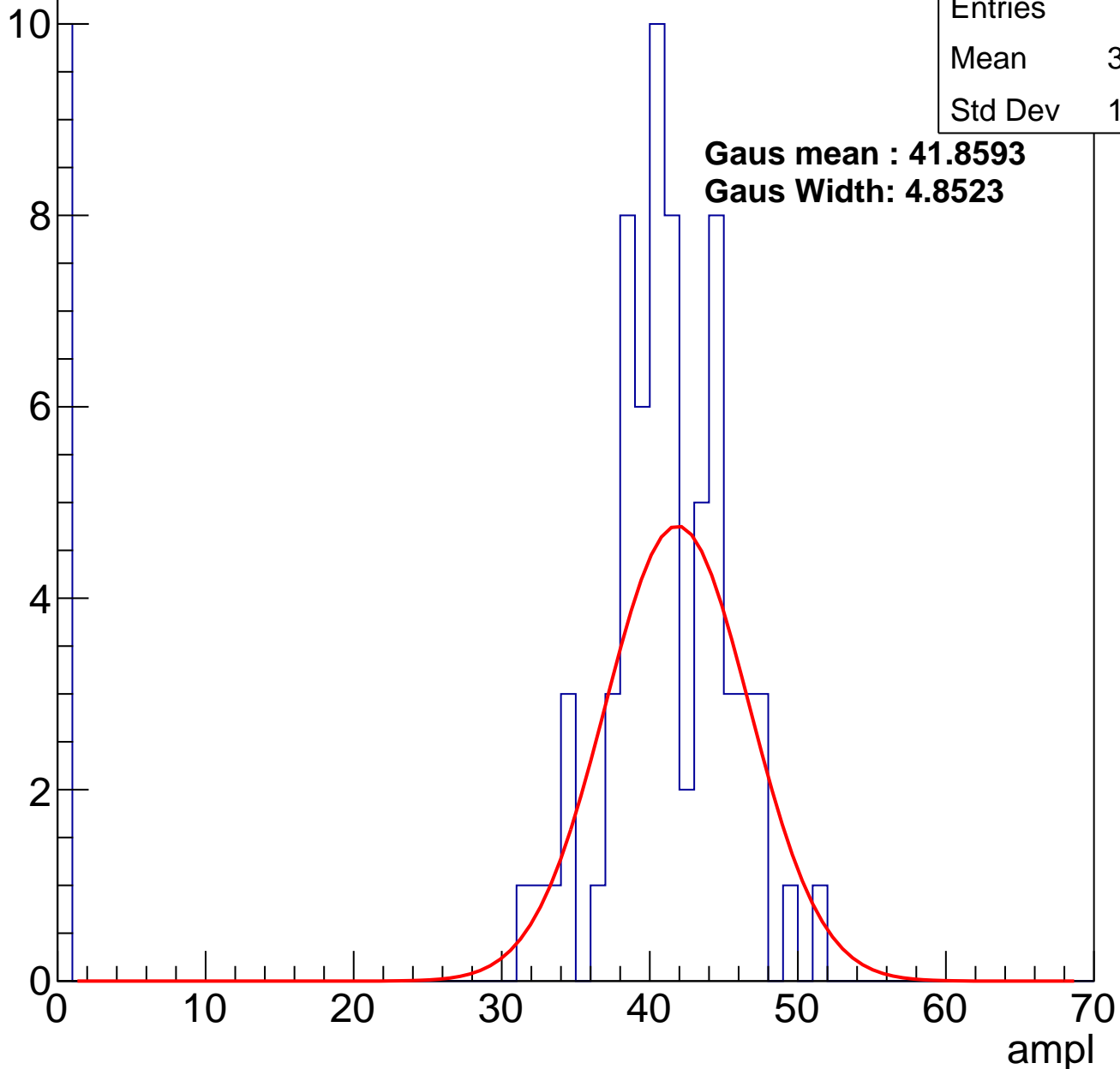
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	35.59
Std Dev	14.14

**Gaus mean : 41.8593**

**Gaus Width: 4.8523**

Entry

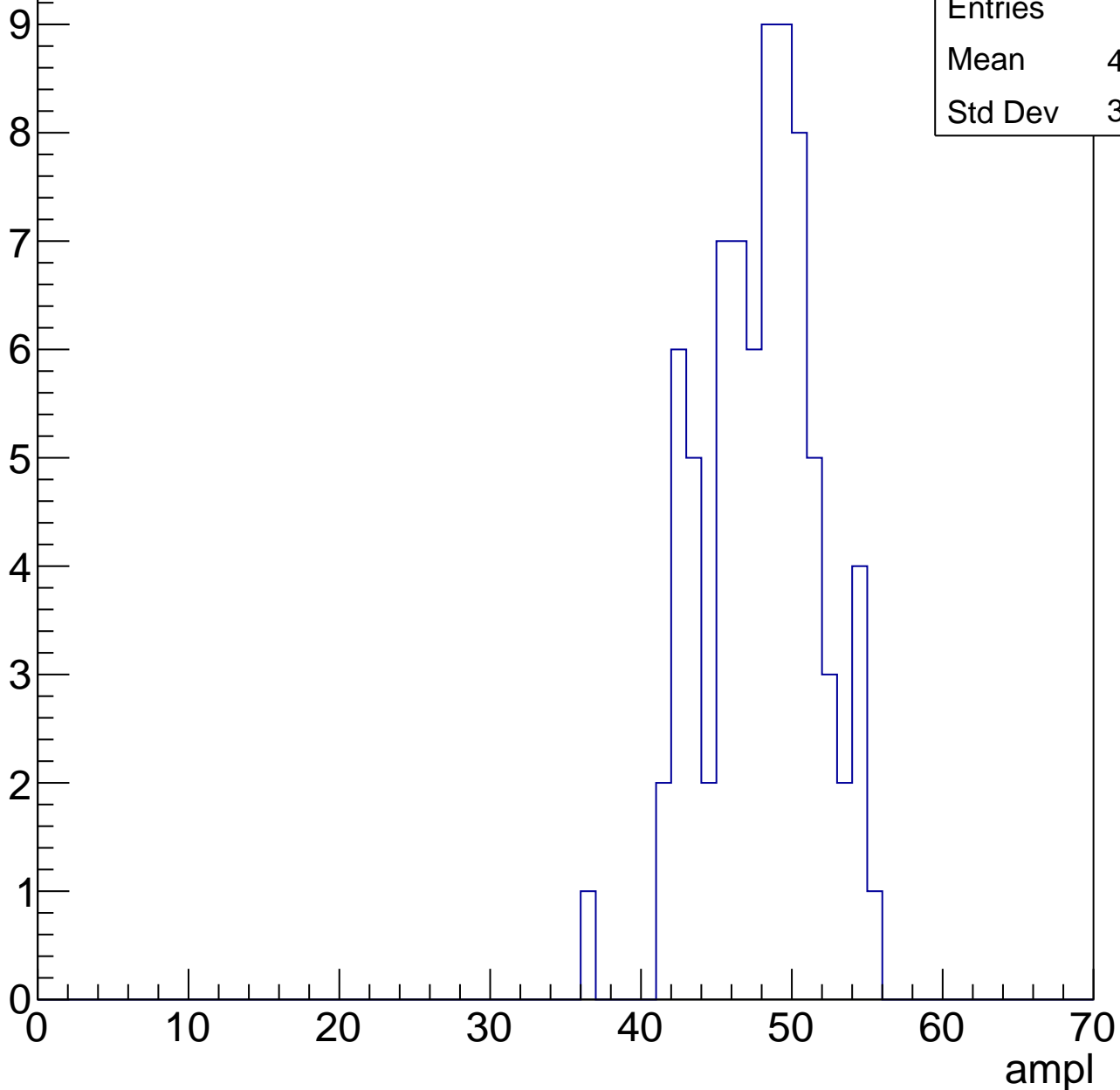


# B1L103S, U22-ch74, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	77
Mean	47.44
Std Dev	3.719

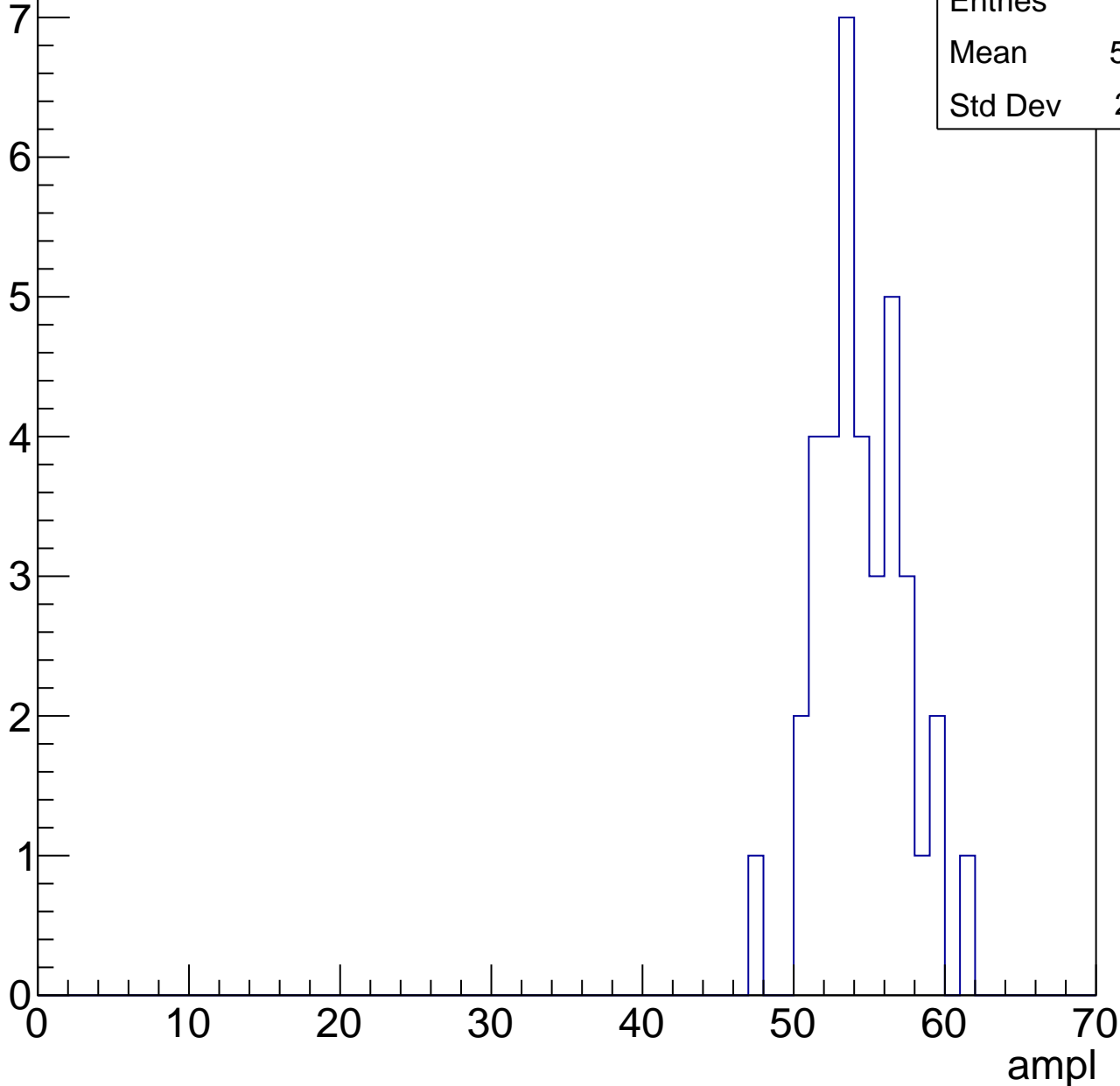


# B1L103S, U22-ch74, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

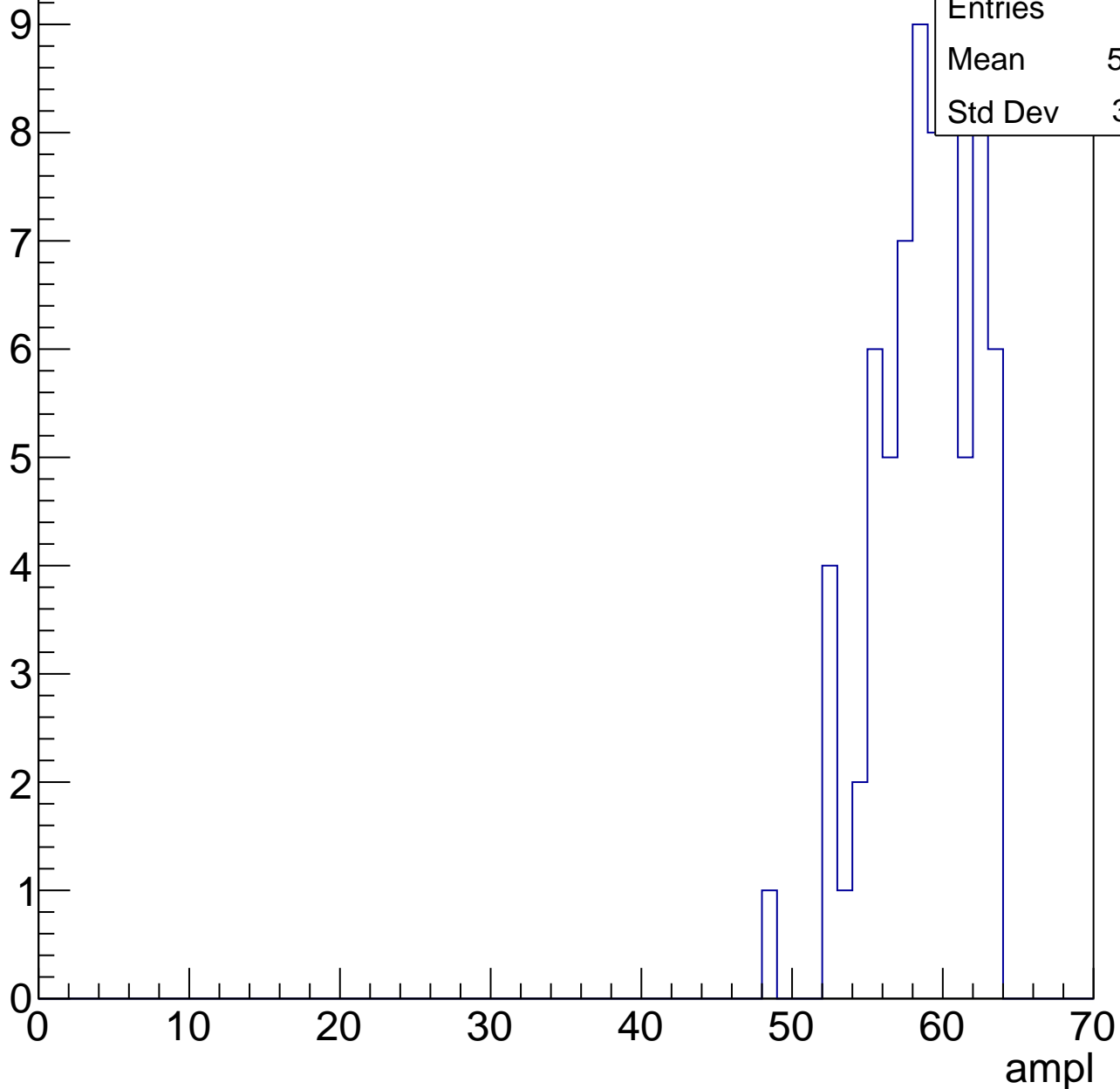
Entries	37
Mean	54.03
Std Dev	2.871



# B1L103S, U22-ch74, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

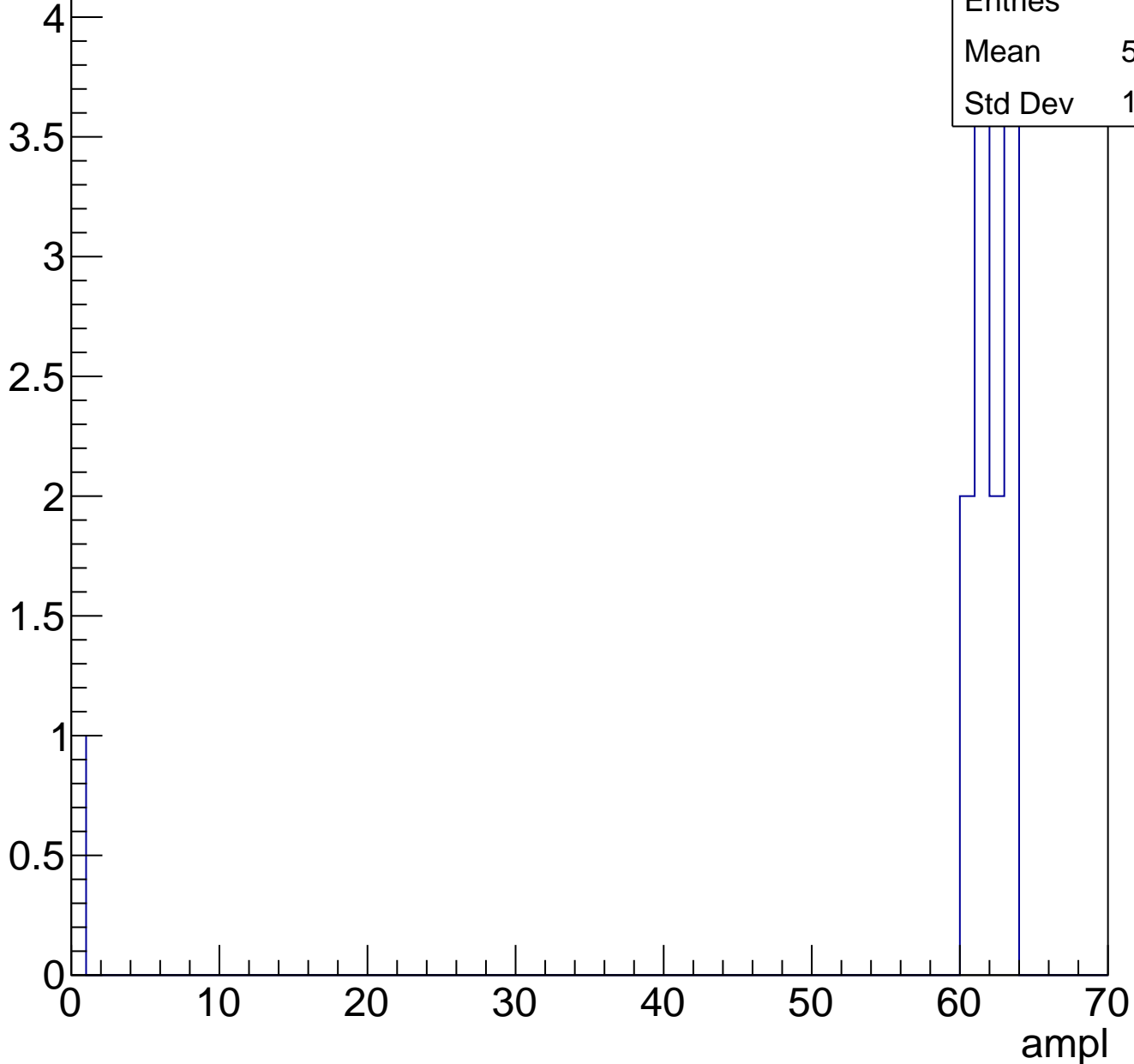
Entry



# B1L103S, U22-ch74, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch74, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch75, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	83
Mean	22.45
Std Dev	13.06

**Gaus mean : 29.5471**

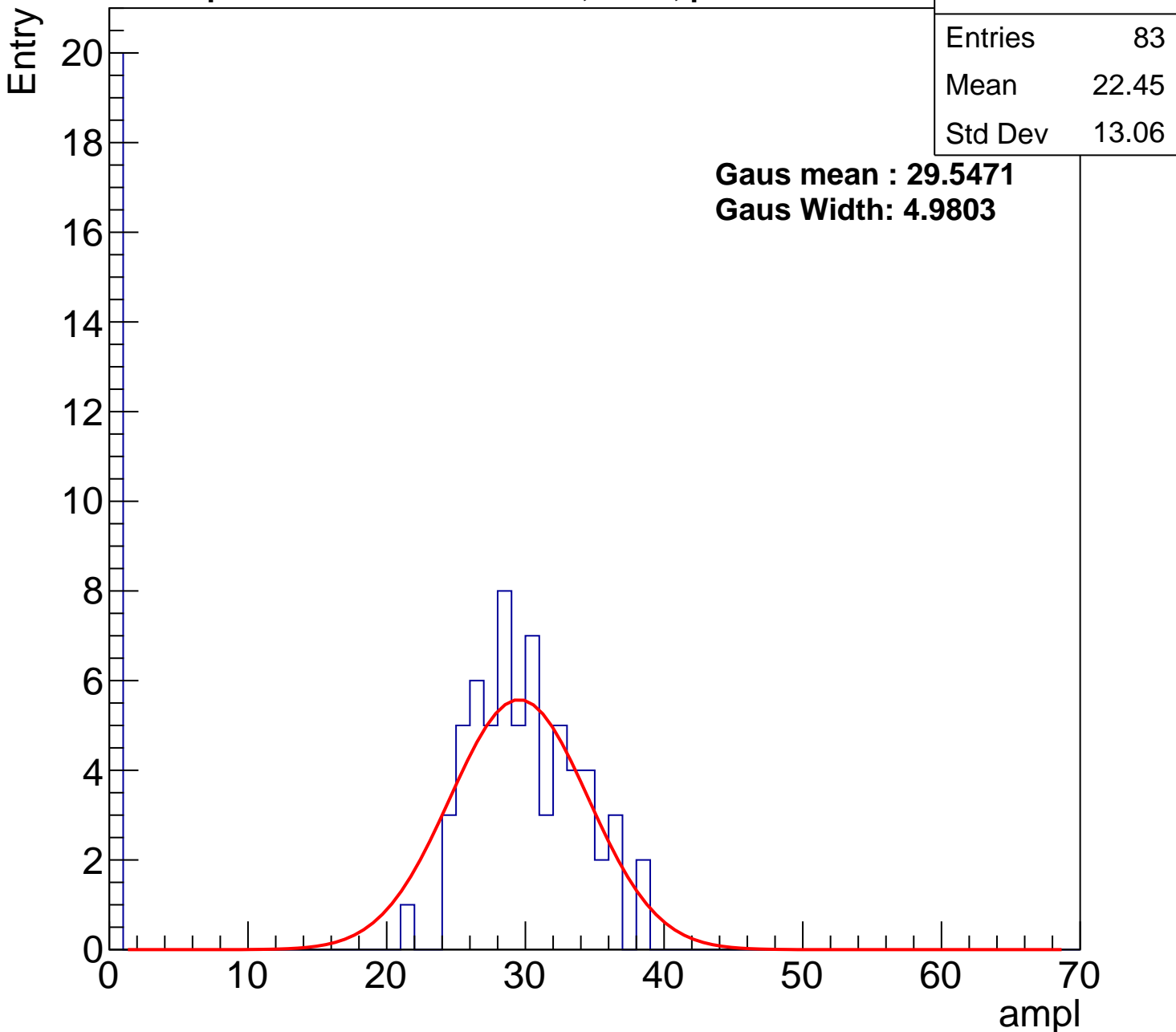
**Gaus Width: 4.9803**

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch75, adc1

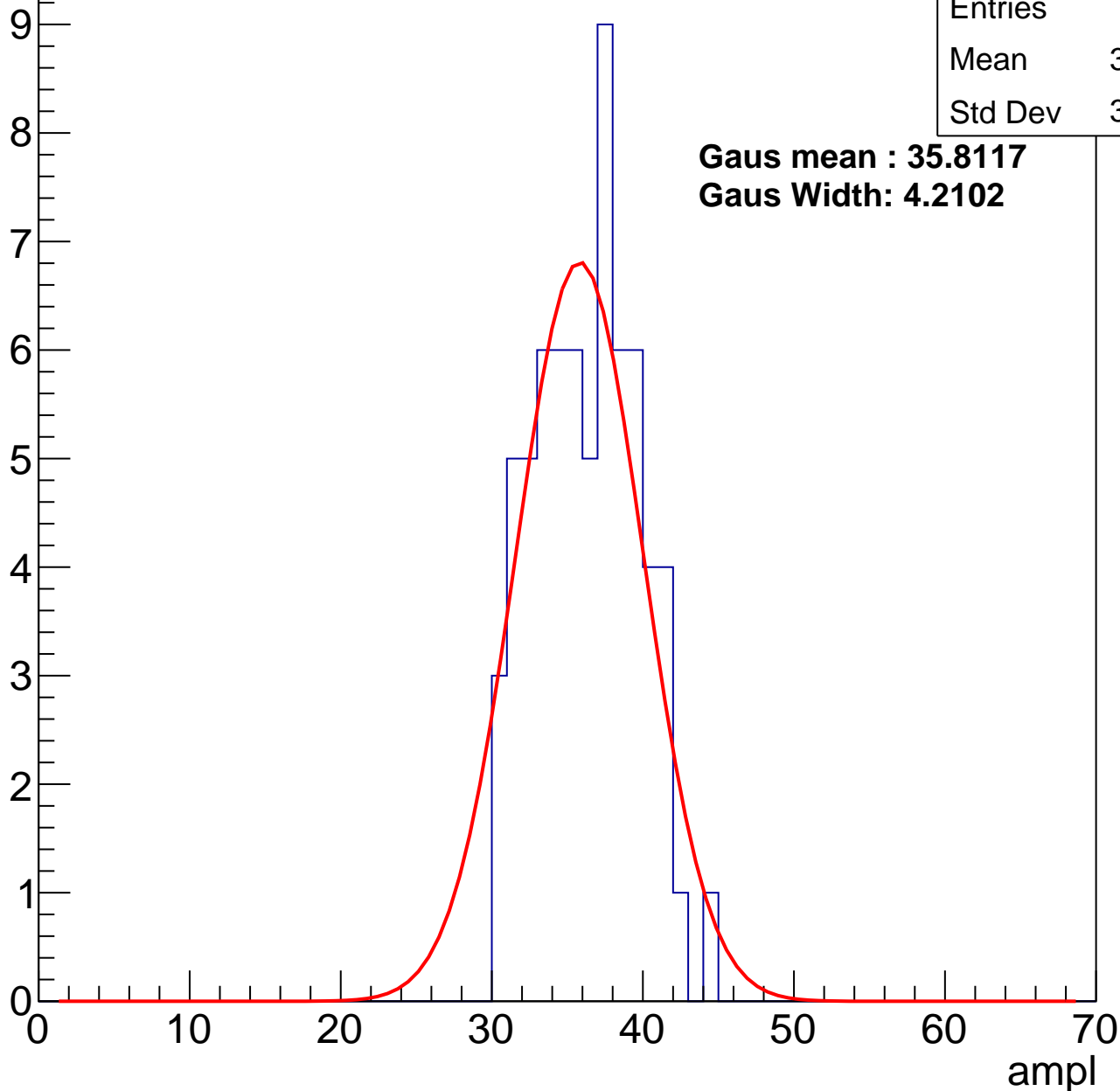
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	35.85
Std Dev	3.338

**Gaus mean : 35.8117**

**Gaus Width: 4.2102**



# B1L103S, U22-ch75, adc2

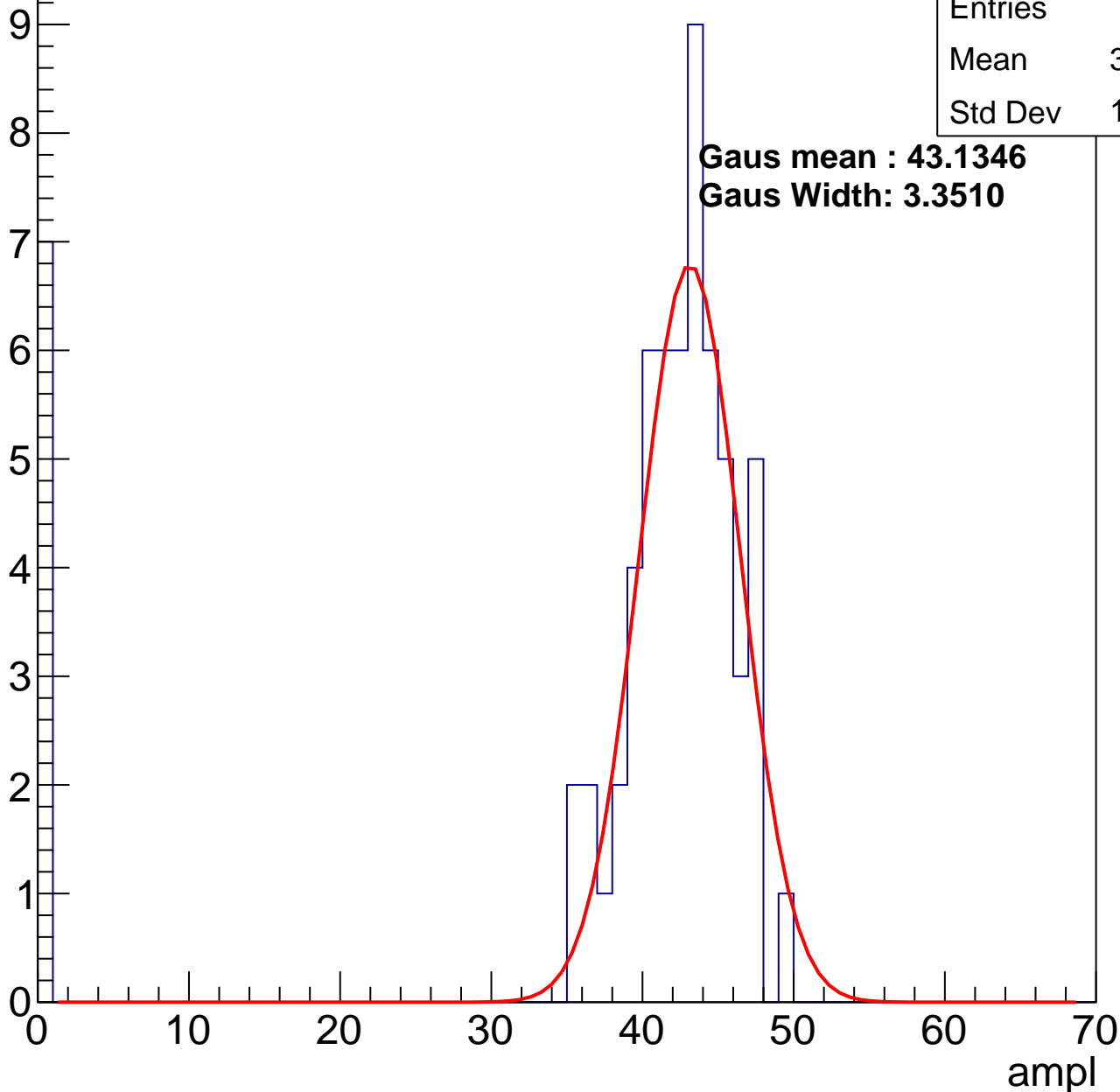
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	37.65
Std Dev	13.42

**Gaus mean : 43.1346**

**Gaus Width: 3.3510**

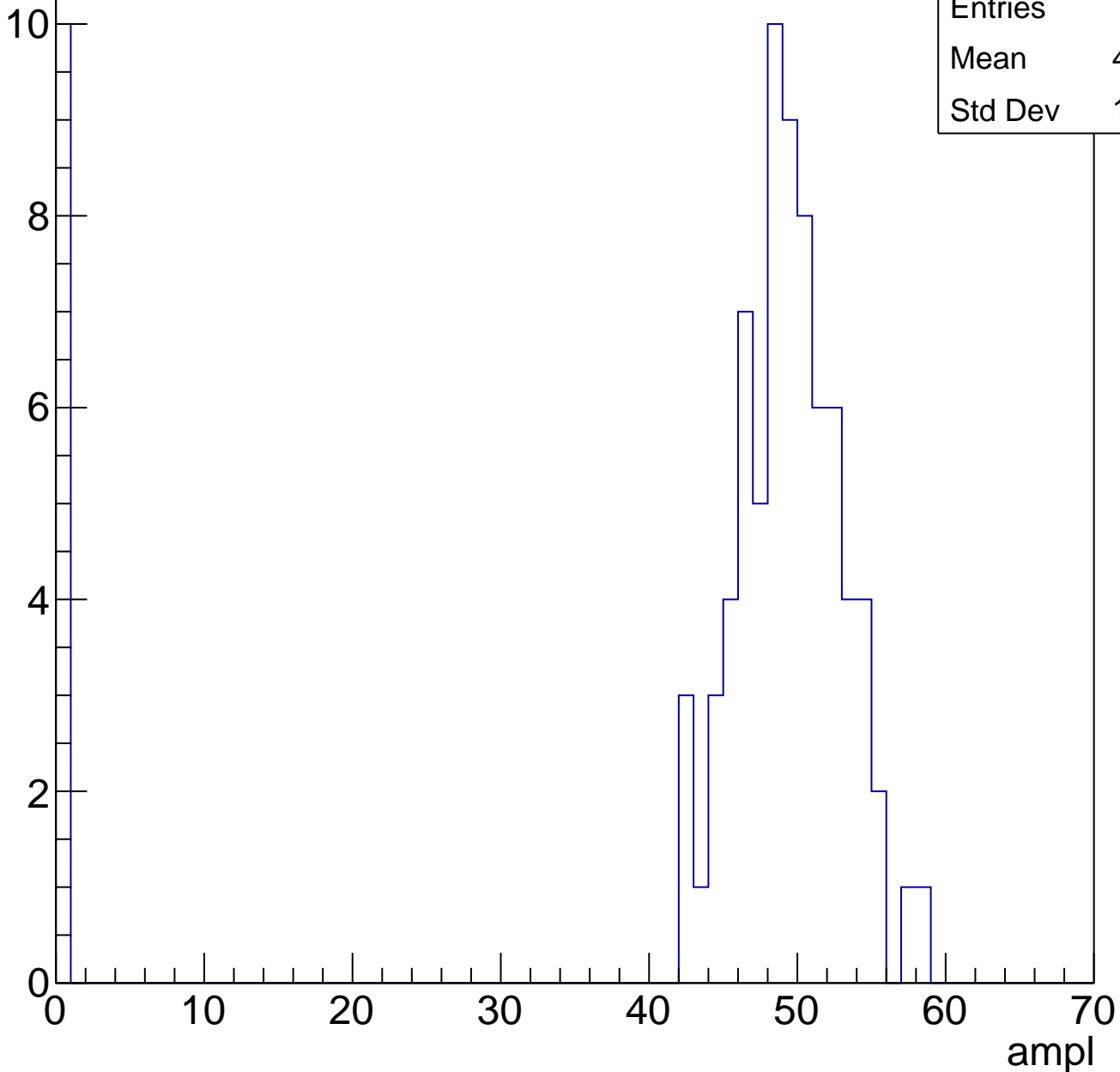


# B1L103S, U22-ch75, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	43.21
Std Dev	16.21

Entry

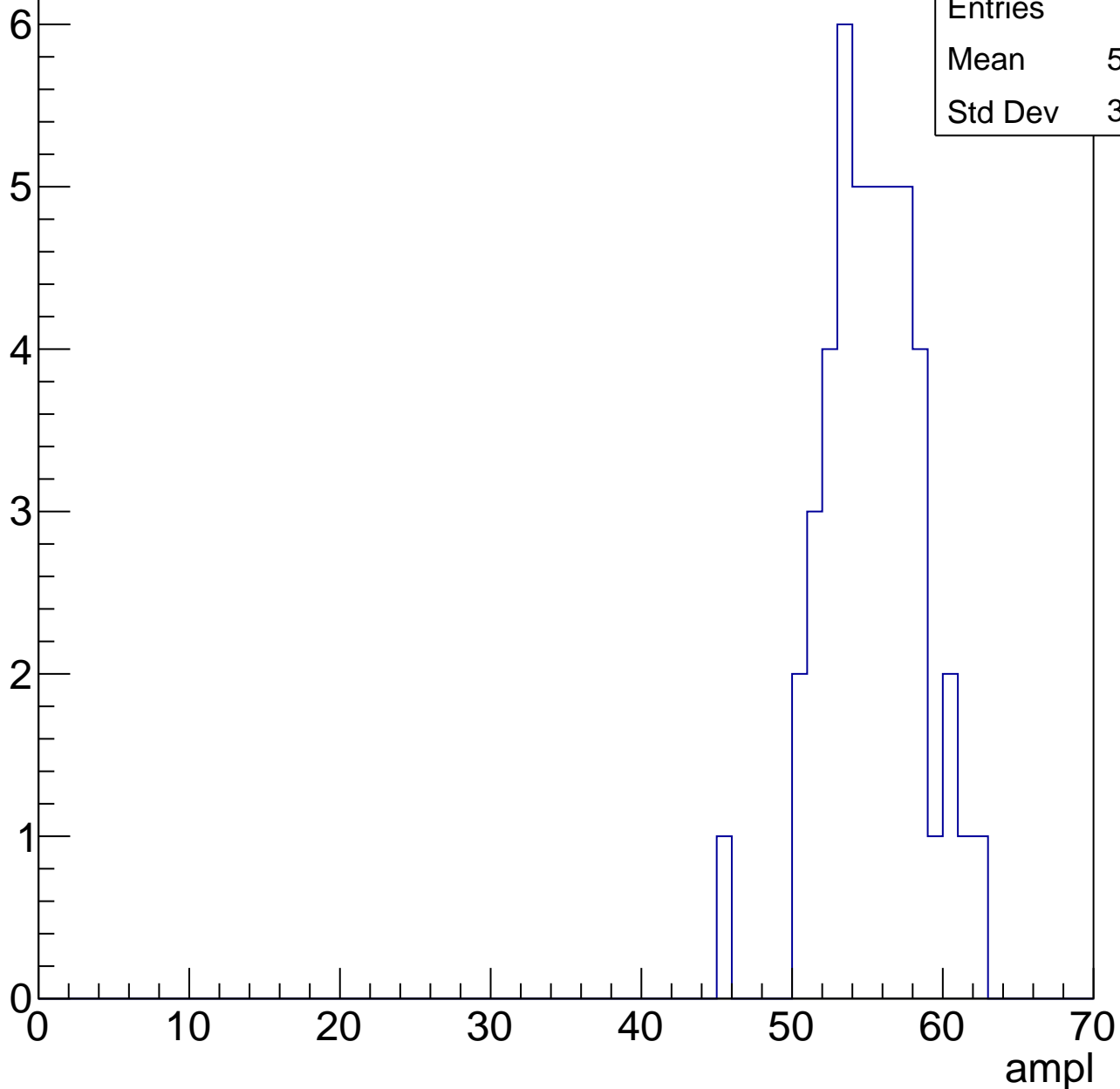


# B1L103S, U22-ch75, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	45
Mean	54.84
Std Dev	3.245

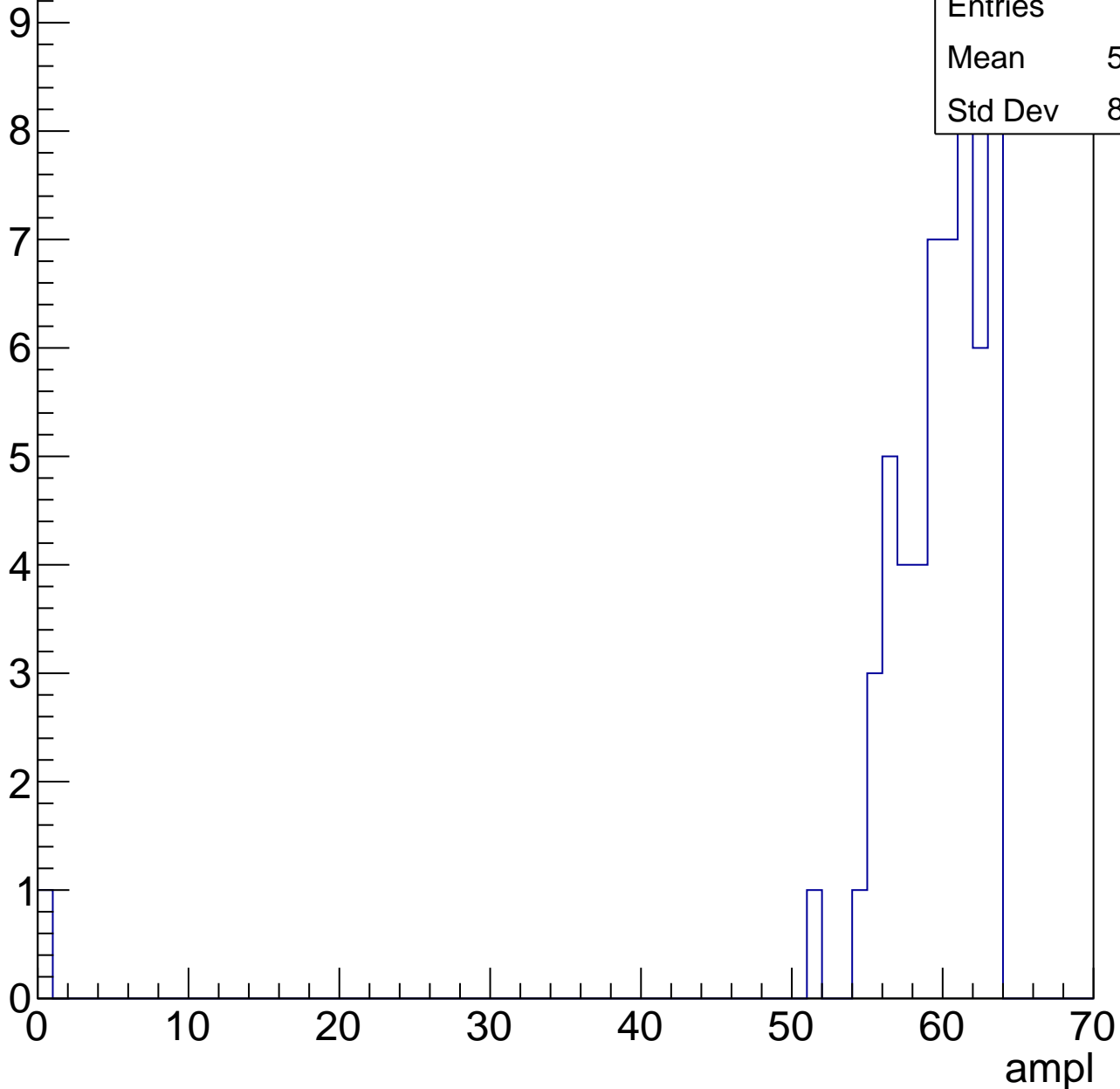


# B1L103S, U22-ch75, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

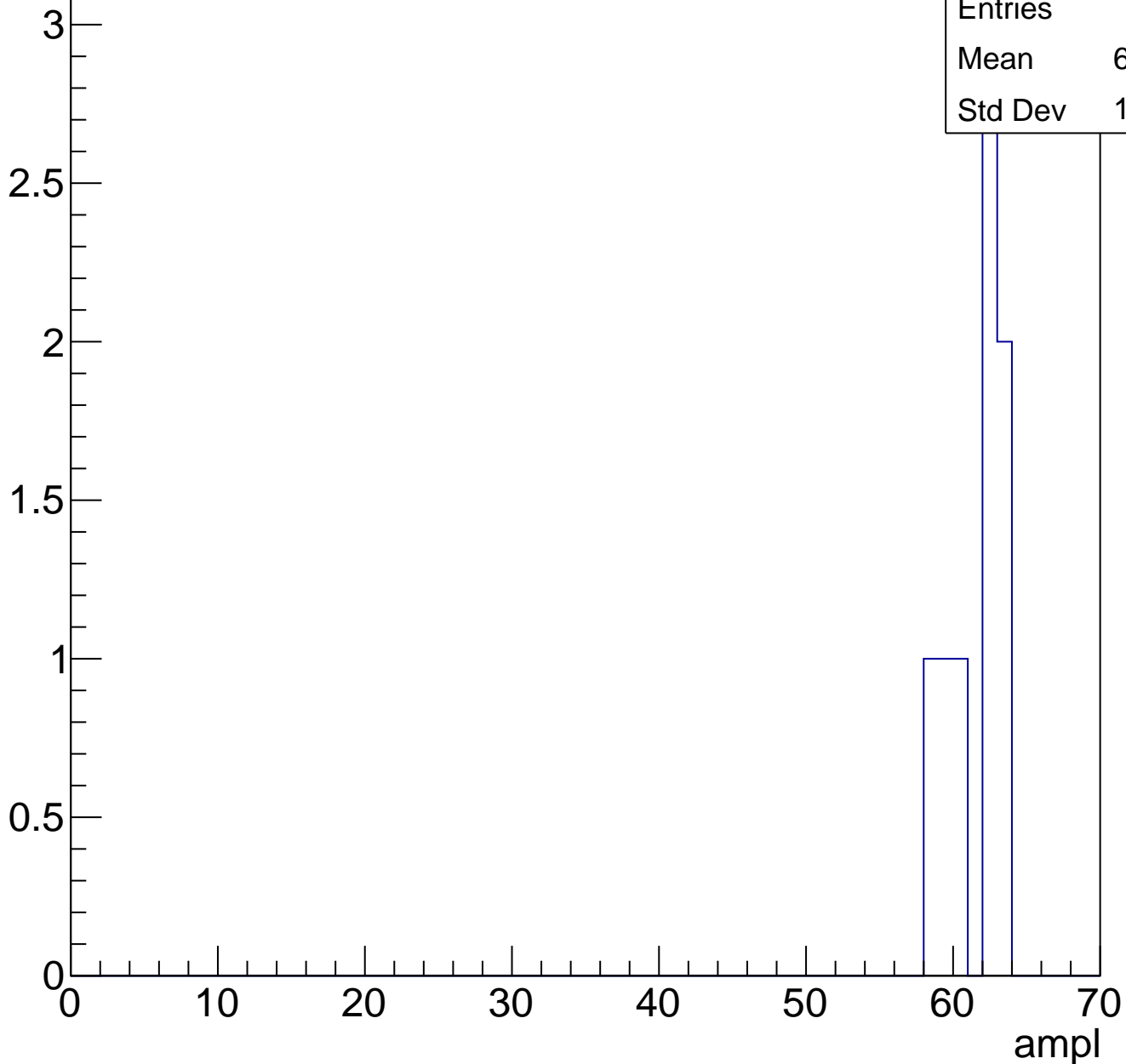
Entries	56
Mean	58.36
Std Dev	8.329



# B1L103S, U22-ch75, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



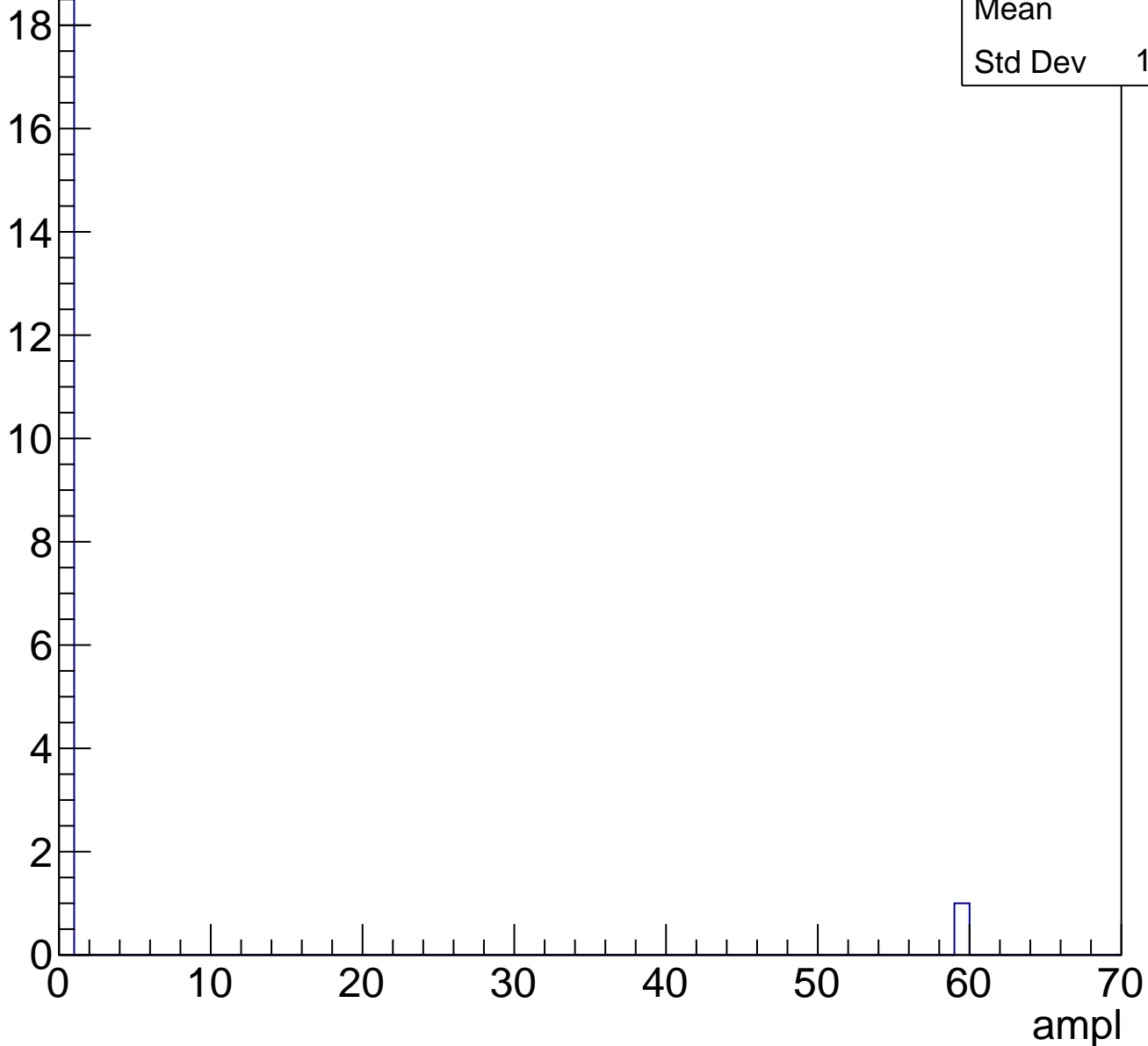


# B1L103S, U22-ch75, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	2.95
Std Dev	12.86

Entry



# B1L103S, U22-ch76, adc0

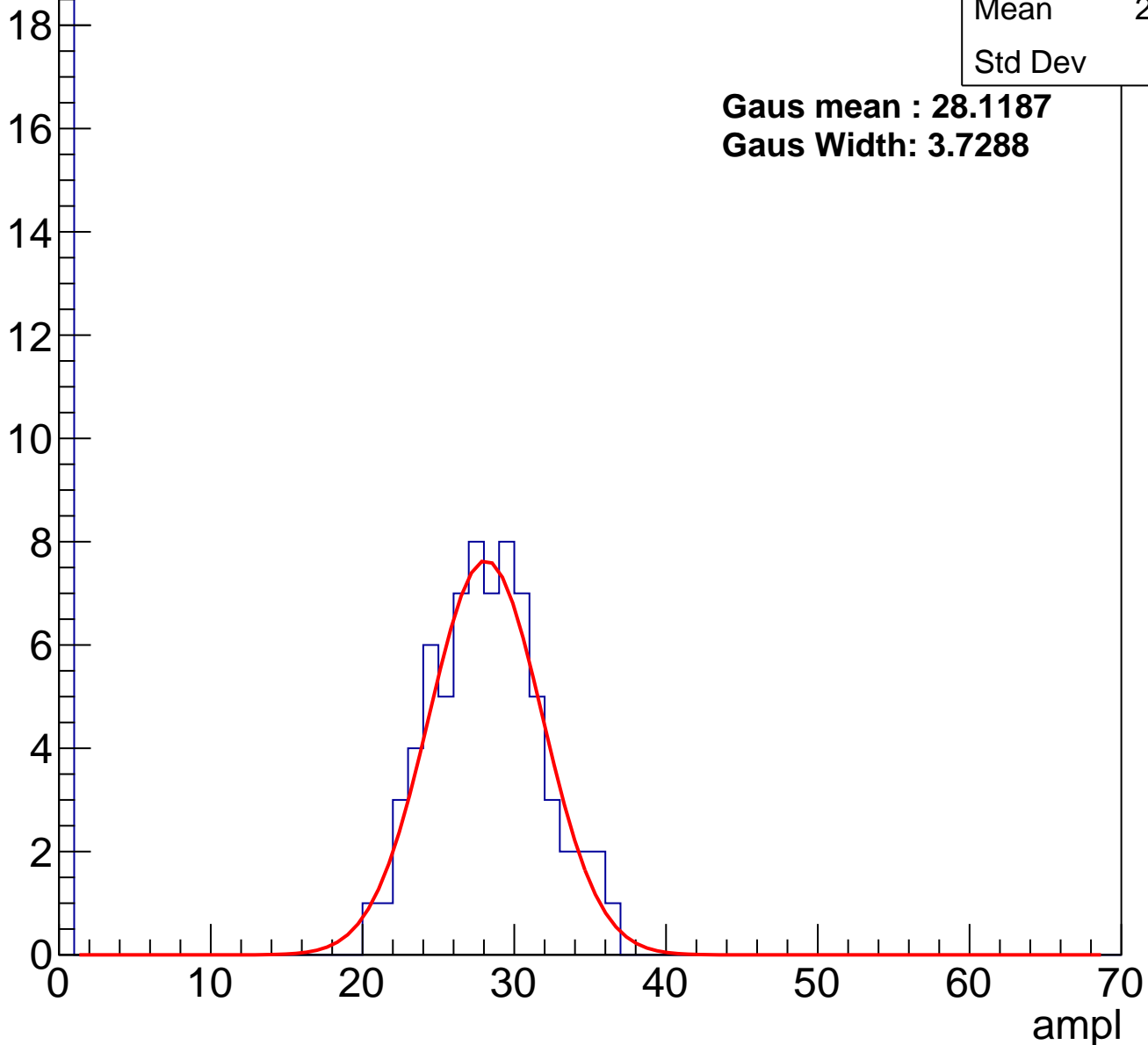
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	21.92
Std Dev	11.7

**Gaus mean : 28.1187**

**Gaus Width: 3.7288**

Entry



# B1L103S, U22-ch76, adc1

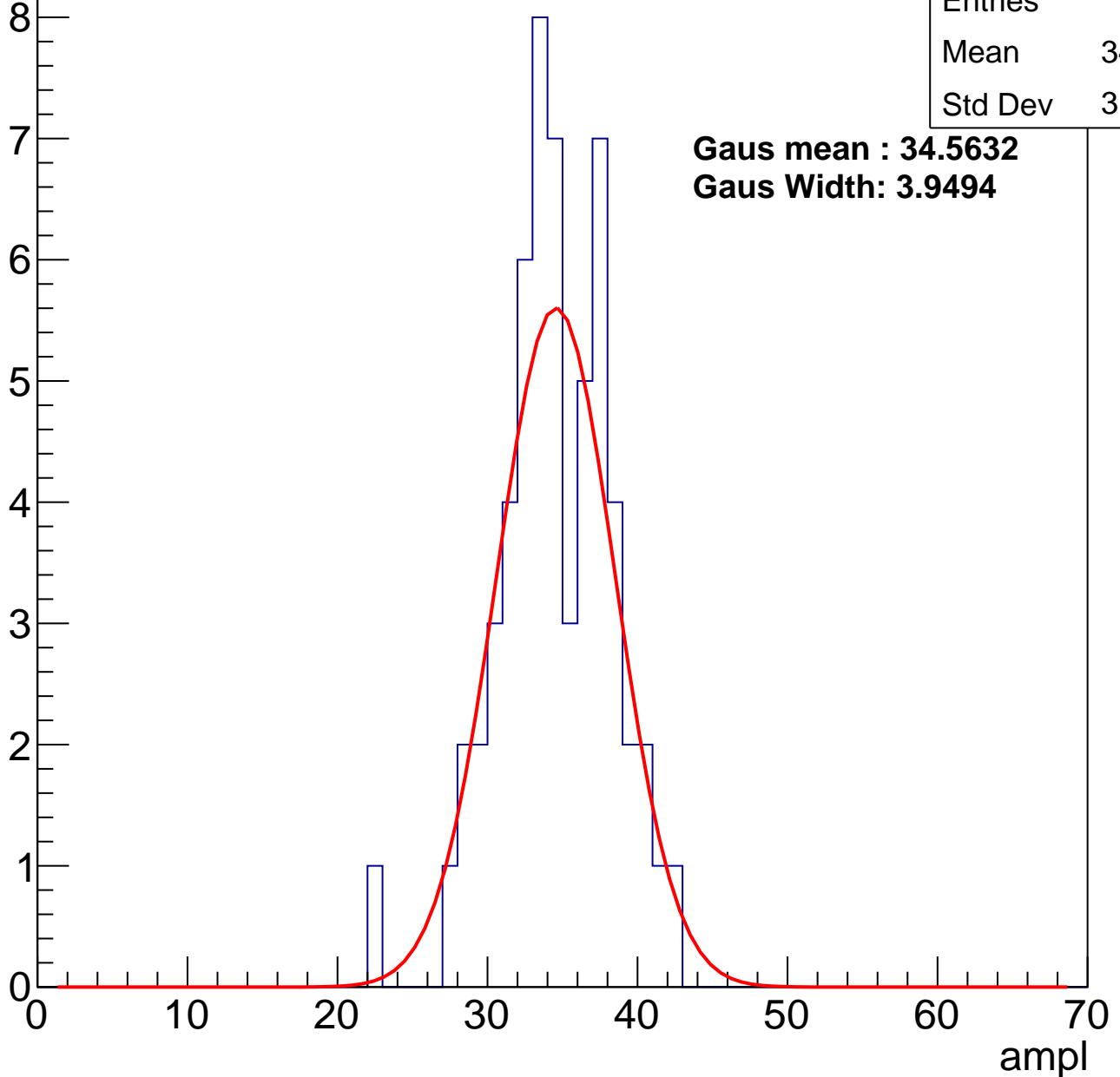
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	34.03
Std Dev	3.719

**Gaus mean : 34.5632**

**Gaus Width: 3.9494**



# B1L103S, U22-ch76, adc2

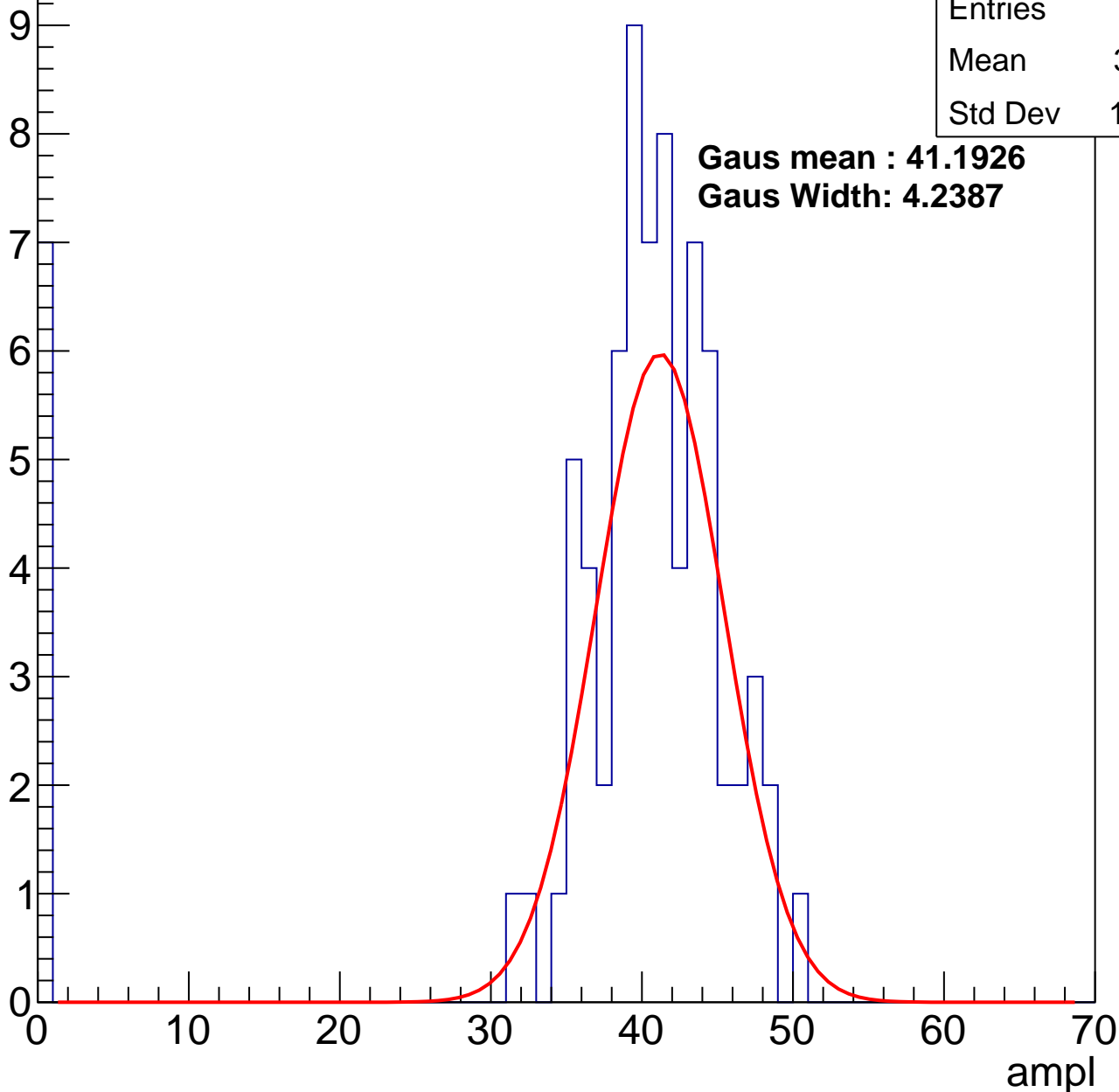
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	36.91
Std Dev	12.17

**Gaus mean : 41.1926**

**Gaus Width: 4.2387**

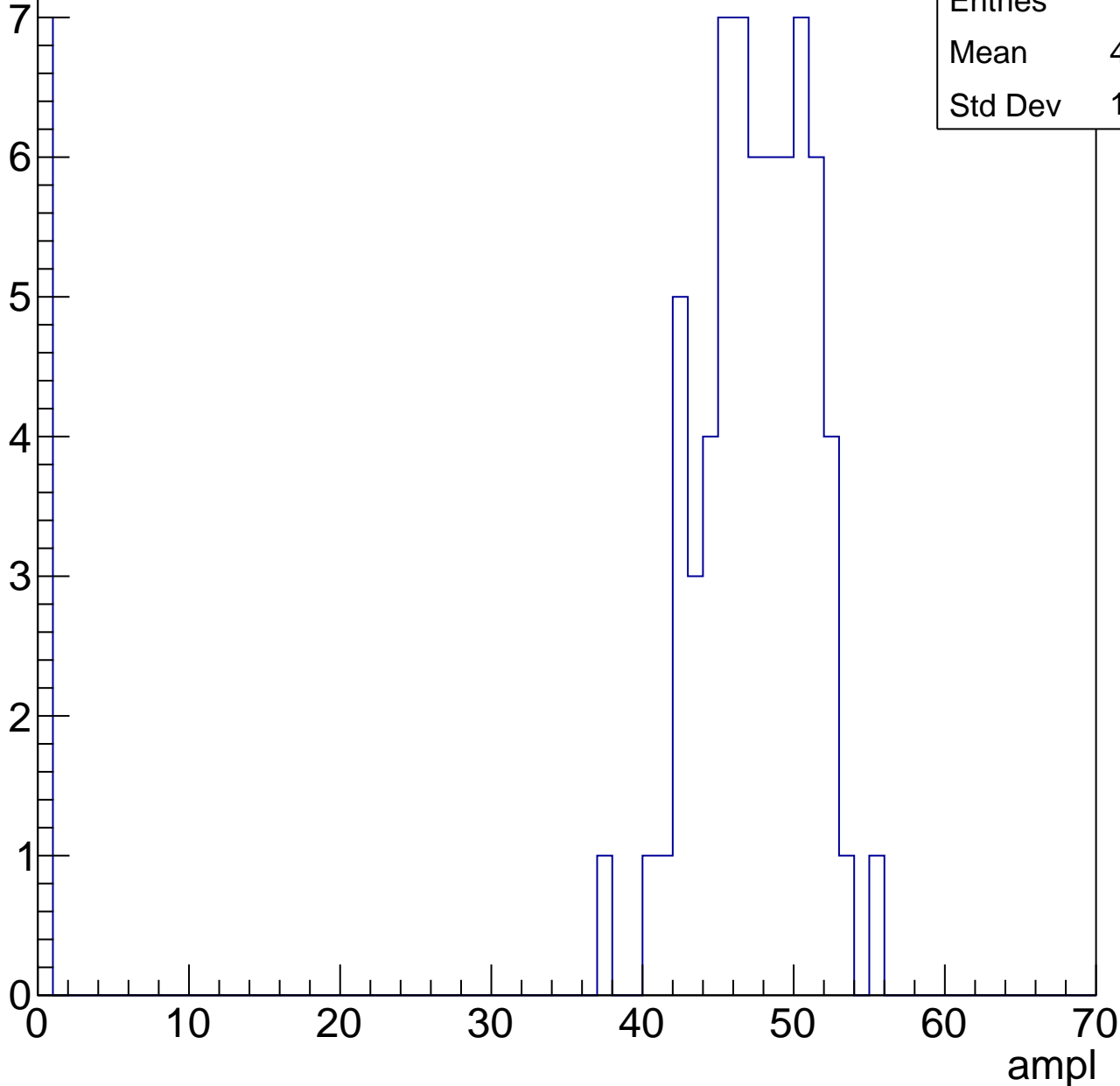


# B1L103S, U22-ch76, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	42.55
Std Dev	14.26

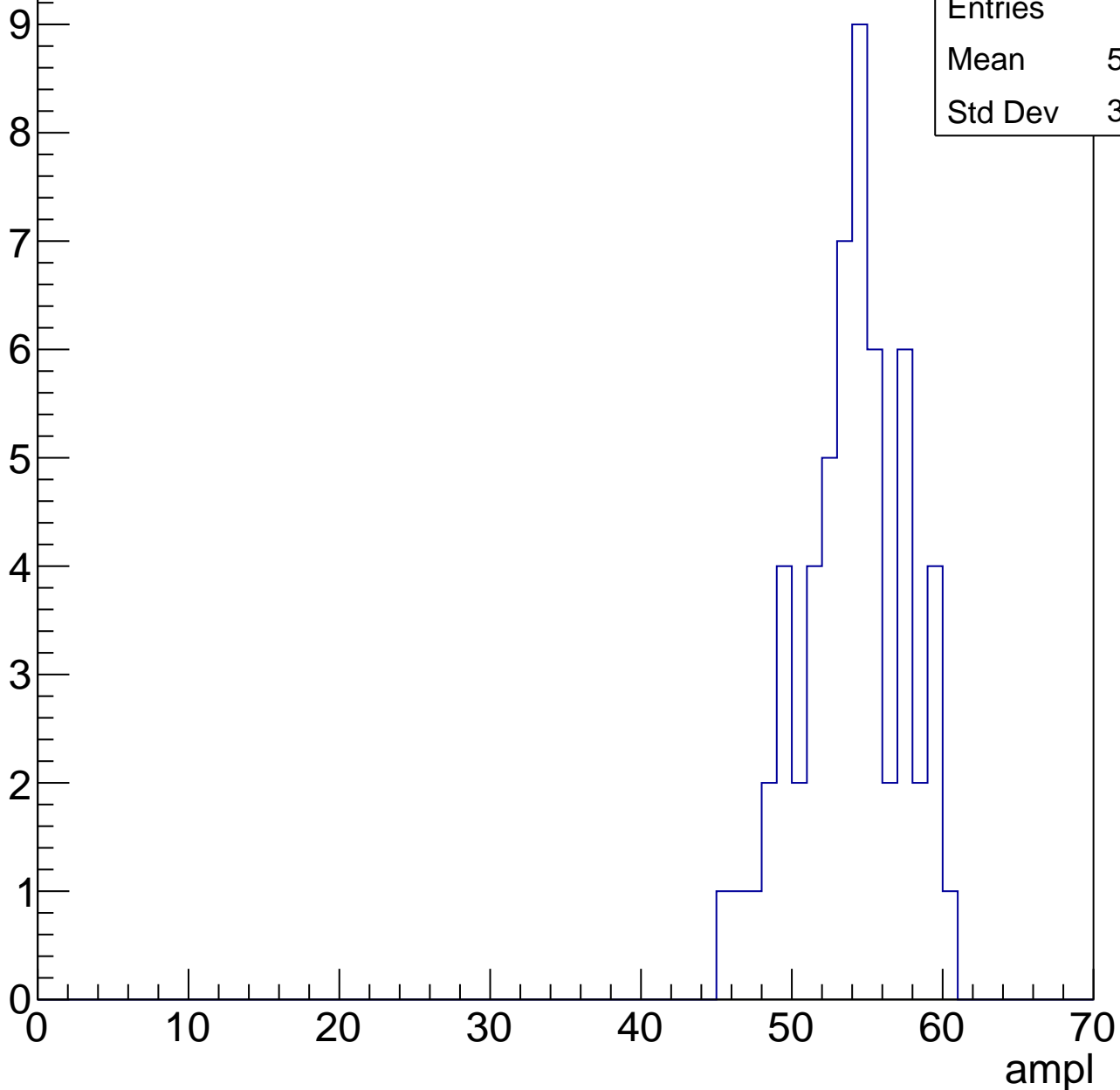


# B1L103S, U22-ch76, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

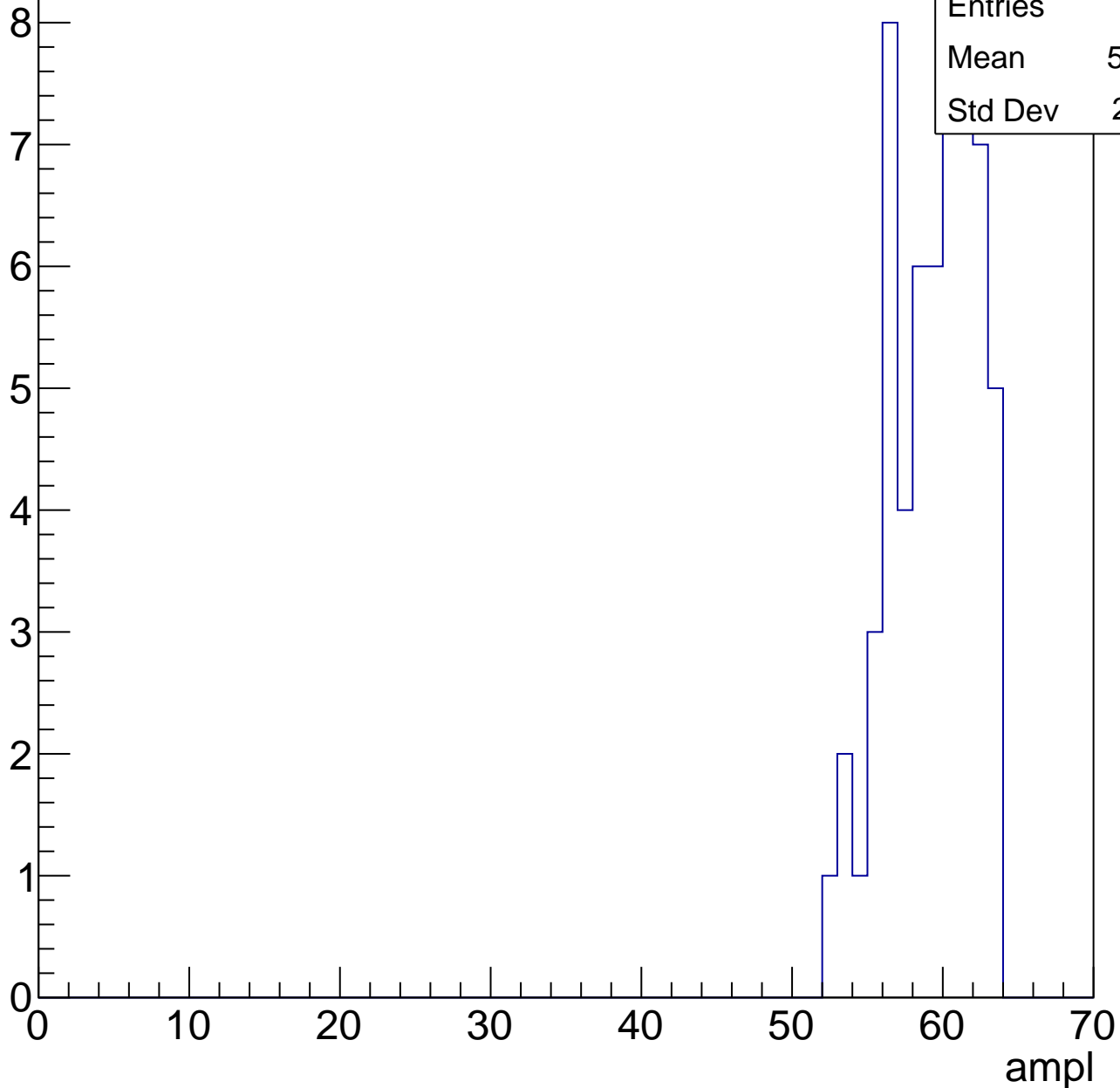
Entries	57
Mean	53.46
Std Dev	3.454



# B1L103S, U22-ch76, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

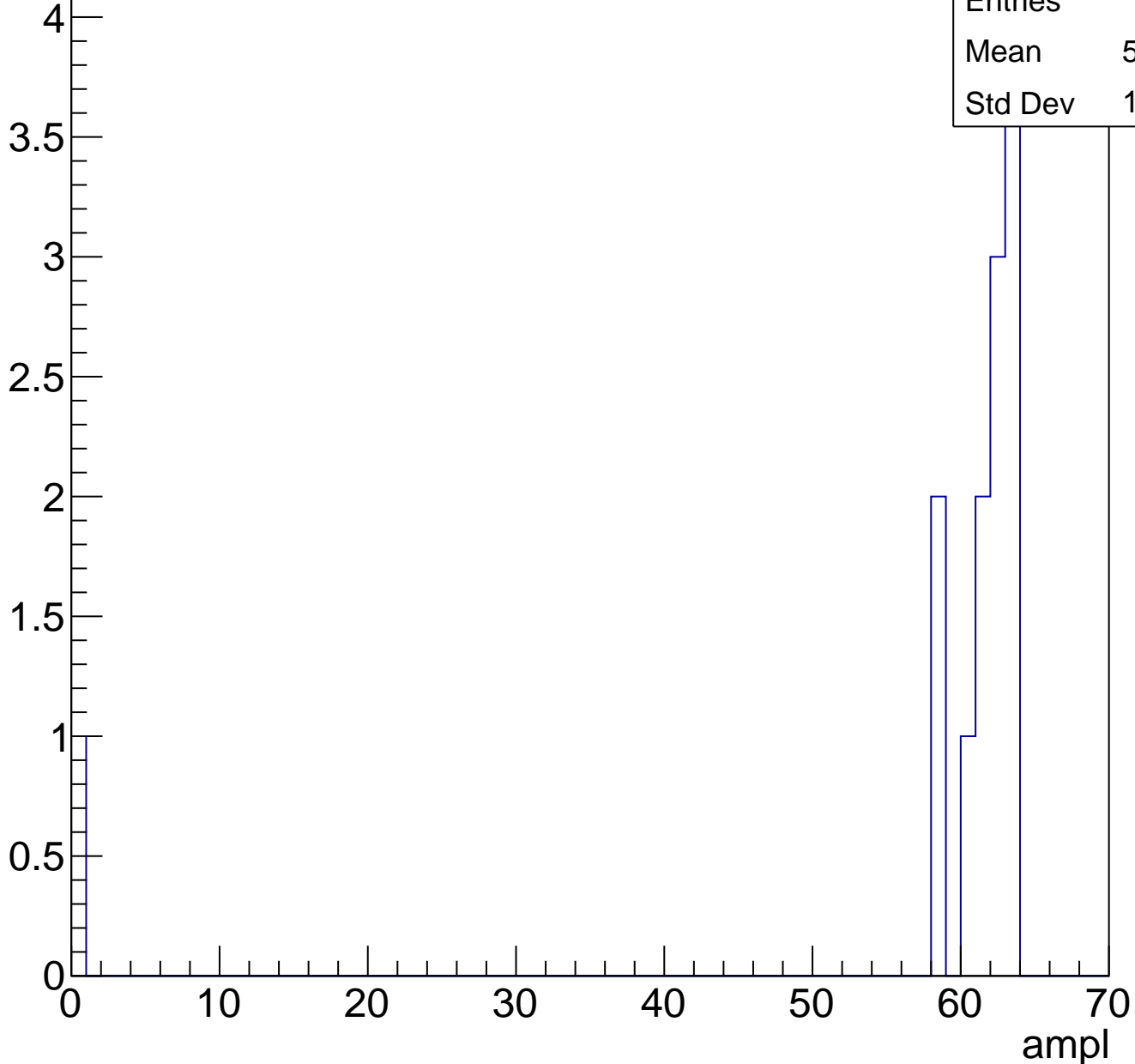
Entry



# B1L103S, U22-ch76, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch76, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



# B1L103S, U22-ch77, adc0

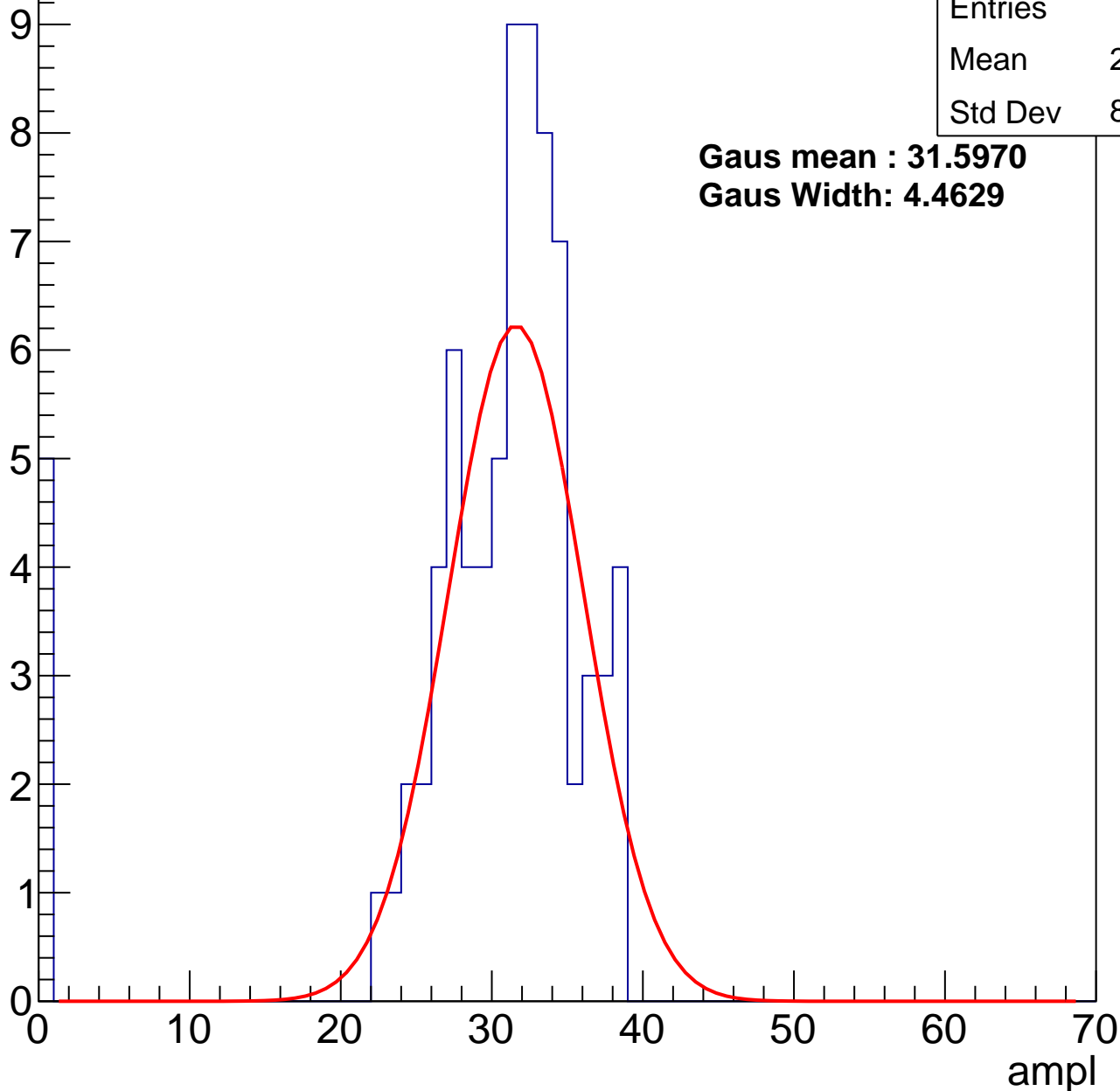
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	29.08
Std Dev	8.416

**Gaus mean : 31.5970**

**Gaus Width: 4.4629**



# B1L103S, U22-ch77, adc1

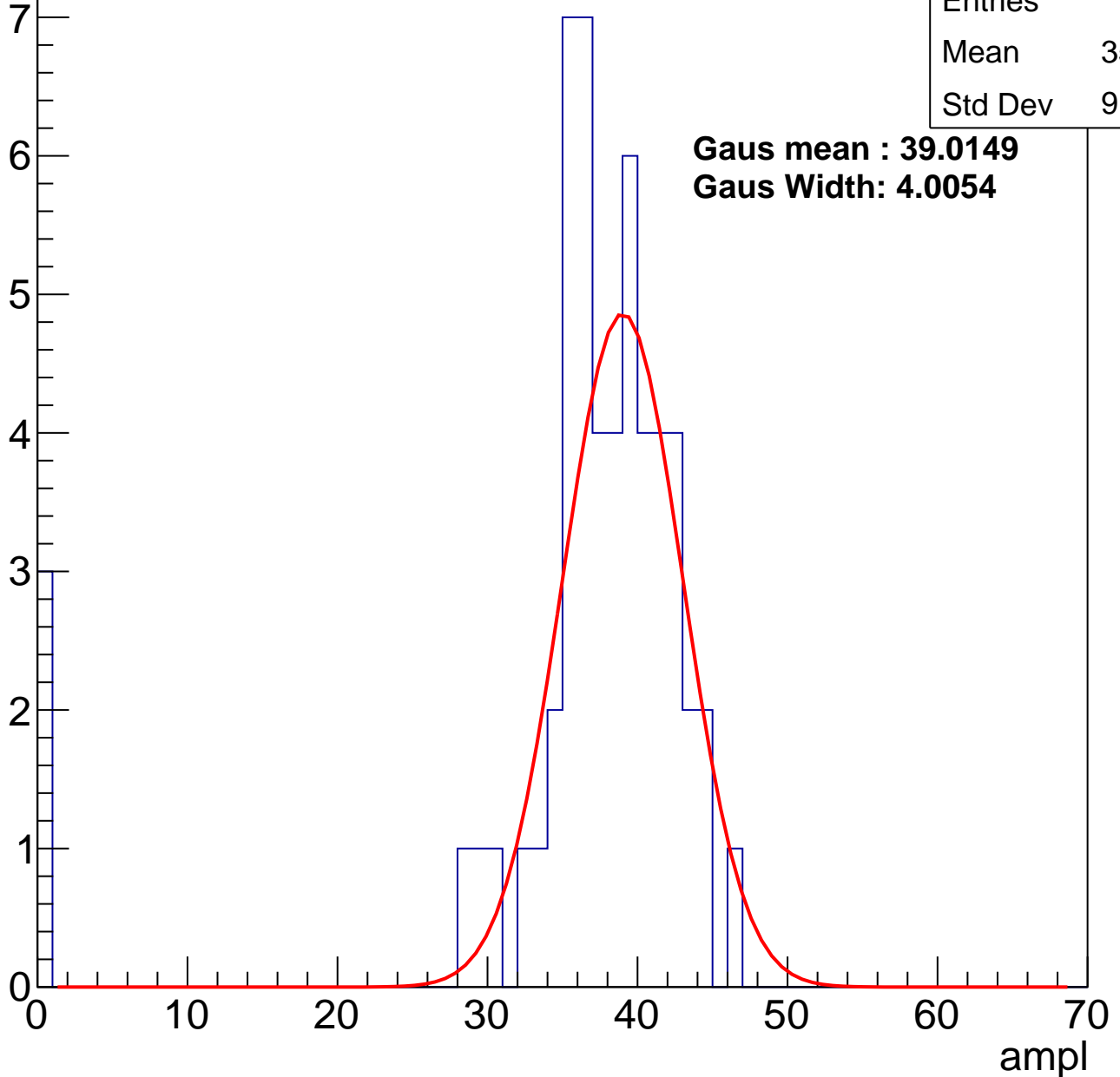
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	35.69
Std Dev	9.322

**Gaus mean : 39.0149**

**Gaus Width: 4.0054**



# B1L103S, U22-ch77, adc2

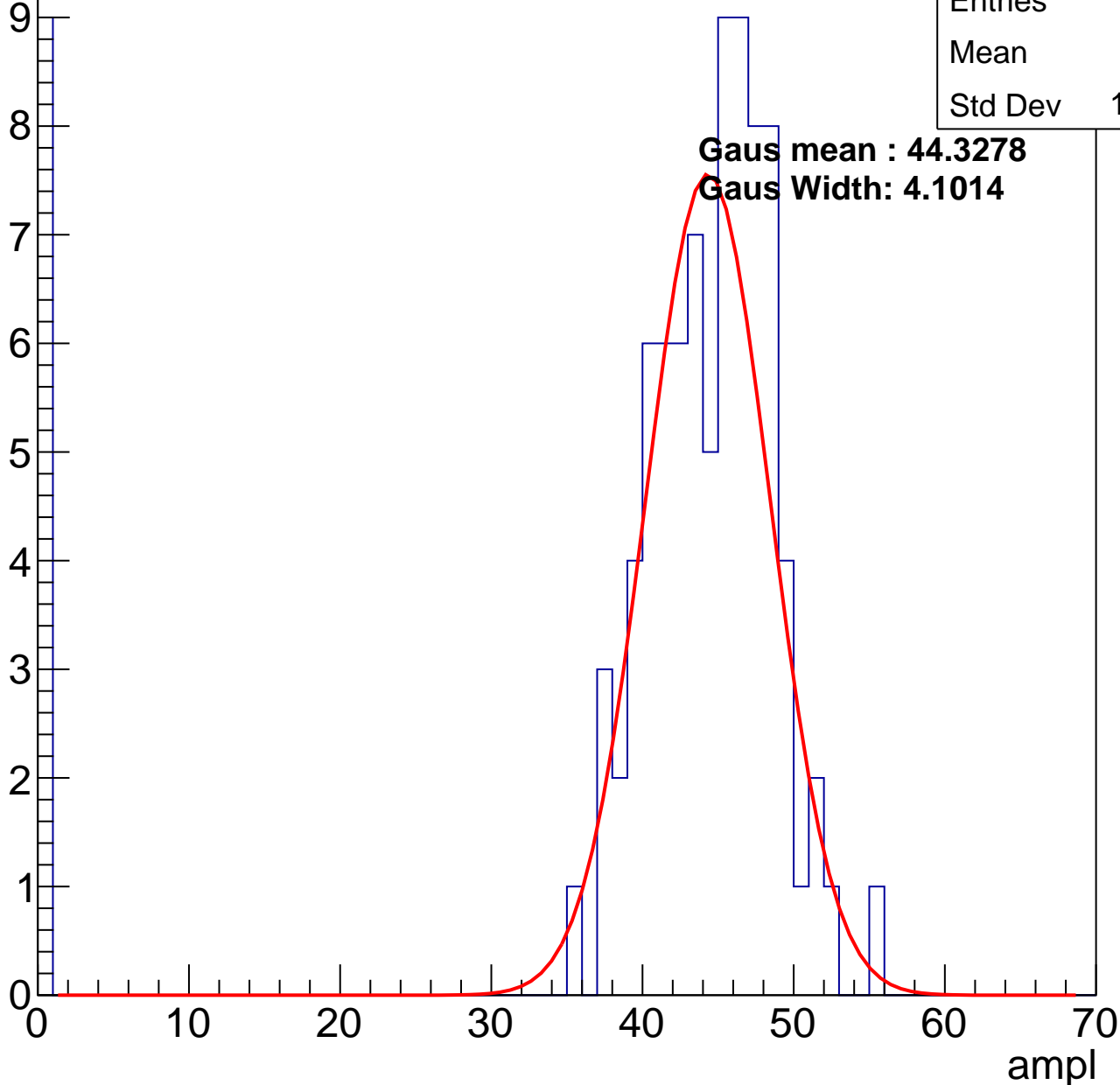
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	92
Mean	39.9
Std Dev	13.64

**Gaus mean : 44.3278**

**Gaus Width: 4.1014**

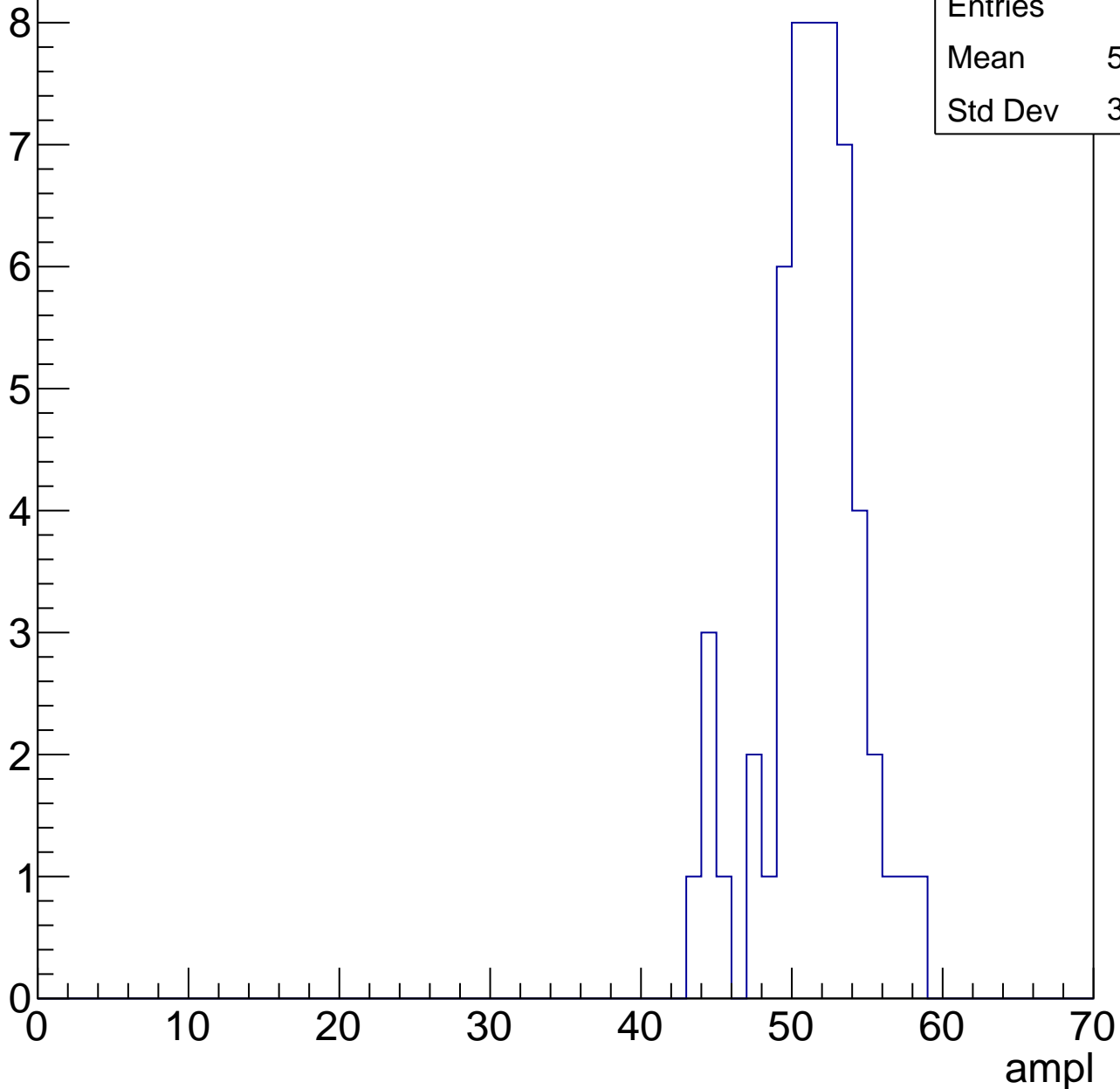


# B1L103S, U22-ch77, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	50.89
Std Dev	3.143

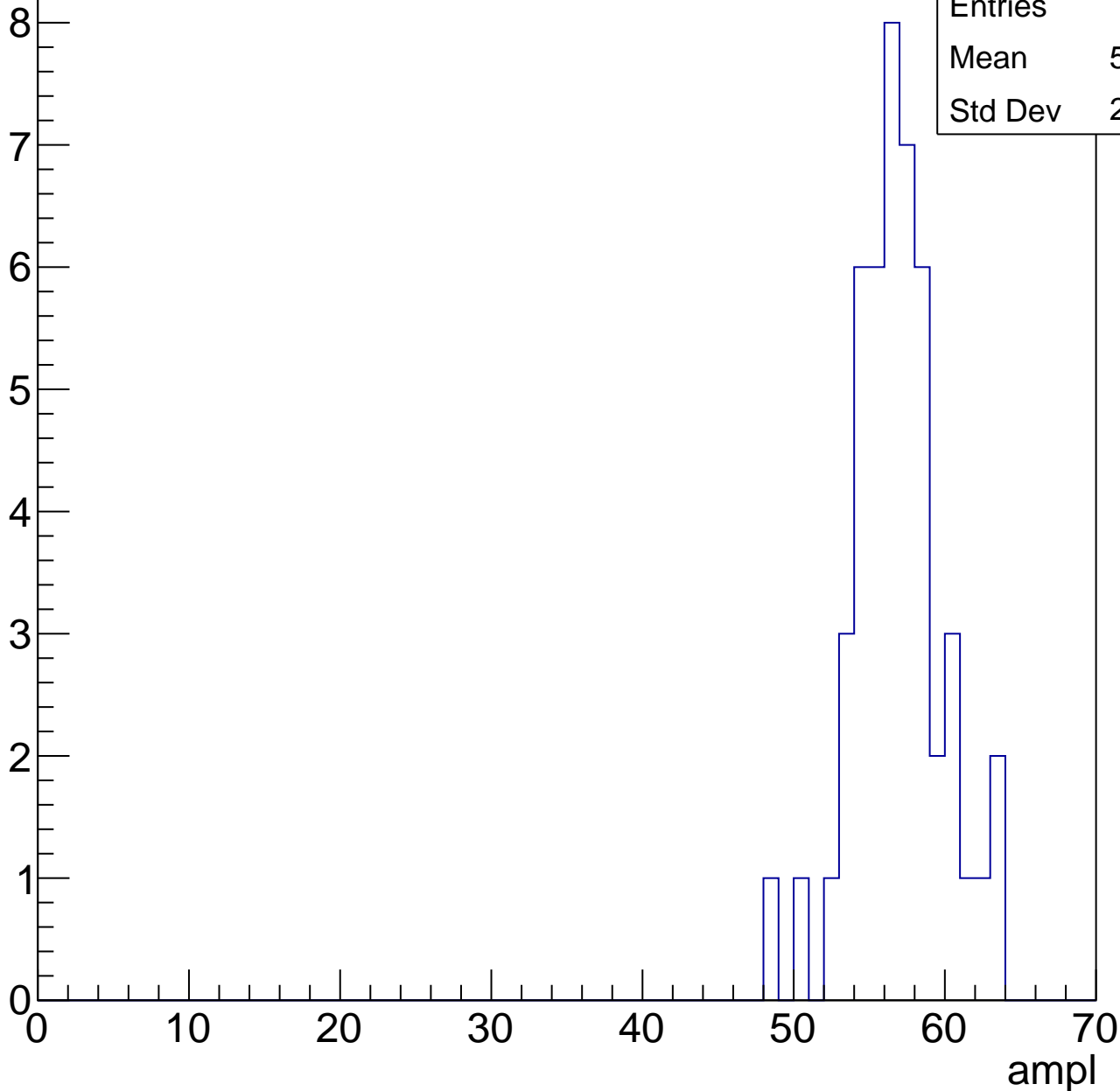


# B1L103S, U22-ch77, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	56.35
Std Dev	2.969

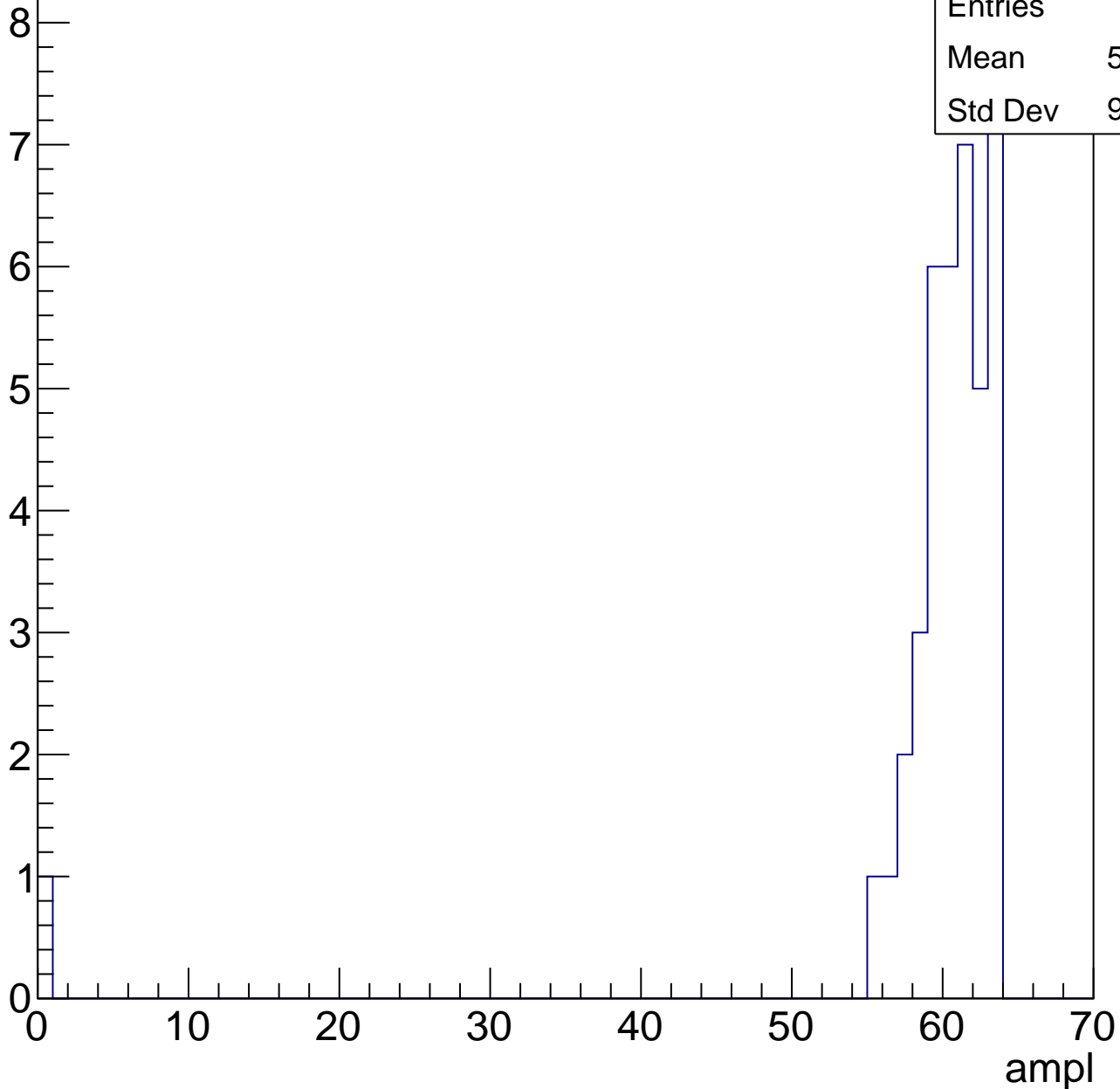


# B1L103S, U22-ch77, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

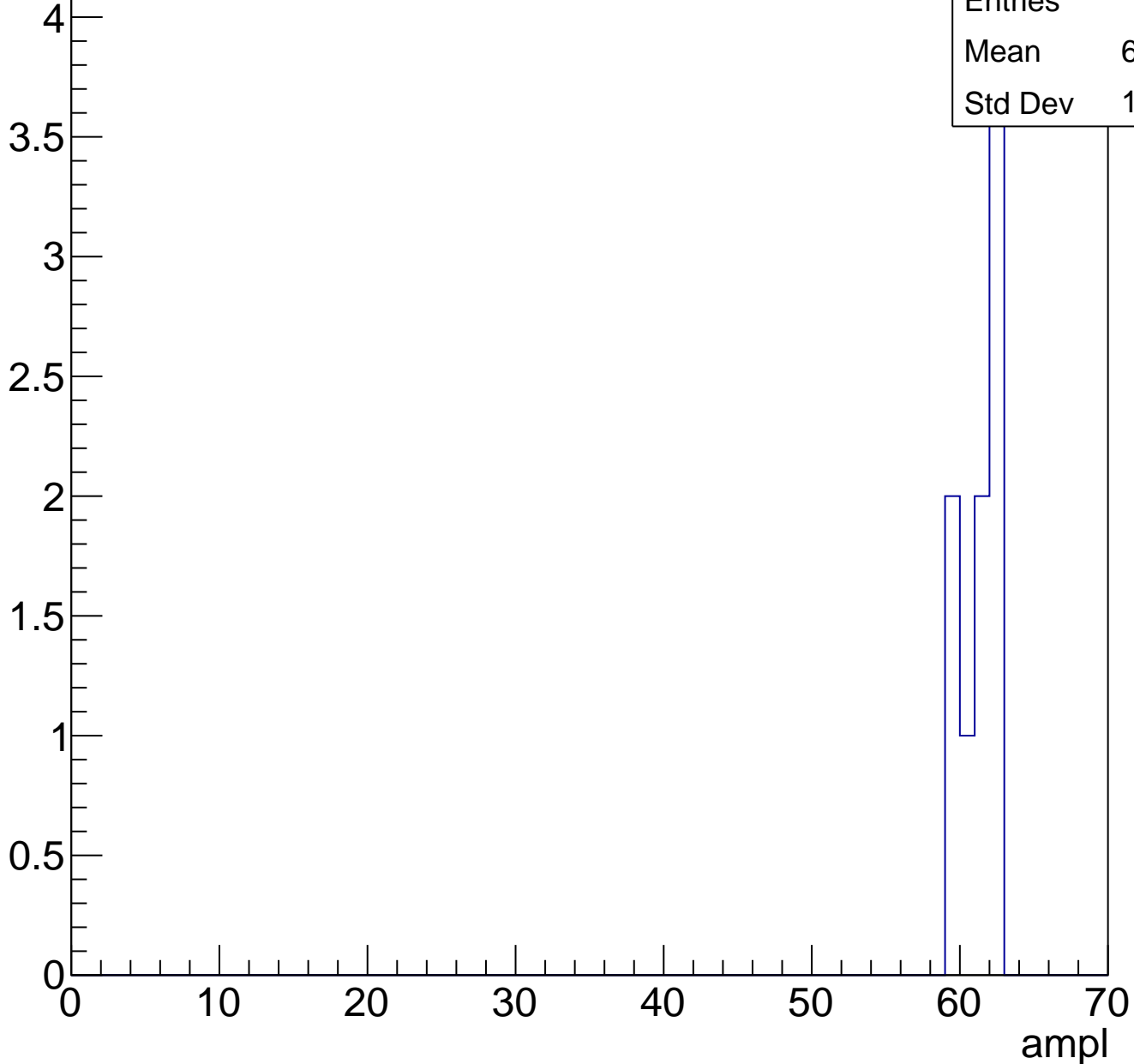
Entries	40
Mean	58.85
Std Dev	9.648



# B1L103S, U22-ch77, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch77, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch78, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	103
Mean	19.95
Std Dev	13.12

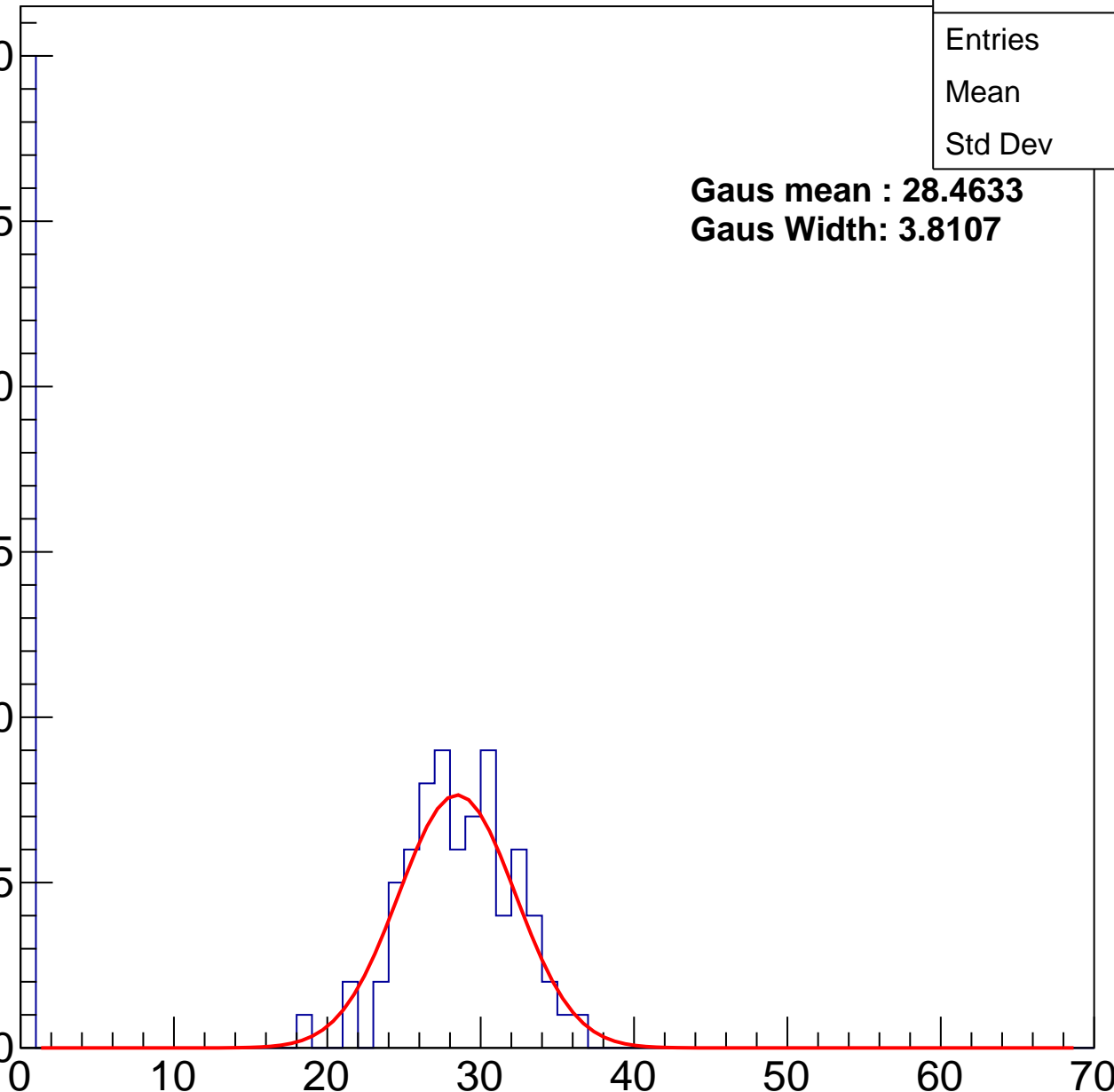
**Gaus mean : 28.4633**

**Gaus Width: 3.8107**

Entry

30  
25  
20  
15  
10  
5  
0

ampl



# B1L103S, U22-ch78, adc1

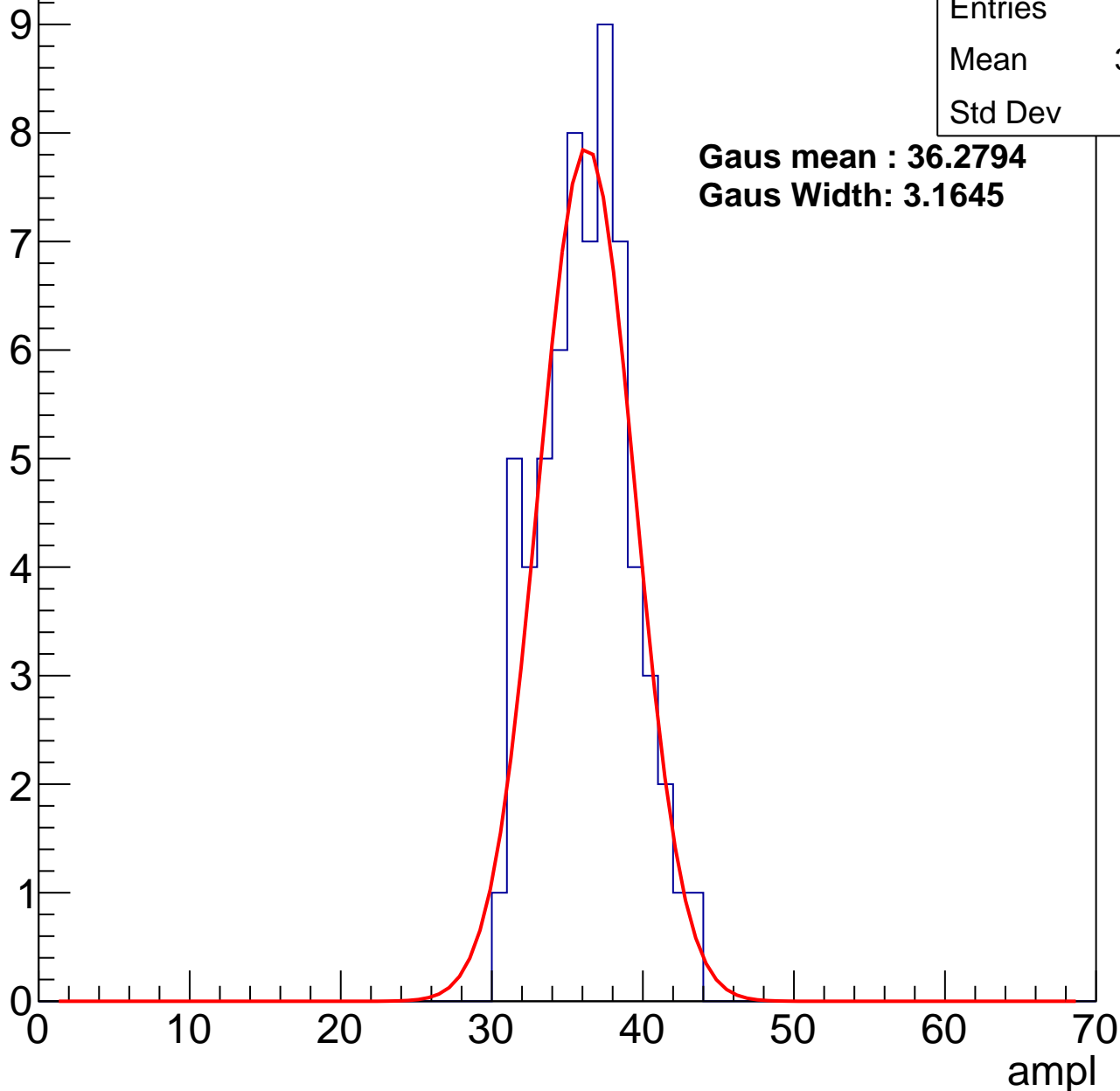
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	35.81
Std Dev	2.97

**Gaus mean : 36.2794**

**Gaus Width: 3.1645**



# B1L103S, U22-ch78, adc2

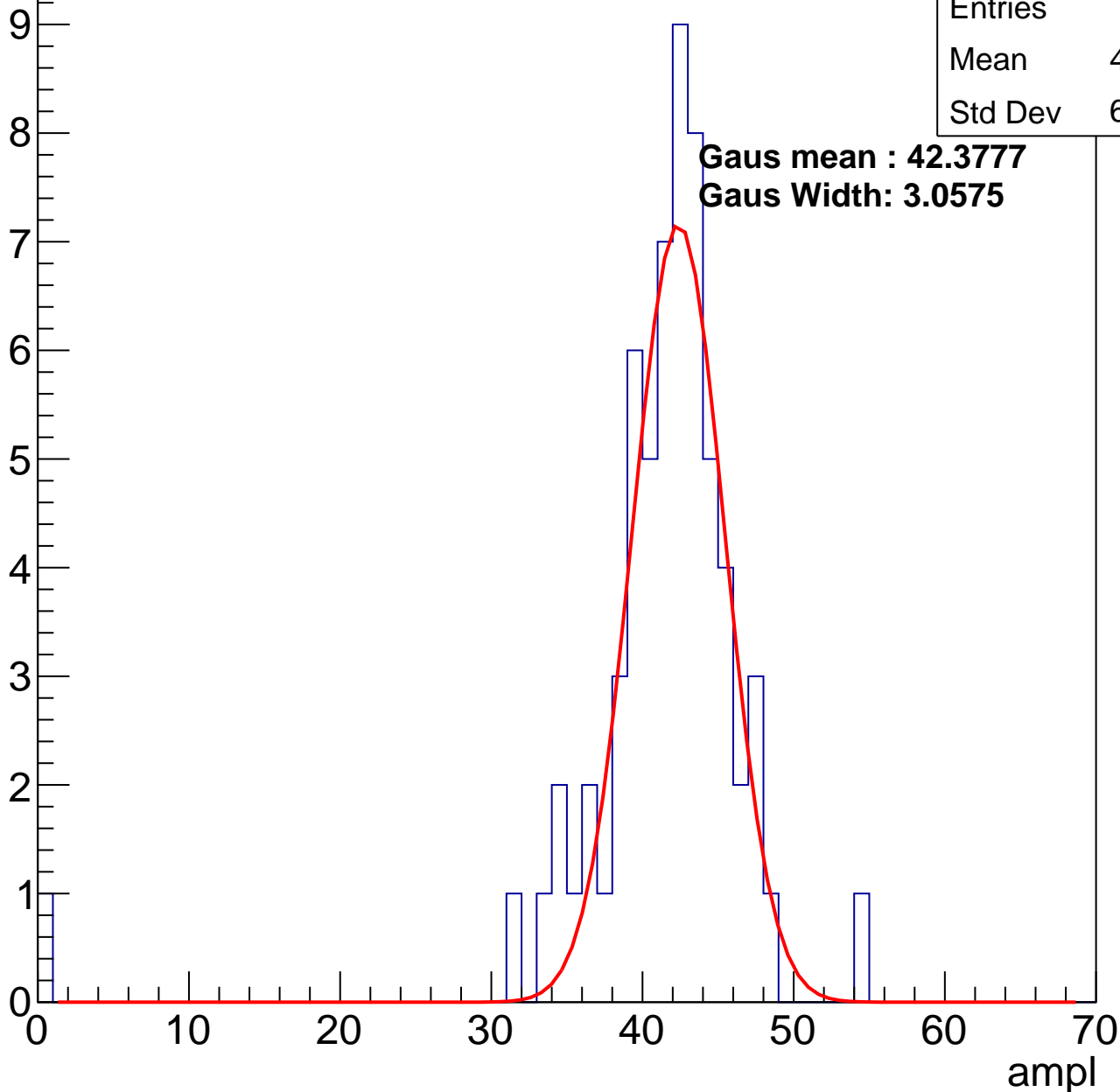
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	40.76
Std Dev	6.453

**Gaus mean : 42.3777**

**Gaus Width: 3.0575**

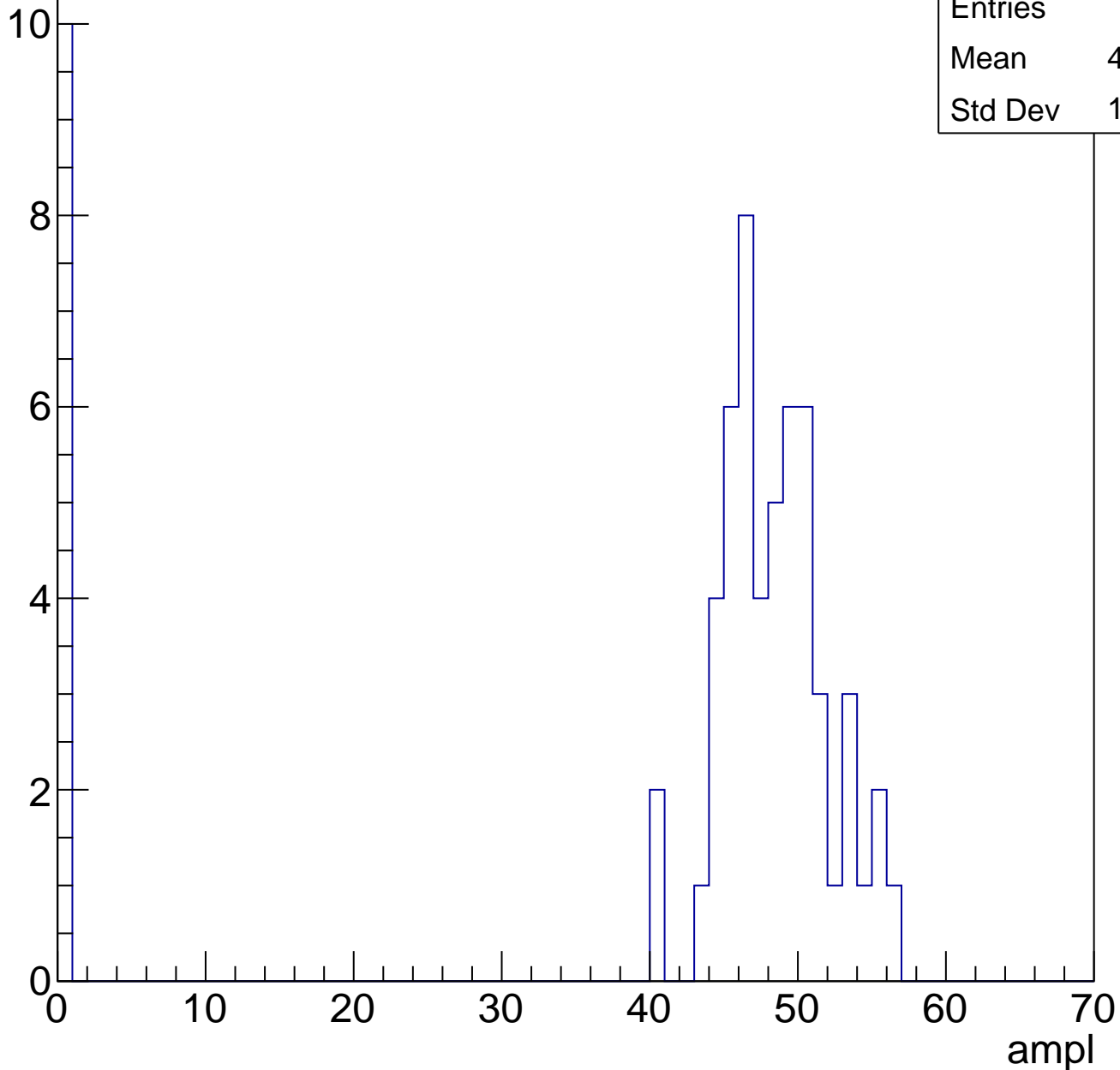


# B1L103S, U22-ch78, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	63
Mean	40.37
Std Dev	17.83

Entry

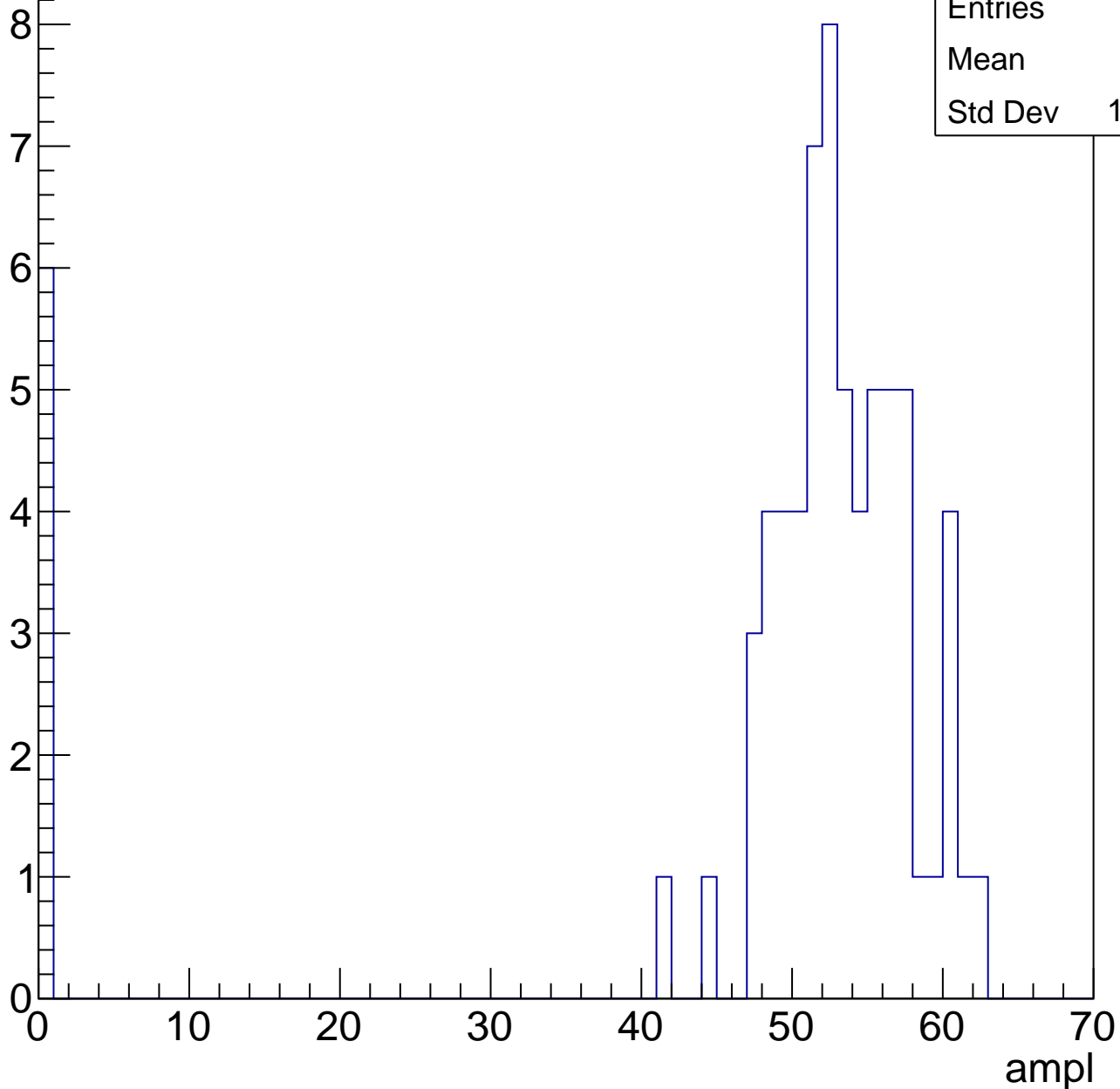


# B1L103S, U22-ch78, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	48.4
Std Dev	15.35

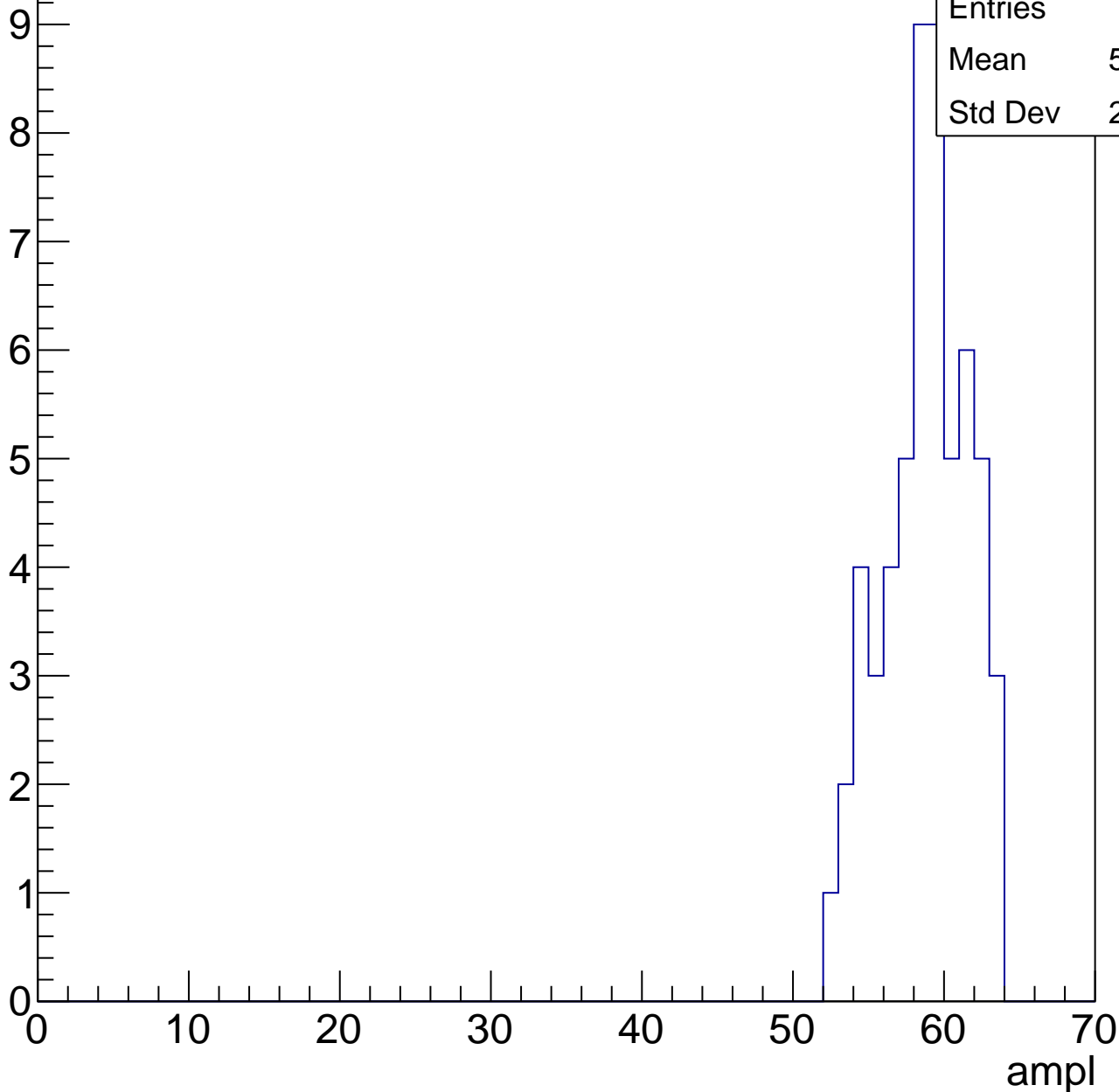


# B1L103S, U22-ch78, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.32
Std Dev	2.772

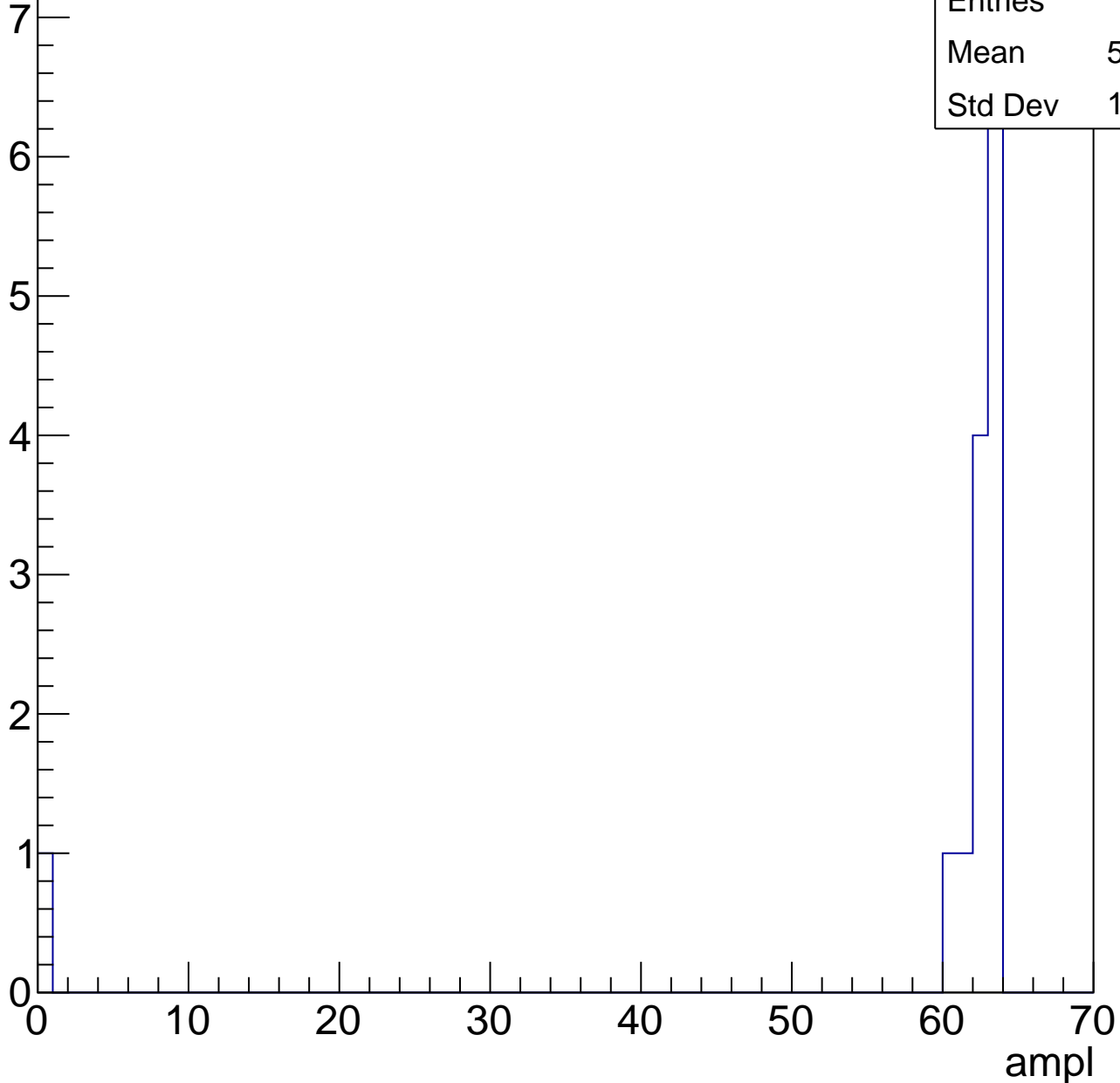


# B1L103S, U22-ch78, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.86
Std Dev	16.07



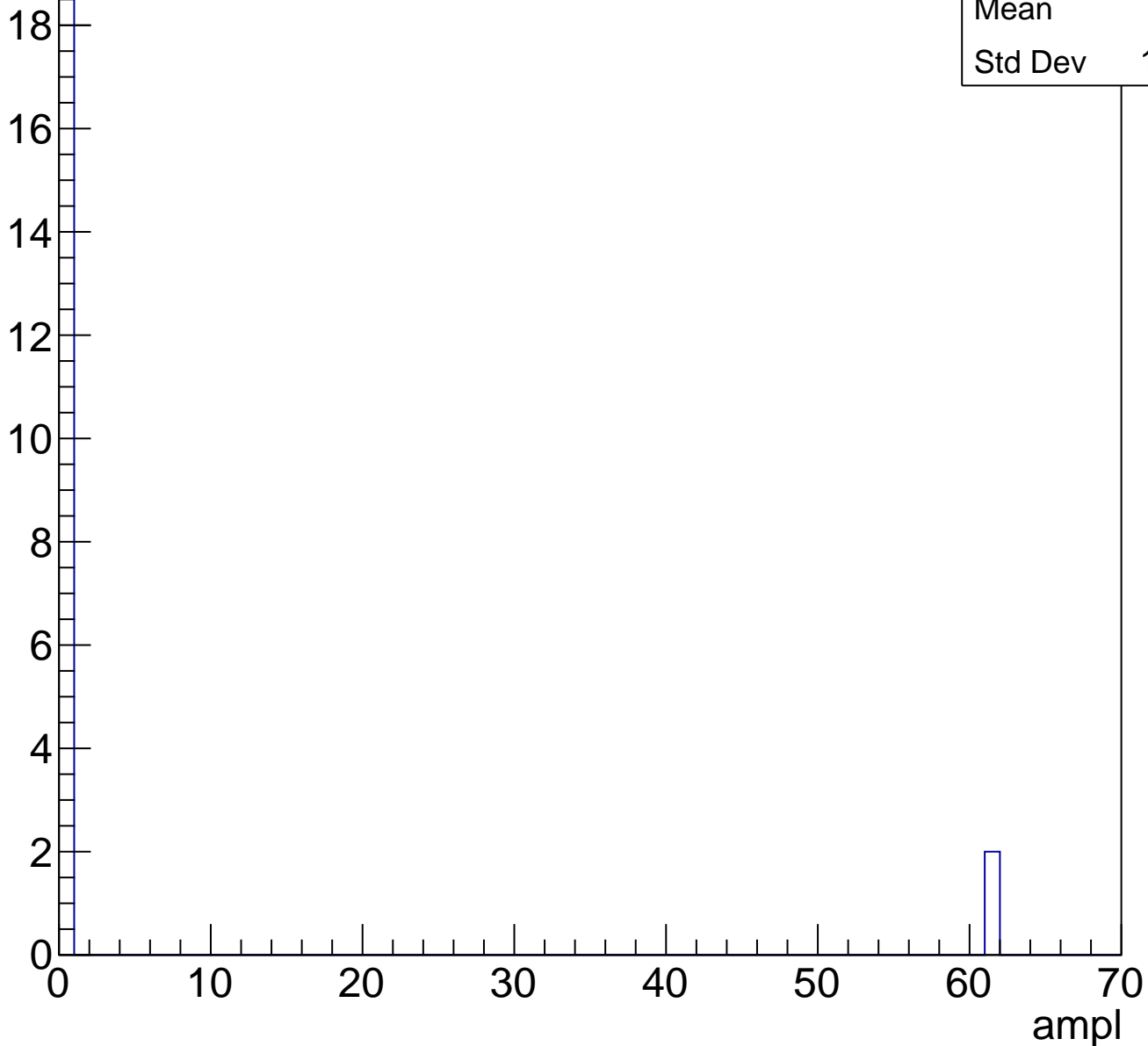


# B1L103S, U22-ch78, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91

Entry



# B1L103S, U22-ch79, adc0

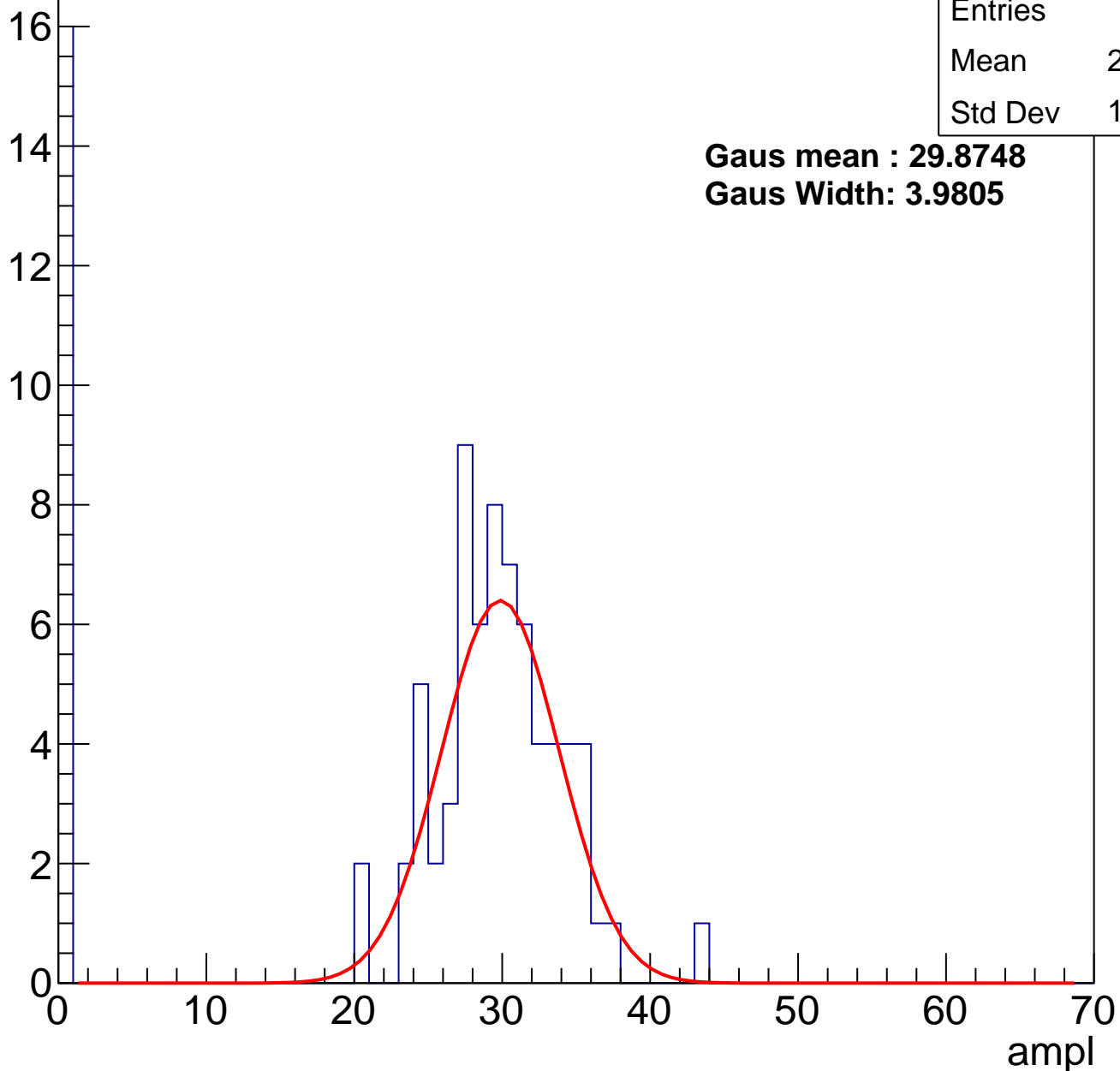
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	23.82
Std Dev	12.04

**Gaus mean : 29.8748**

**Gaus Width: 3.9805**

Entry



# B1L103S, U22-ch79, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	32.25
Std Dev	12.8

**Gaus mean : 37.1817**

**Gaus Width: 3.8517**

Entry

10

8

6

4

2

0

0

10

20

30

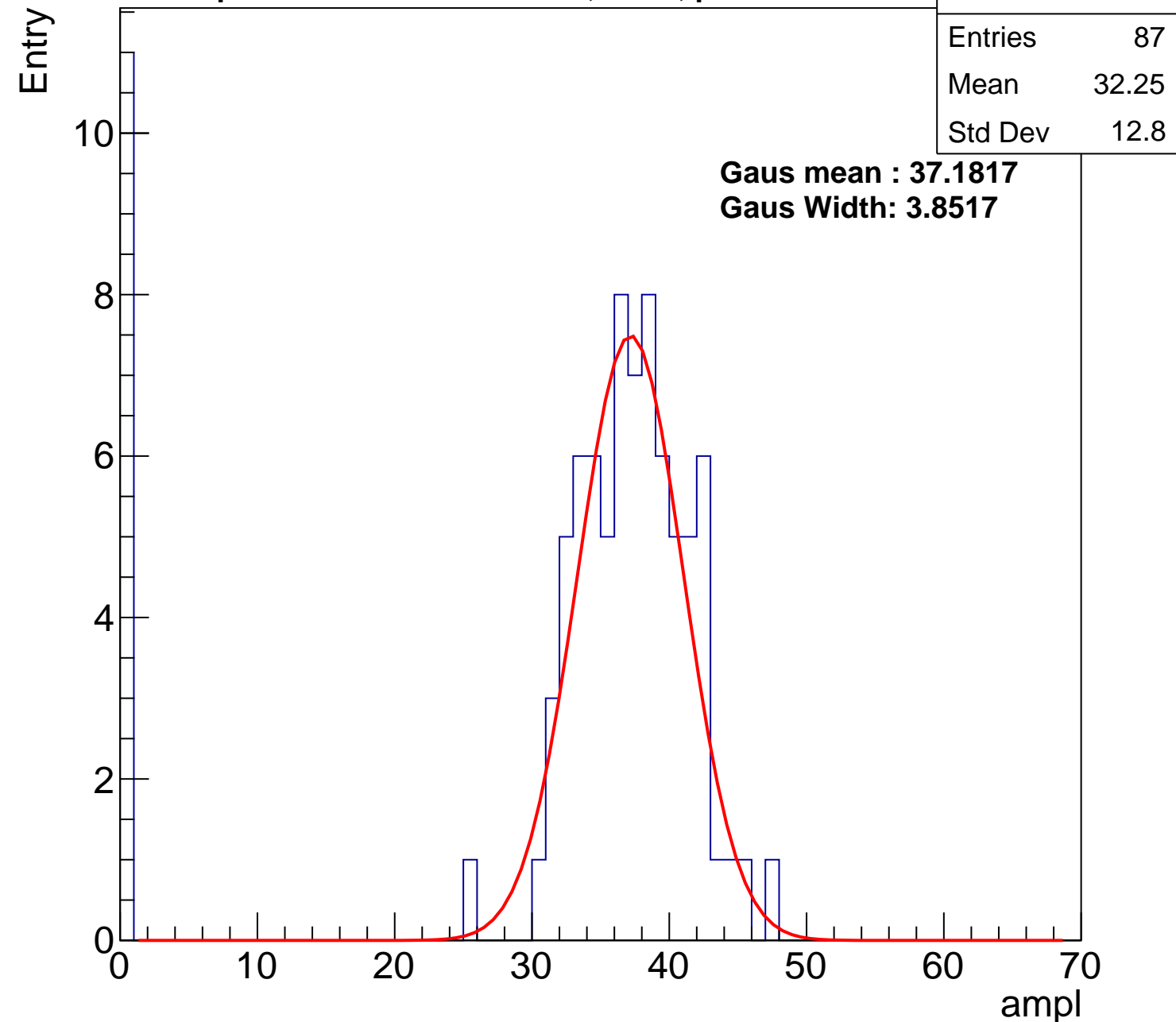
40

50

60

70

ampl



# B1L103S, U22-ch79, adc2

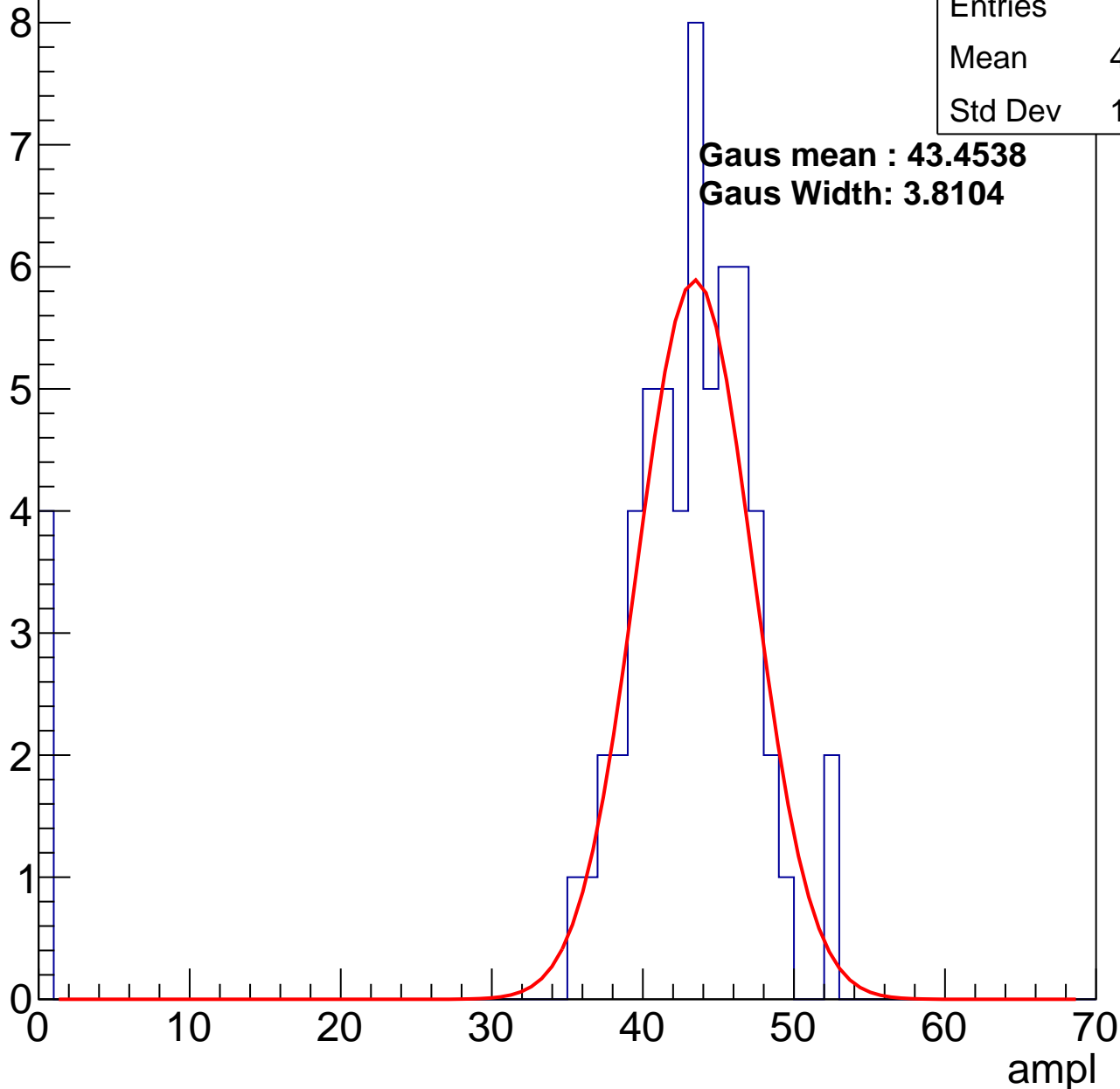
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	40.27
Std Dev	11.14

**Gaus mean : 43.4538**

**Gaus Width: 3.8104**

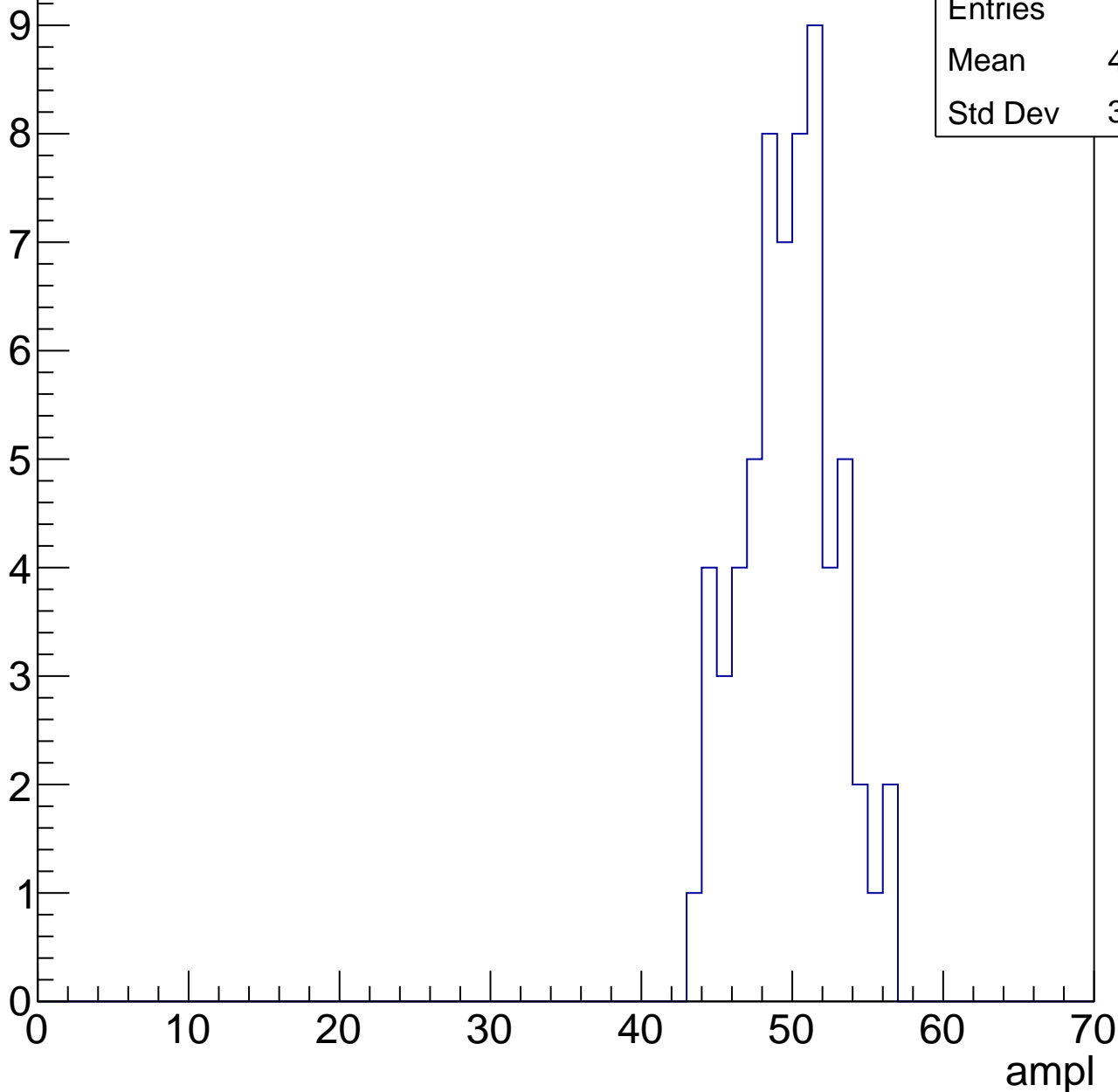


# B1L103S, U22-ch79, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	49.32
Std Dev	3.049

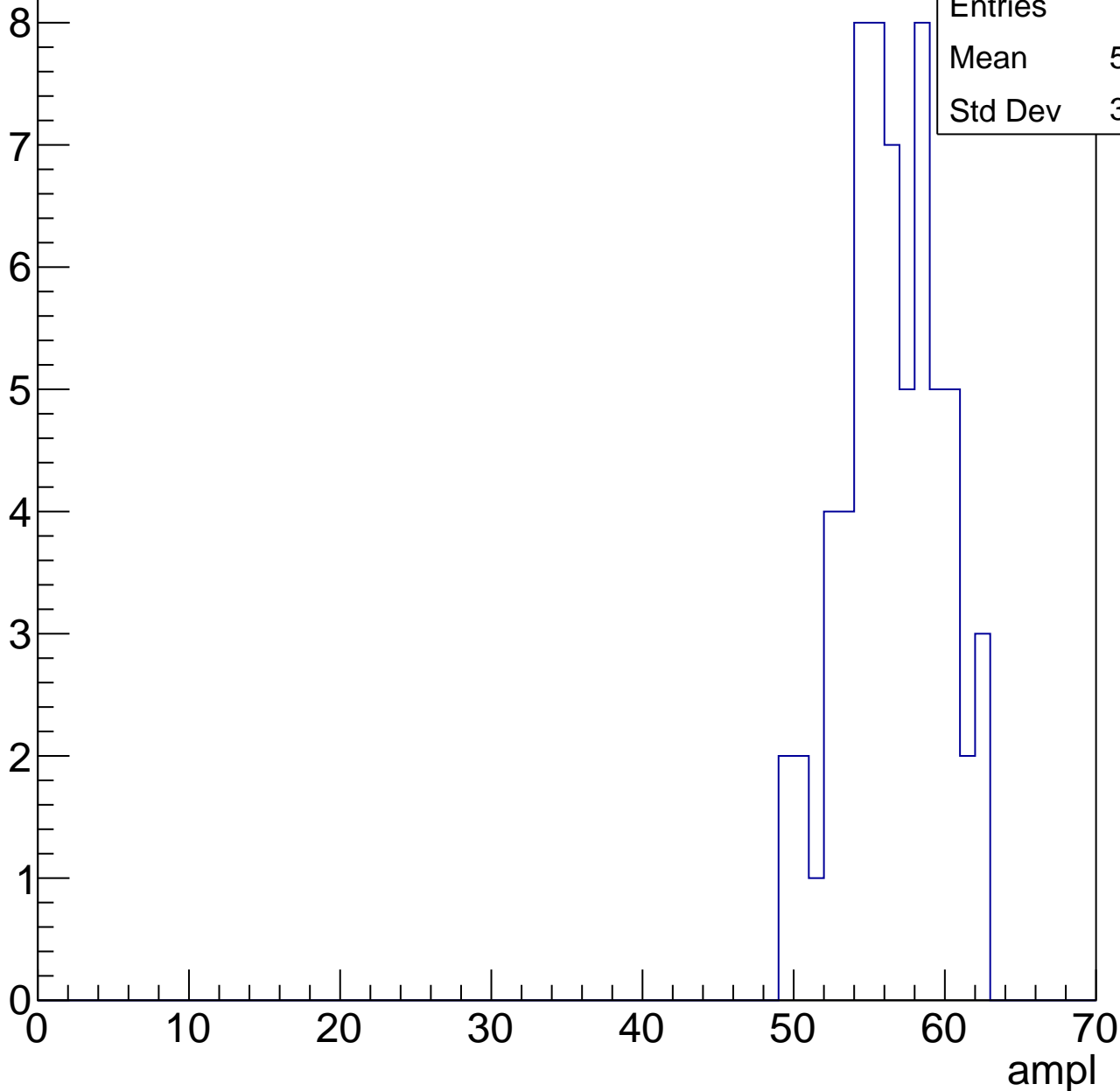


# B1L103S, U22-ch79, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	56.02
Std Dev	3.199

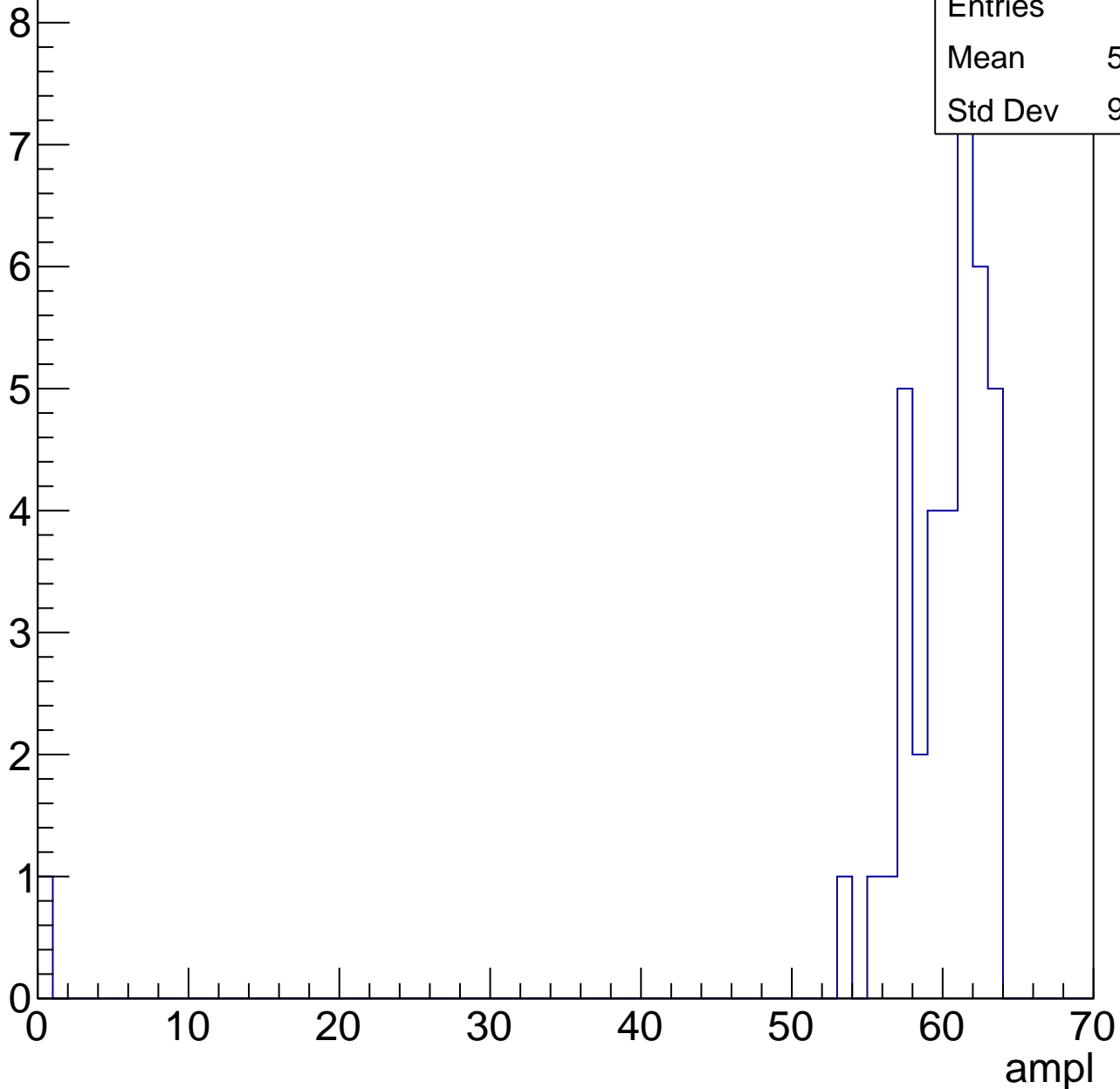


# B1L103S, U22-ch79, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	38
Mean	58.32
Std Dev	9.889

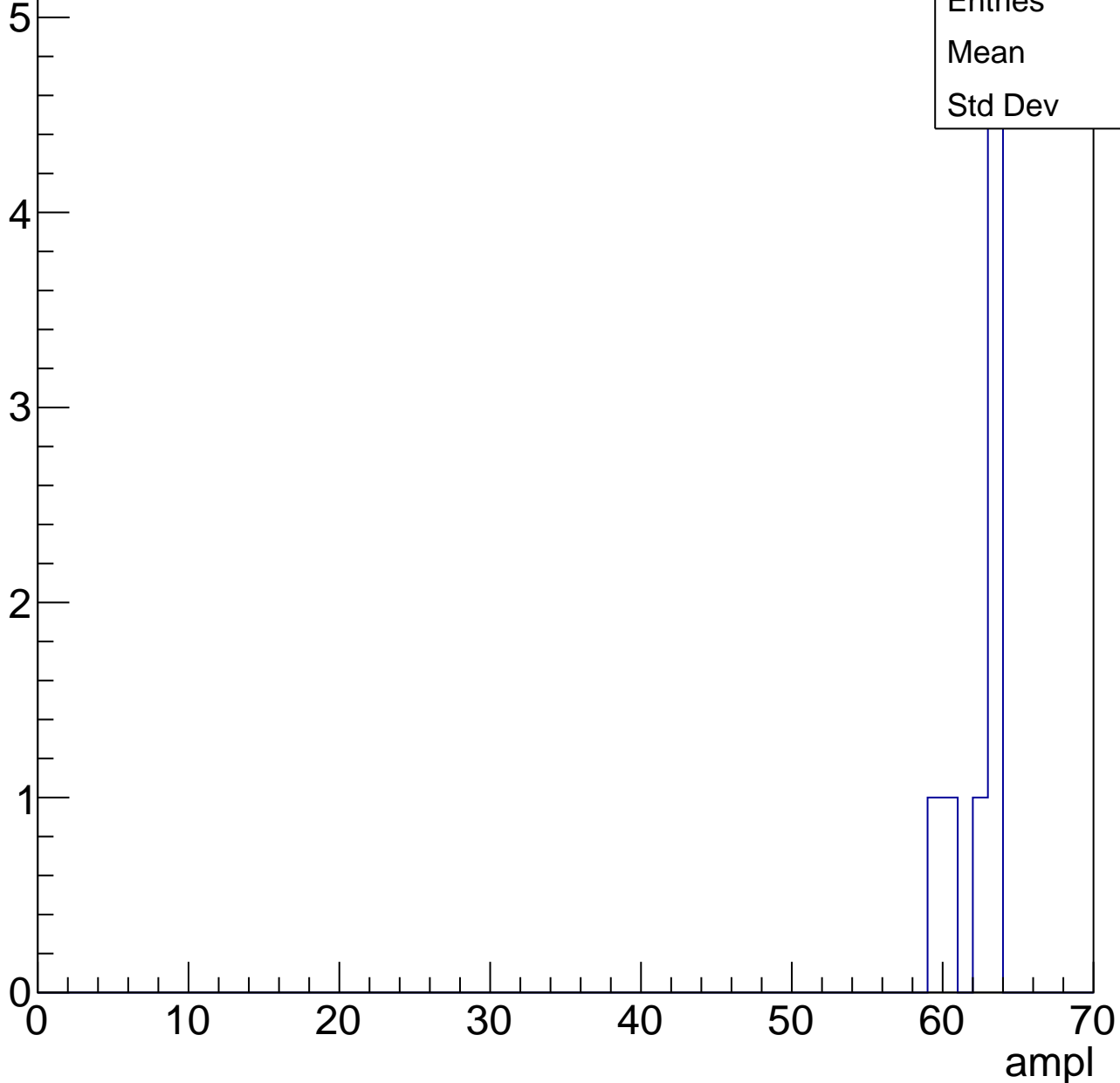


# B1L103S, U22-ch79, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	8
Mean	62
Std Dev	1.5





# B1L103S, U22-ch79, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch80, adc0

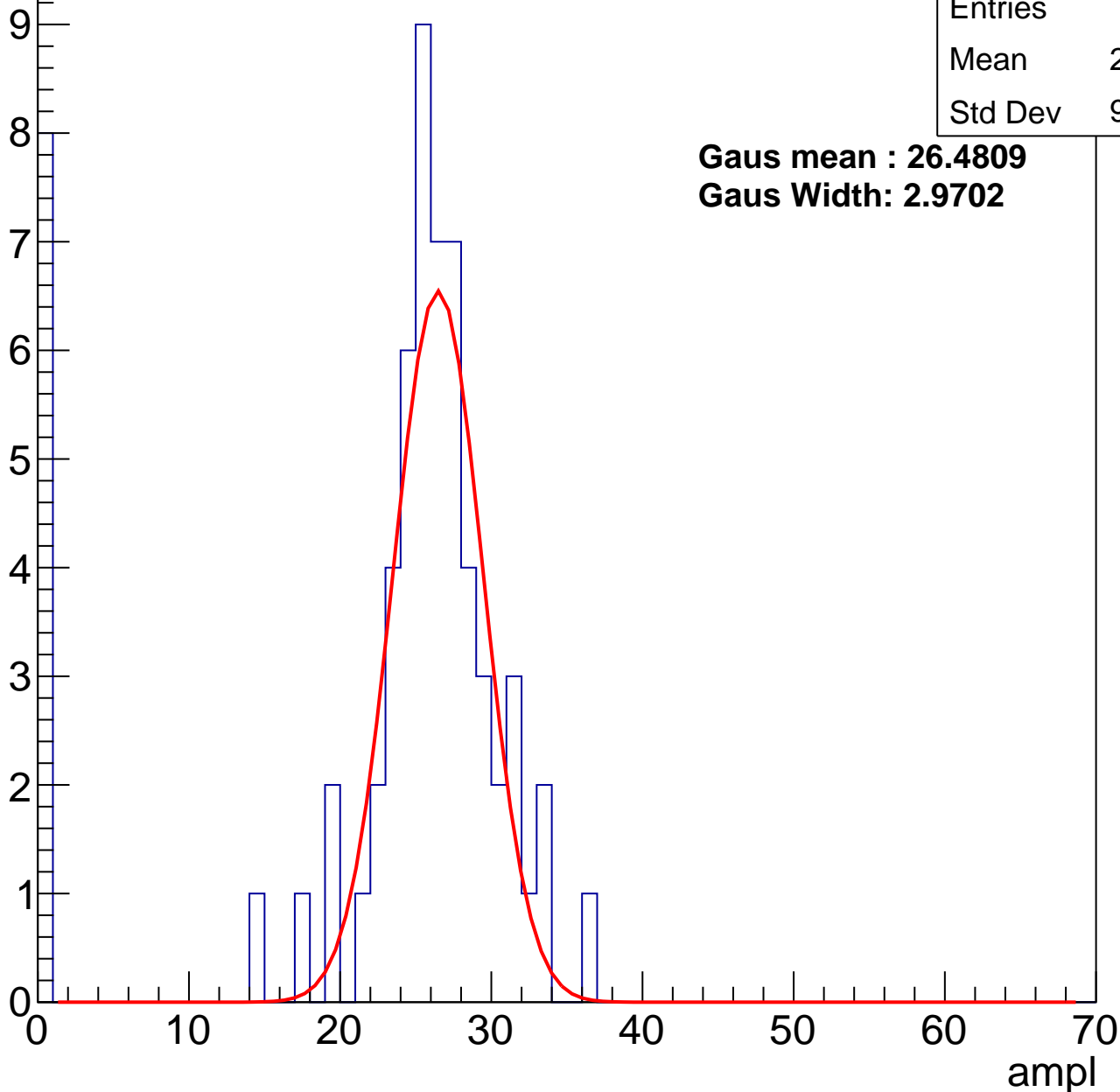
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	22.69
Std Dev	9.309

**Gaus mean : 26.4809**

**Gaus Width: 2.9702**



# B1L103S, U22-ch80, adc1

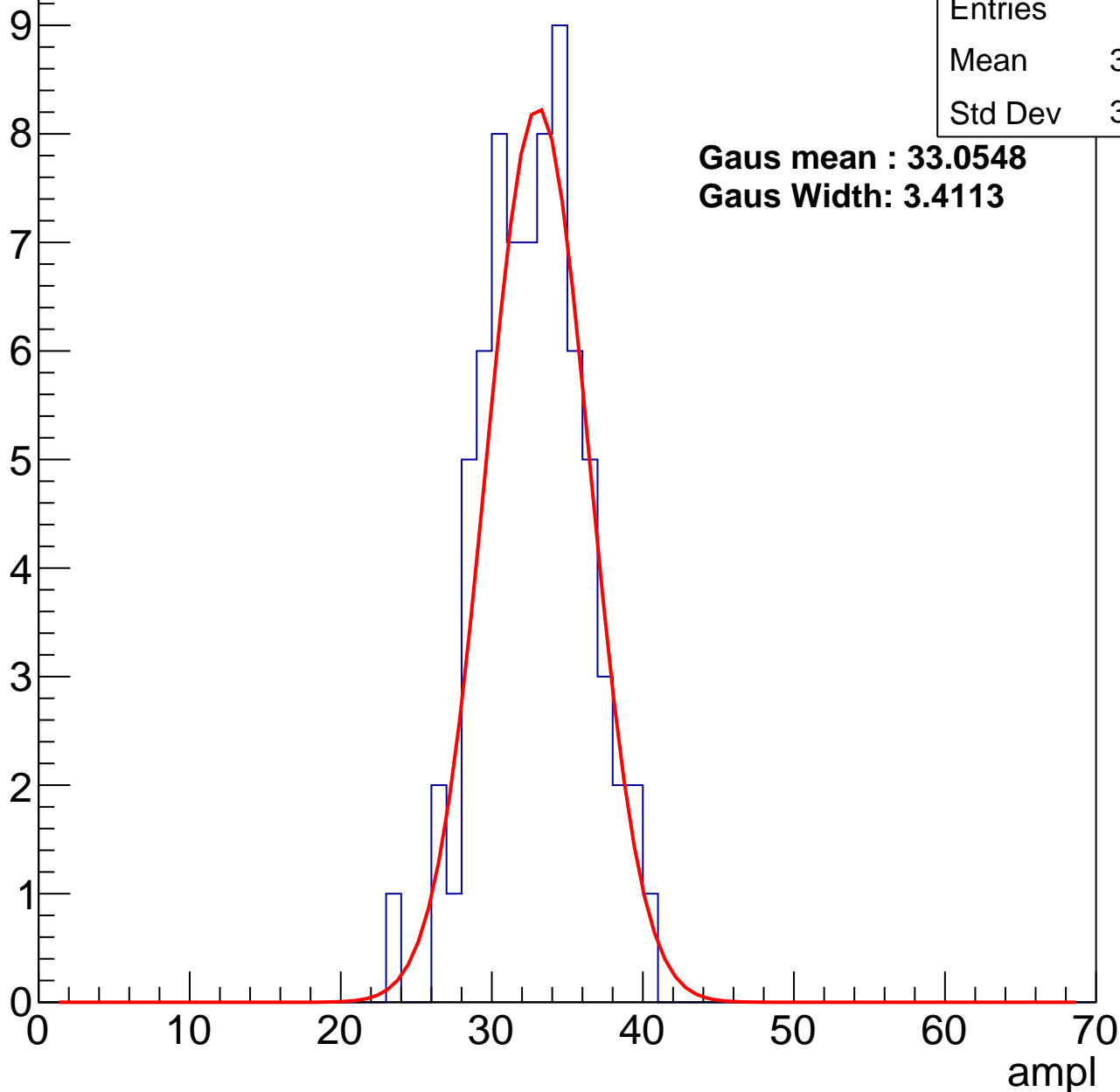
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	32.36
Std Dev	3.369

**Gaus mean : 33.0548**

**Gaus Width: 3.4113**



# B1L103S, U22-ch80, adc2

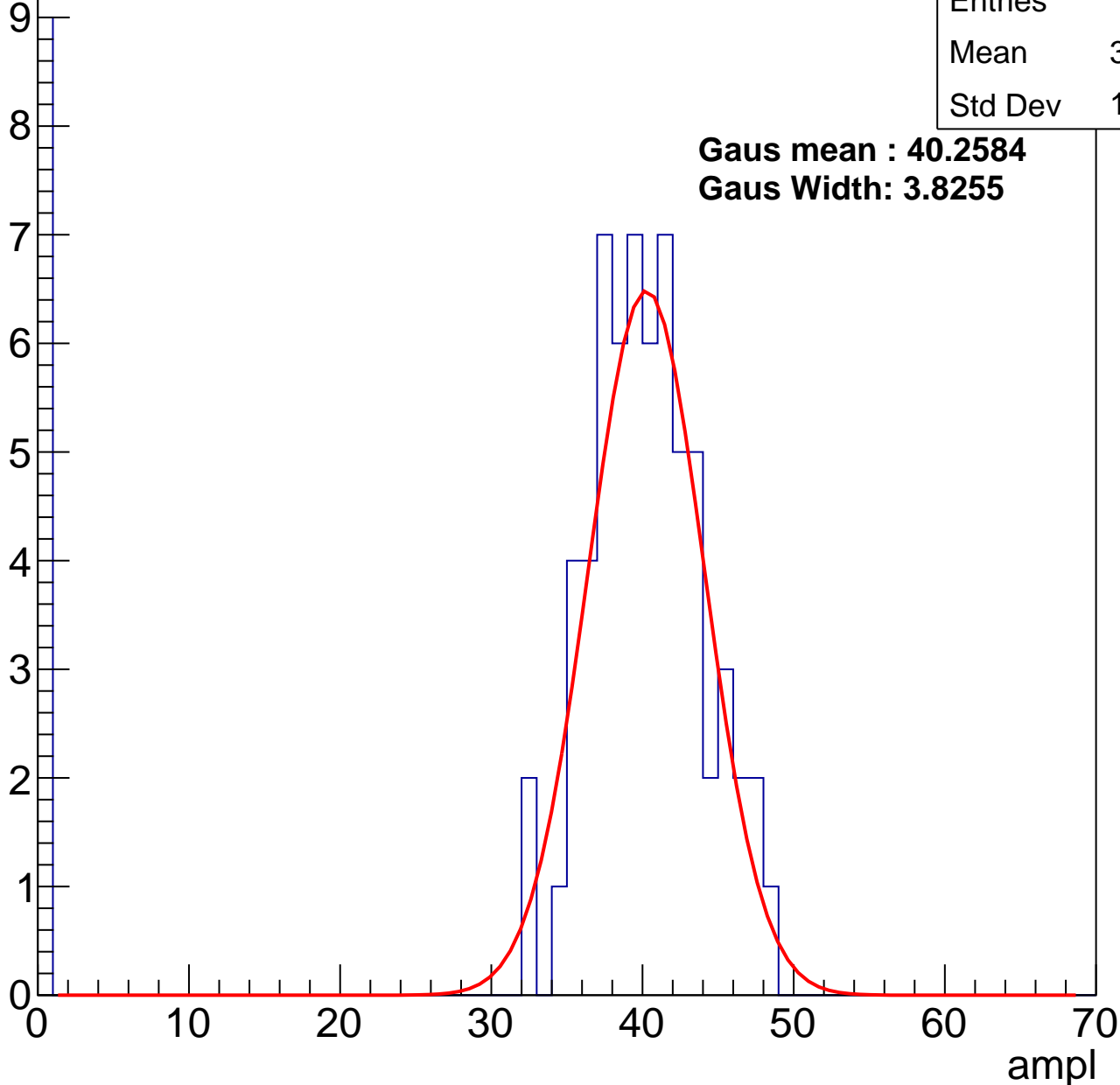
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.95
Std Dev	13.53

**Gaus mean : 40.2584**

**Gaus Width: 3.8255**

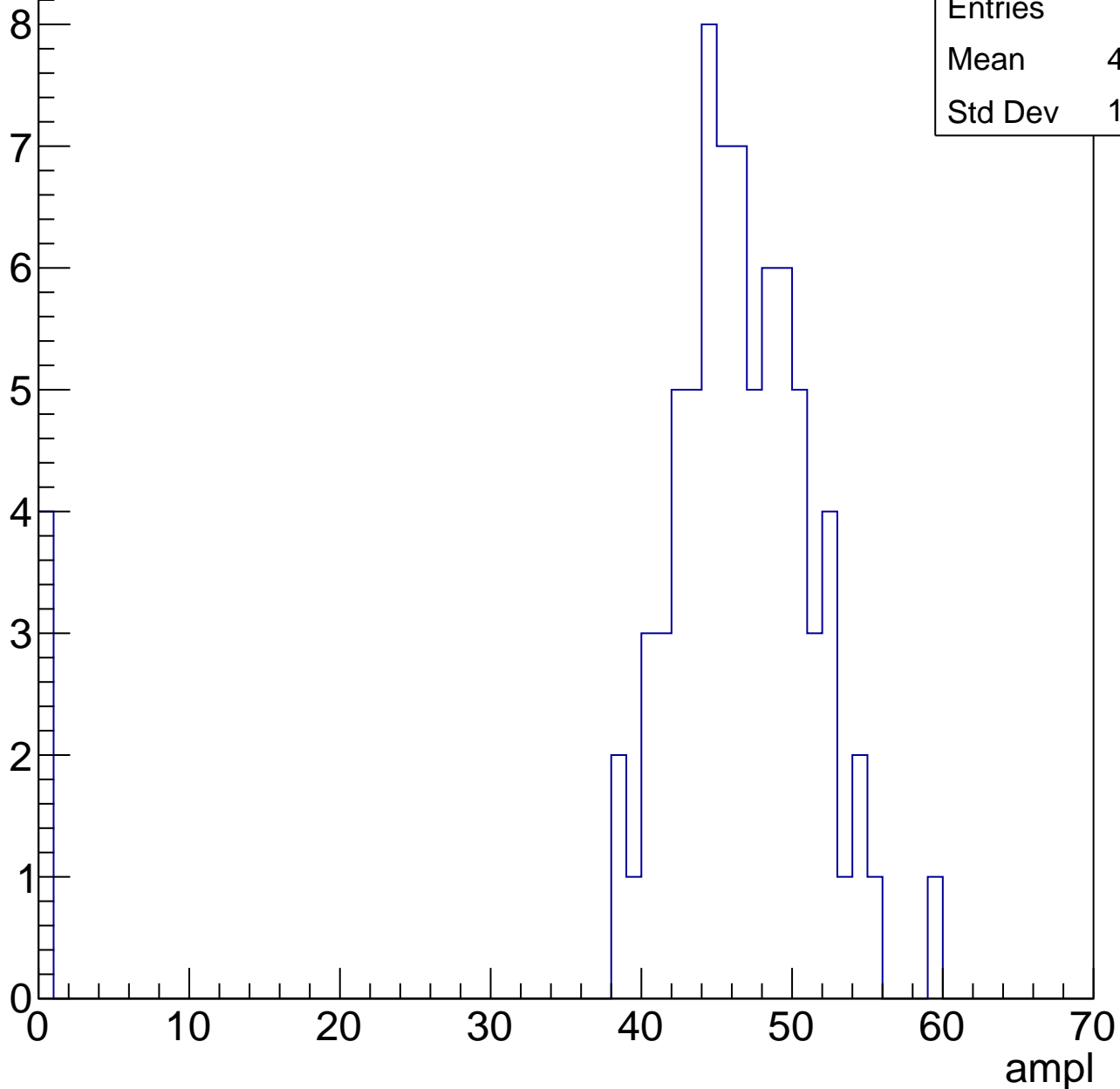


# B1L103S, U22-ch80, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

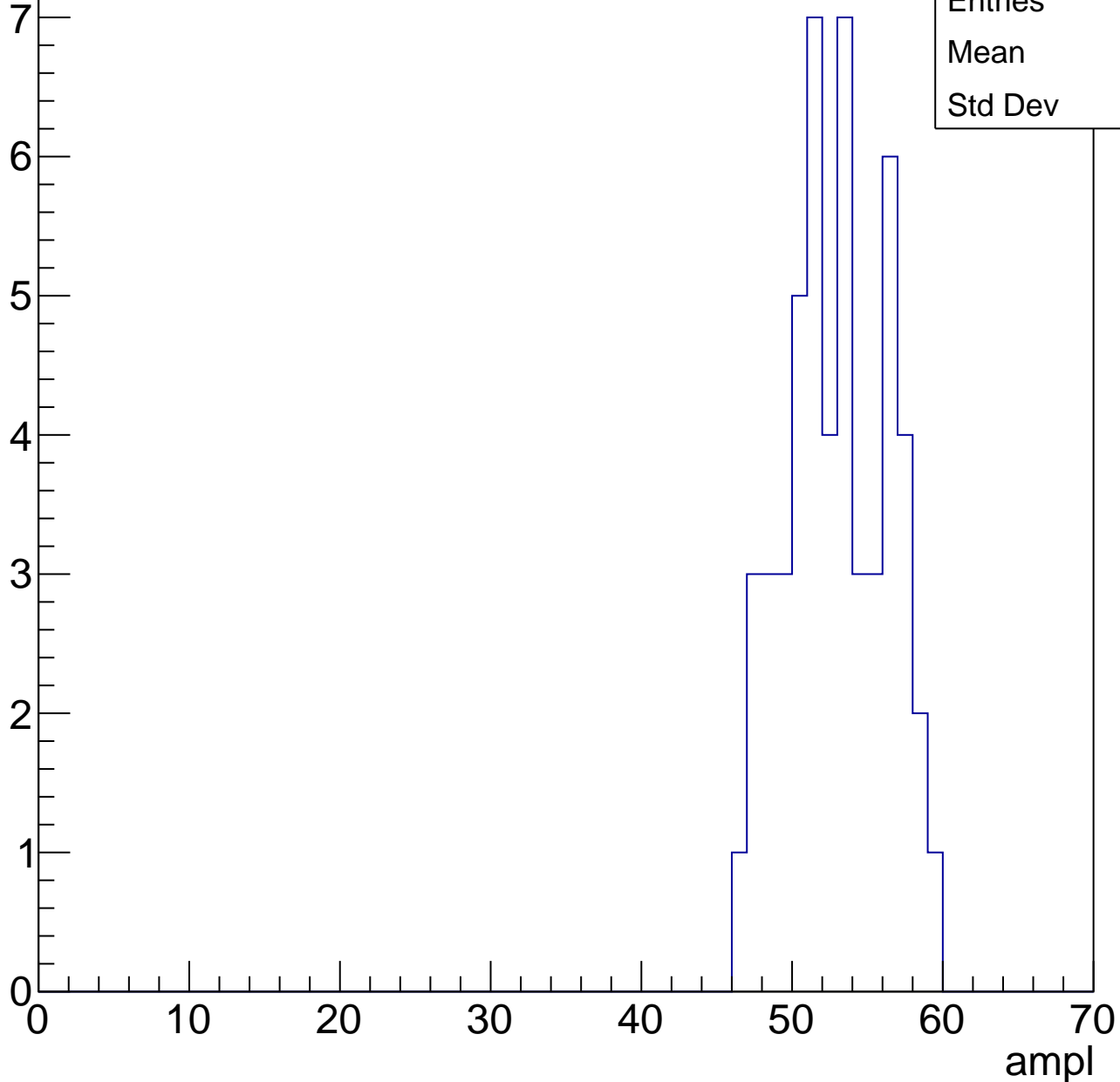
Entries	79
Mean	43.99
Std Dev	10.95



# B1L103S, U22-ch80, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



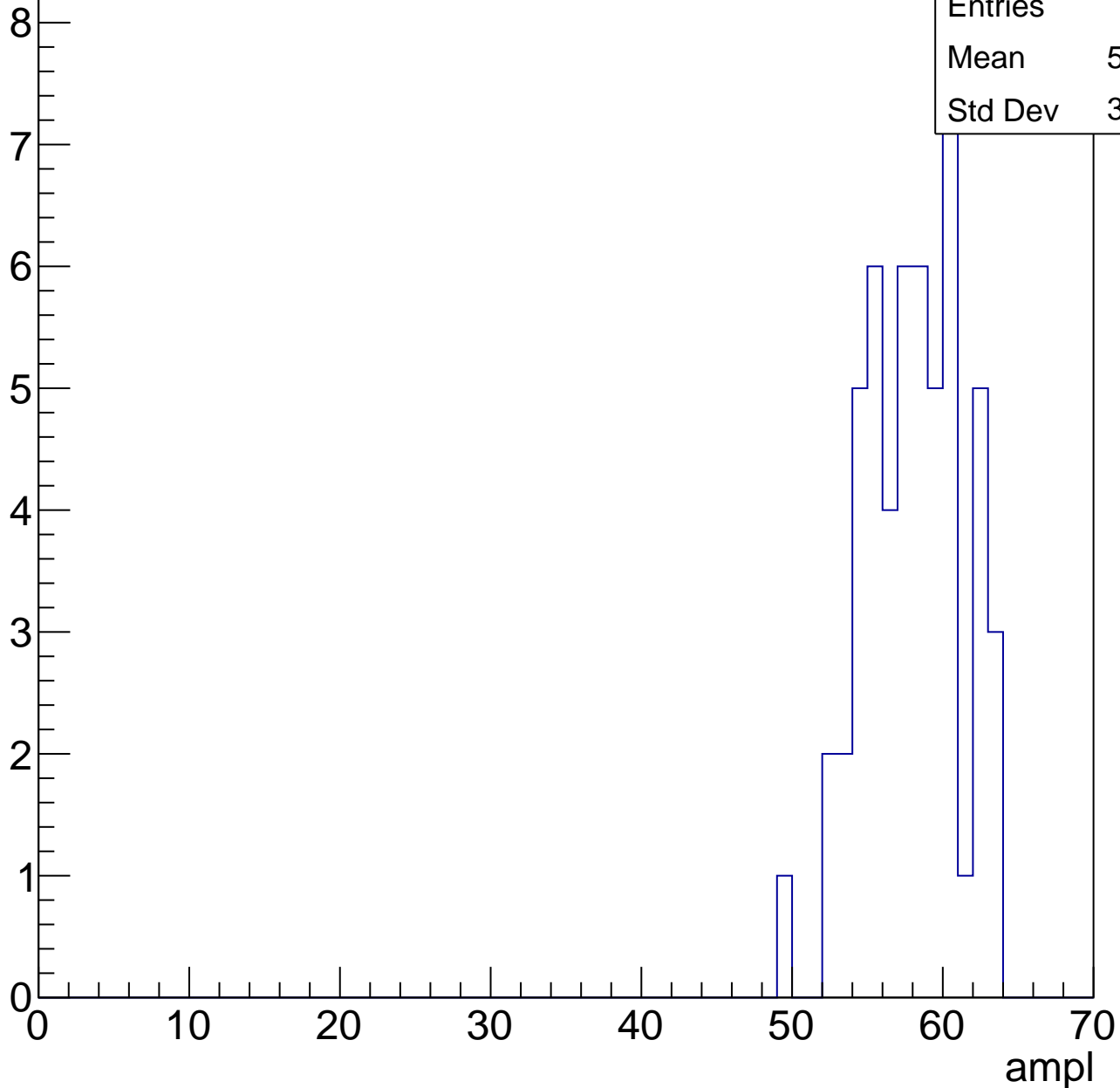
Entries	52
Mean	52.5
Std Dev	3.29

# B1L103S, U22-ch80, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	57.56
Std Dev	3.189

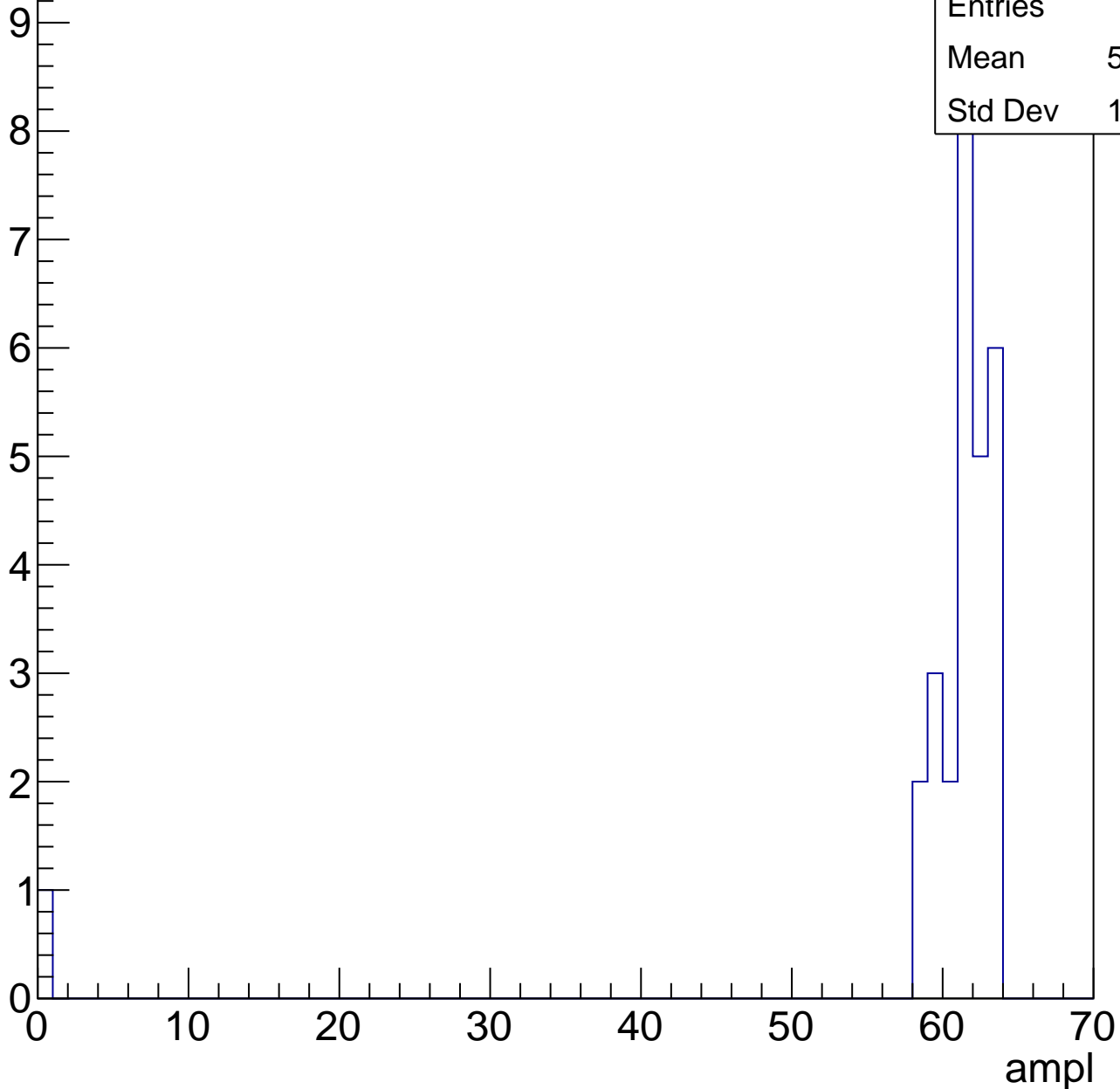


# B1L103S, U22-ch80, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	28
Mean	58.93
Std Dev	11.44





# B1L103S, U22-ch80, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



# B1L103S, U22-ch81, adc0

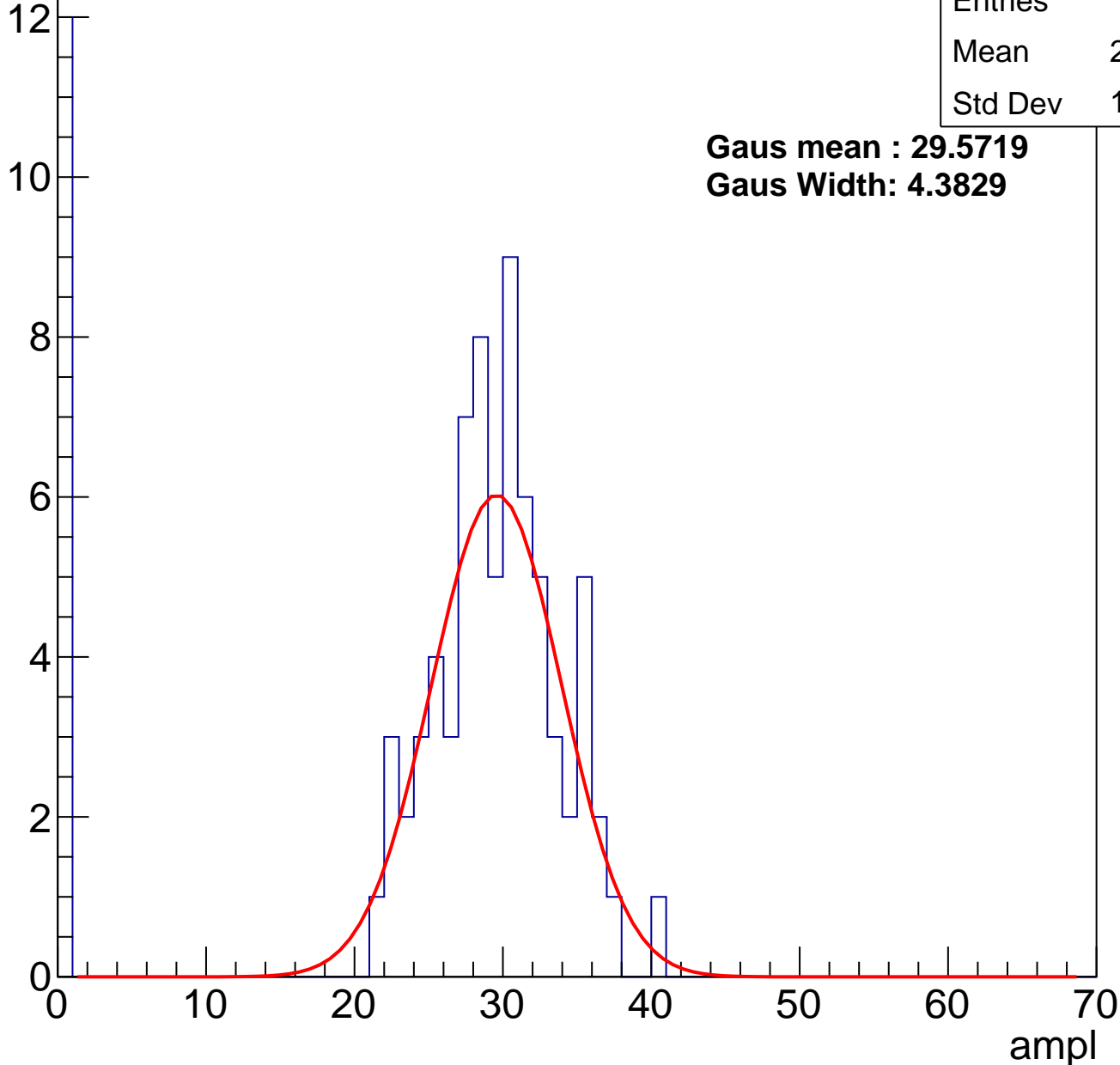
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	24.98
Std Dev	10.98

**Gaus mean : 29.5719**

**Gaus Width: 4.3829**

Entry



# B1L103S, U22-ch81, adc1

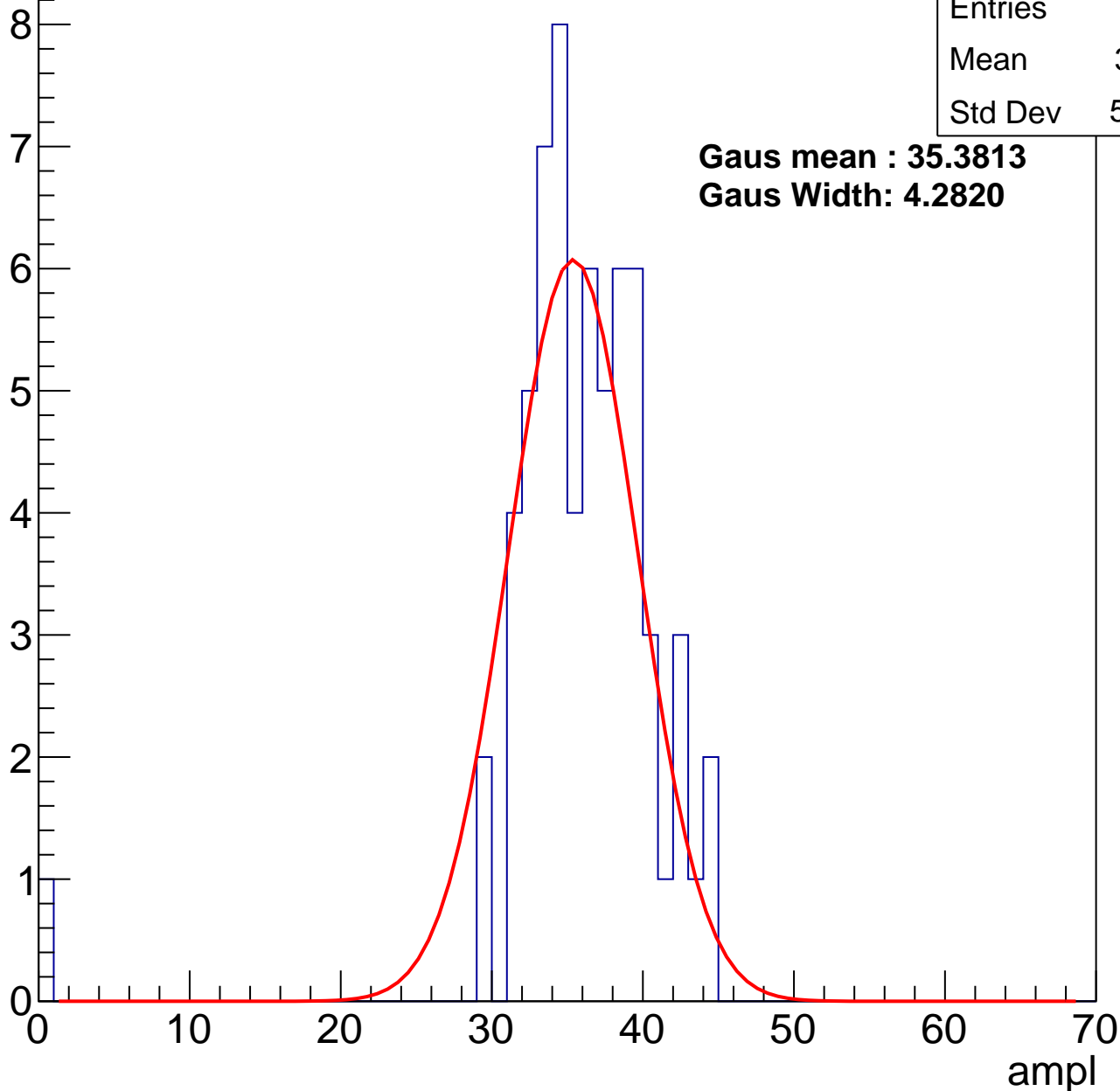
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.41
Std Dev	5.717

**Gaus mean : 35.3813**

**Gaus Width: 4.2820**



# B1L103S, U22-ch81, adc2

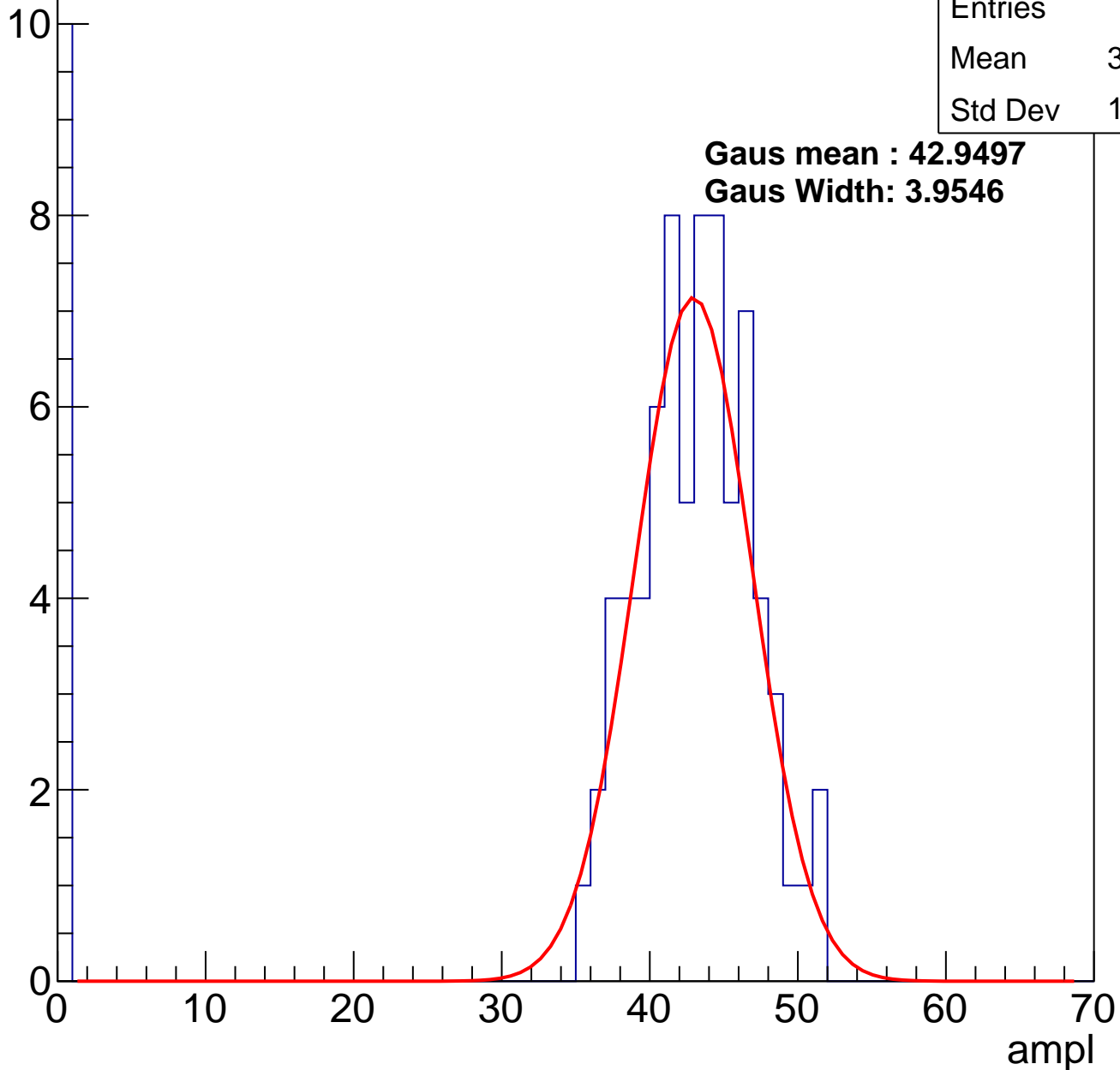
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	83
Mean	37.55
Std Dev	14.33

**Gaus mean : 42.9497**

**Gaus Width: 3.9546**

Entry

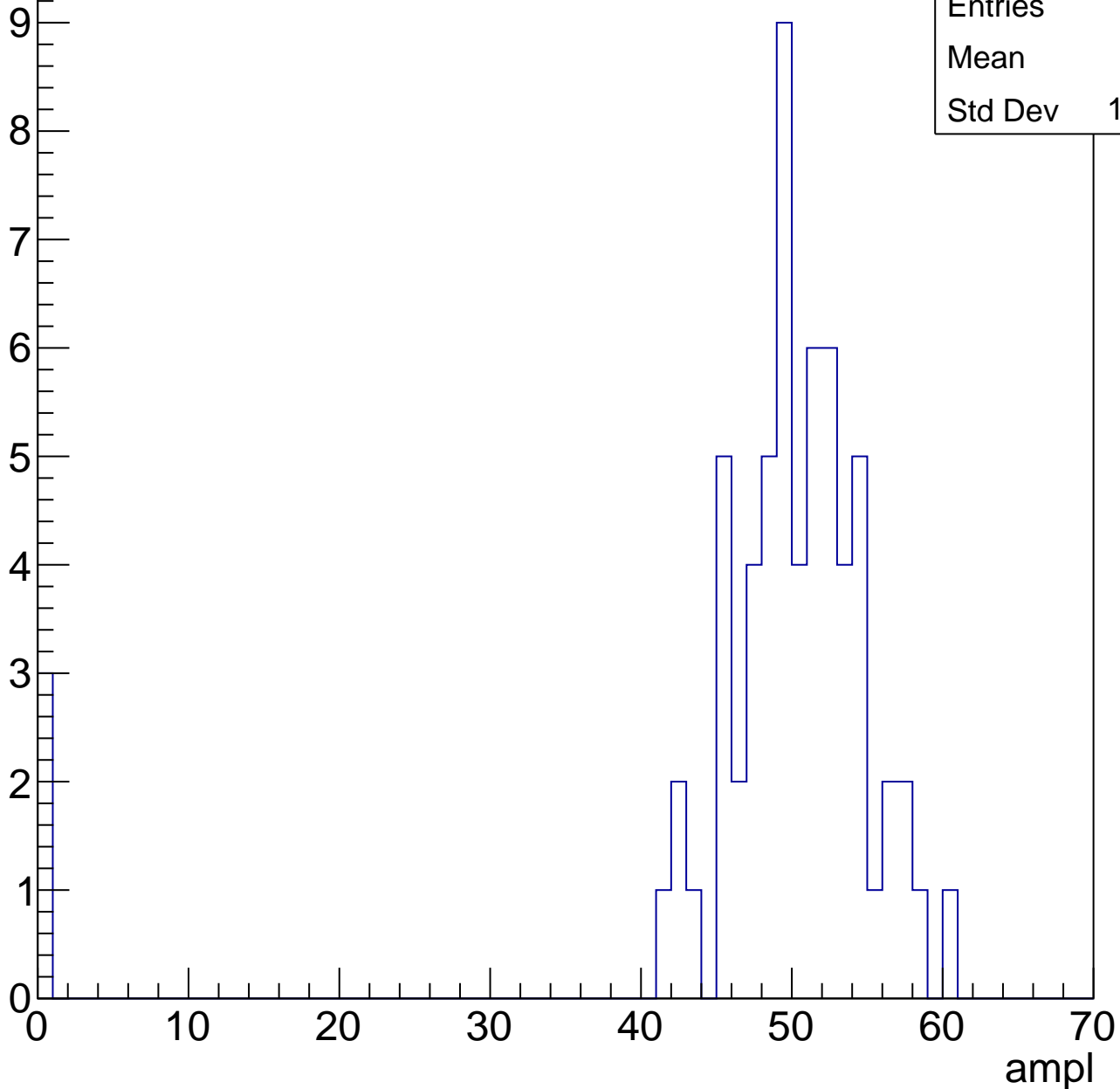


# B1L103S, U22-ch81, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	47.7
Std Dev	11.28

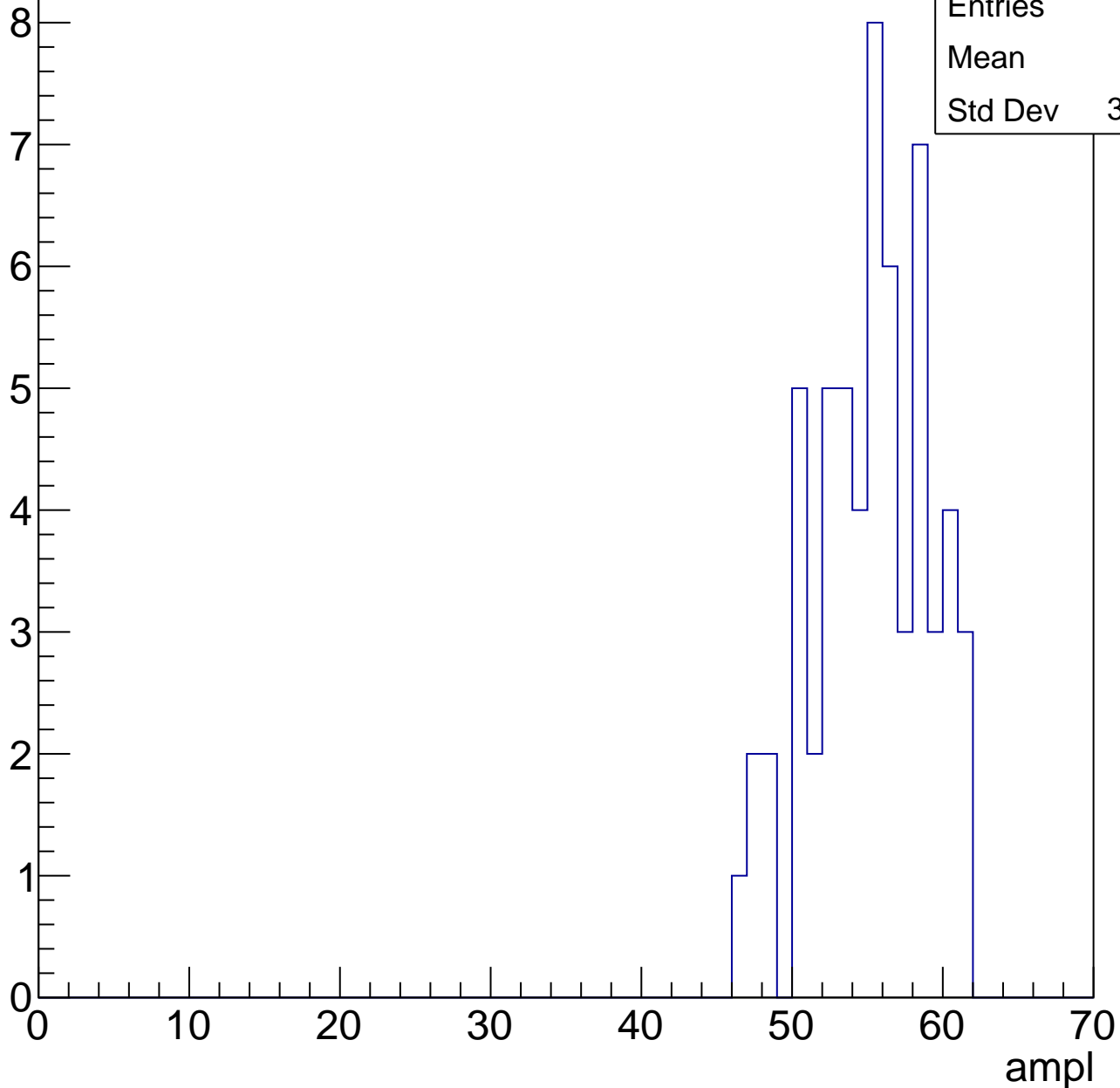


# B1L103S, U22-ch81, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	54.7
Std Dev	3.796

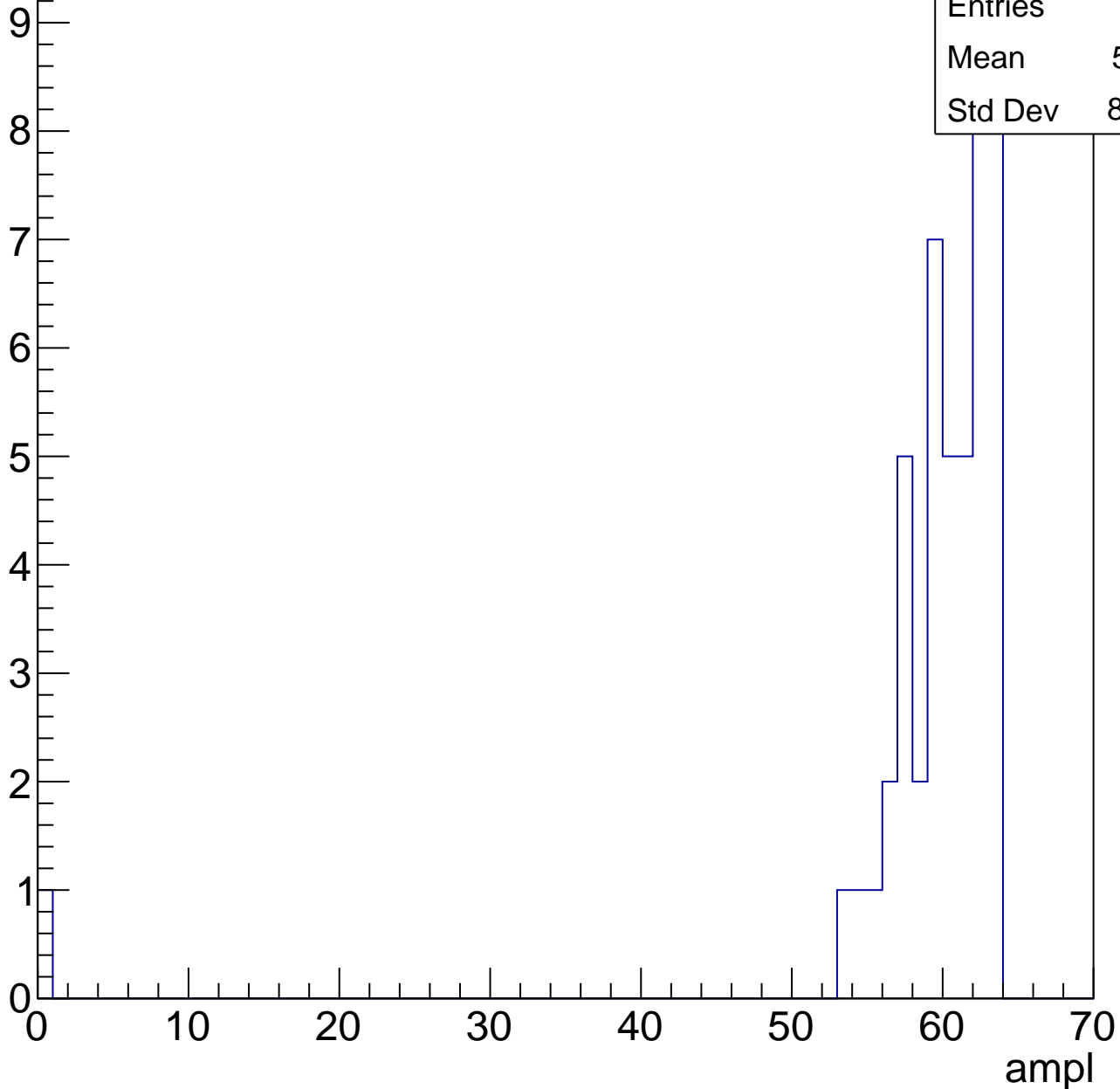


# B1L103S, U22-ch81, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

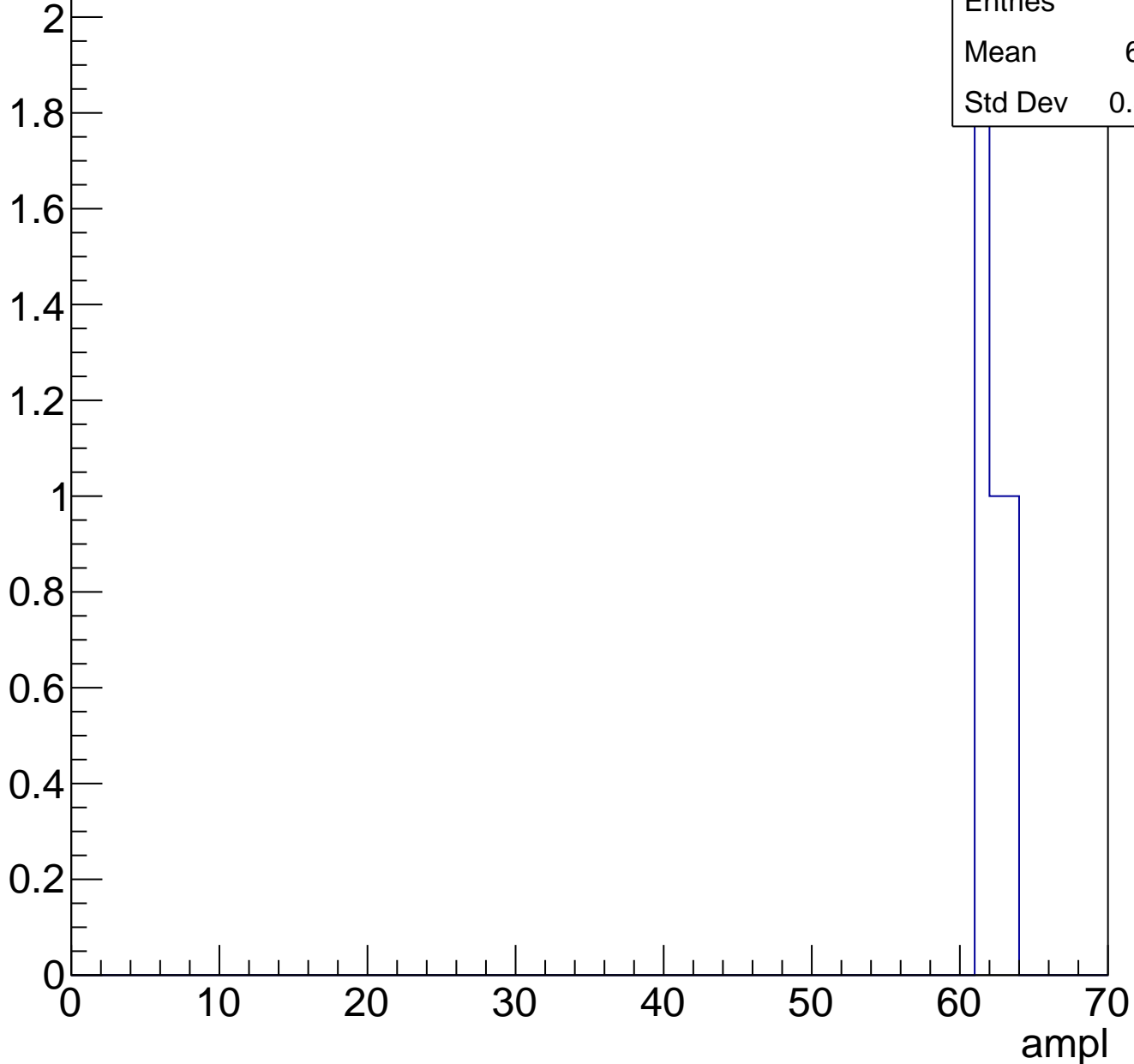
Entries	48
Mean	58.71
Std Dev	8.949



# B1L103S, U22-ch81, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch81, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

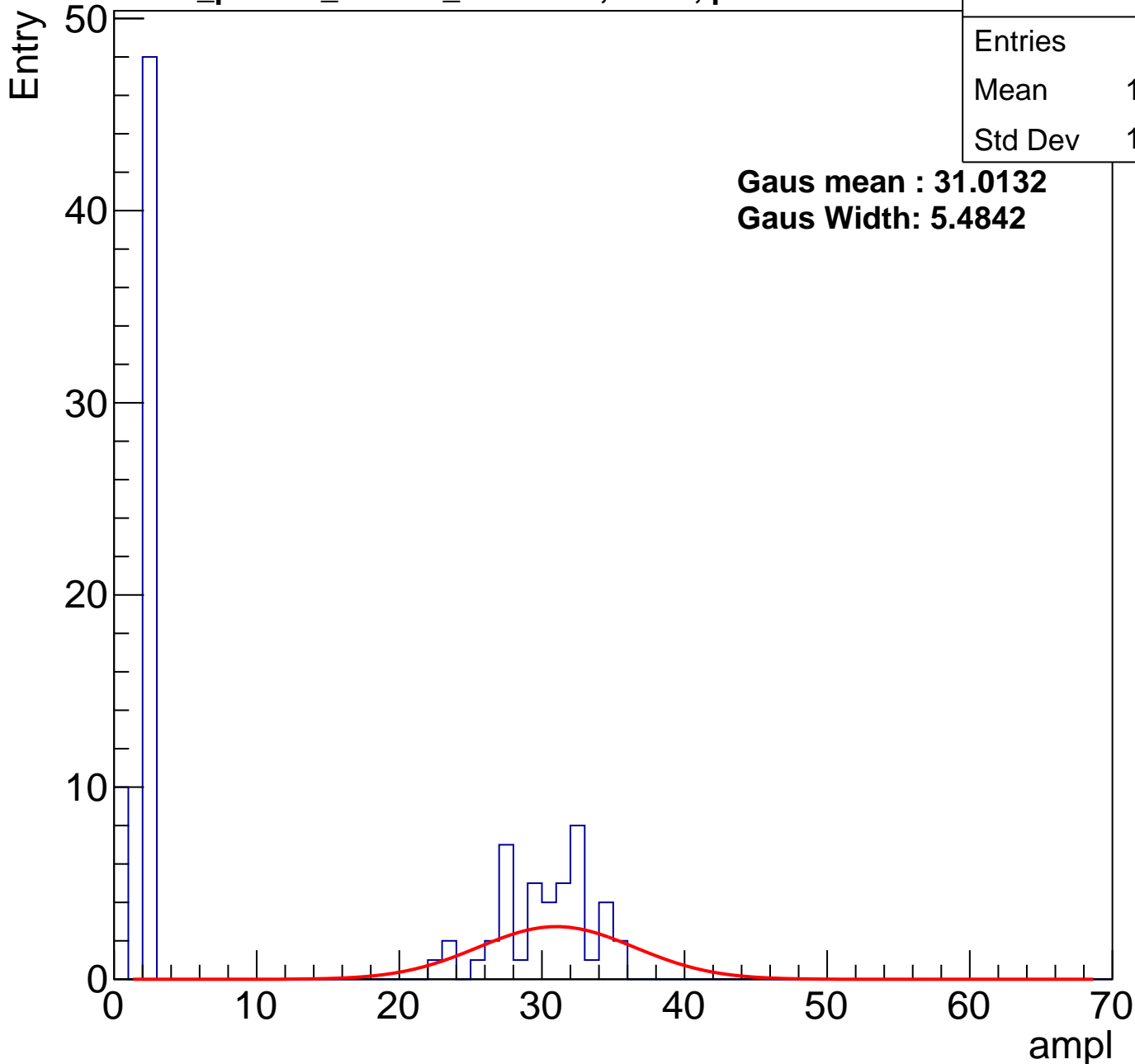
# B1L103S, U22-ch82, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	101
Mean	13.59
Std Dev	14.04

**Gaus mean : 31.0132**

**Gaus Width: 5.4842**



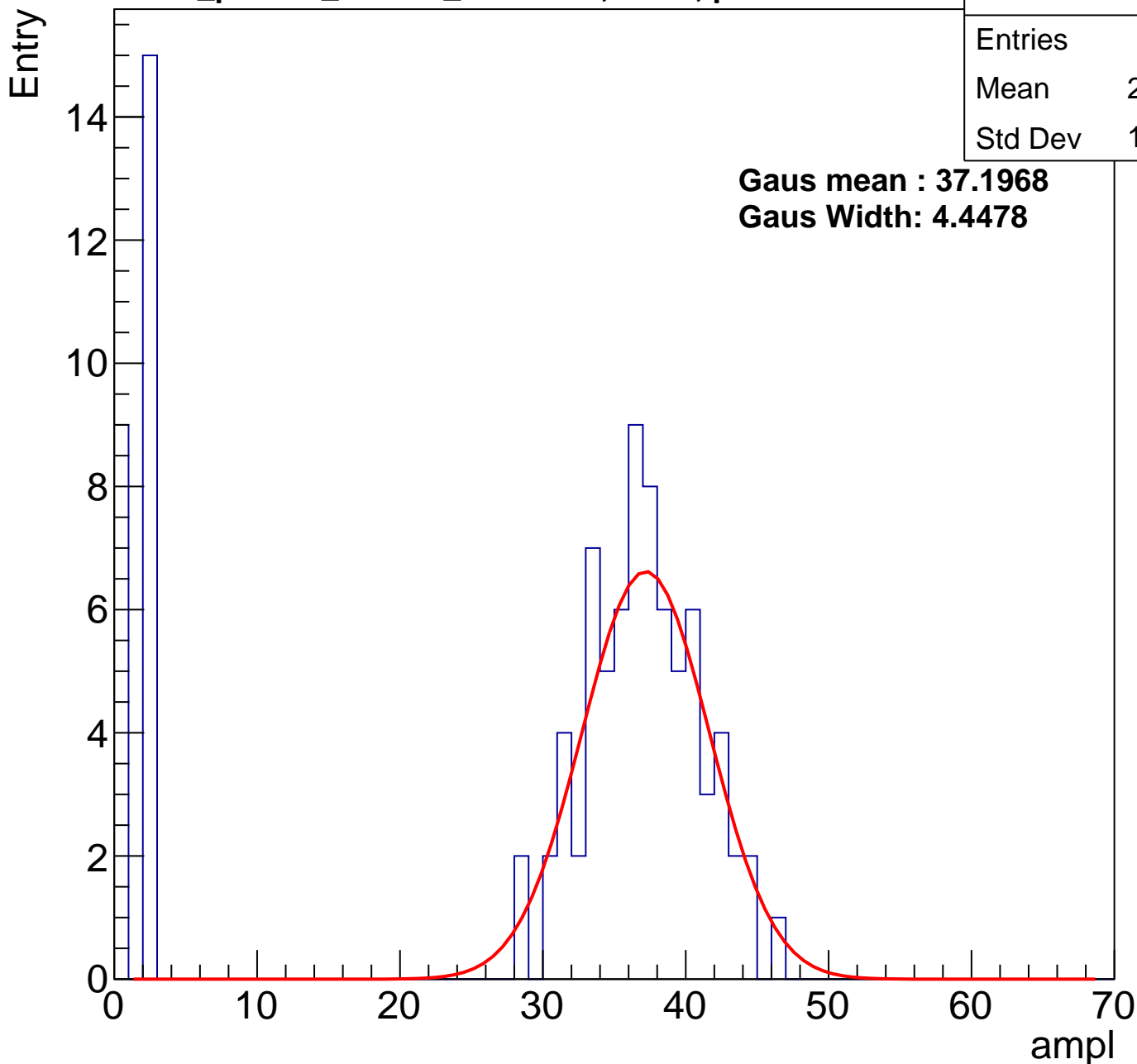
# B1L103S, U22-ch82, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	98
Mean	27.95
Std Dev	15.58

**Gaus mean : 37.1968**

**Gaus Width: 4.4478**



# B1L103S, U22-ch82, adc2

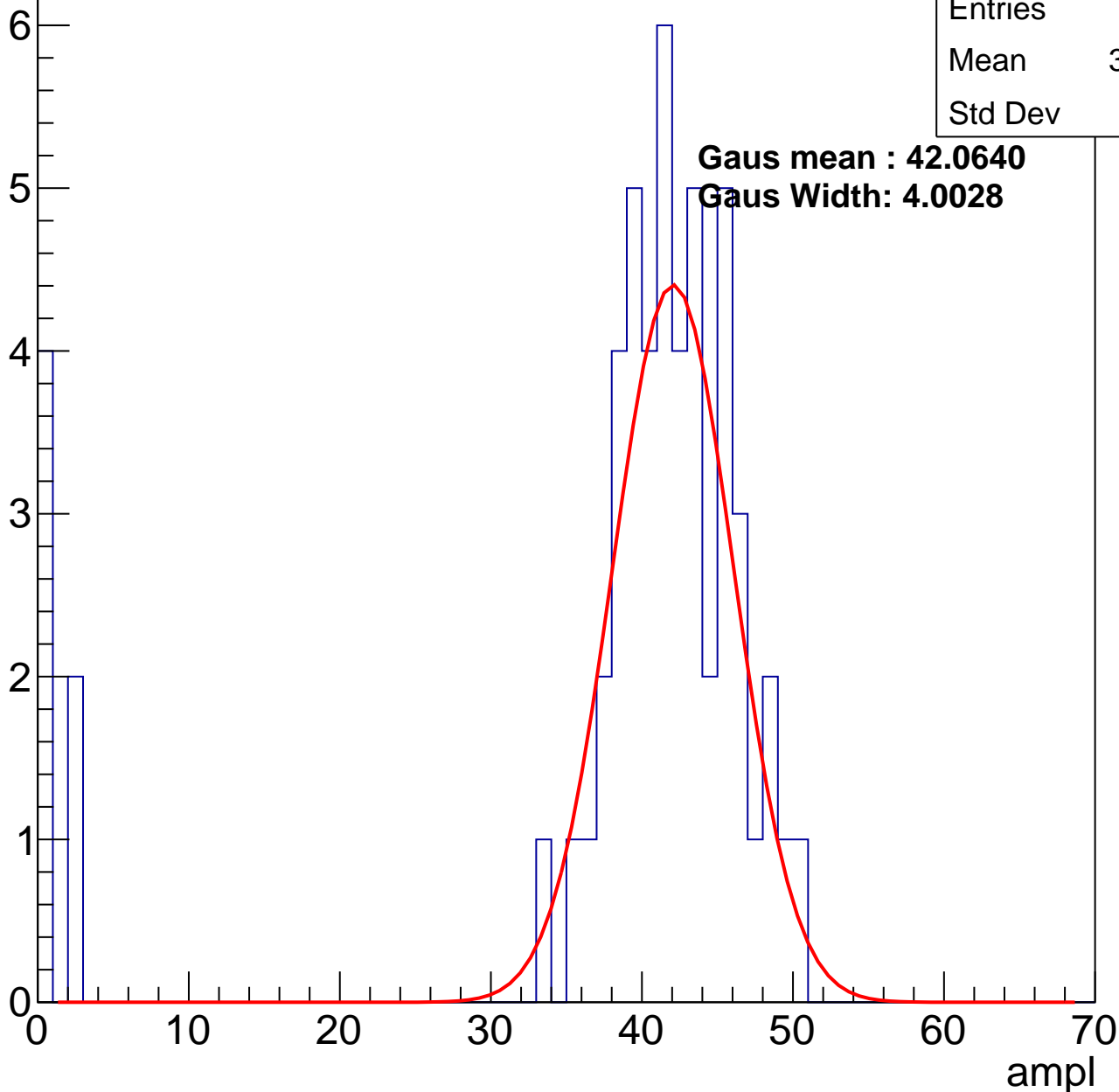
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	37.24
Std Dev	13.4

**Gaus mean : 42.0640**

**Gaus Width: 4.0028**

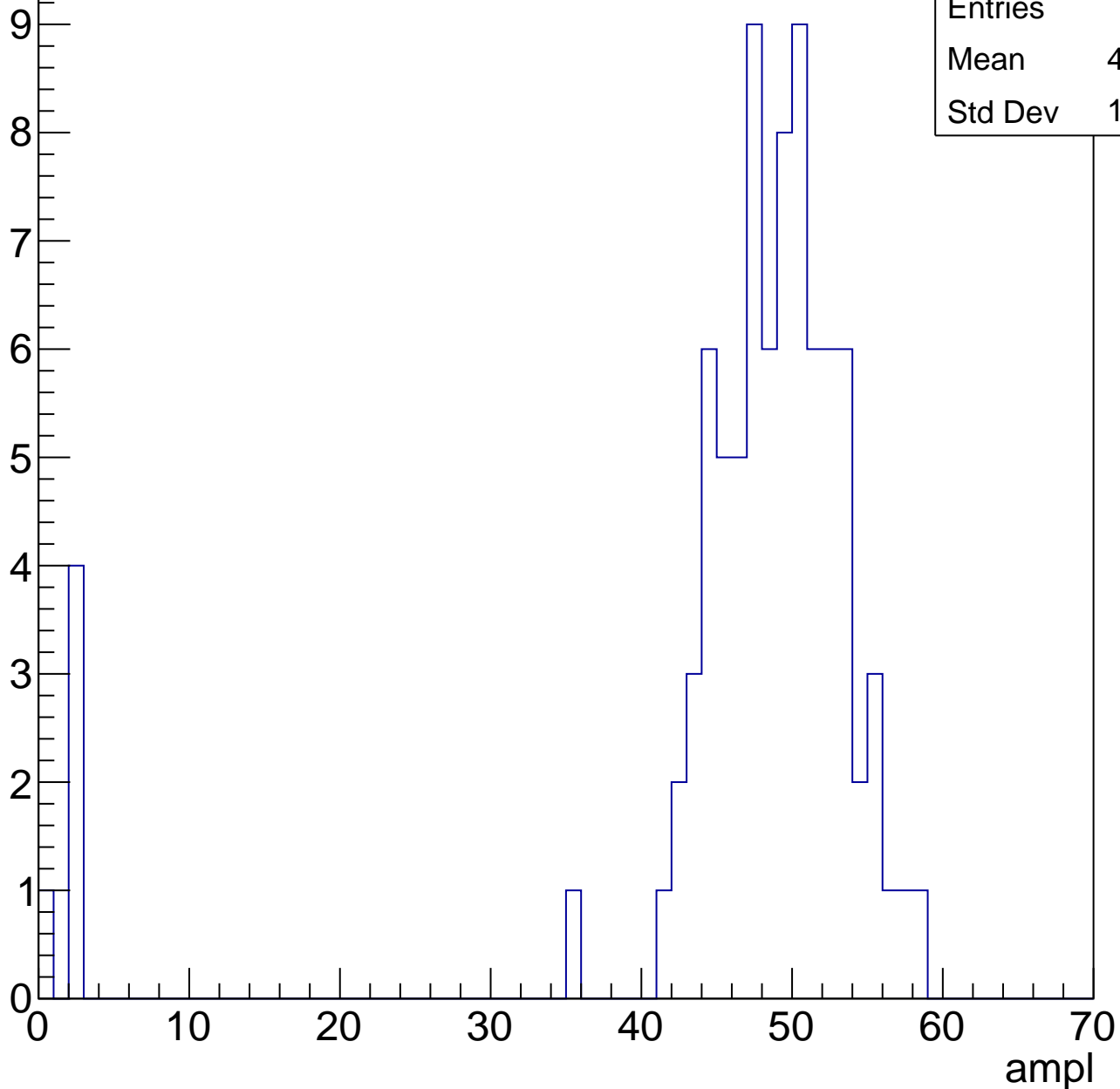


# B1L103S, U22-ch82, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	86
Mean	45.92
Std Dev	11.68

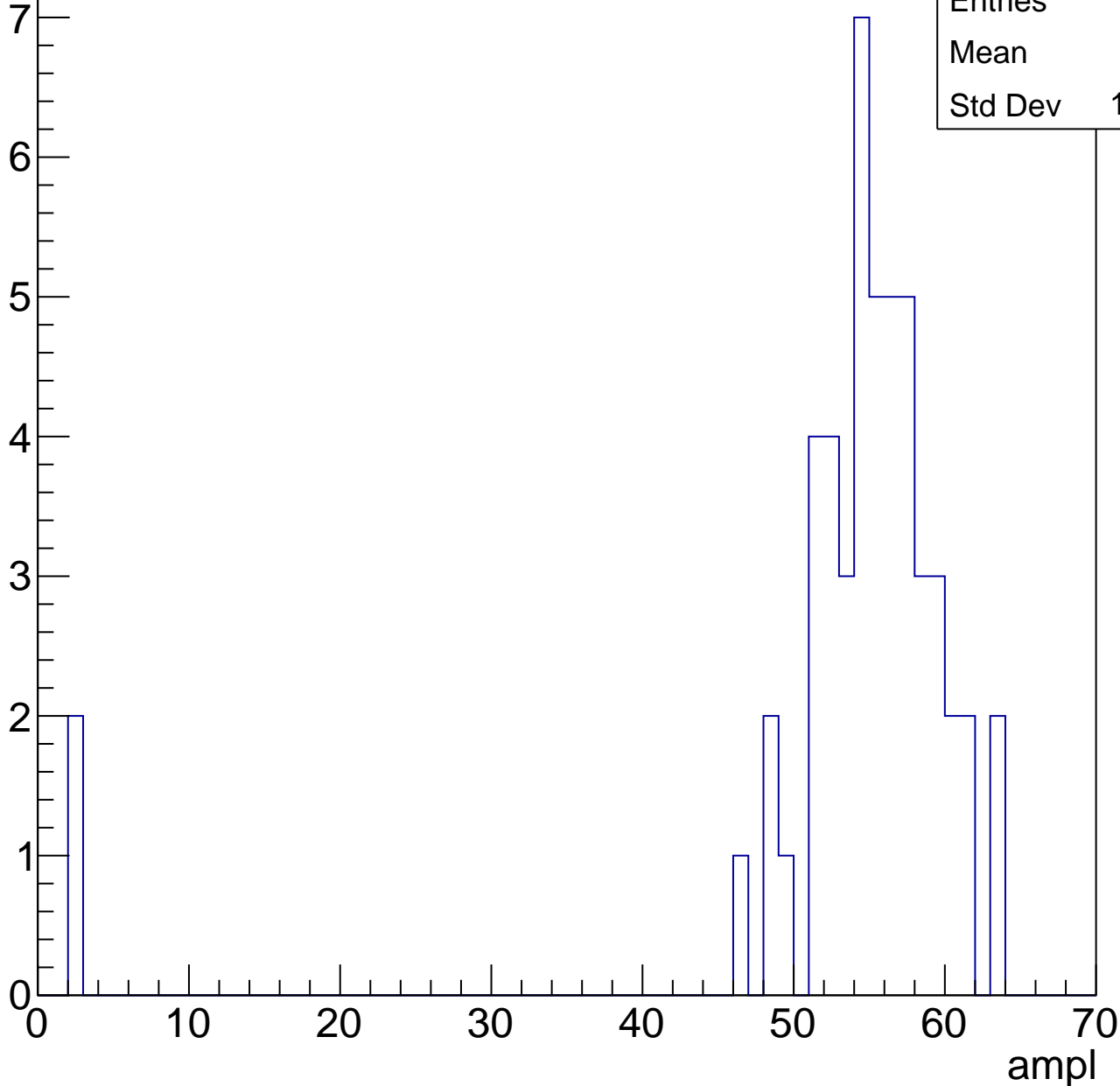


# B1L103S, U22-ch82, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

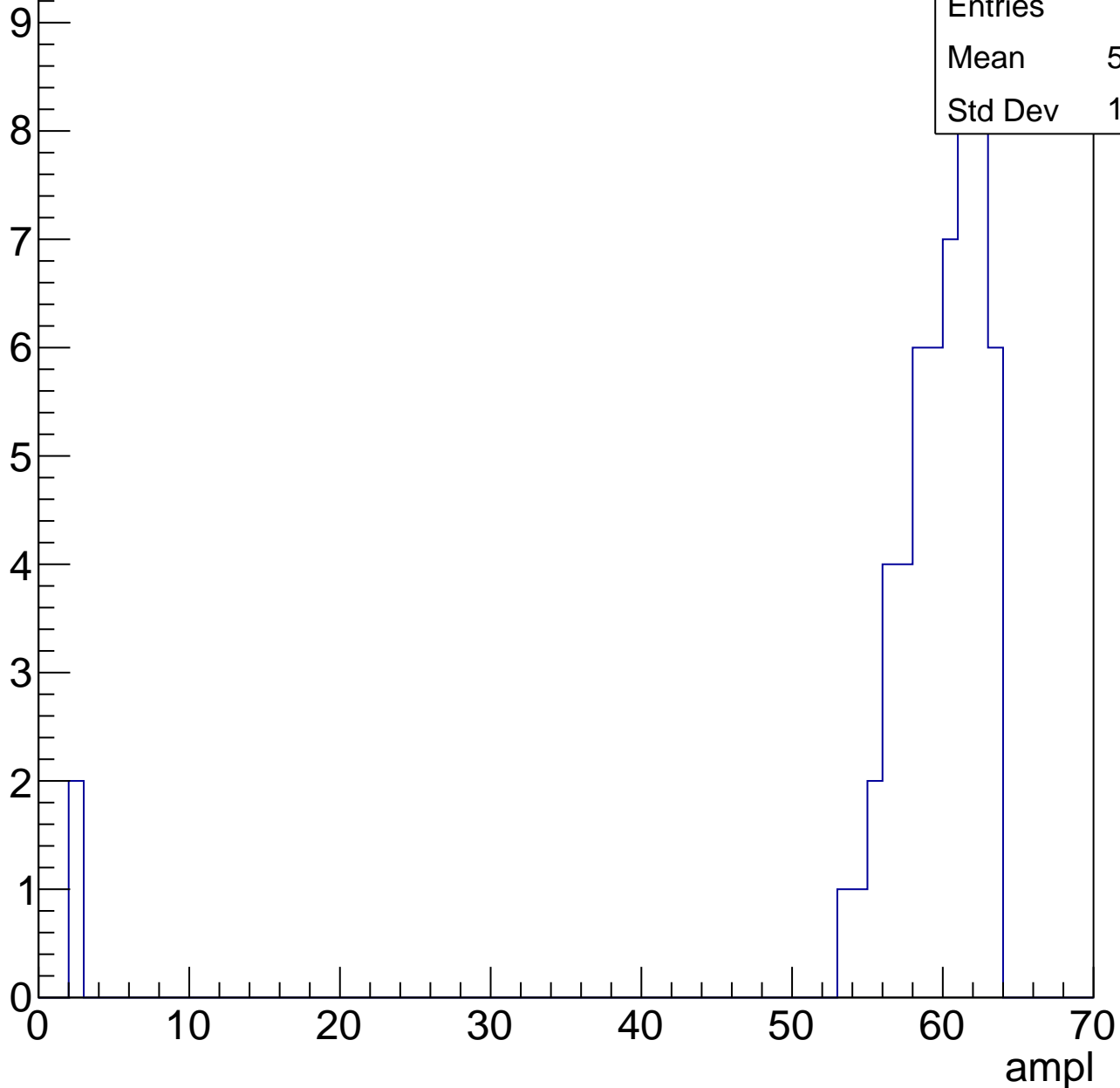
Entries	51
Mean	53
Std Dev	10.93



# B1L103S, U22-ch82, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch82, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

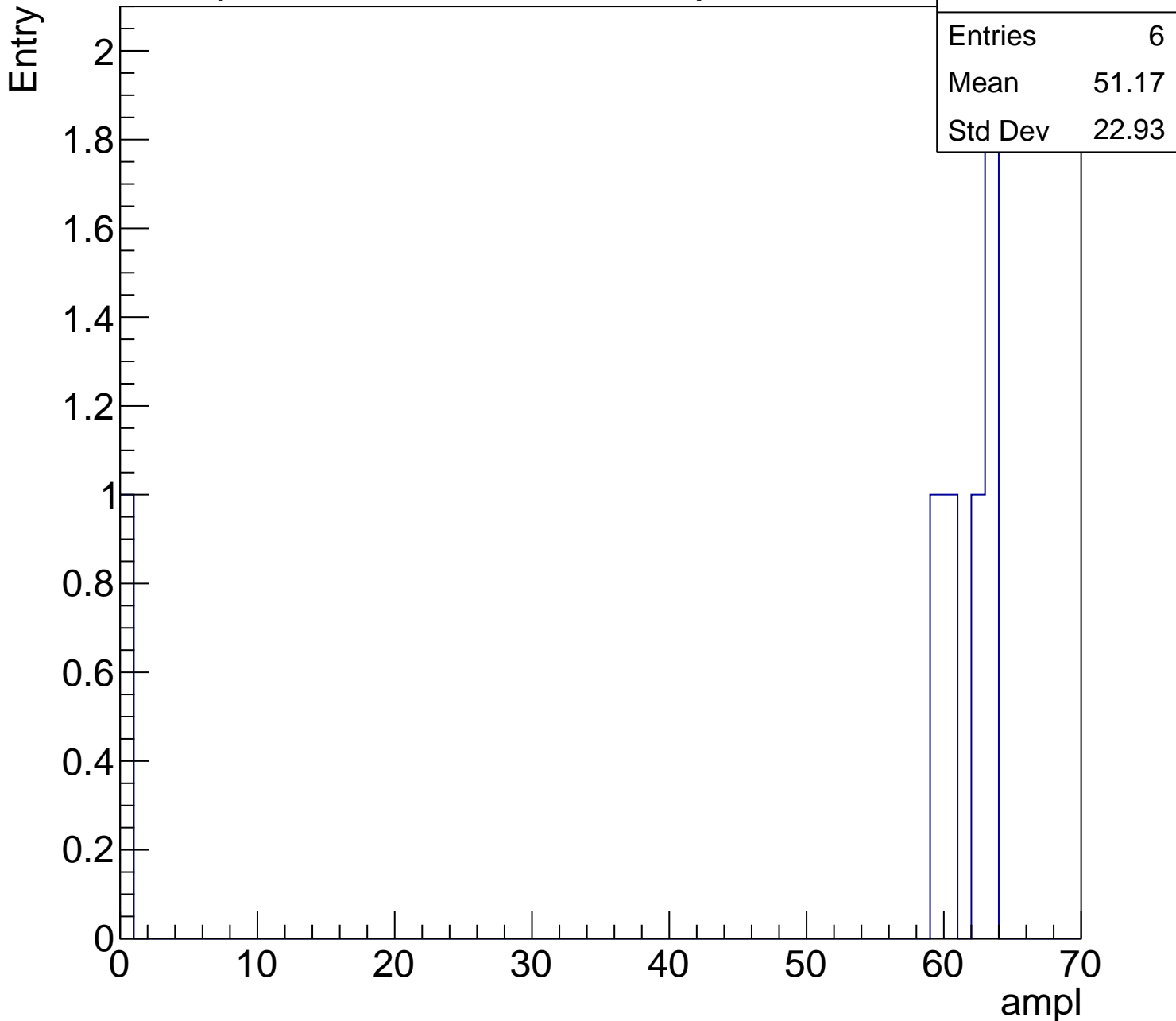
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.17
Std Dev	22.93

0 10 20 30 40 50 60 70

ampl



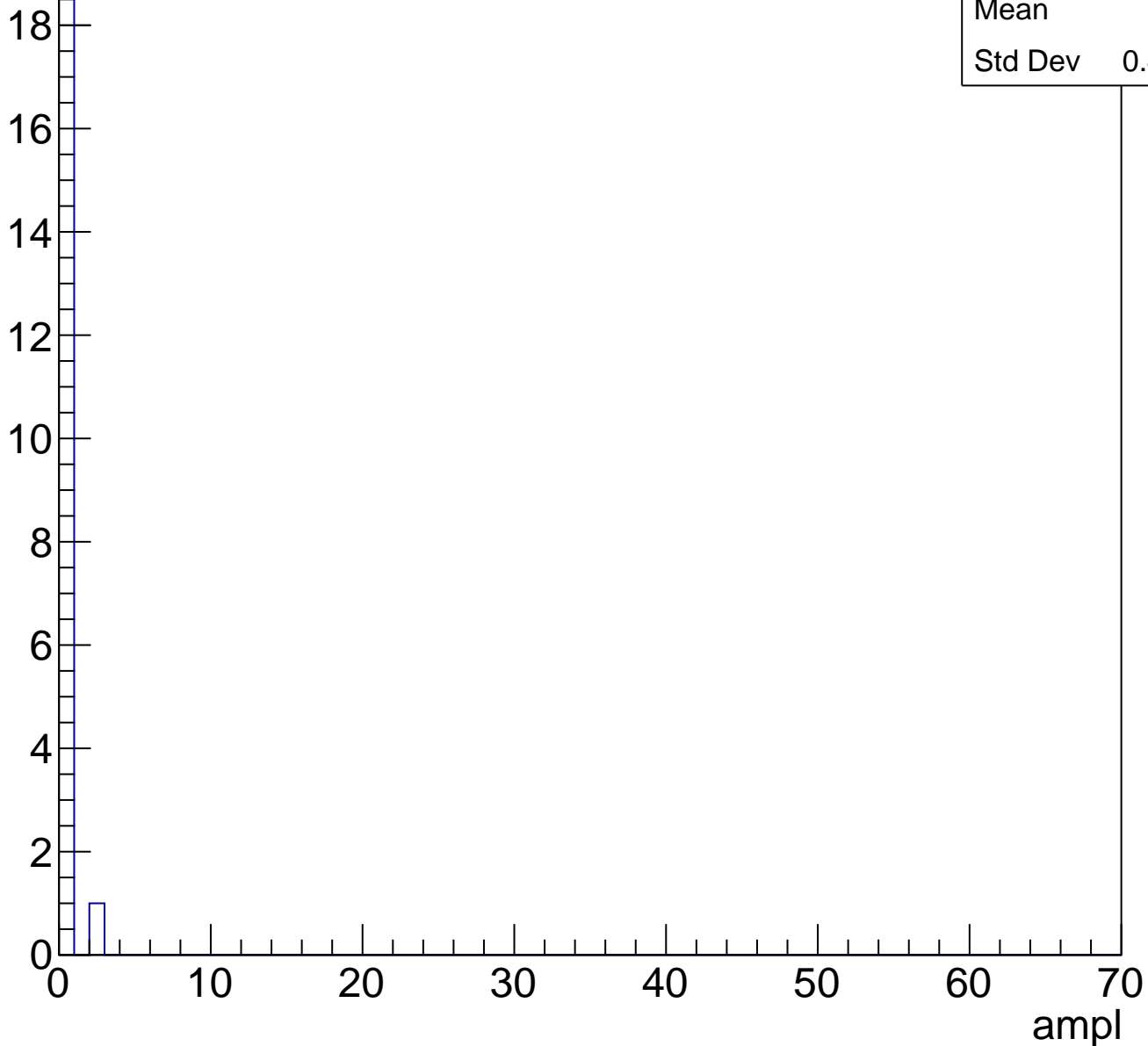


# B1L103S, U22-ch82, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	0.1
Std Dev	0.4359

Entry



# B1L103S, U22-ch83, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	98
Mean	24.56
Std Dev	10.61

**Gaus mean : 28.7335**

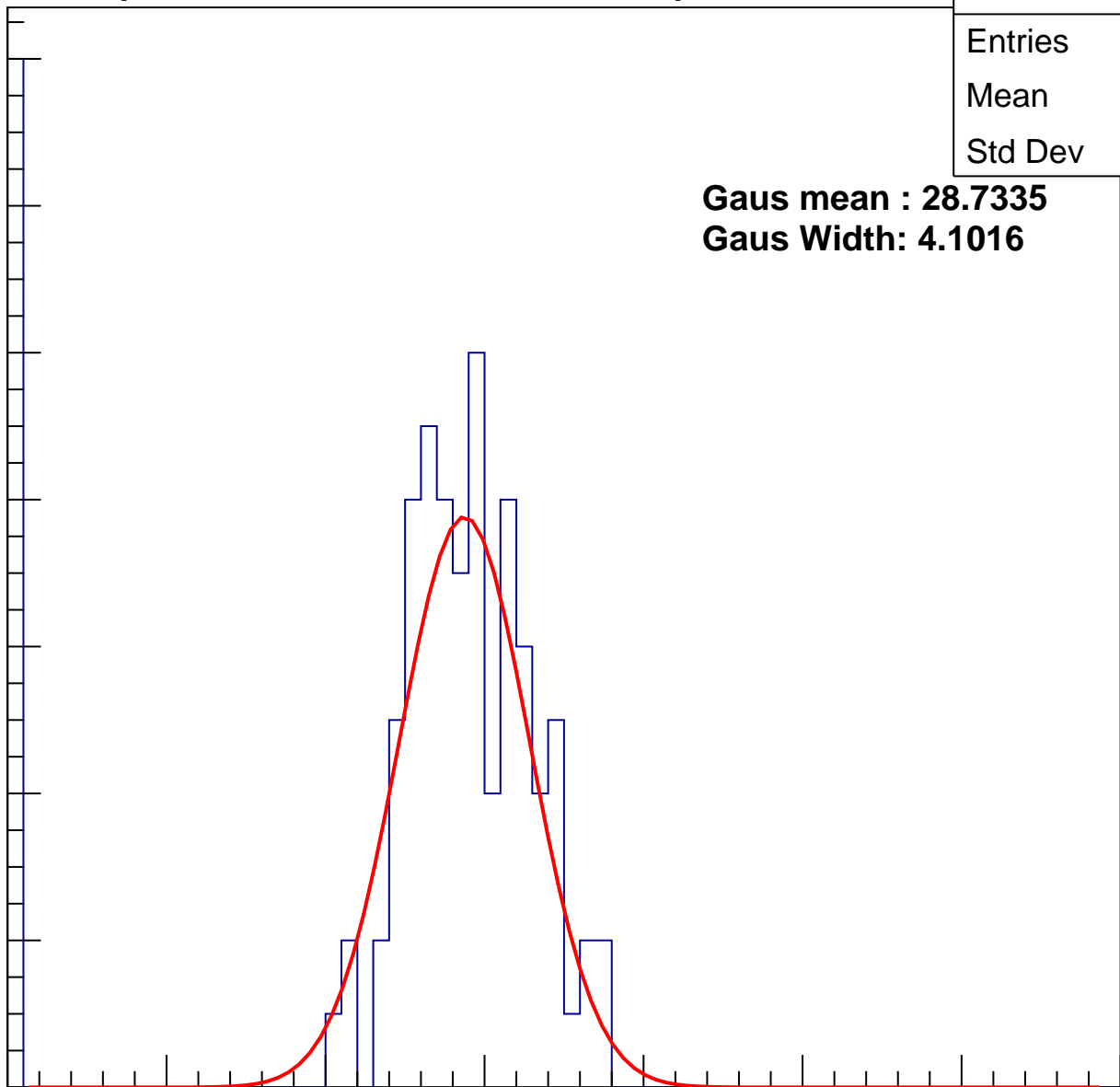
**Gaus Width: 4.1016**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch83, adc1

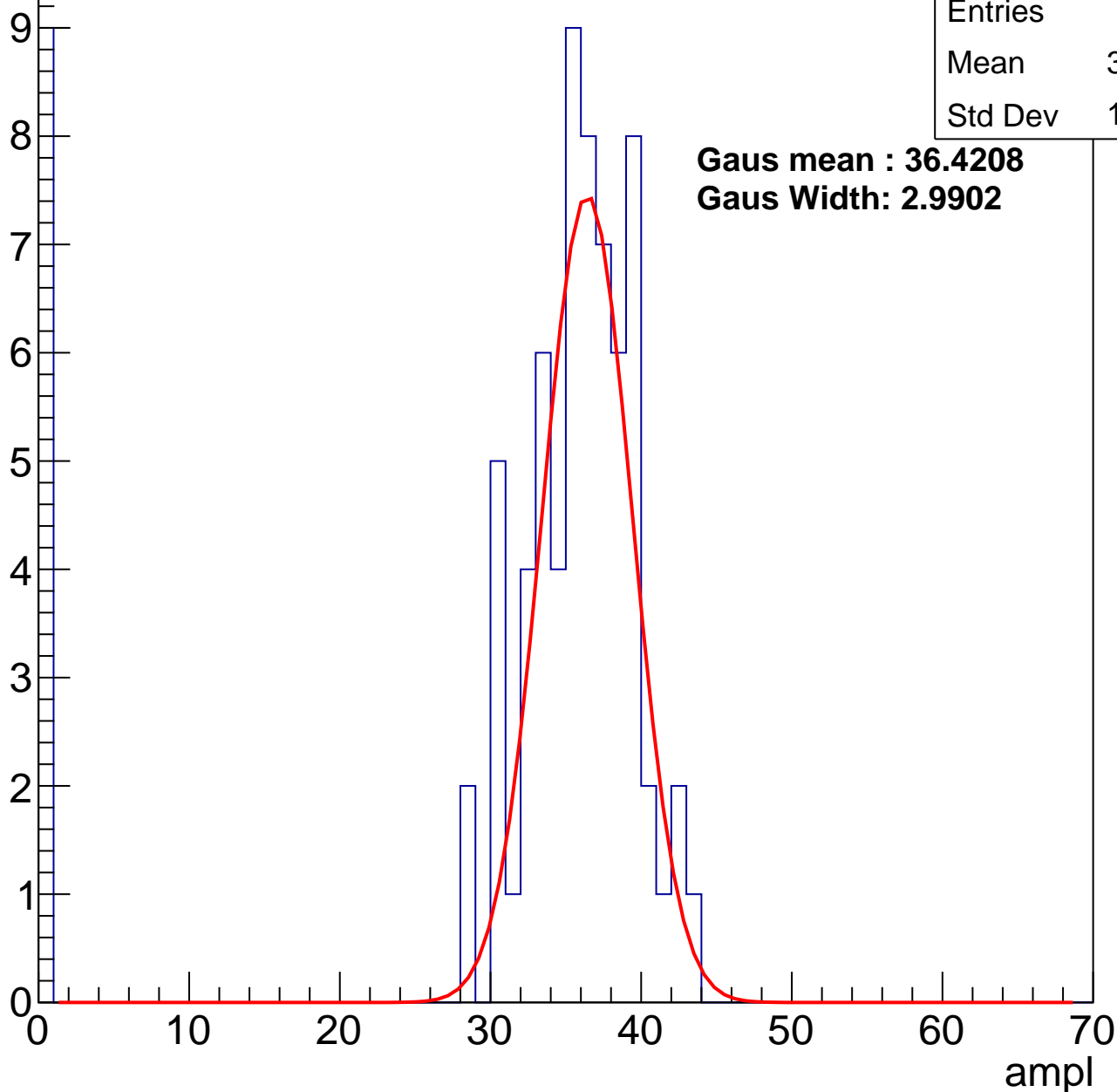
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31.32
Std Dev	11.99

**Gaus mean : 36.4208**

**Gaus Width: 2.9902**



# B1L103S, U22-ch83, adc2

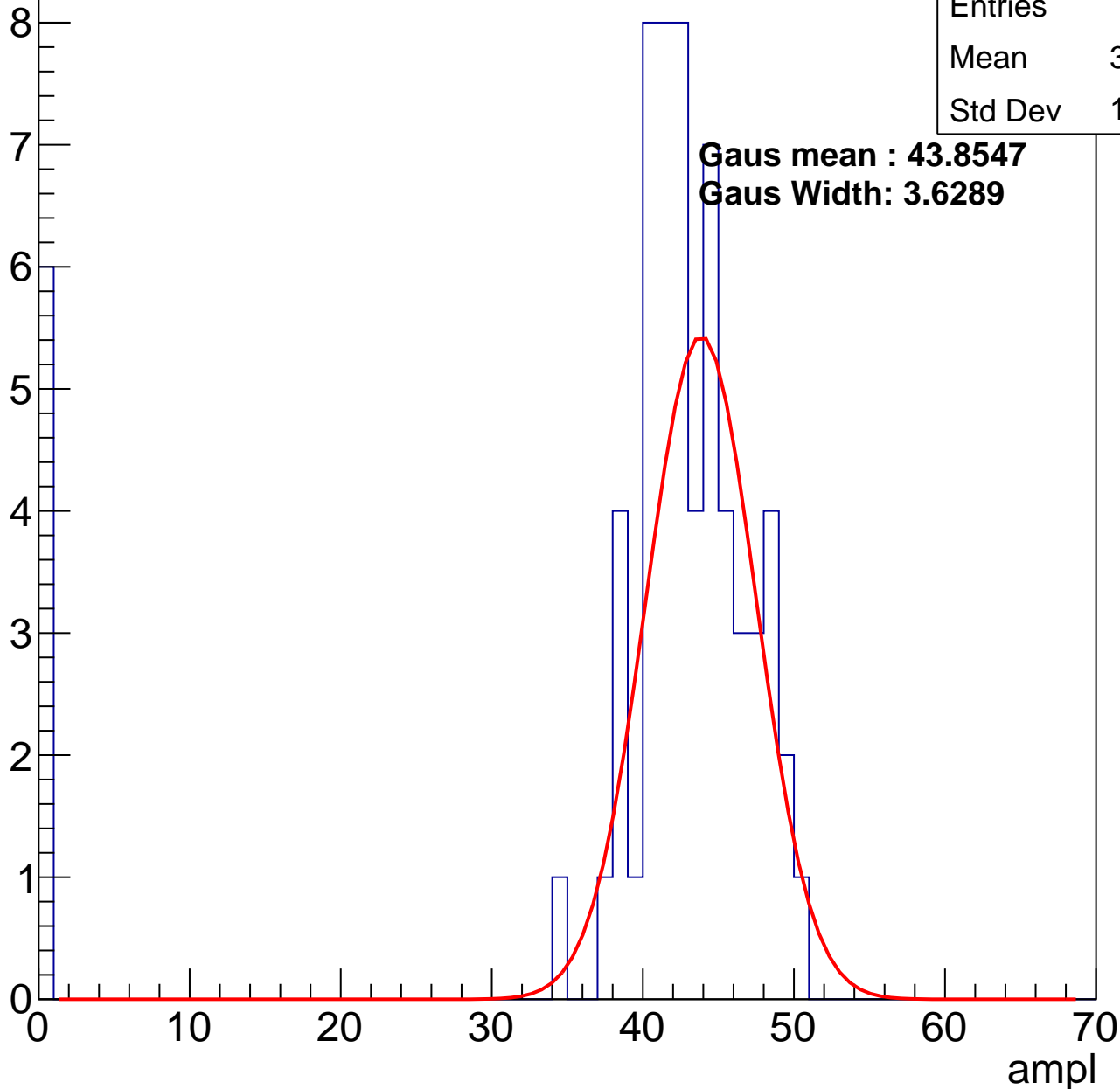
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.85
Std Dev	12.79

**Gaus mean : 43.8547**

**Gaus Width: 3.6289**

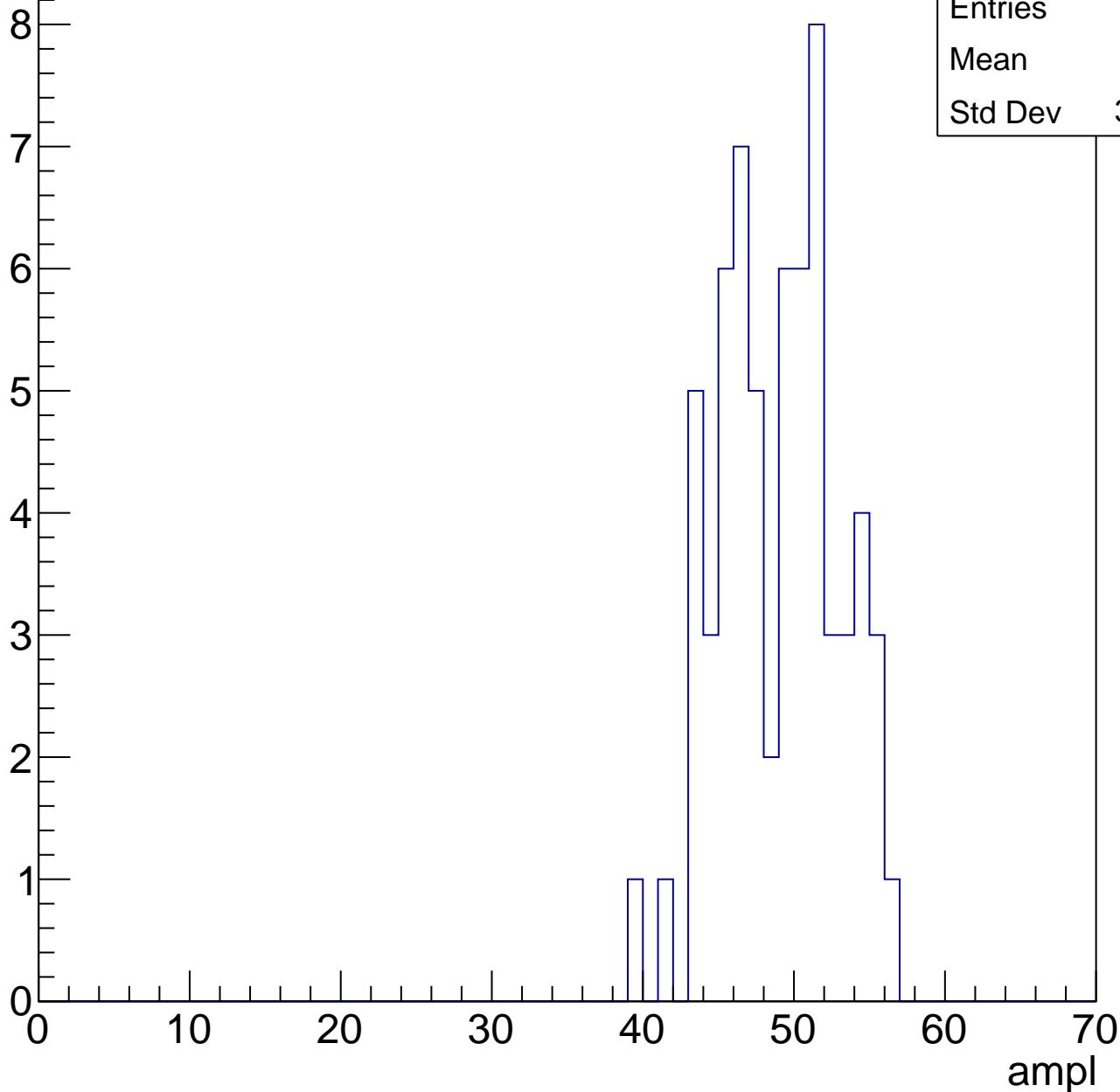


# B1L103S, U22-ch83, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	48.5
Std Dev	3.861

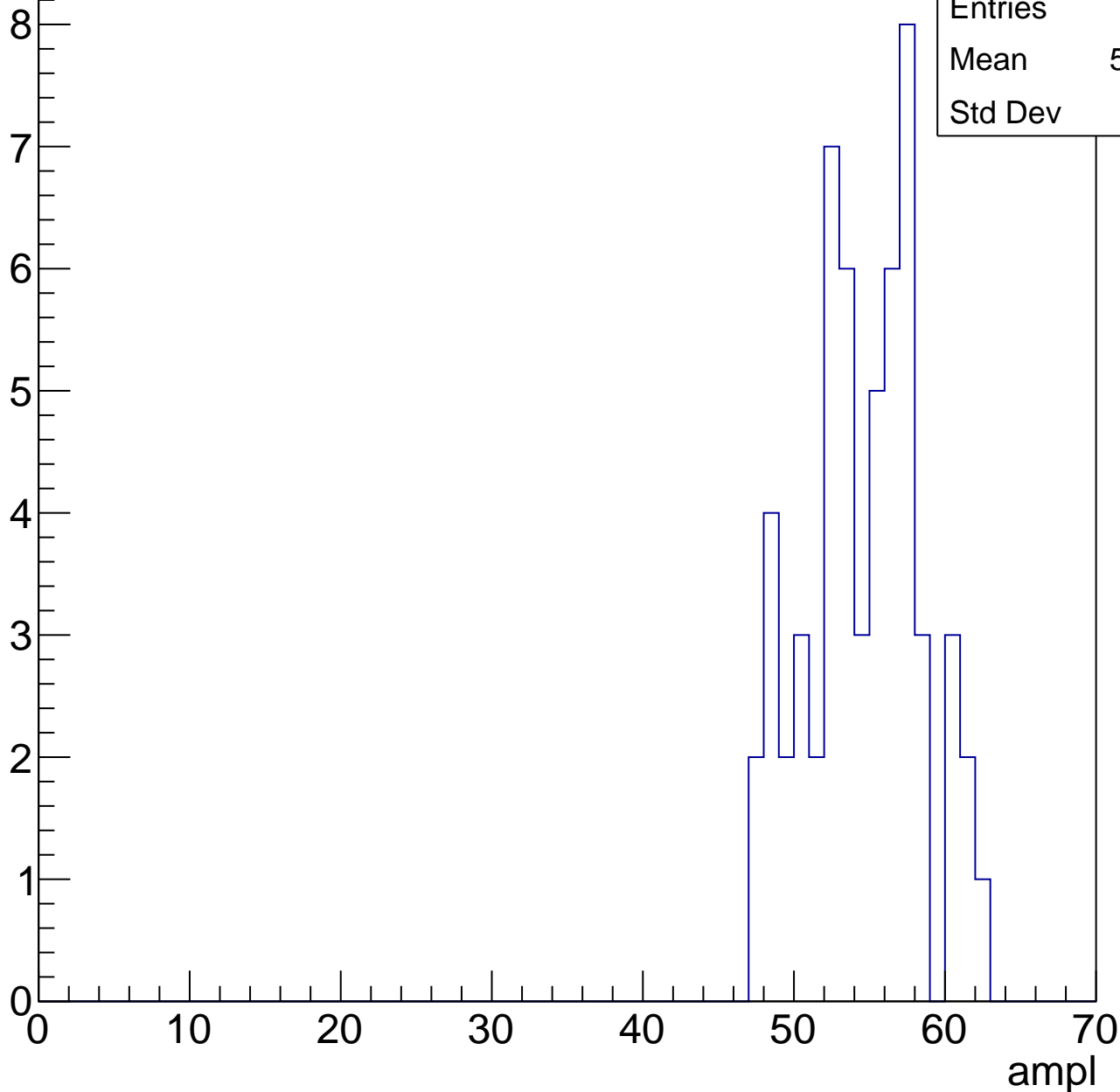


# B1L103S, U22-ch83, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	54.12
Std Dev	3.77

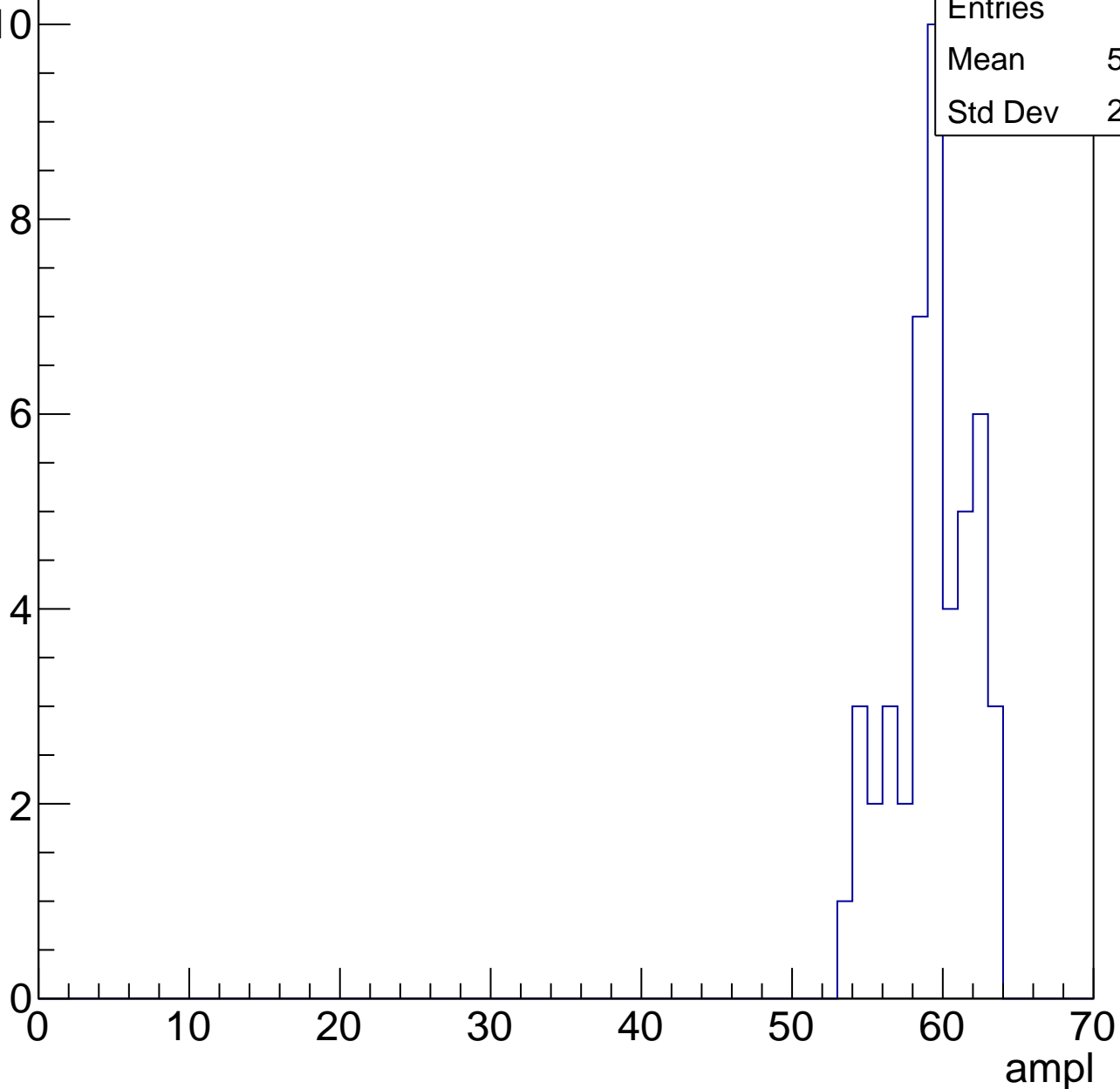


# B1L103S, U22-ch83, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.89
Std Dev	2.598

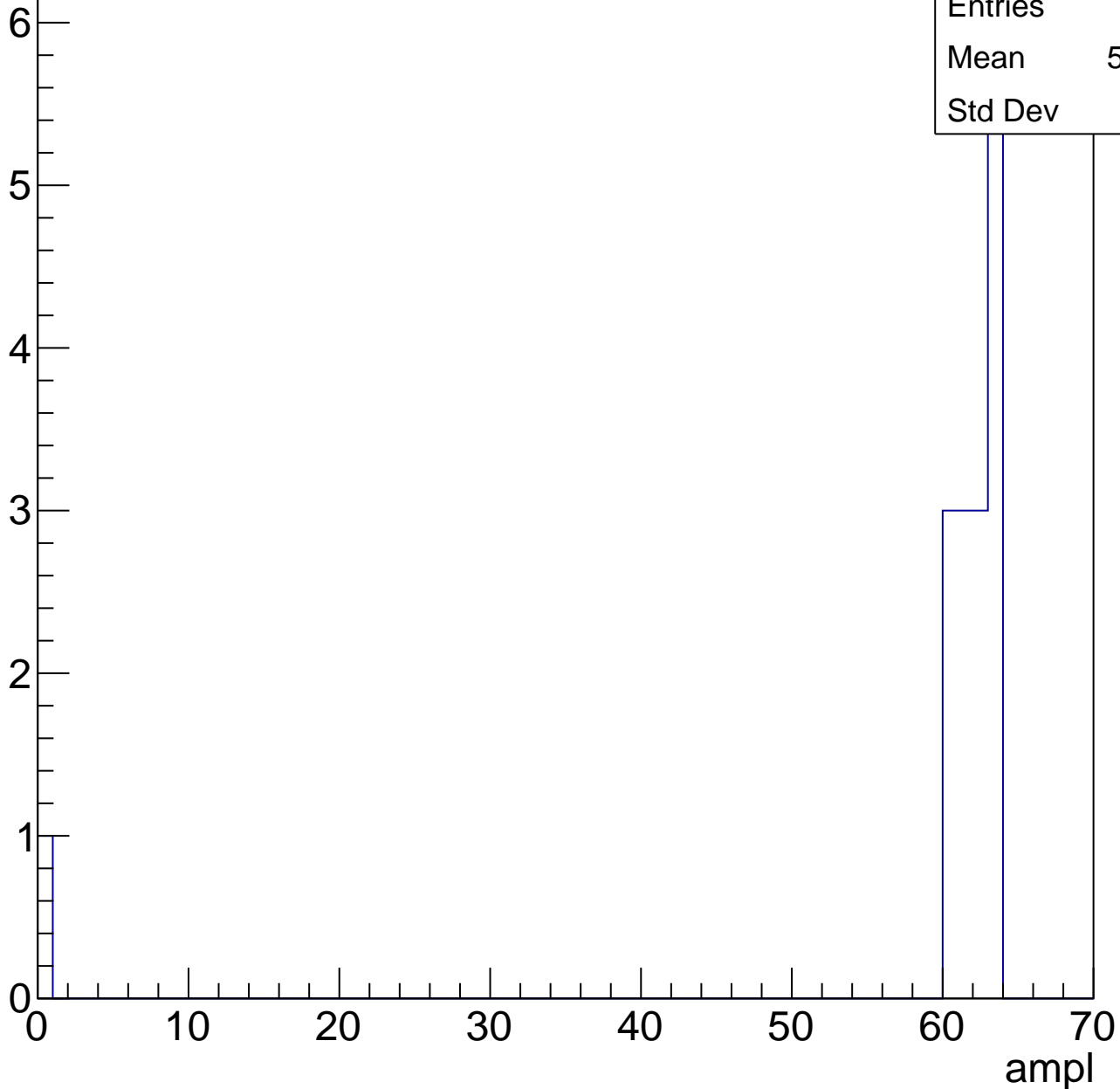


# B1L103S, U22-ch83, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	16
Mean	57.94
Std Dev	15





# B1L103S, U22-ch83, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	3.316
Std Dev	14.07

Entry



# B1L103S, U22-ch84, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	16.29
Std Dev	13.04

**Gaus mean : 27.9782**

**Gaus Width: 3.1631**

Entry

30

25

20

15

10

5

0

0

10

20

30

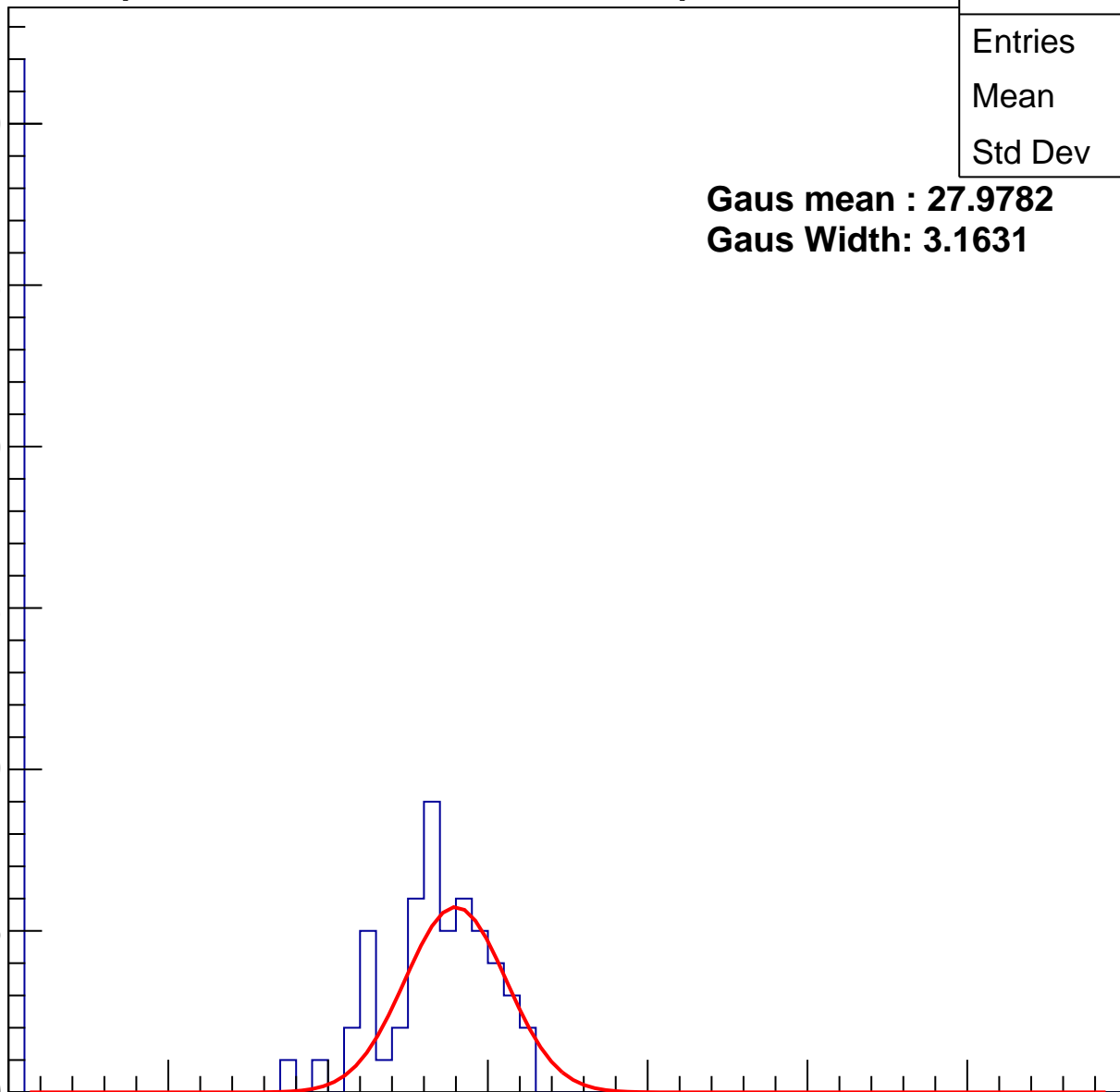
40

50

60

70

ampl



# B1L103S, U22-ch84, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	29.84
Std Dev	10.72

**Gaus mean : 33.8899**

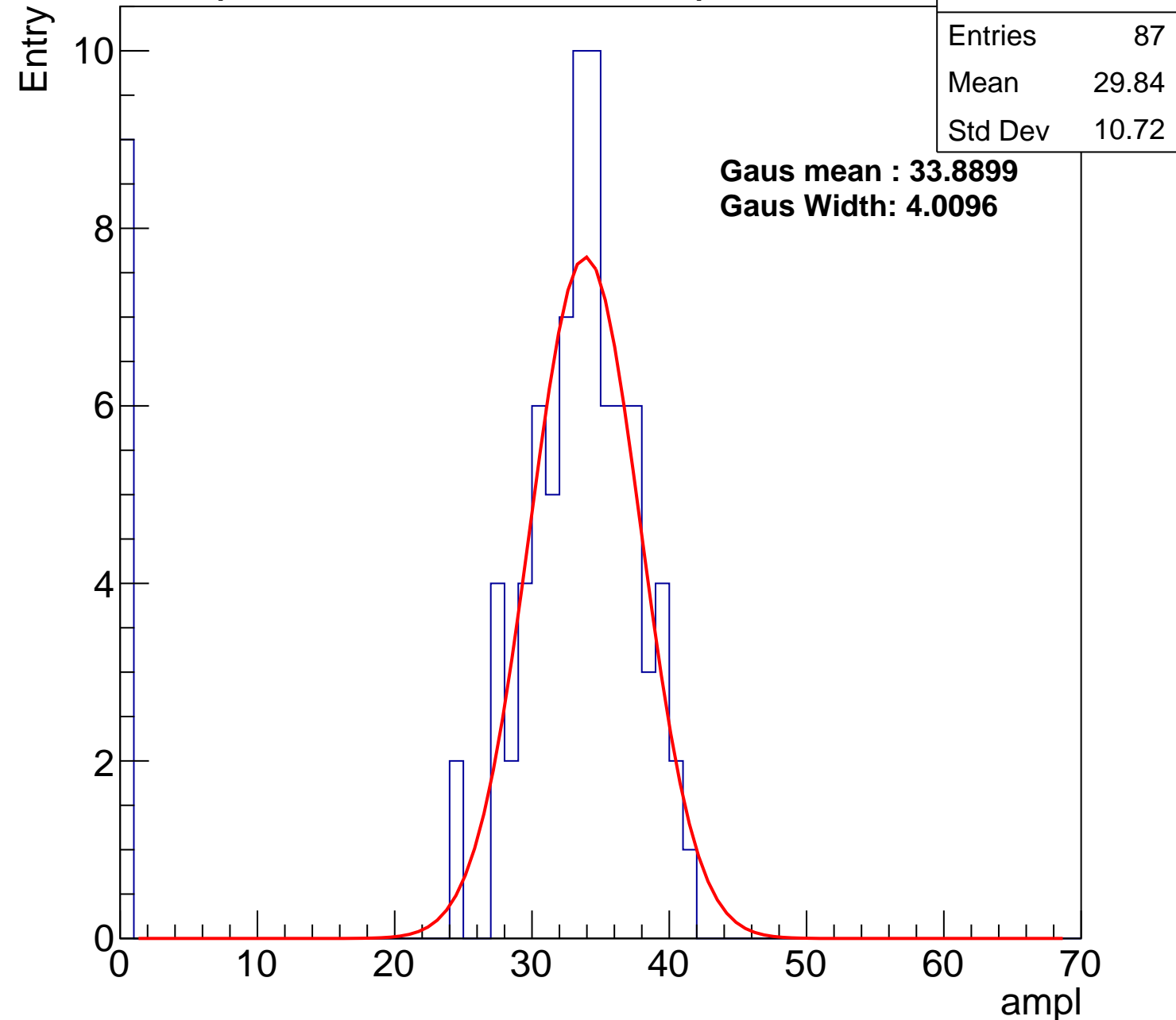
**Gaus Width: 4.0096**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch84, adc2

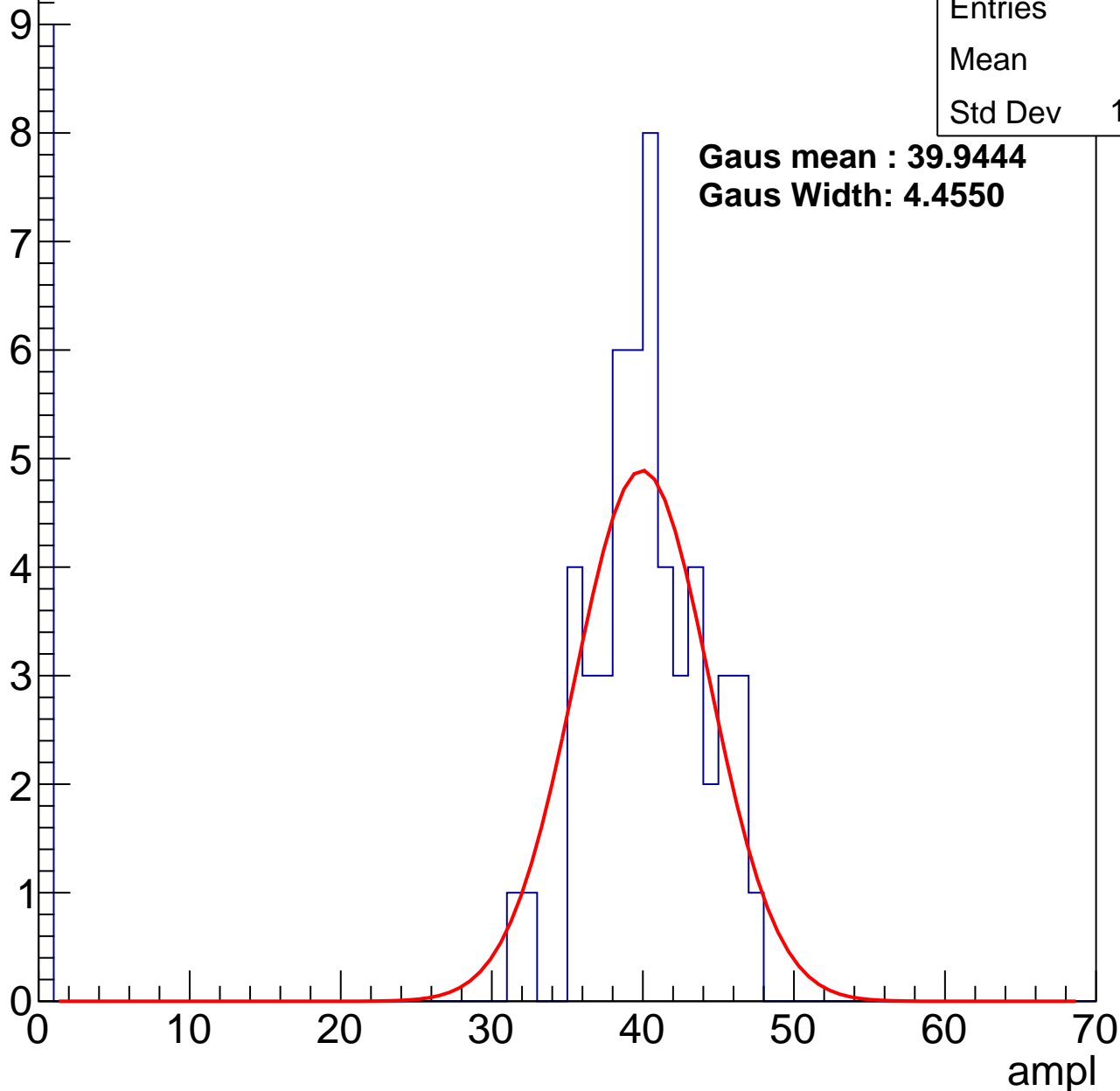
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34
Std Dev	14.53

**Gaus mean : 39.9444**

**Gaus Width: 4.4550**

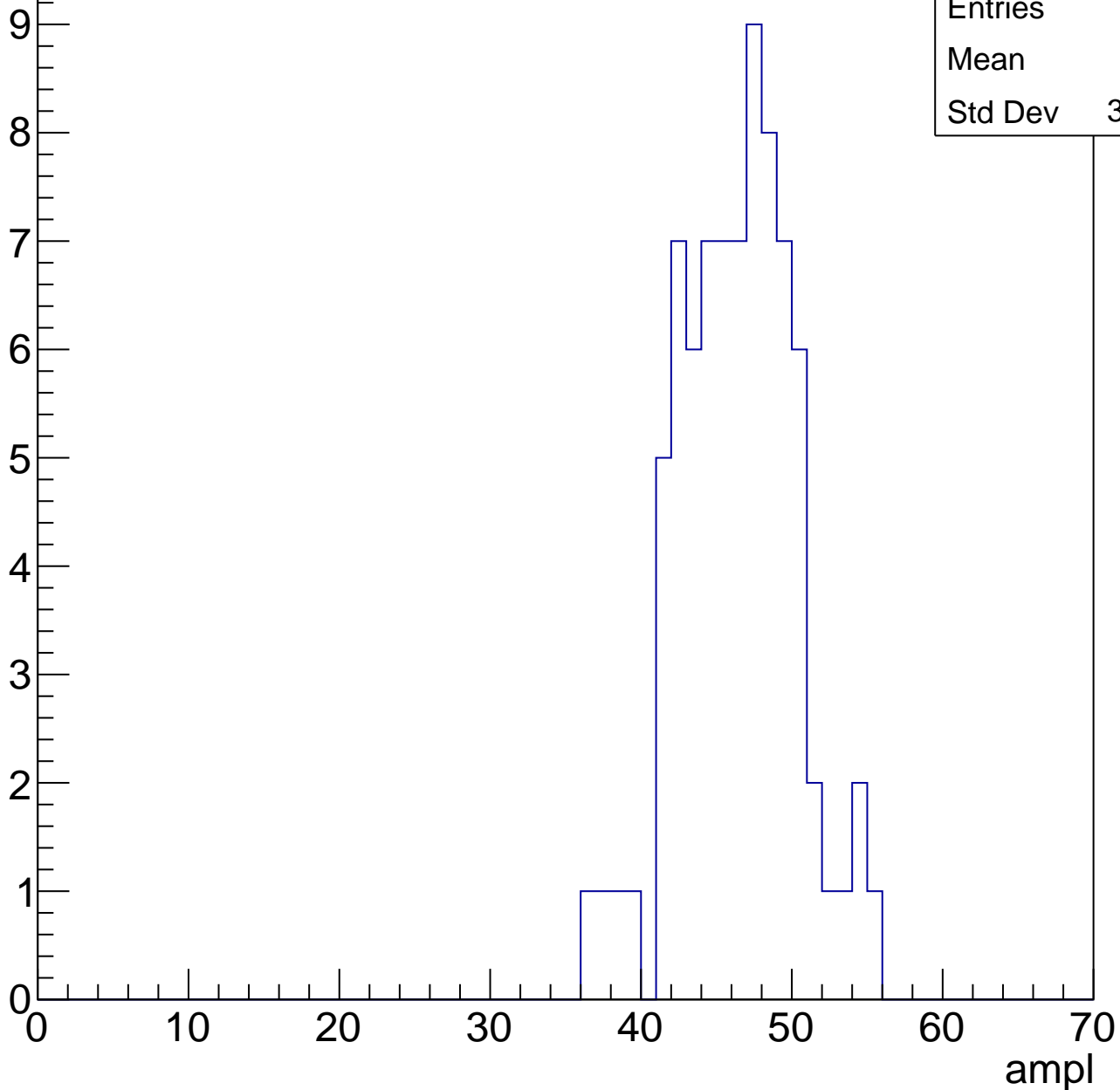


# B1L103S, U22-ch84, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	80
Mean	45.9
Std Dev	3.813

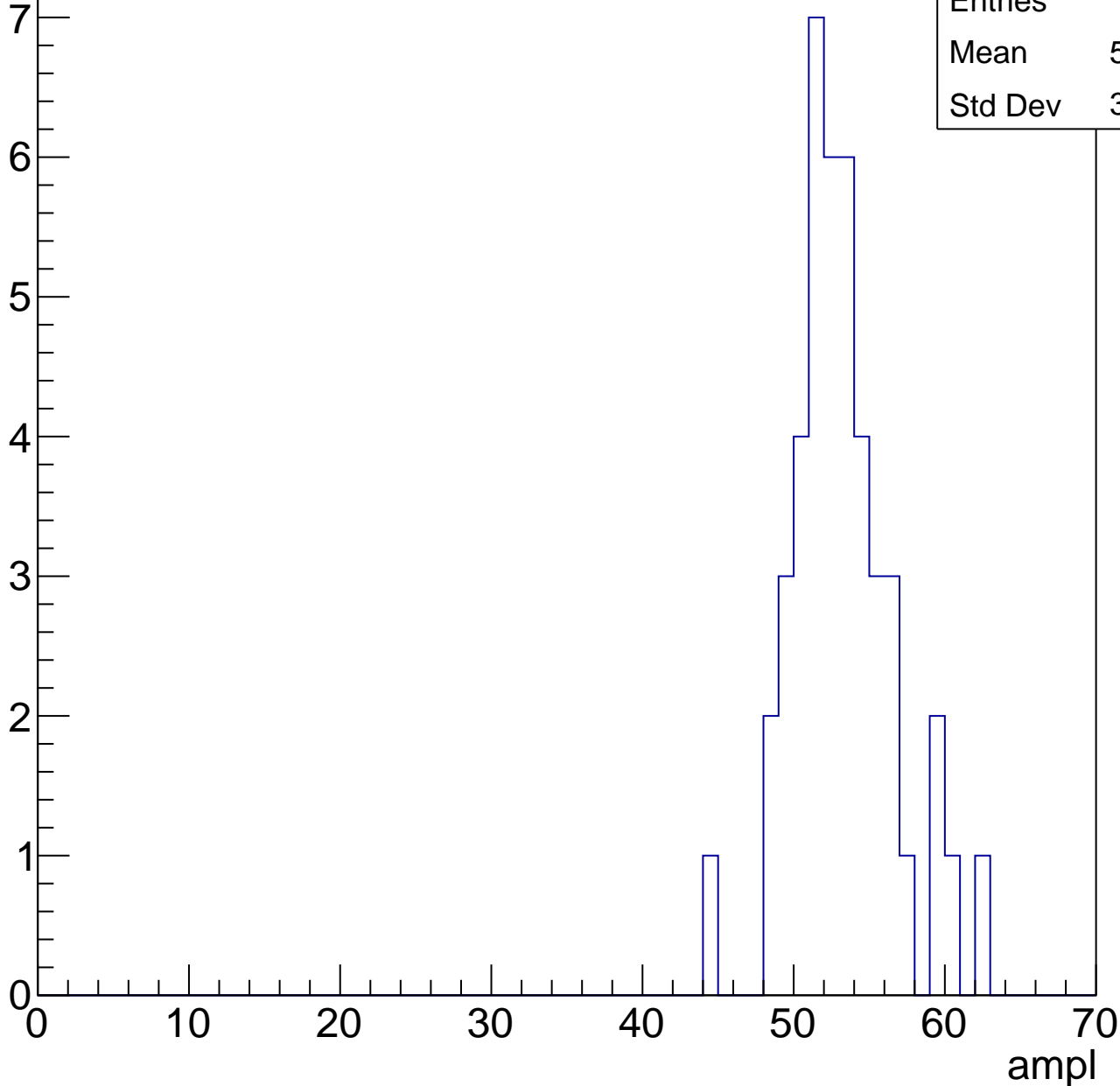


# B1L103S, U22-ch84, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	44
Mean	52.73
Std Dev	3.394

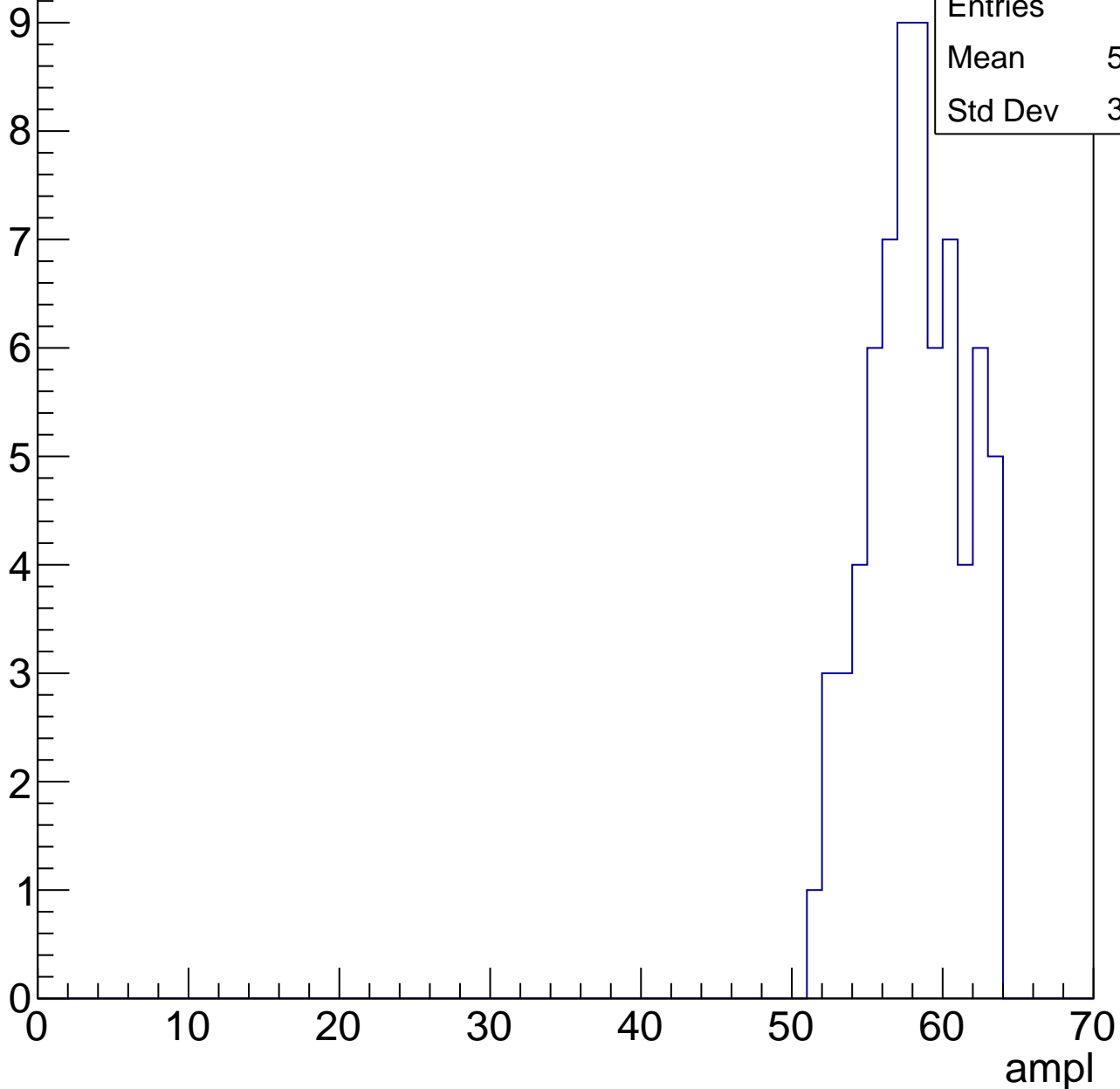


# B1L103S, U22-ch84, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	57.77
Std Dev	3.104

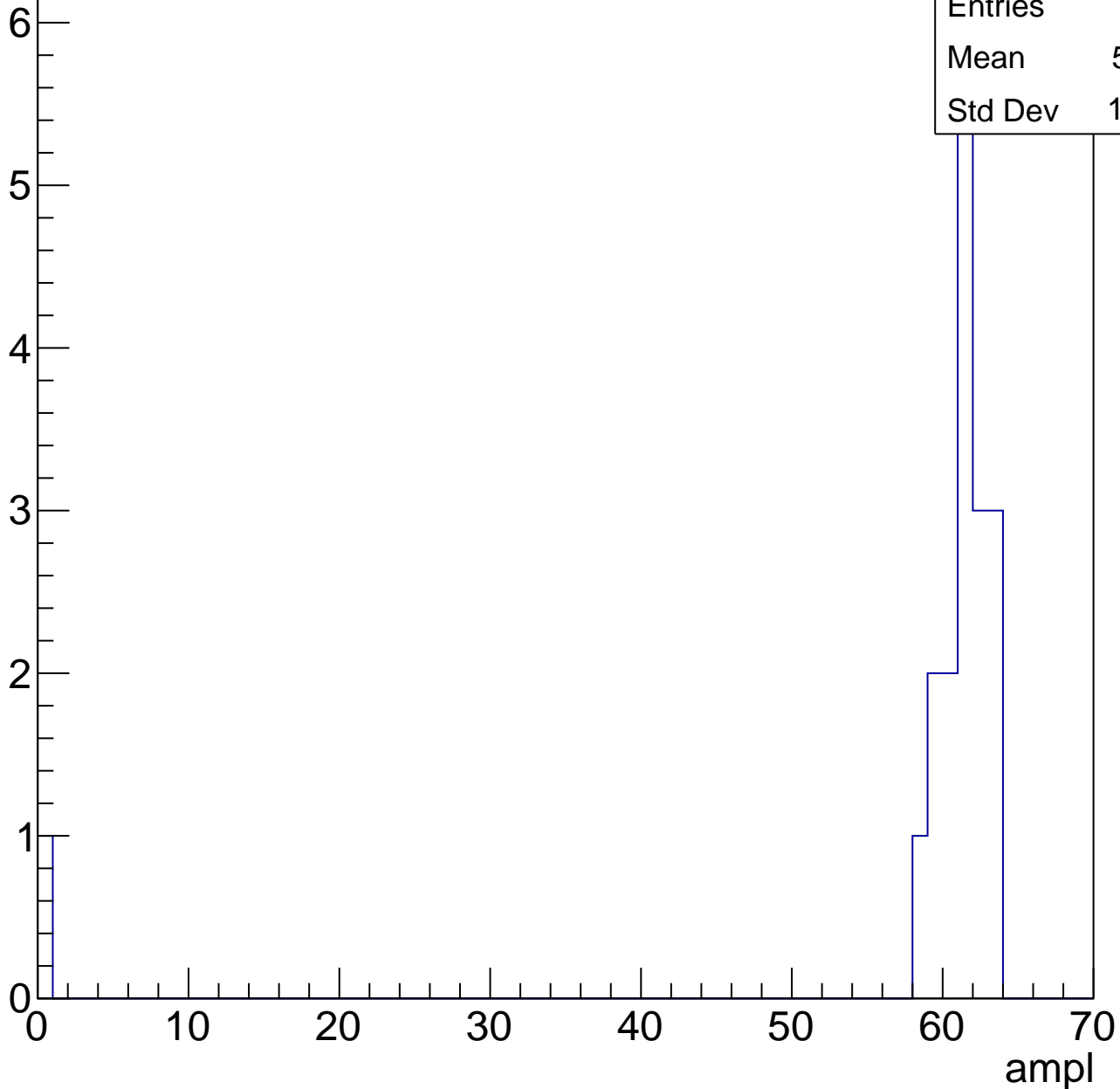


# B1L103S, U22-ch84, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.61
Std Dev	14.04





# B1L103S, U22-ch84, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch85, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	97
Mean	24.6
Std Dev	11.43

**Gaus mean : 30.0753**

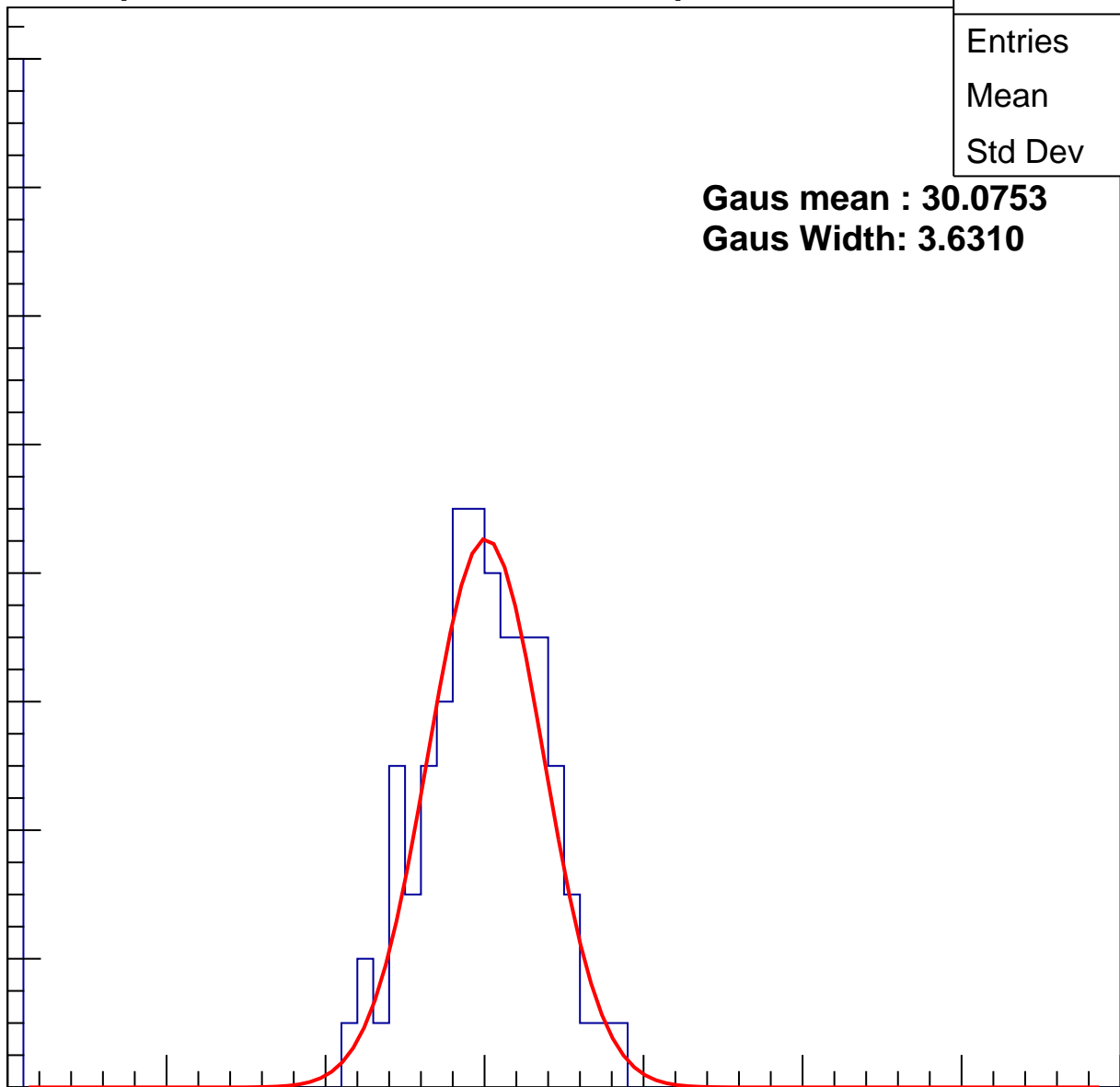
**Gaus Width: 3.6310**

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch85, adc1

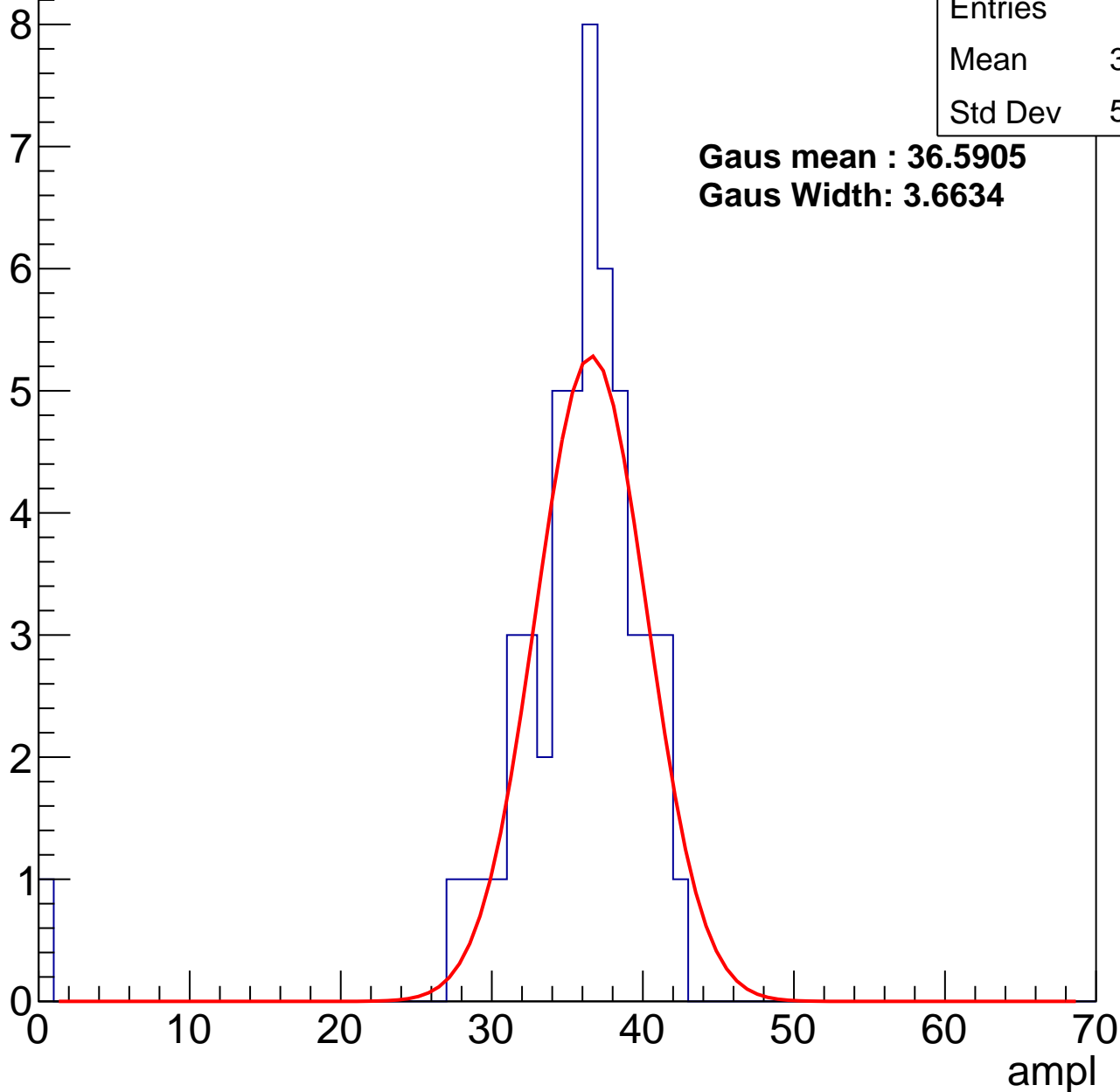
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	34.92
Std Dev	5.954

**Gaus mean : 36.5905**

**Gaus Width: 3.6634**



# B1L103S, U22-ch85, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	36.27
Std Dev	14.96

**Gaus mean : 42.7459**

**Gaus Width: 3.7442**

Entry

10

8

6

4

2

0

0

10

20

30

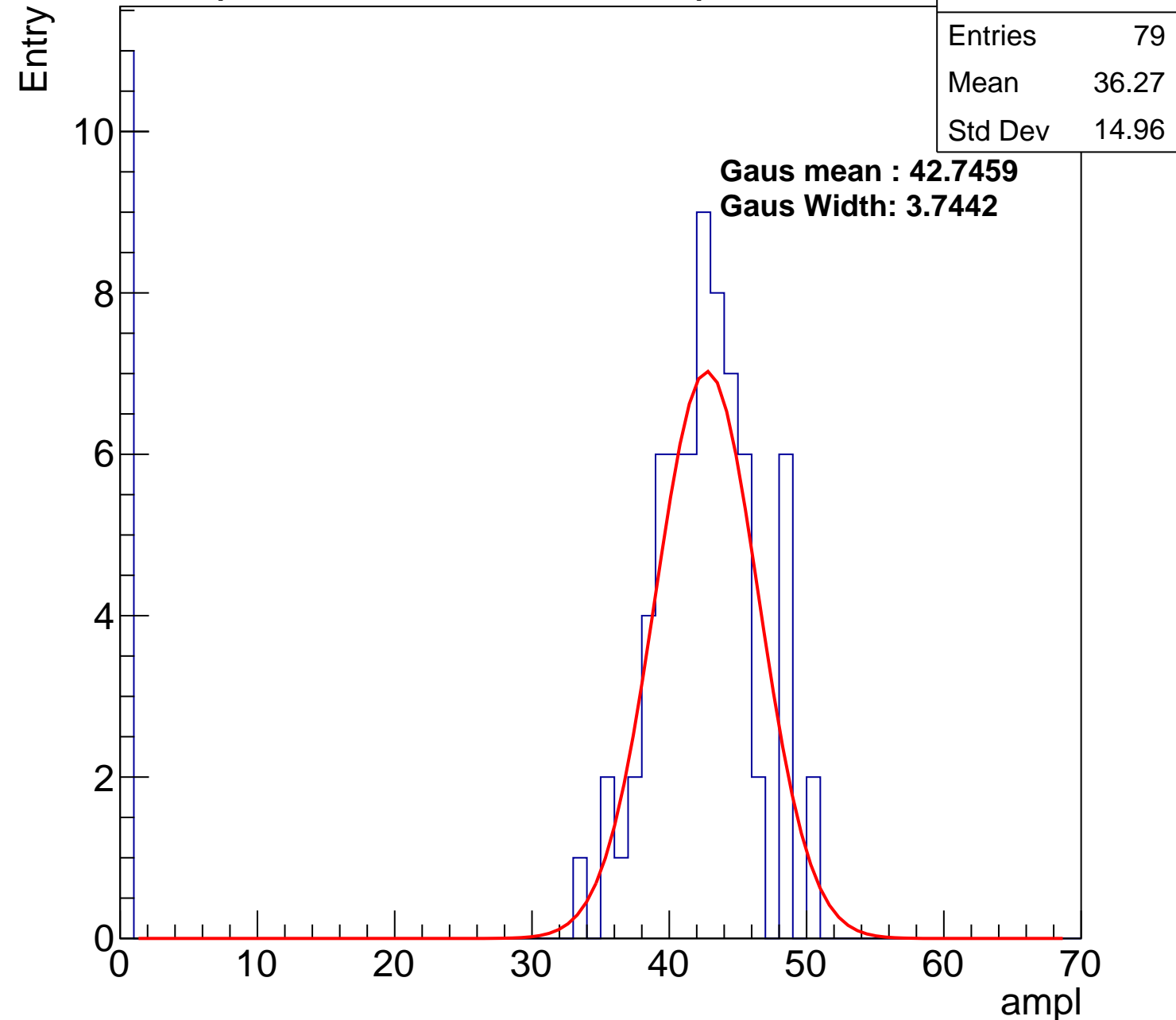
40

50

60

70

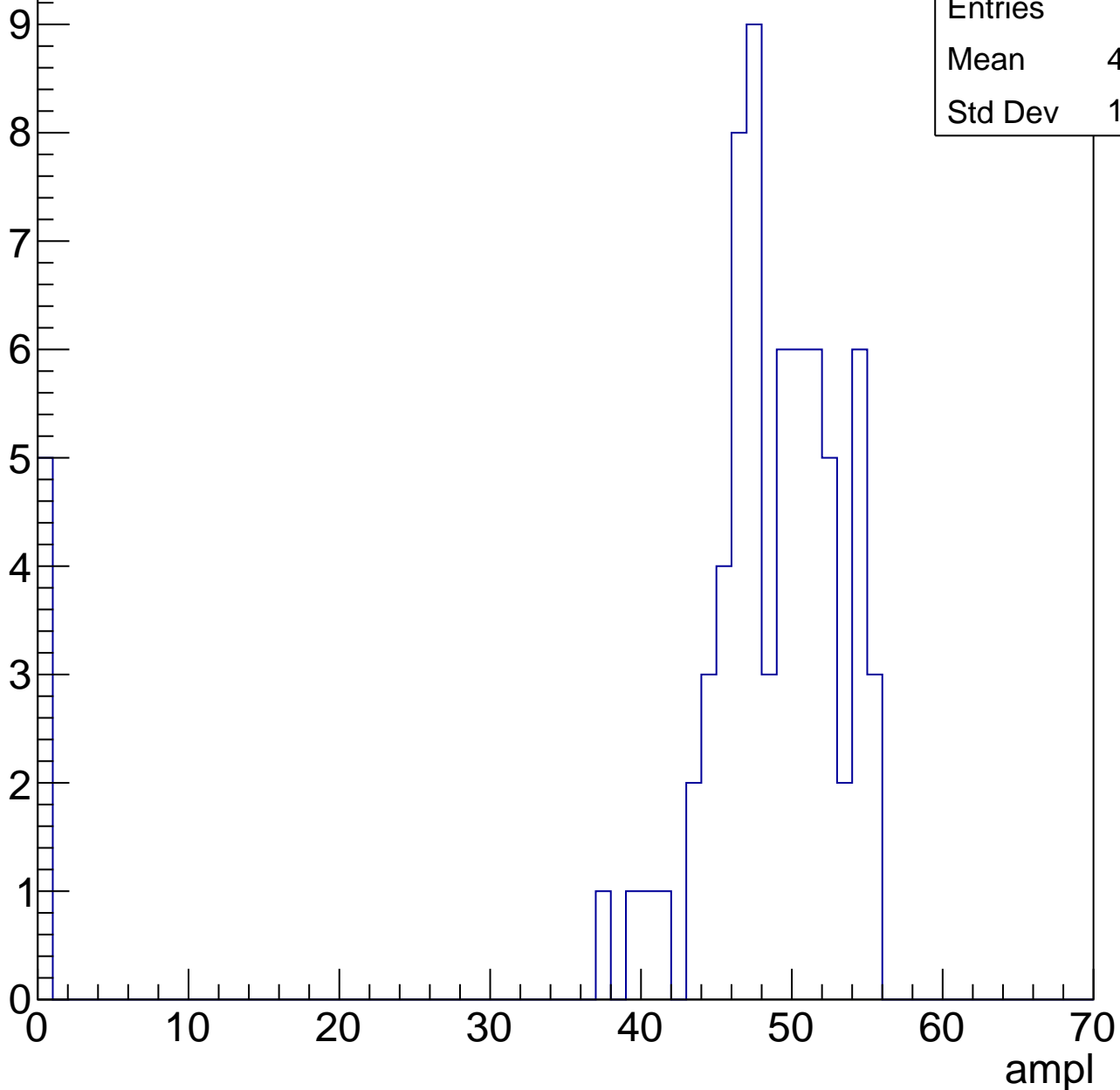
ampl



# B1L103S, U22-ch85, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



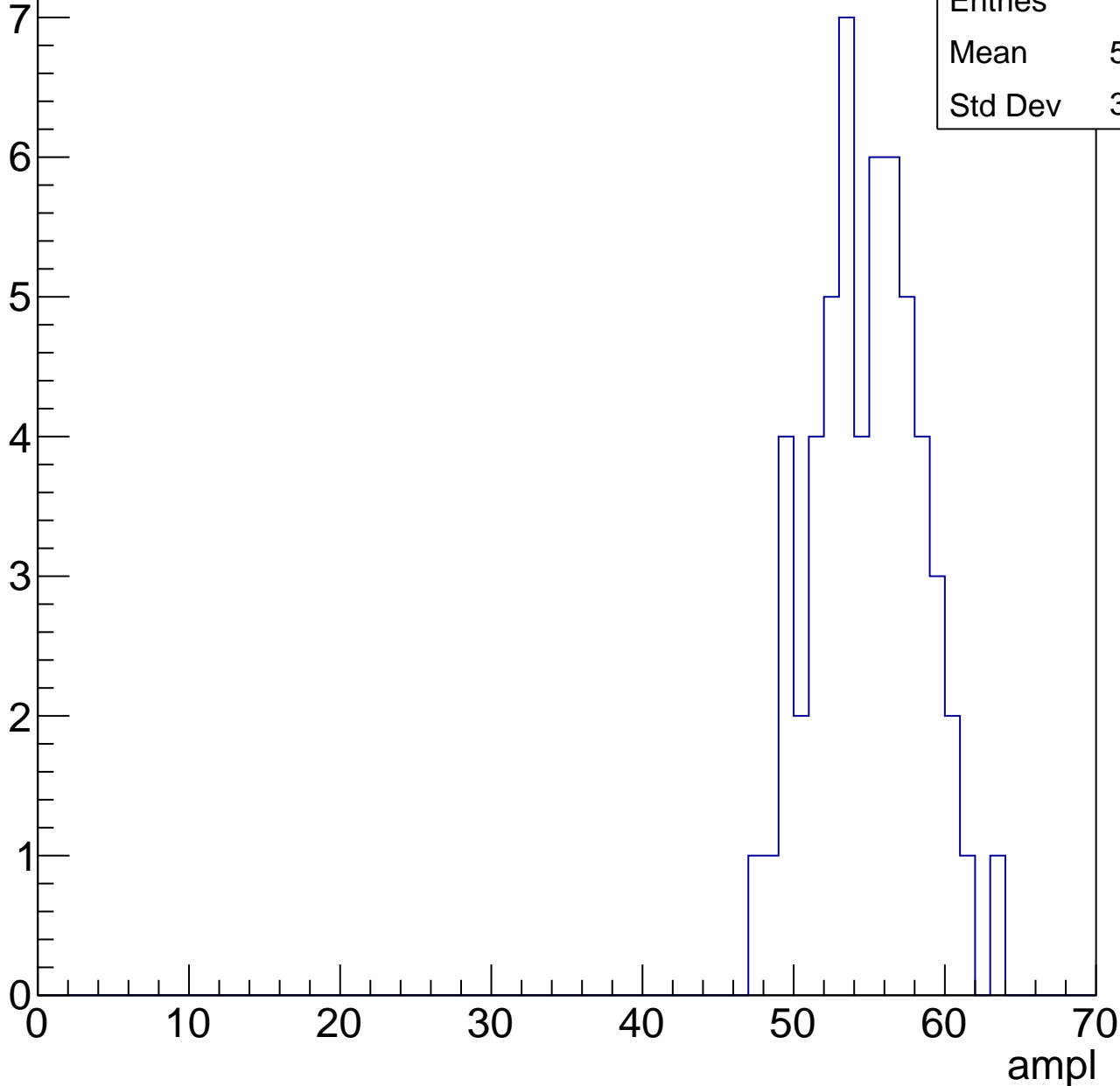
Entries	72
Mean	45.07
Std Dev	12.89

# B1L103S, U22-ch85, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	54.39
Std Dev	3.503

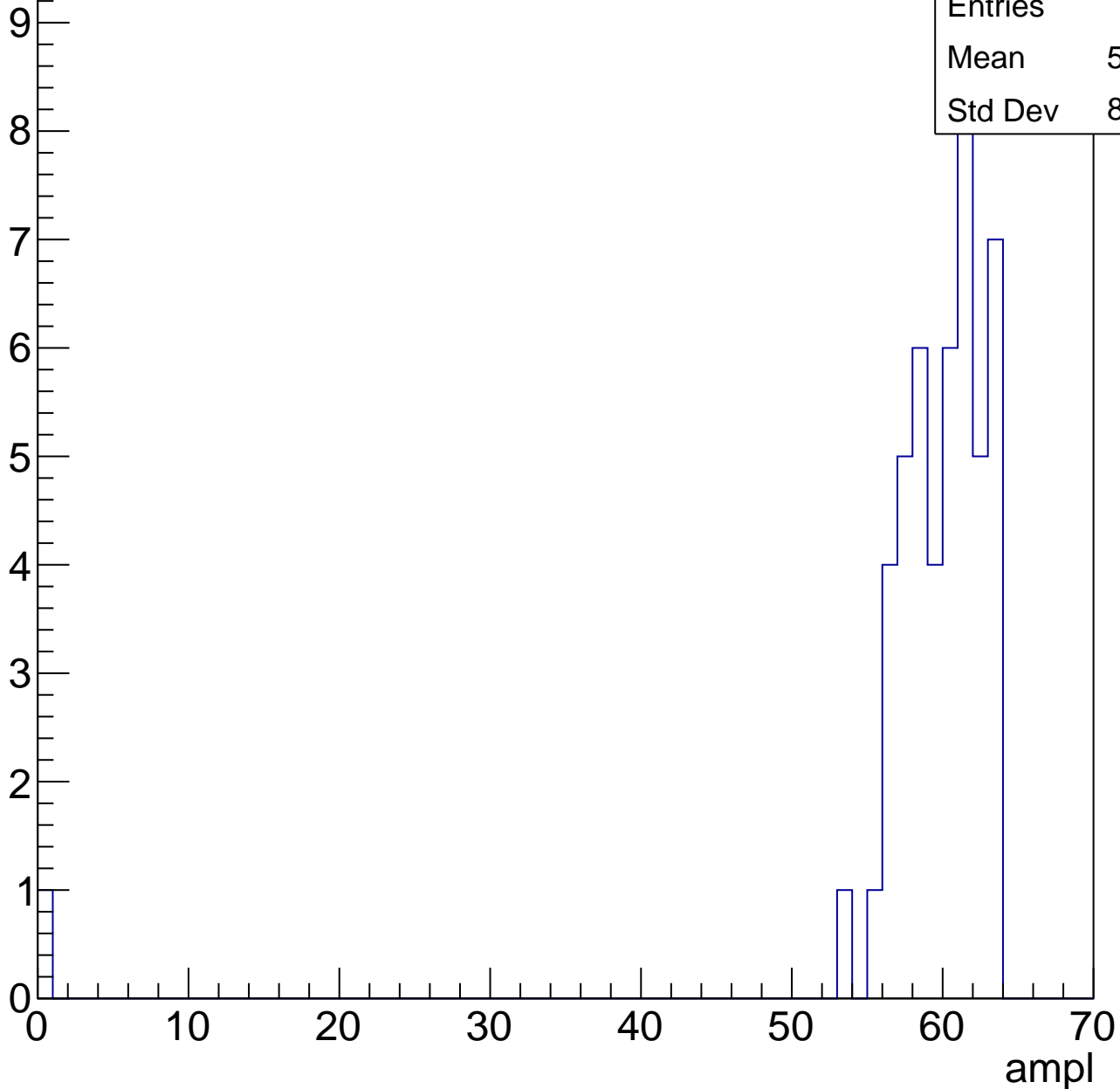


# B1L103S, U22-ch85, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.39
Std Dev	8.778

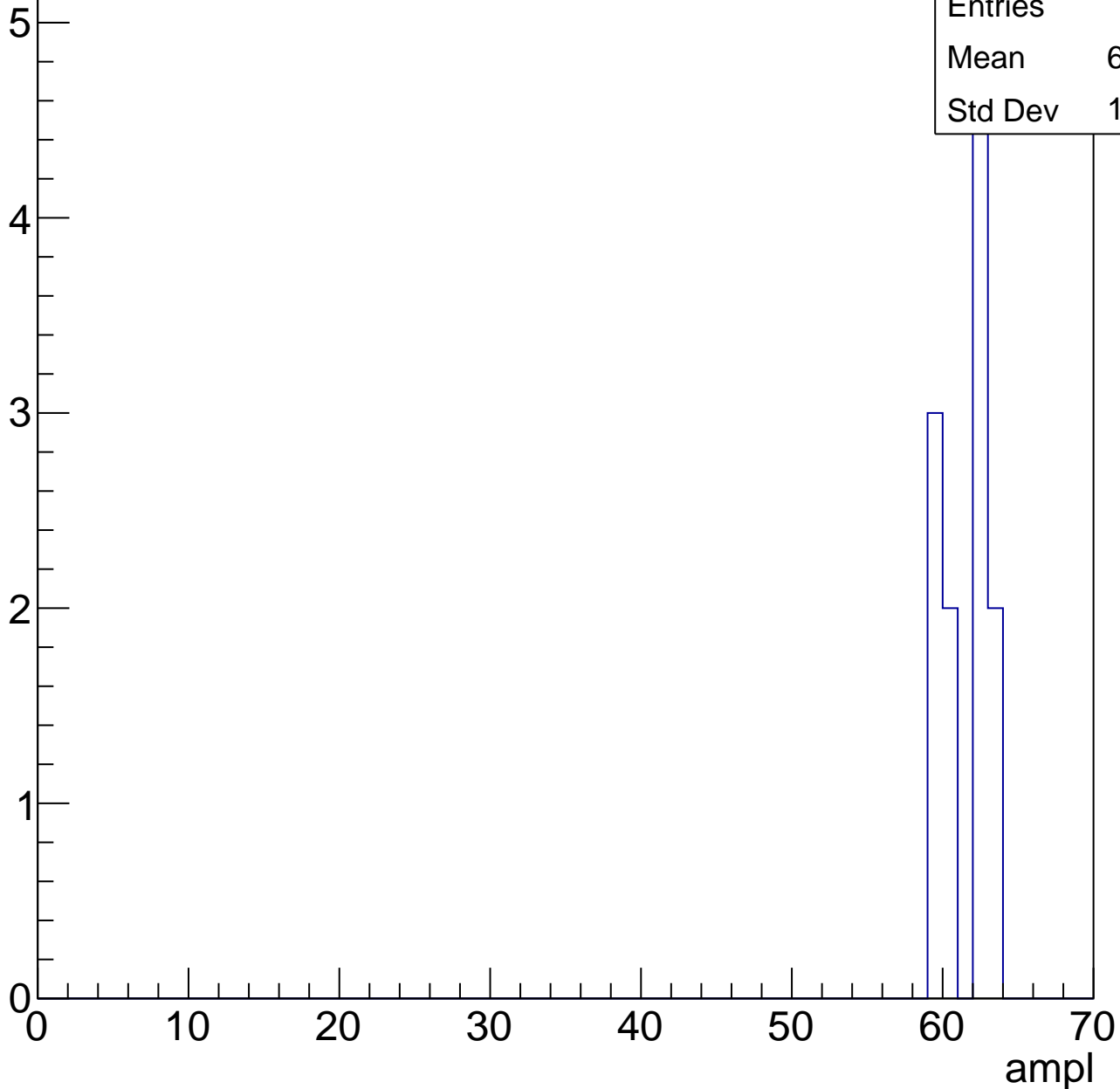


# B1L103S, U22-ch85, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.08
Std Dev	1.498





# B1L103S, U22-ch85, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch86, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	101
Mean	17.51
Std Dev	11.81

**Gaus mean : 25.4356**

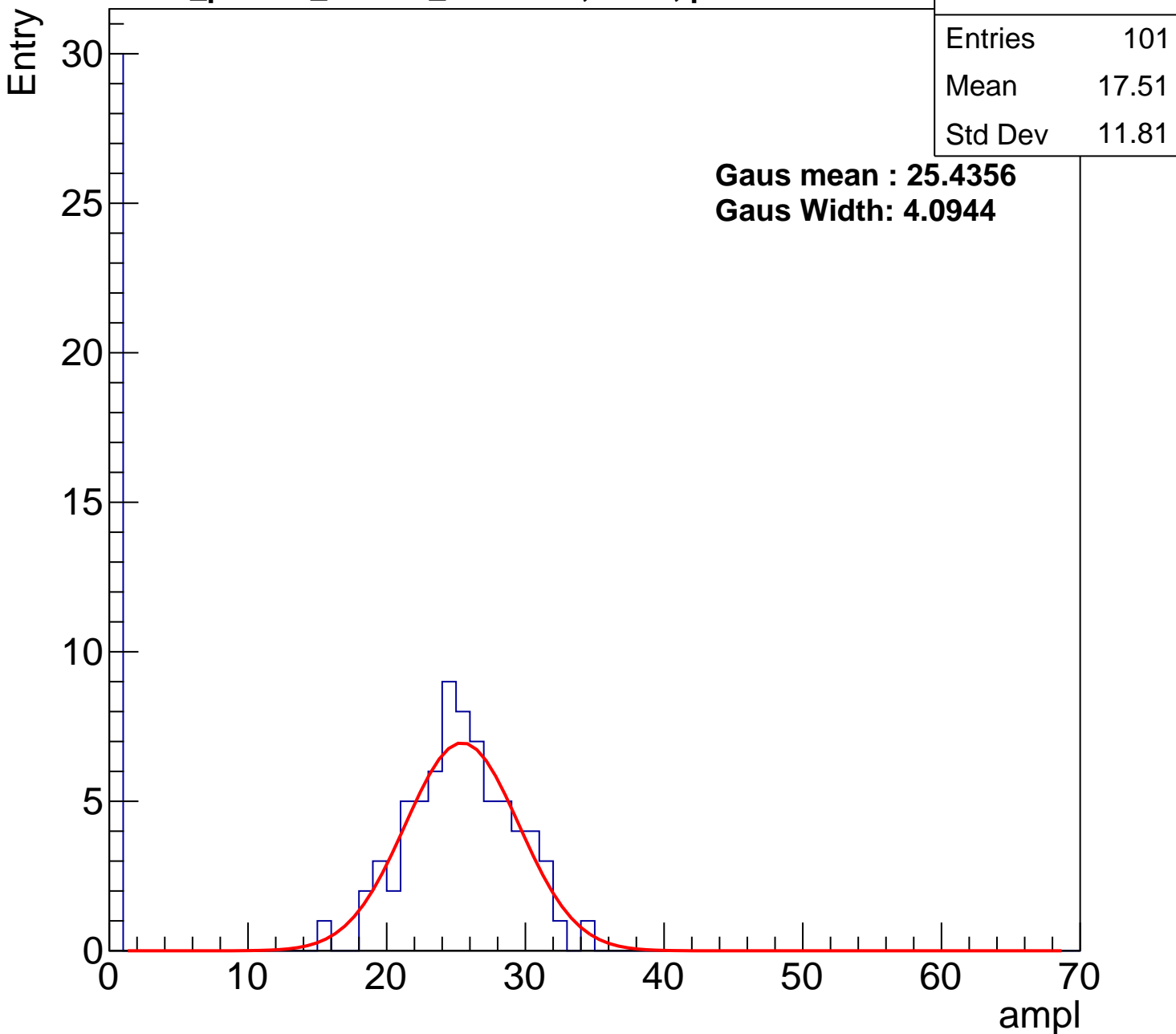
**Gaus Width: 4.0944**

Entry

30  
25  
20  
15  
10  
5  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch86, adc1

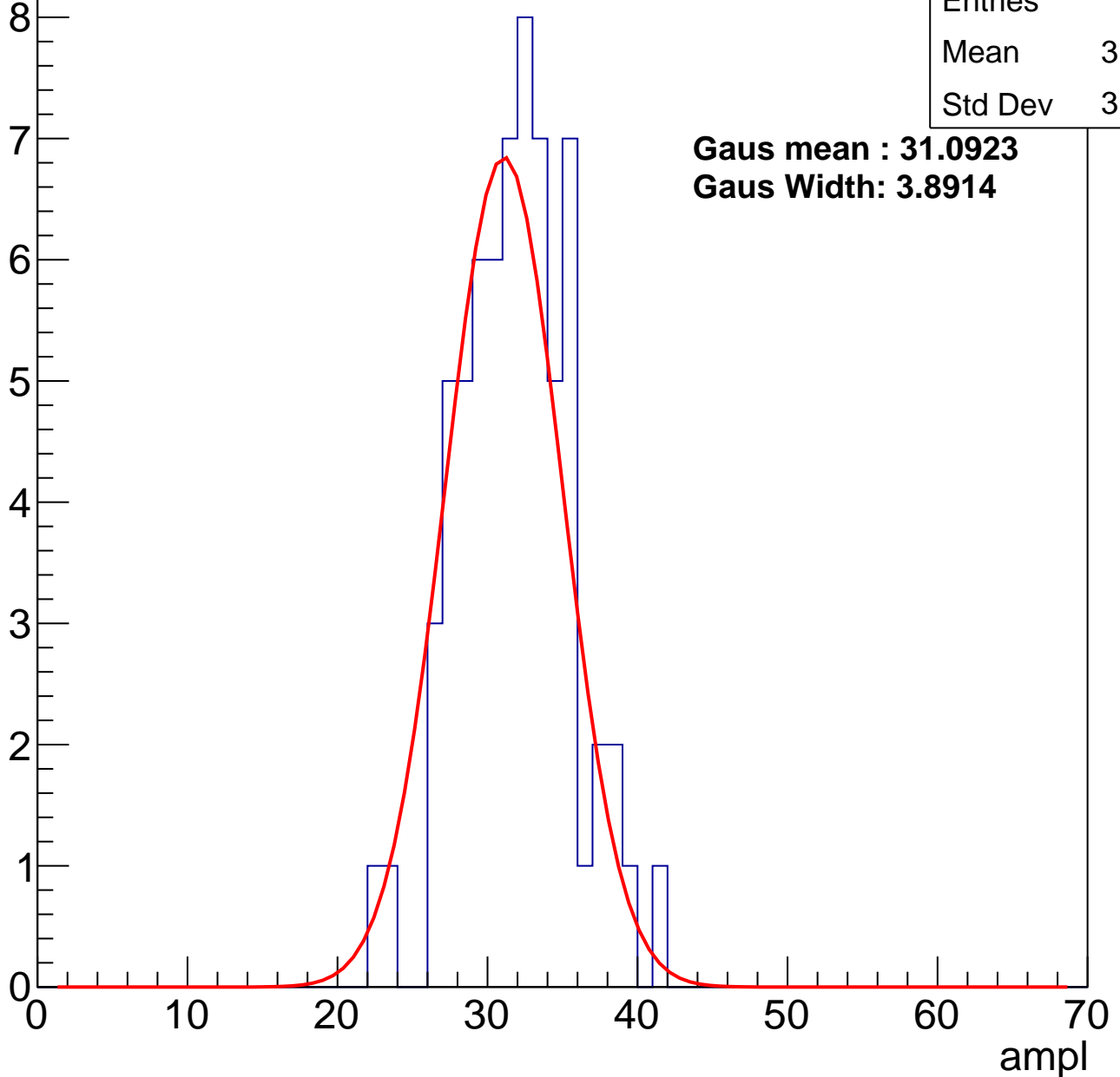
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	31.43
Std Dev	3.667

**Gaus mean : 31.0923**

**Gaus Width: 3.8914**



# B1L103S, U22-ch86, adc2

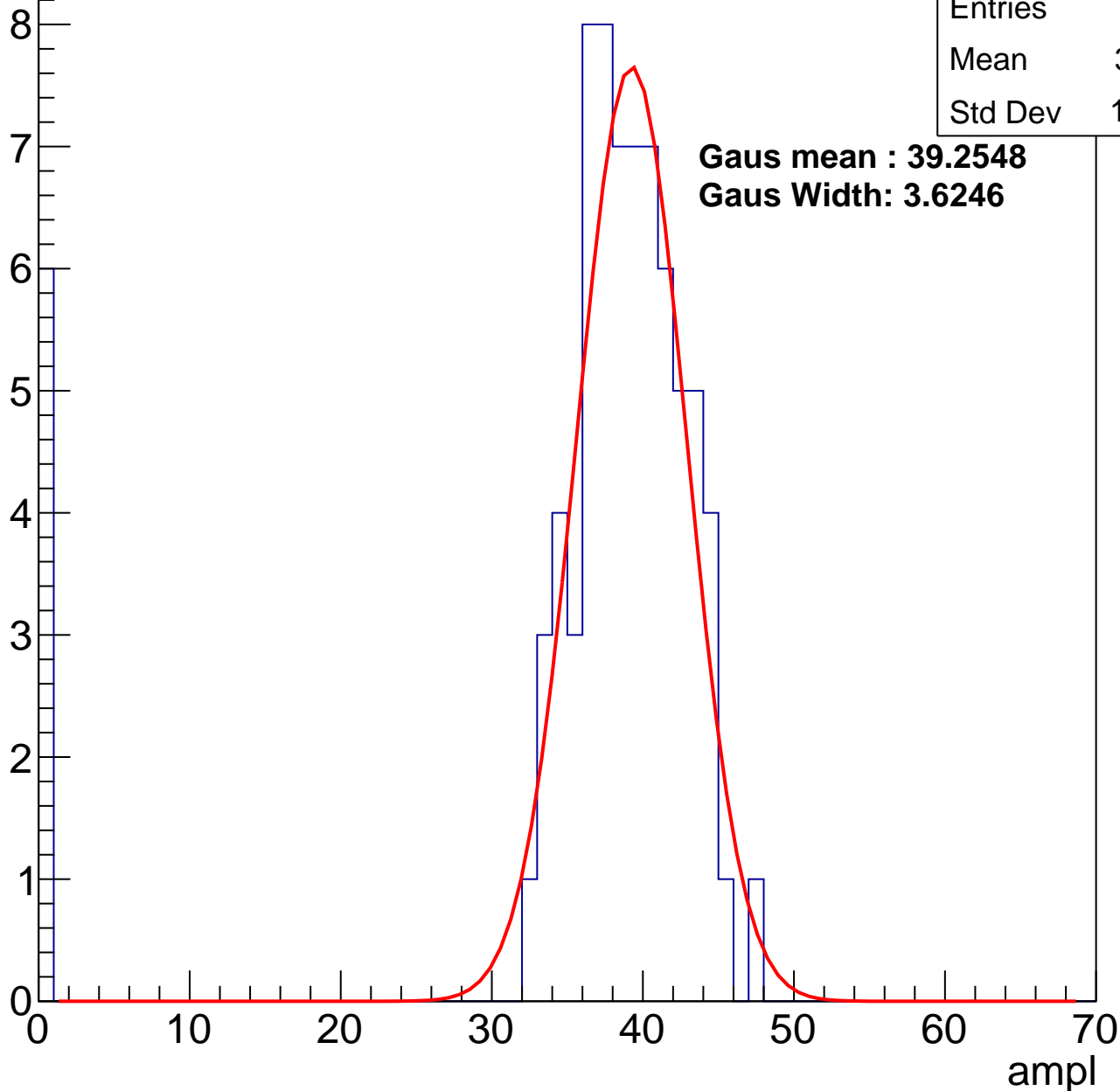
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	35.71
Std Dev	10.93

**Gaus mean : 39.2548**

**Gaus Width: 3.6246**



# B1L103S, U22-ch86, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	39.23
Std Dev	16.32

Entry

10

8

6

4

2

0

0

10

20

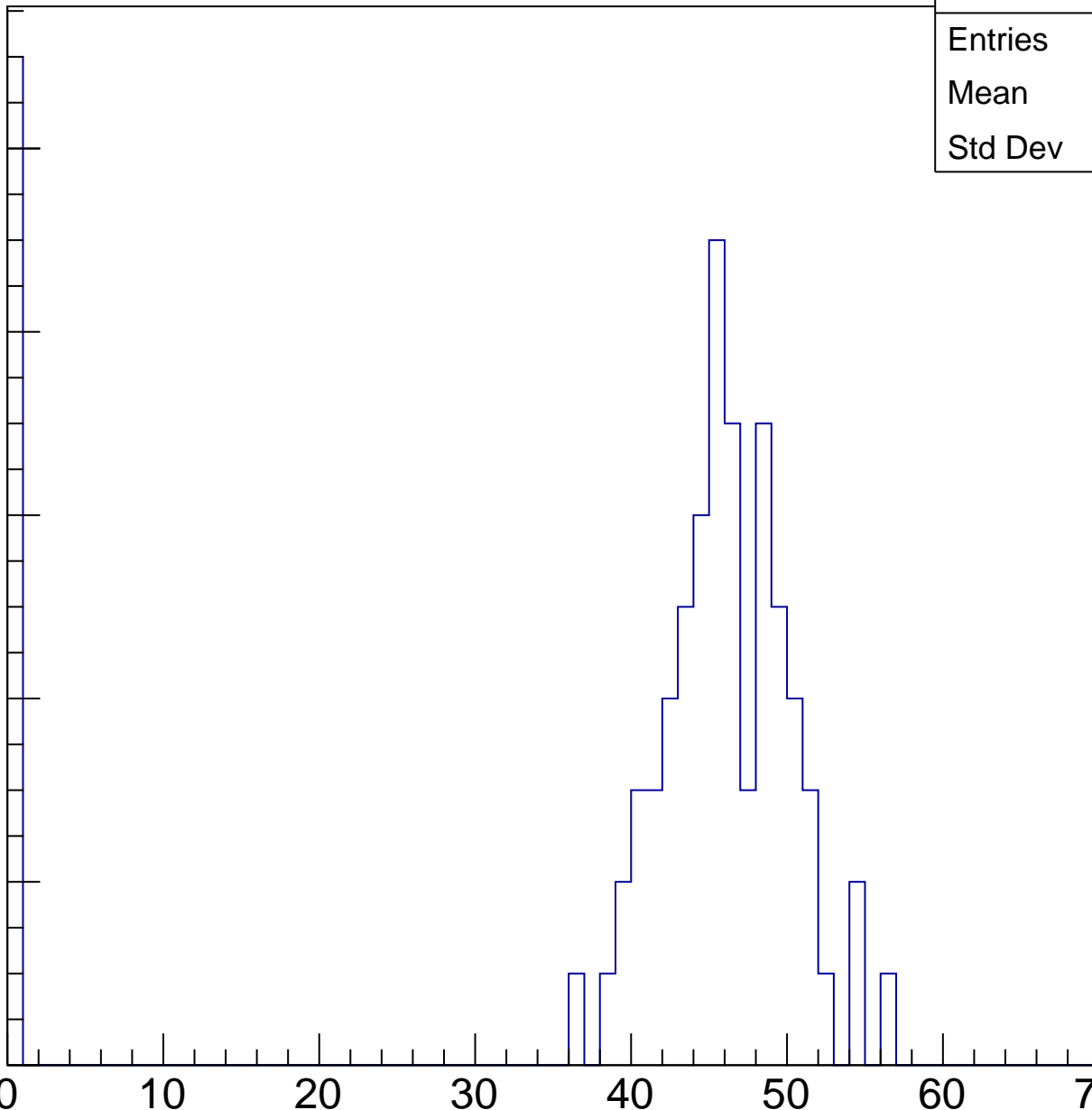
30

40

50

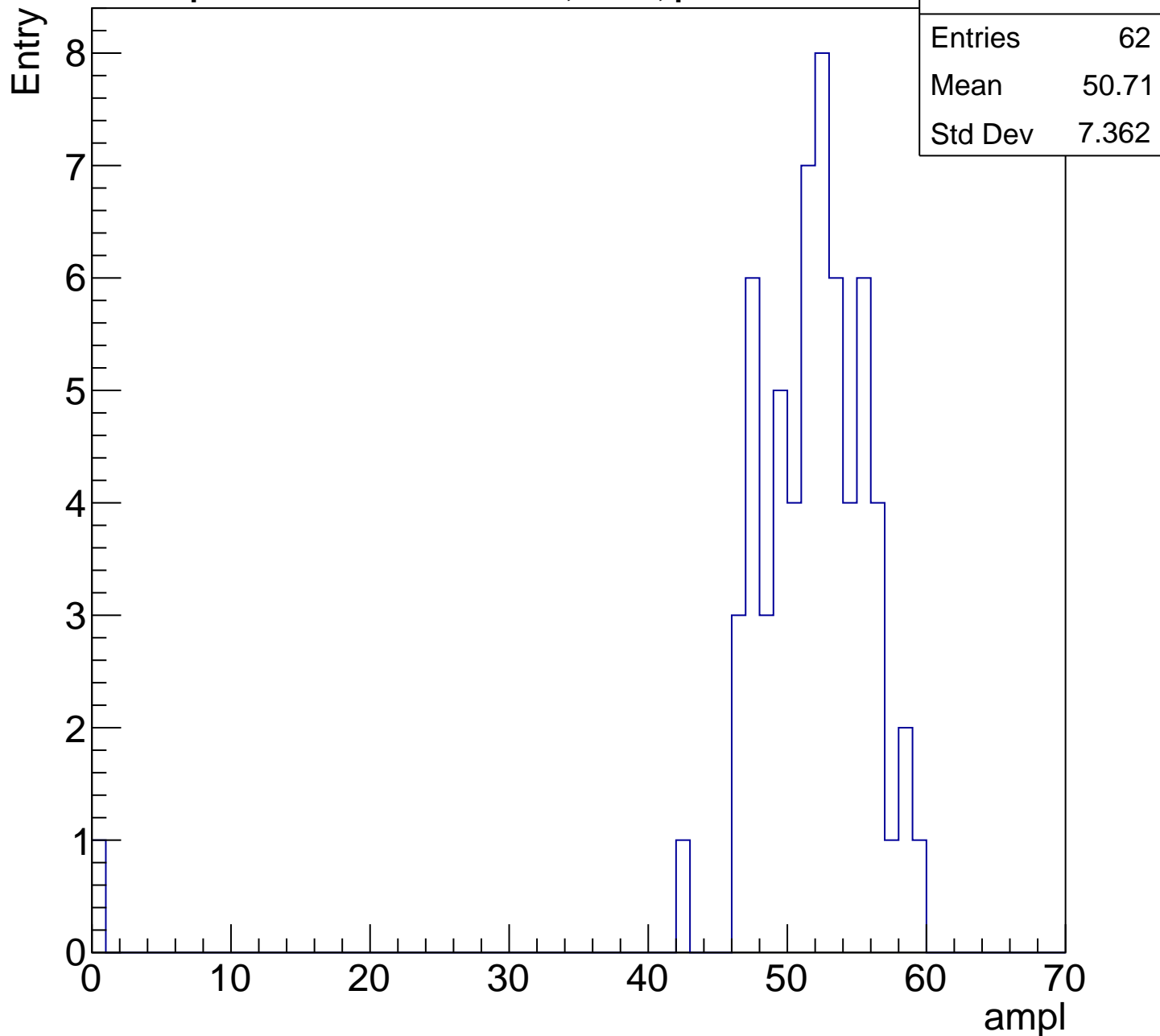
60

ampl



# B1L103S, U22-ch86, adc4

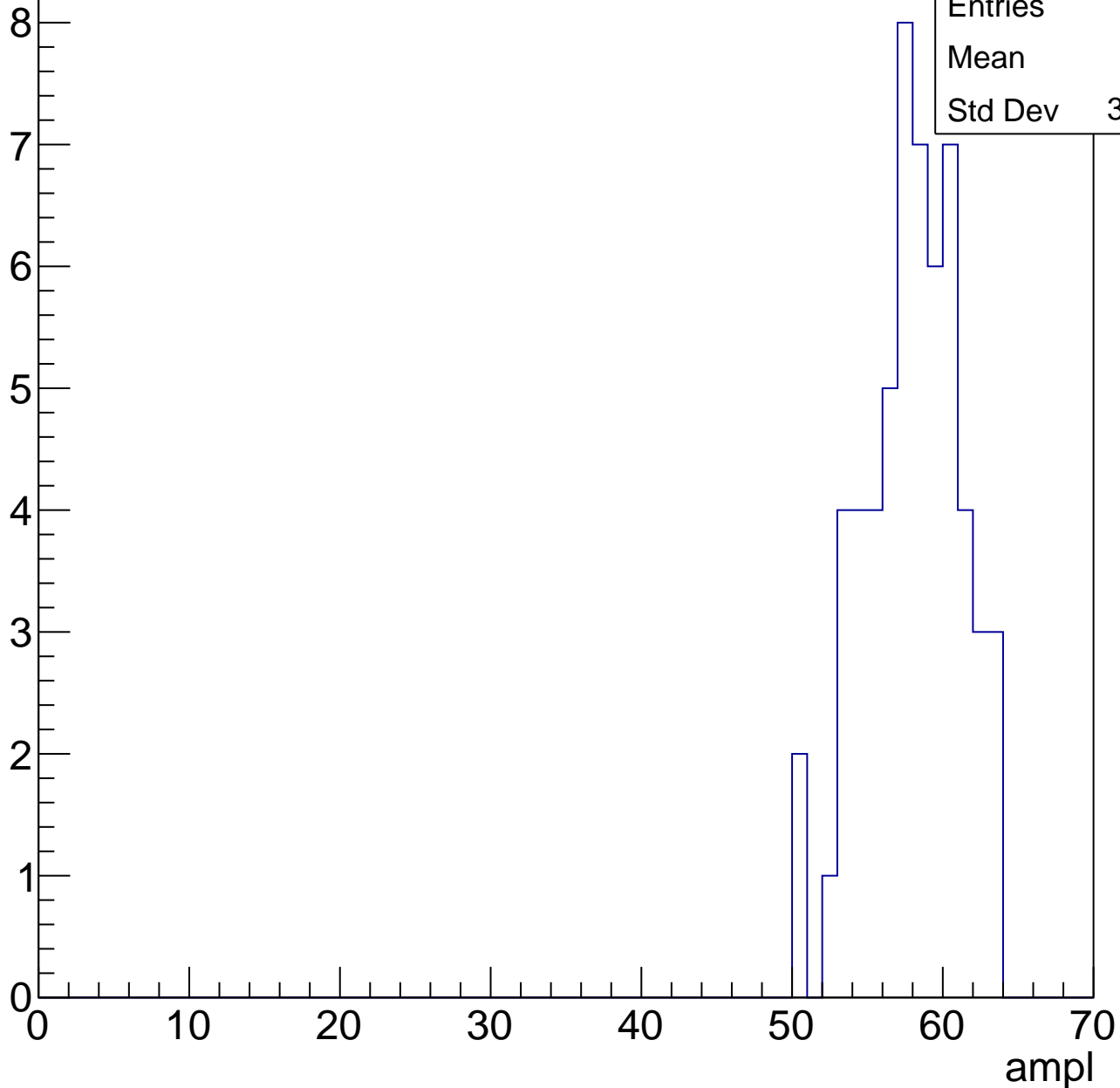
calib\_packv5\_041523\_1651.root, FC#0, port C2



# B1L103S, U22-ch86, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



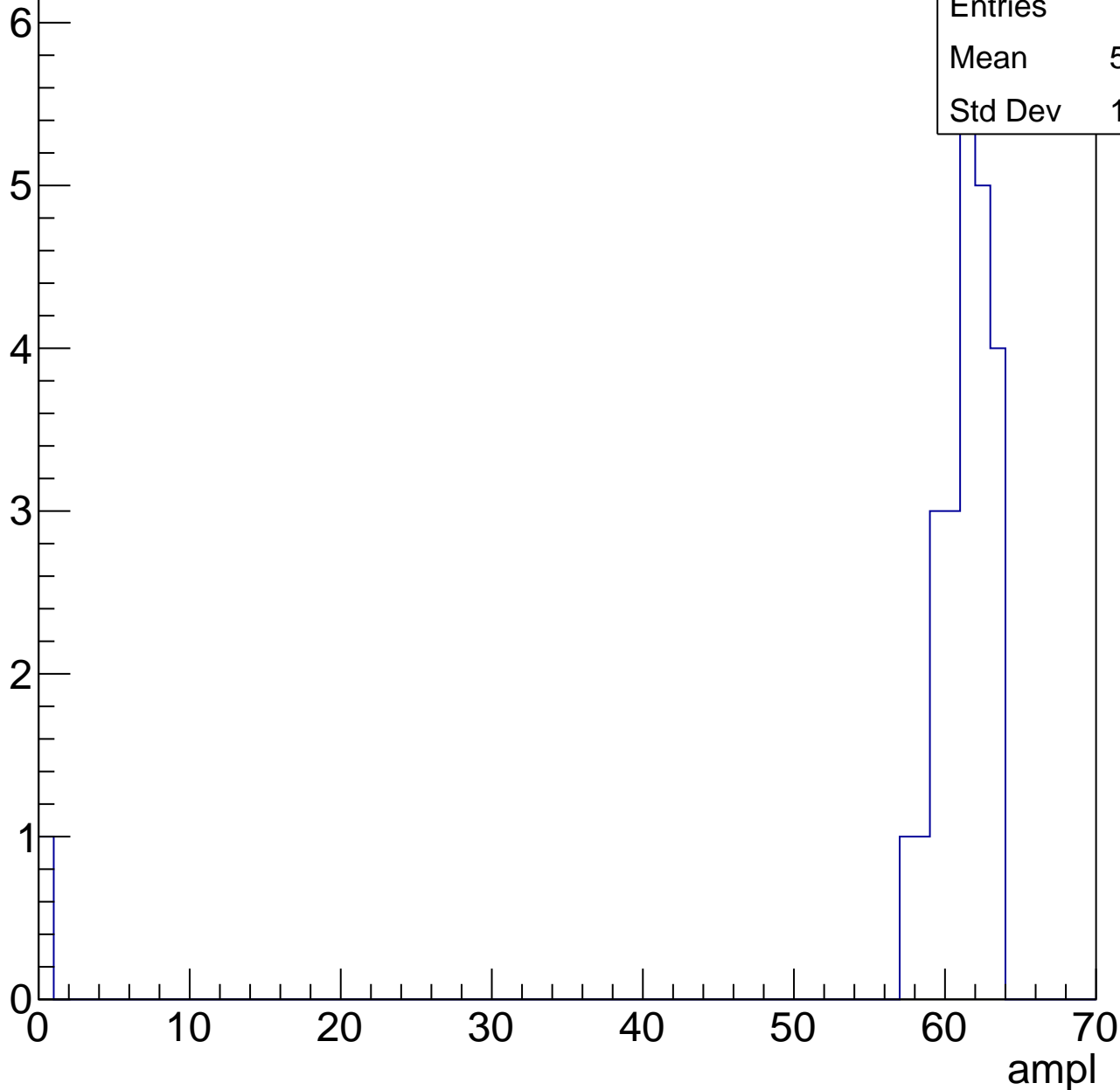
Entries	58
Mean	57.5
Std Dev	3.136

# B1L103S, U22-ch86, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	24
Mean	58.33
Std Dev	12.27



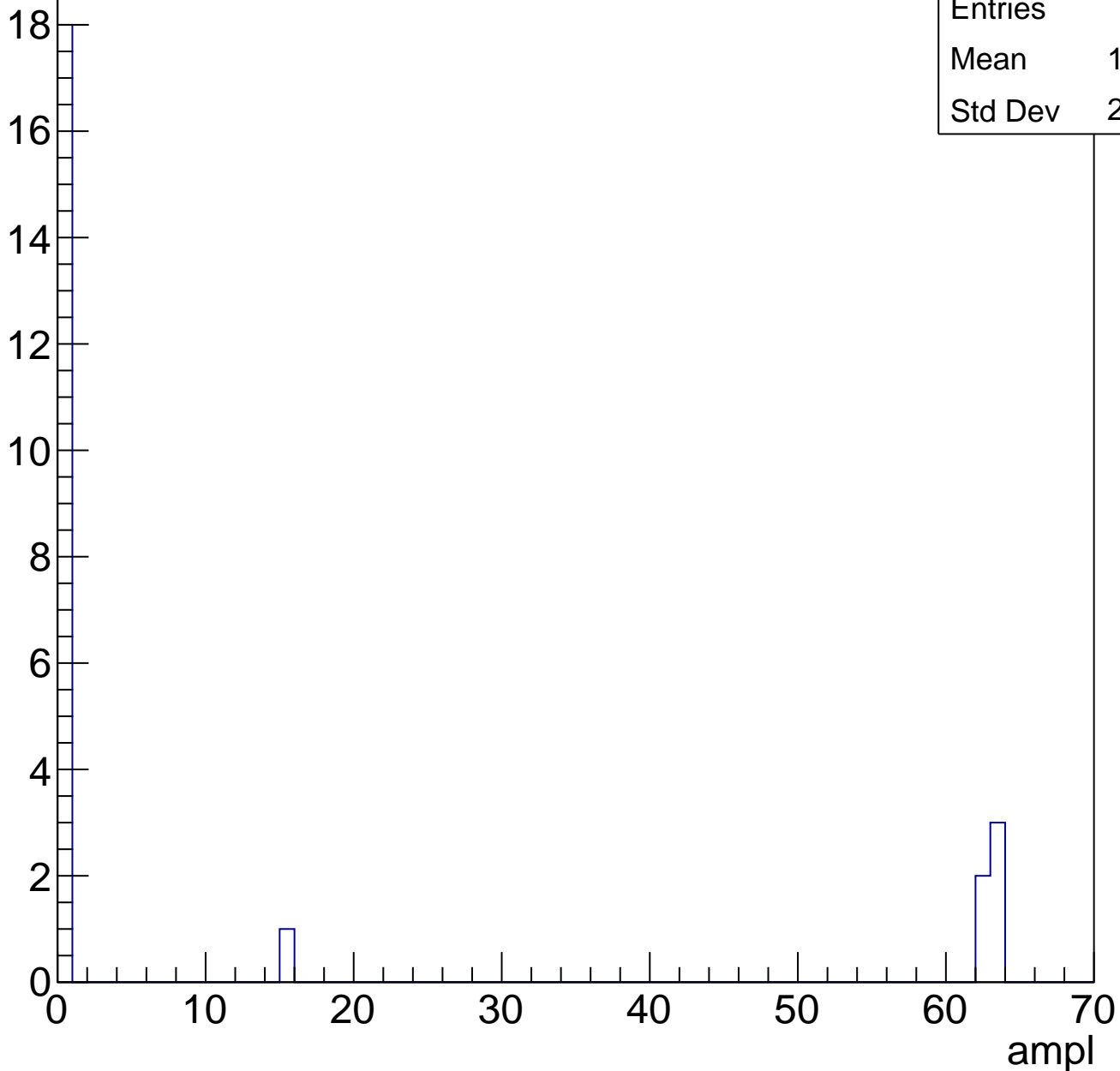


# B1L103S, U22-ch86, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	24
Mean	13.67
Std Dev	25.28

Entry



# B1L103S, U22-ch87, adc0

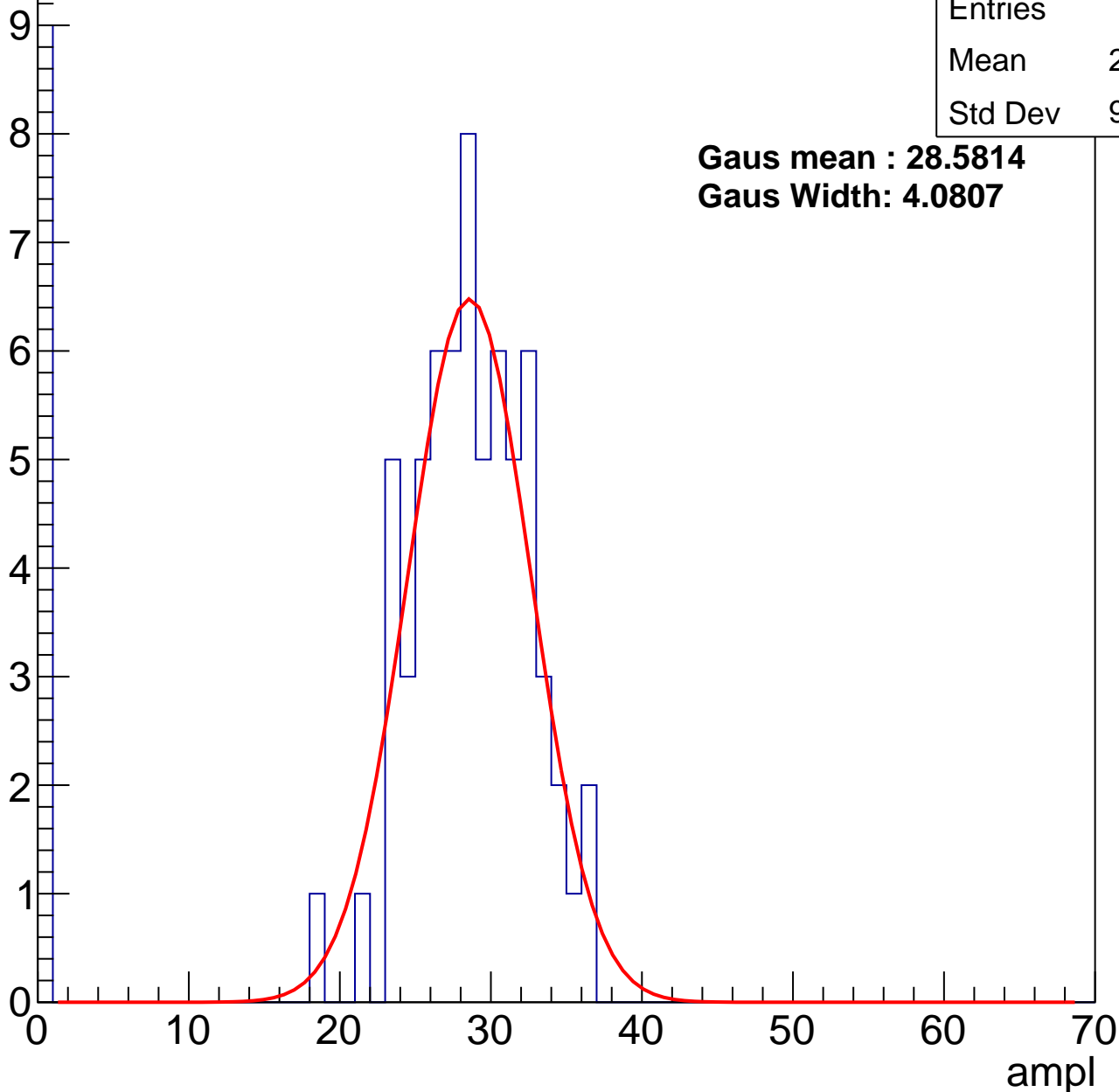
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	24.85
Std Dev	9.873

**Gaus mean : 28.5814**

**Gaus Width: 4.0807**



# B1L103S, U22-ch87, adc1

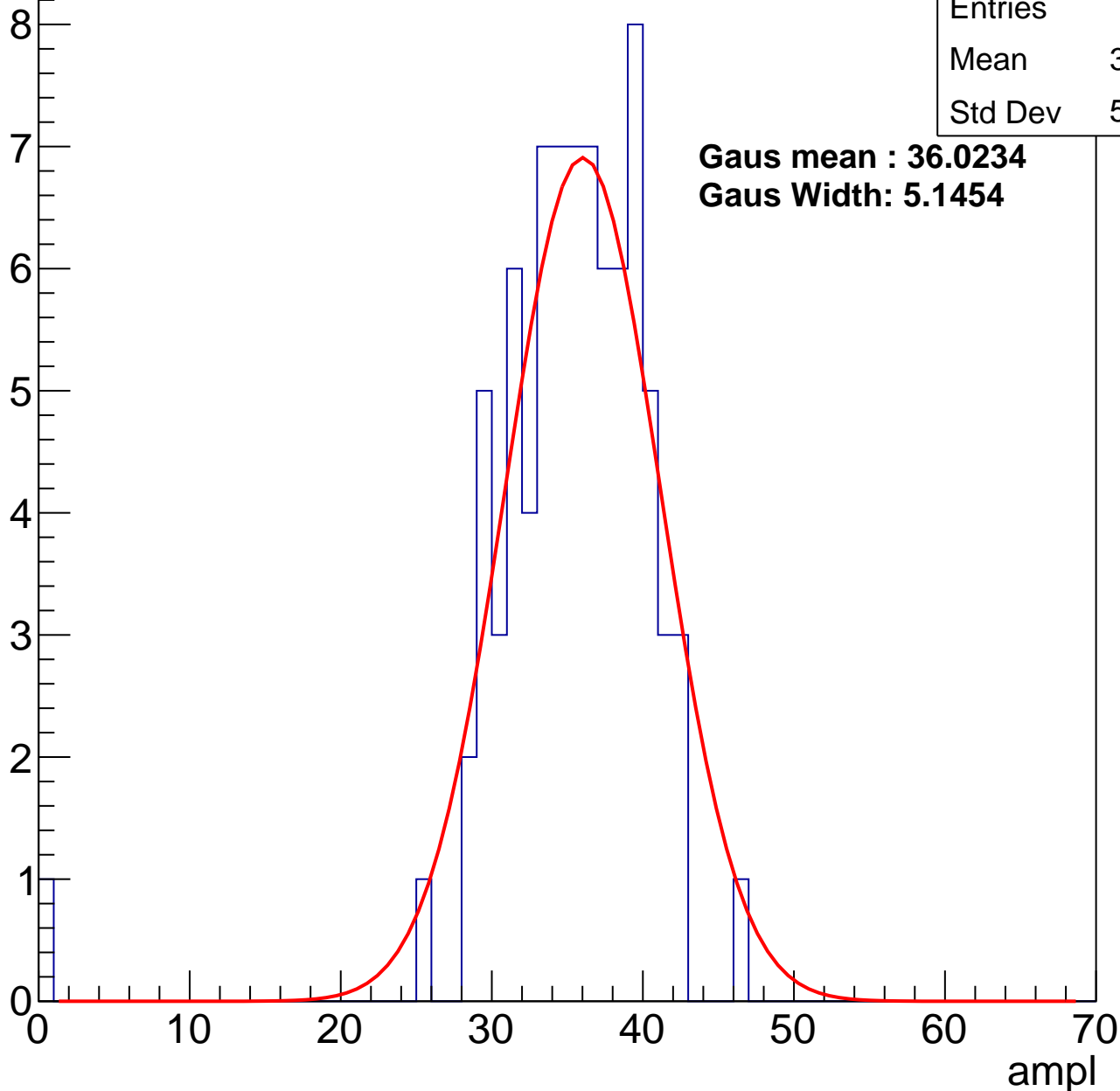
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	82
Mean	34.79
Std Dev	5.583

**Gaus mean : 36.0234**

**Gaus Width: 5.1454**



# B1L103S, U22-ch87, adc2

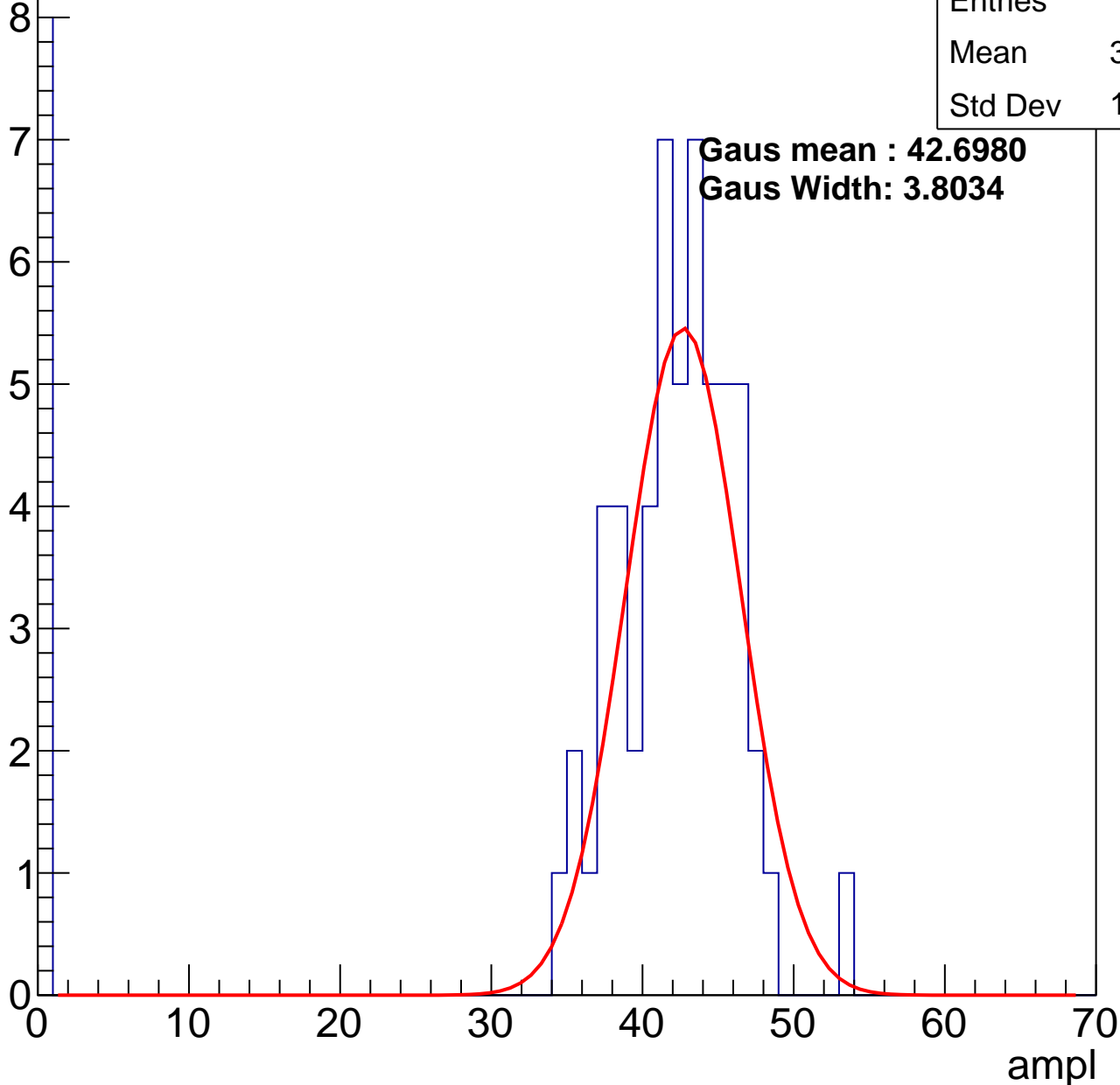
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	36.66
Std Dev	14.28

**Gaus mean : 42.6980**

**Gaus Width: 3.8034**

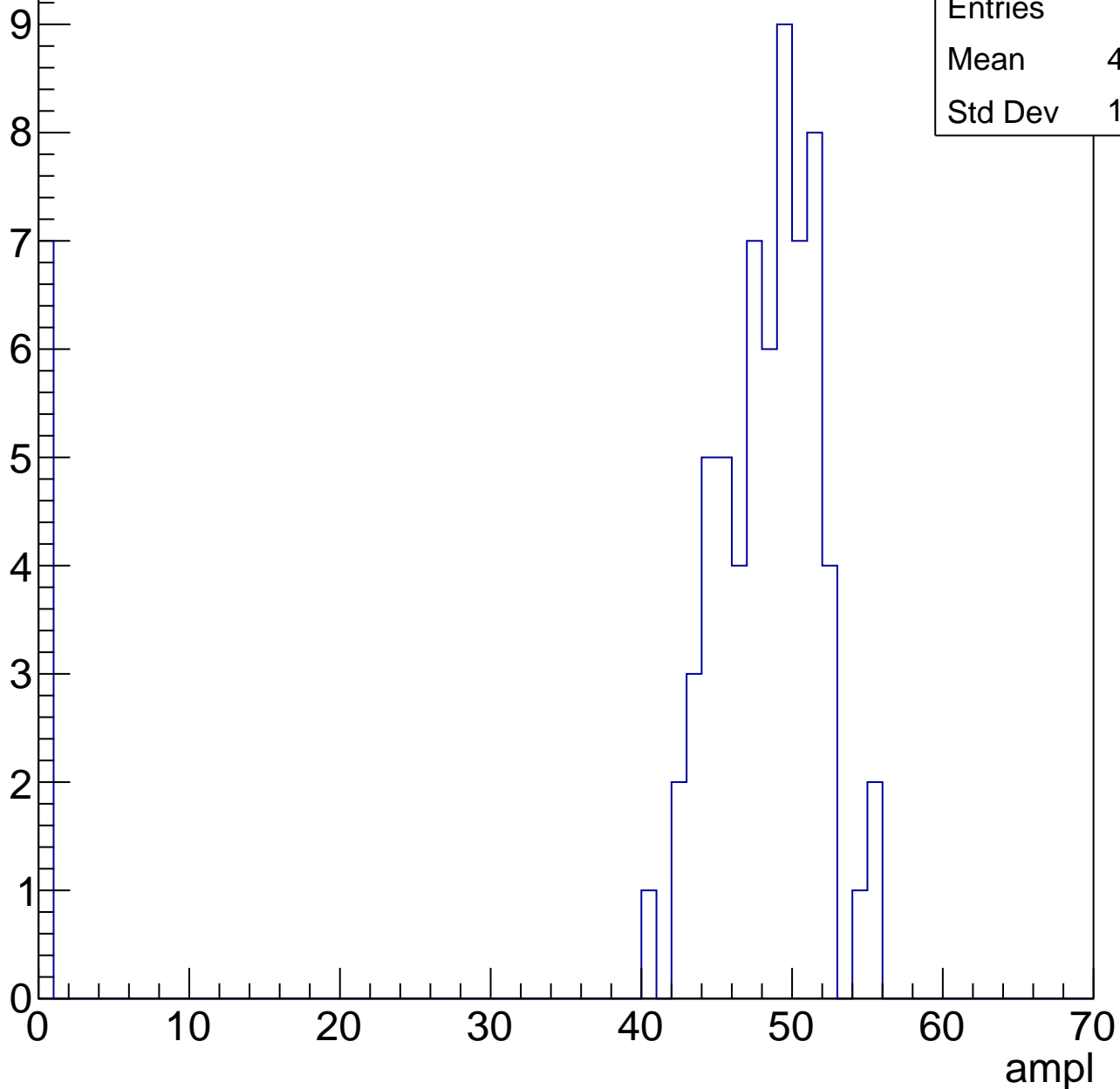


# B1L103S, U22-ch87, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	43.24
Std Dev	14.62

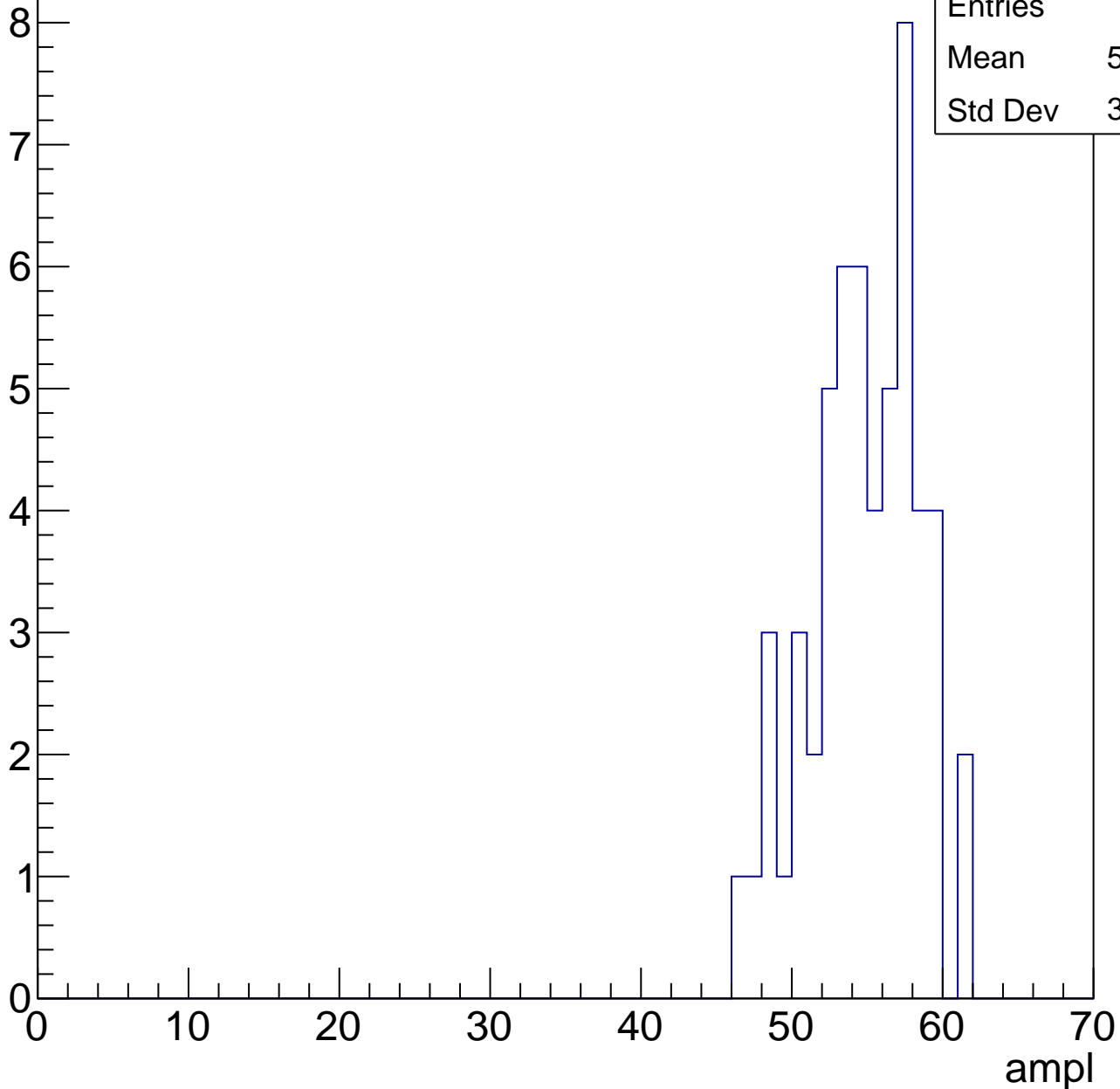


# B1L103S, U22-ch87, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

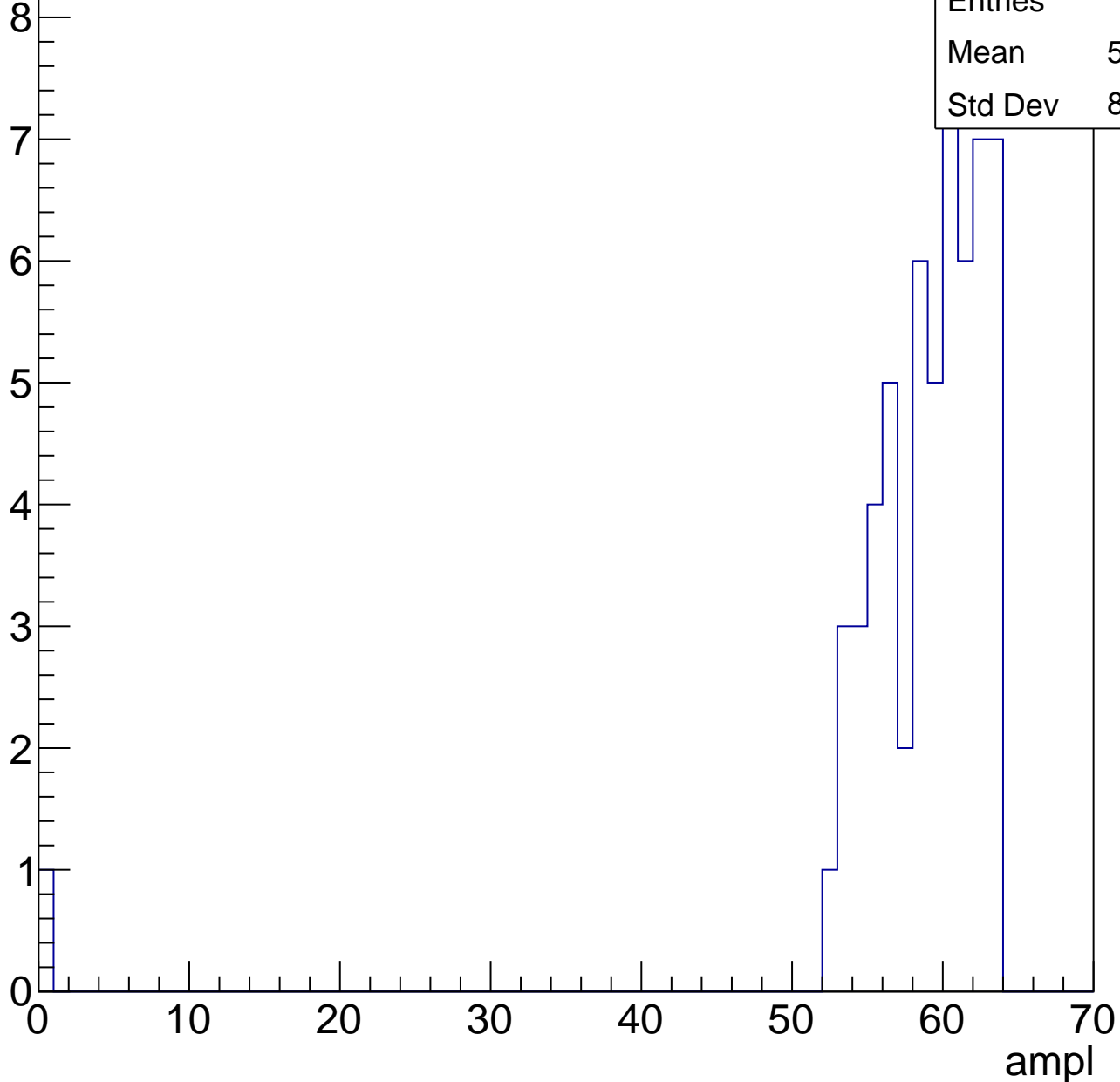
Entries	55
Mean	54.29
Std Dev	3.545



# B1L103S, U22-ch87, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

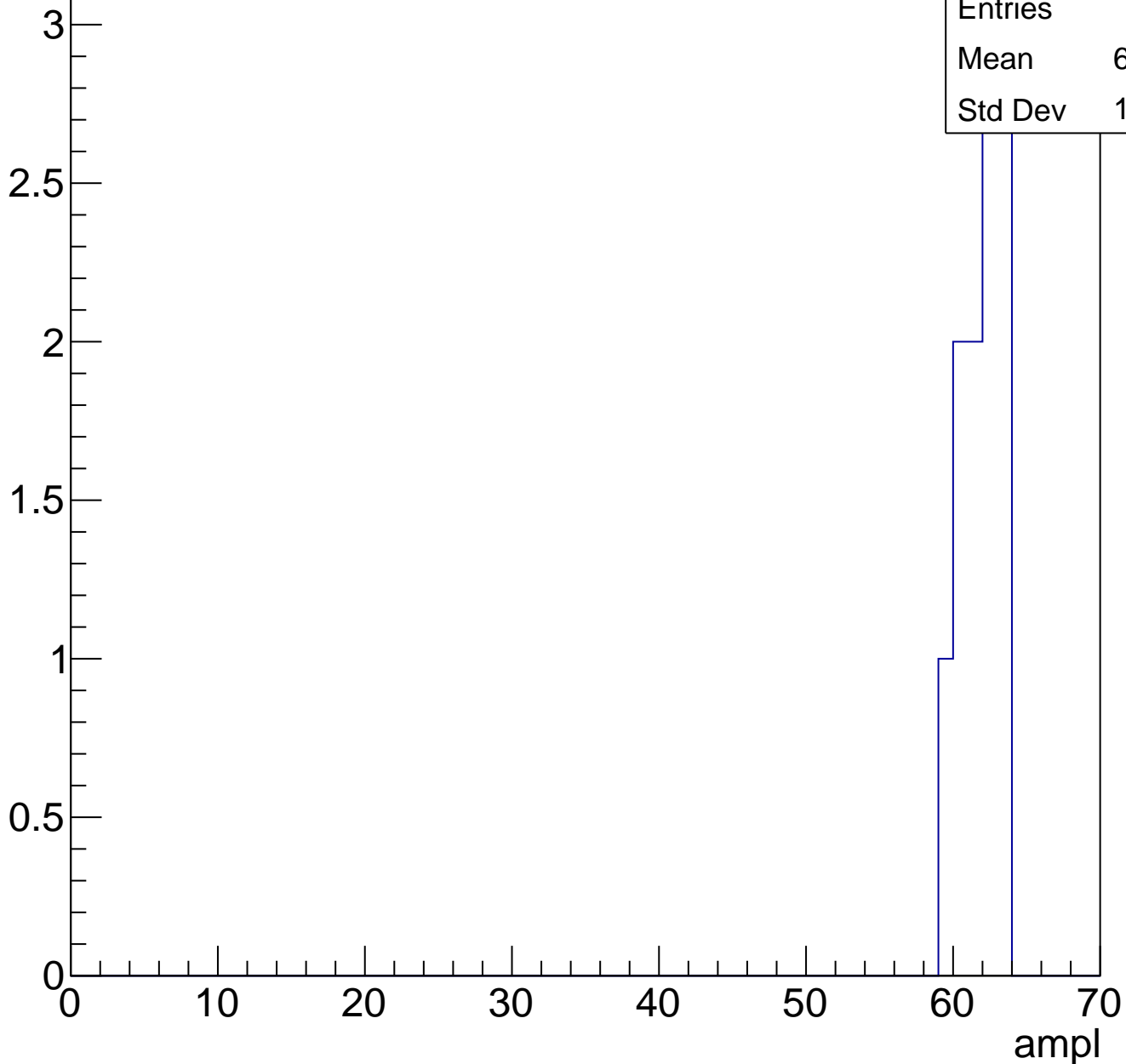


Entries	58
Mean	57.78
Std Dev	8.259

# B1L103S, U22-ch87, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch87, adc7

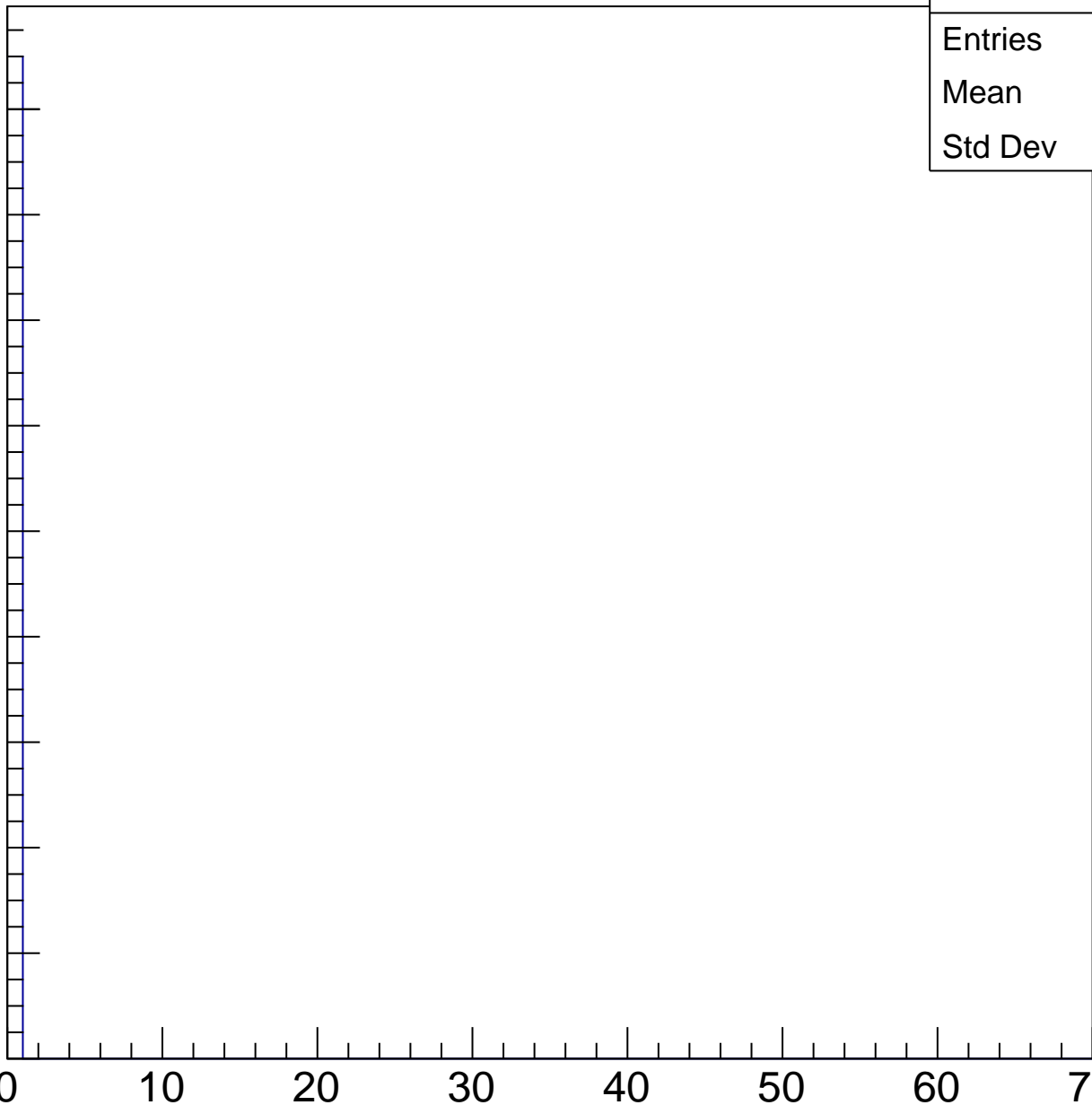
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl



# B1L103S, U22-ch88, adc0

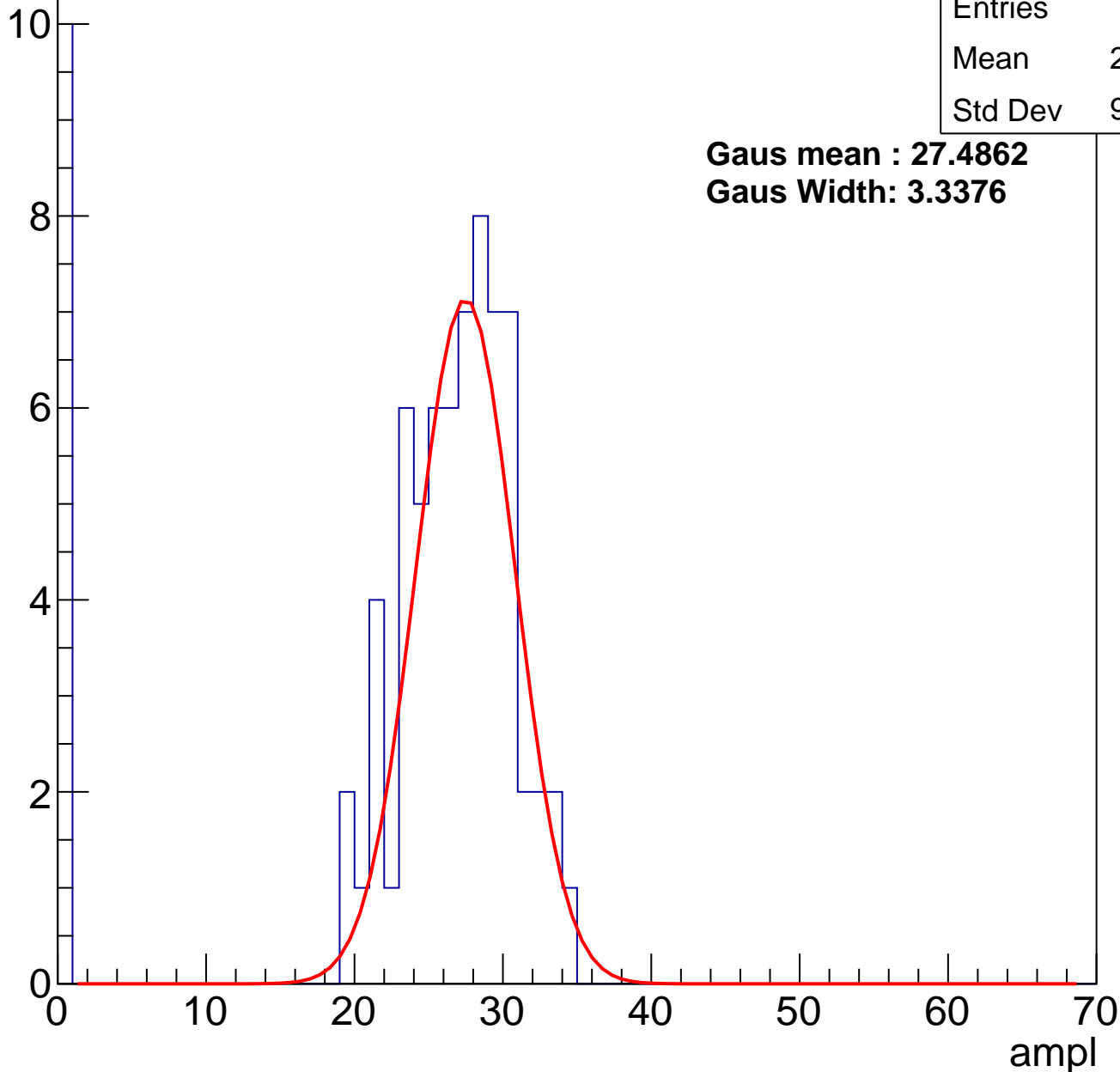
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	23.12
Std Dev	9.499

**Gaus mean : 27.4862**

**Gaus Width: 3.3376**

Entry



# B1L103S, U22-ch88, adc1

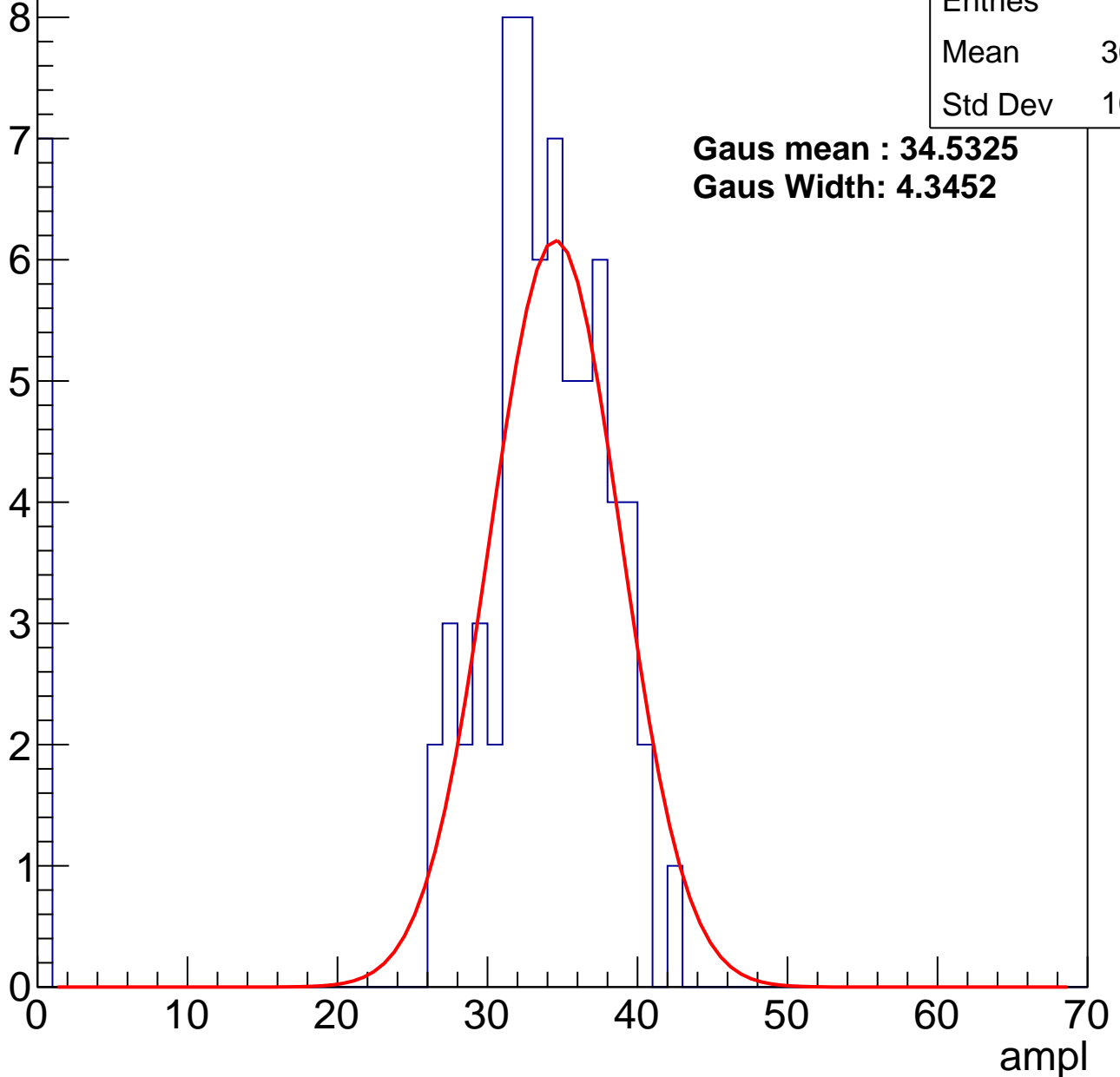
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	30.44
Std Dev	10.38

**Gaus mean : 34.5325**

**Gaus Width: 4.3452**



# B1L103S, U22-ch88, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	34.23
Std Dev	14.47

**Gaus mean : 39.8057**

**Gaus Width: 4.3522**

Entry

10

8

6

4

2

0

0

10

20

30

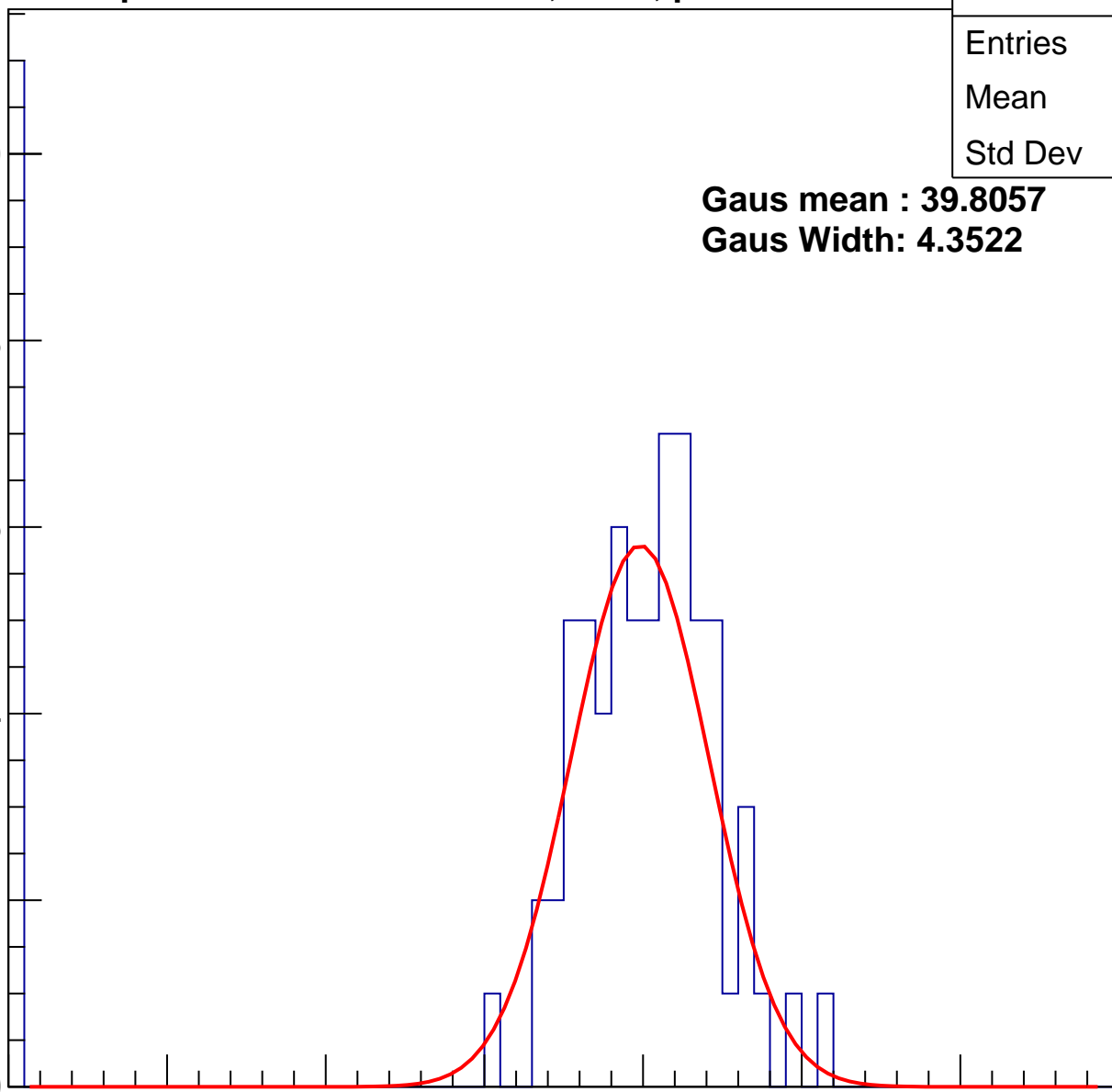
40

50

60

70

ampl

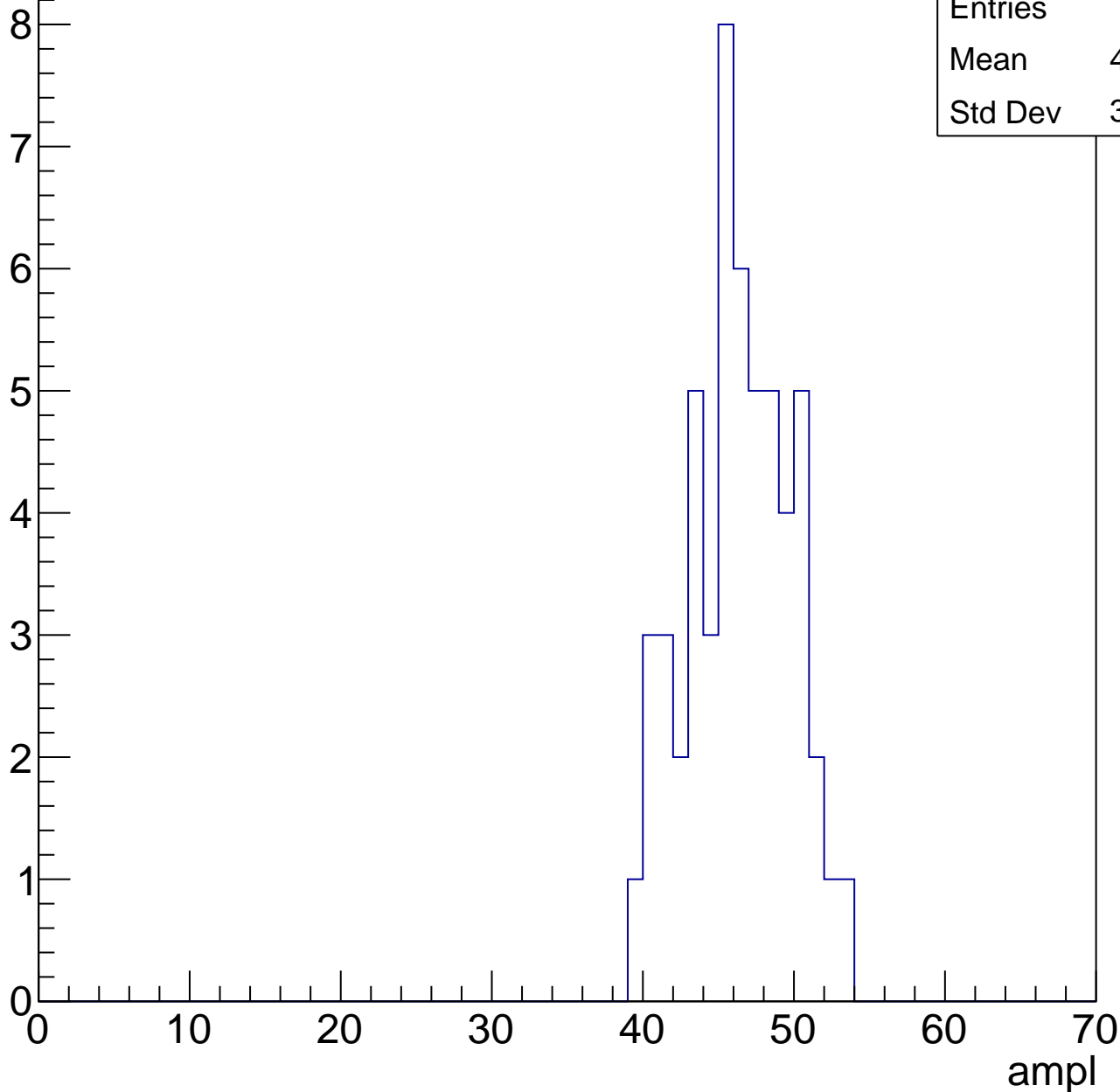


# B1L103S, U22-ch88, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	45.87
Std Dev	3.345

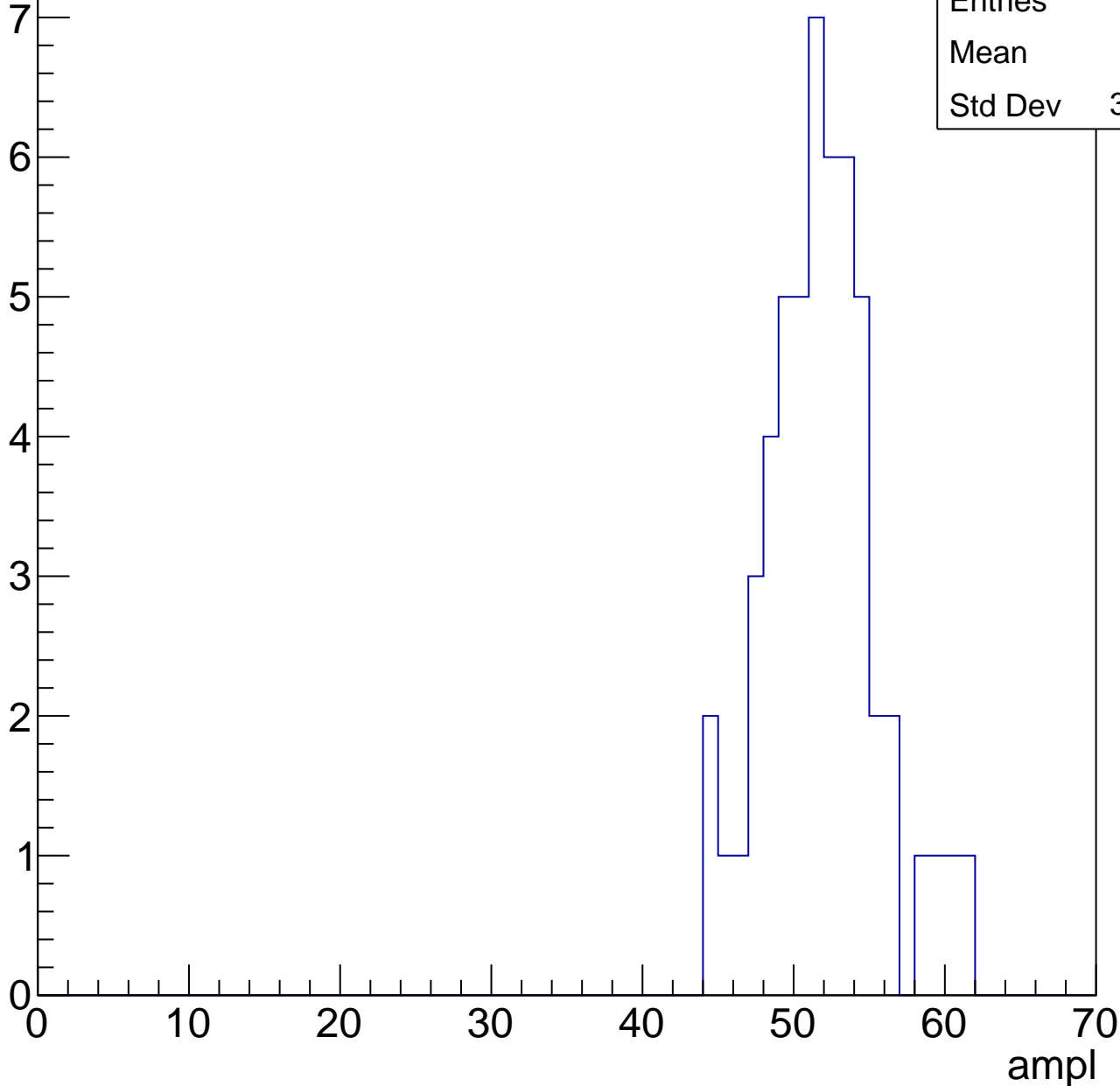


# B1L103S, U22-ch88, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	51.4
Std Dev	3.662

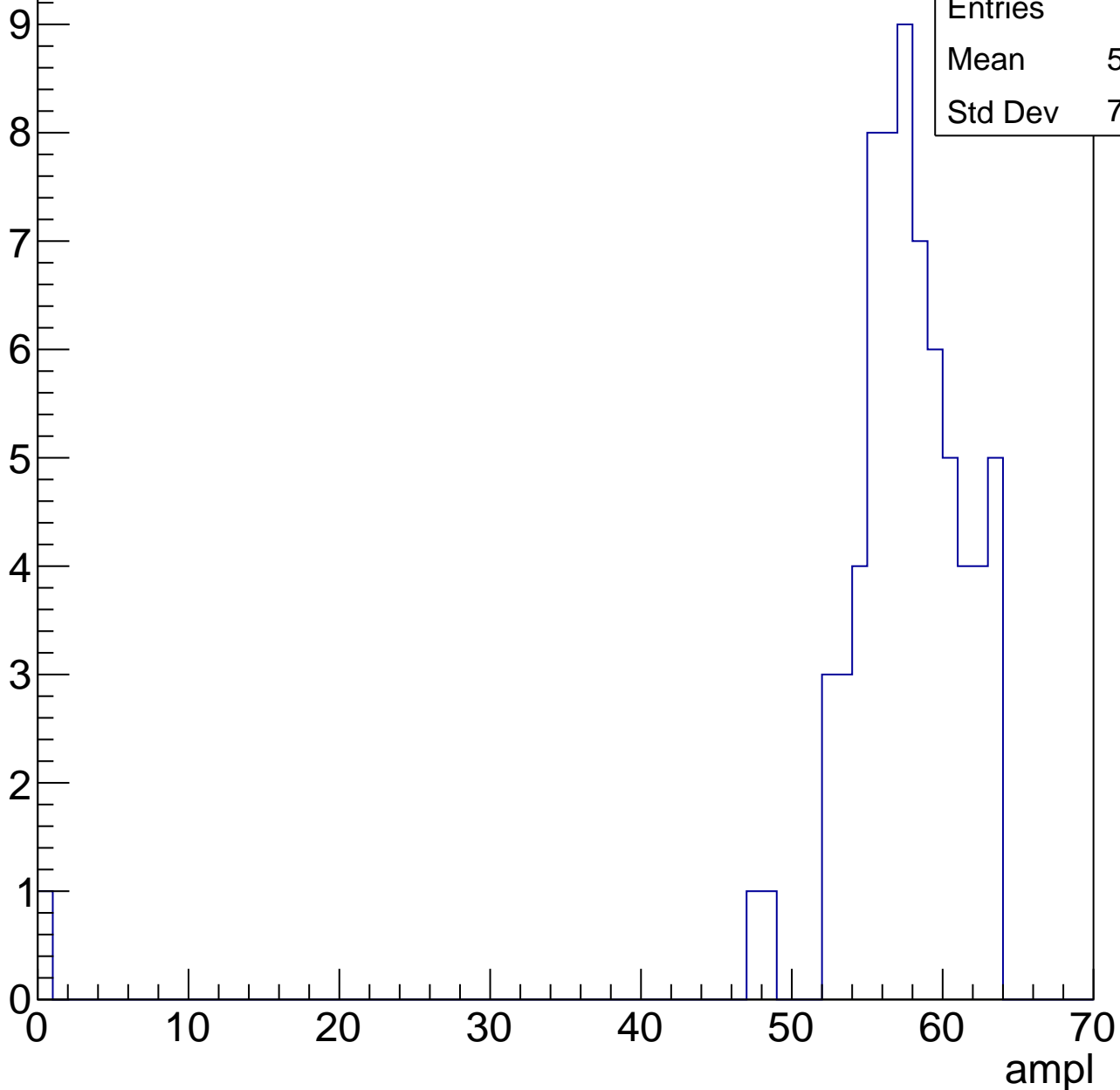


# B1L103S, U22-ch88, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	56.43
Std Dev	7.639

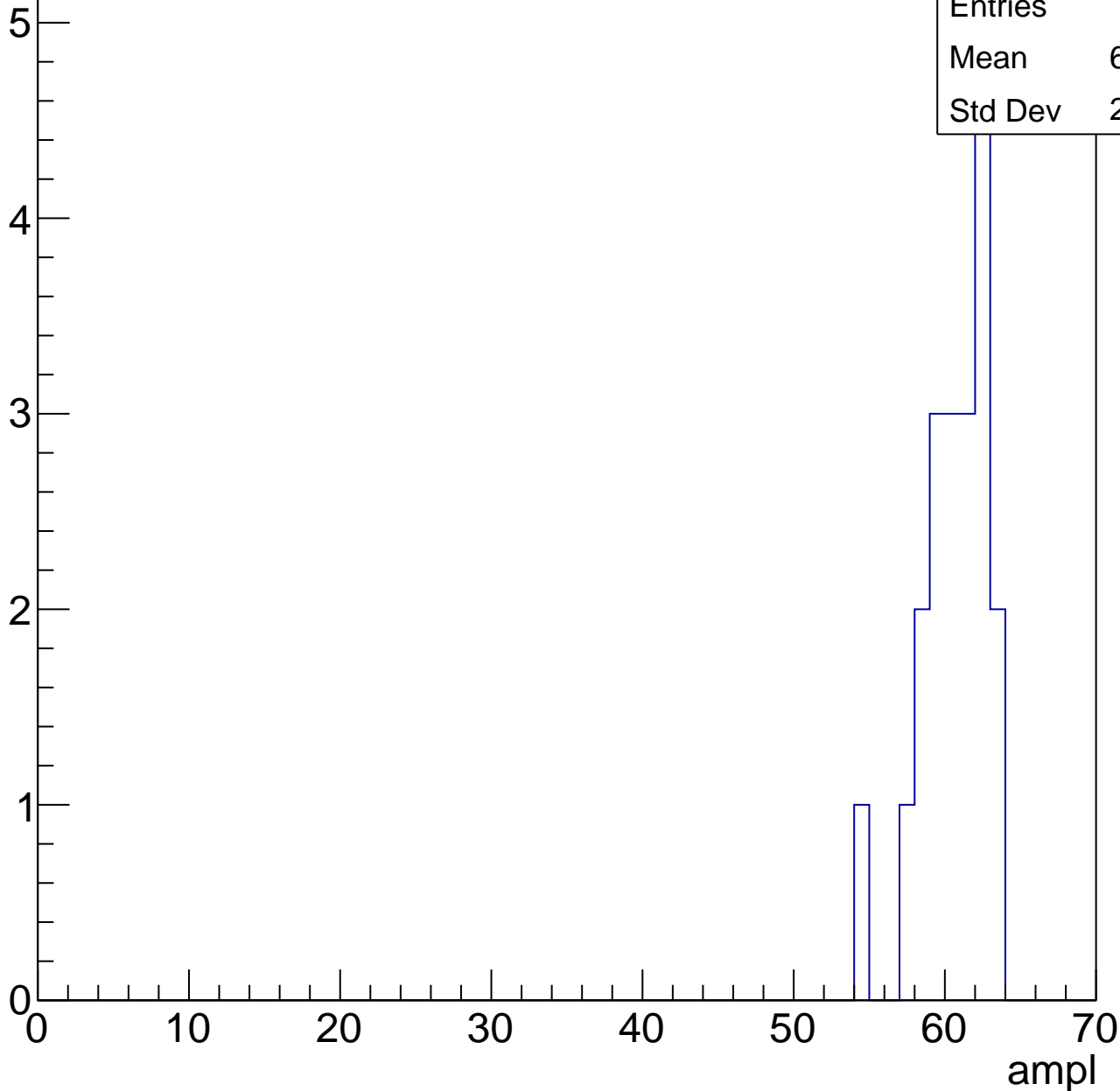


# B1L103S, U22-ch88, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	60.15
Std Dev	2.197





# B1L103S, U22-ch88, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

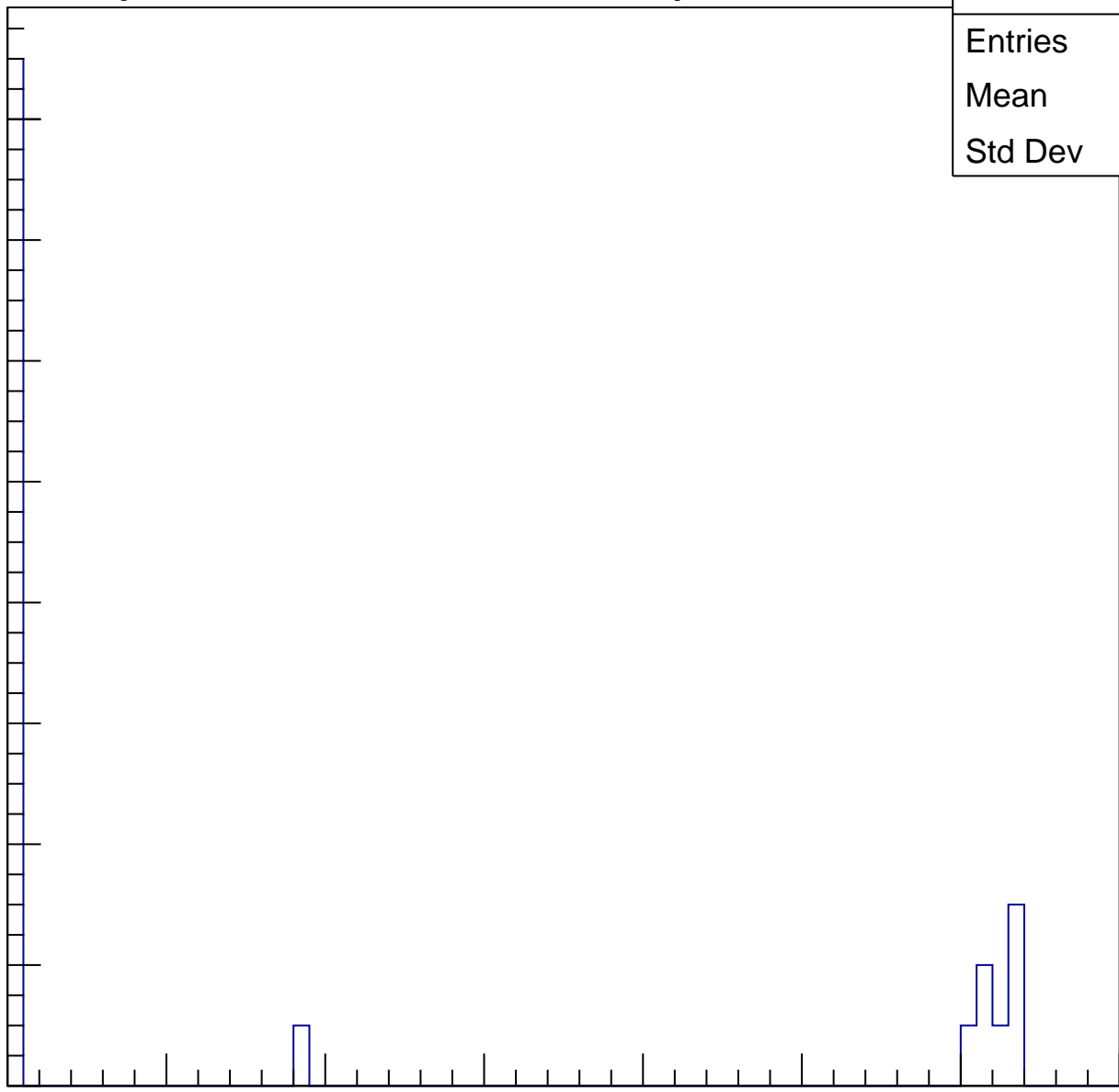
Entries	25
Mean	18.04
Std Dev	27.55

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch89, adc0

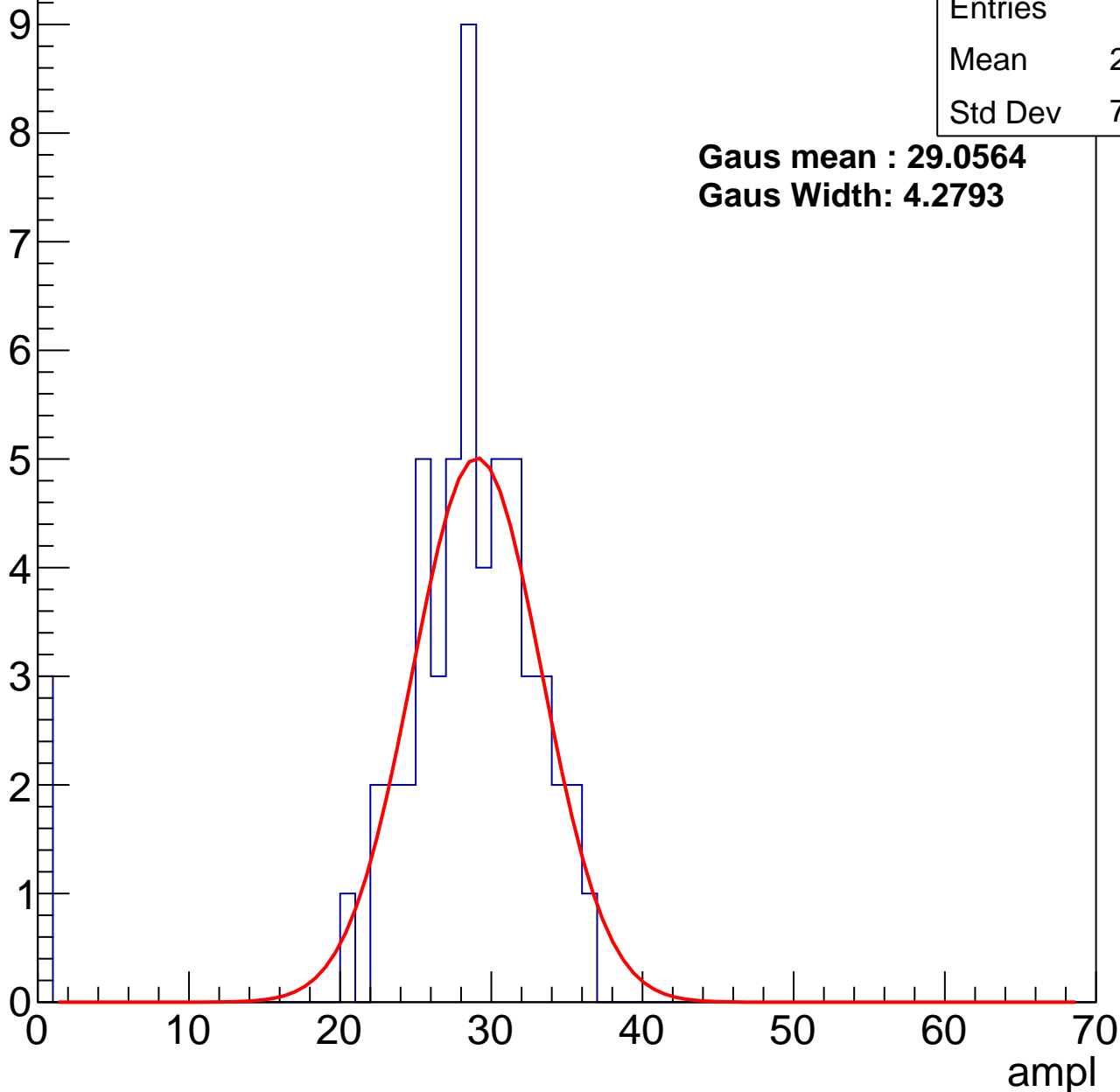
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	26.98
Std Dev	7.254

**Gaus mean : 29.0564**

**Gaus Width: 4.2793**



# B1L103S, U22-ch89, adc1

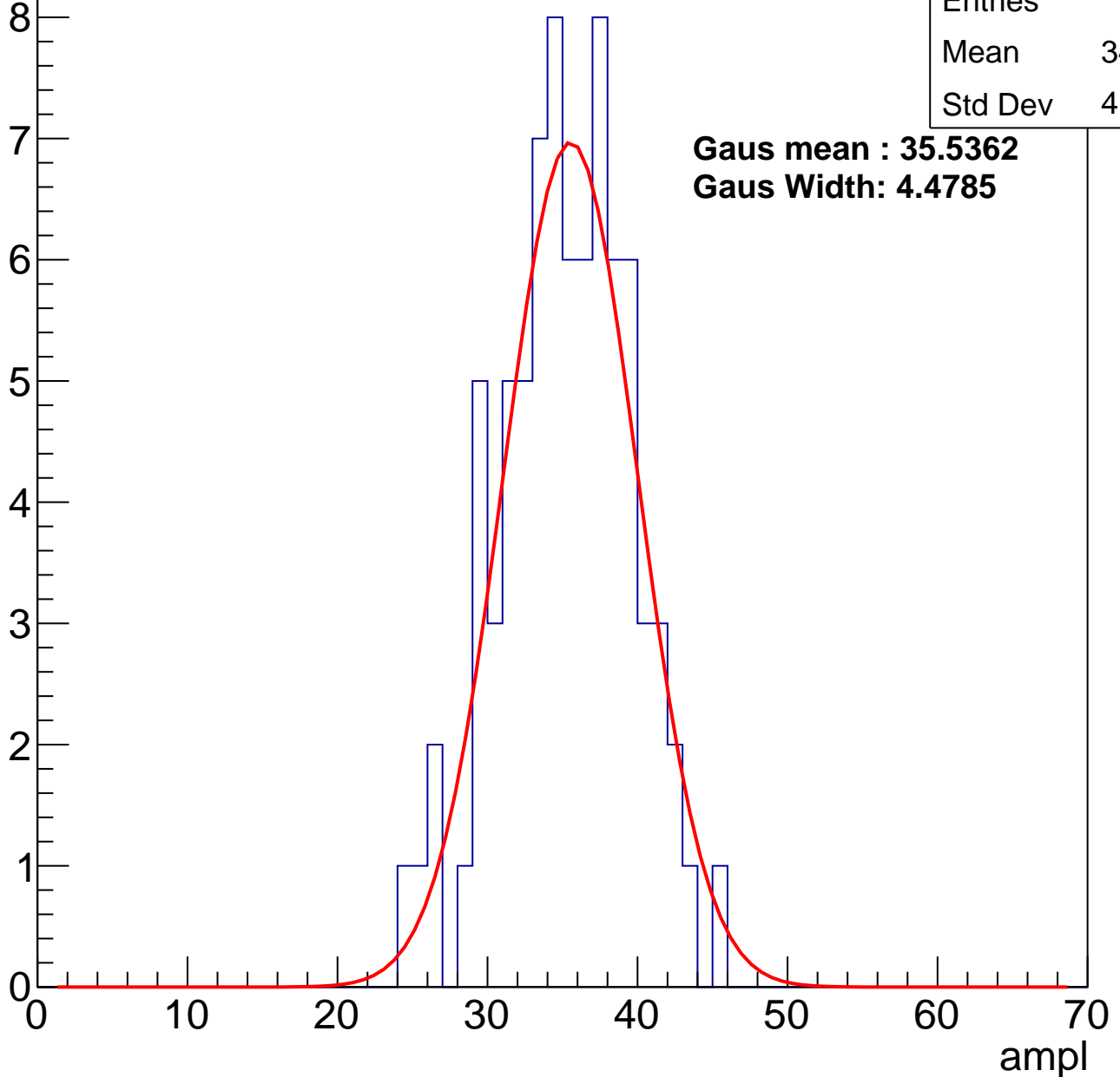
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	80
Mean	34.76
Std Dev	4.296

**Gaus mean : 35.5362**

**Gaus Width: 4.4785**



# B1L103S, U22-ch89, adc2

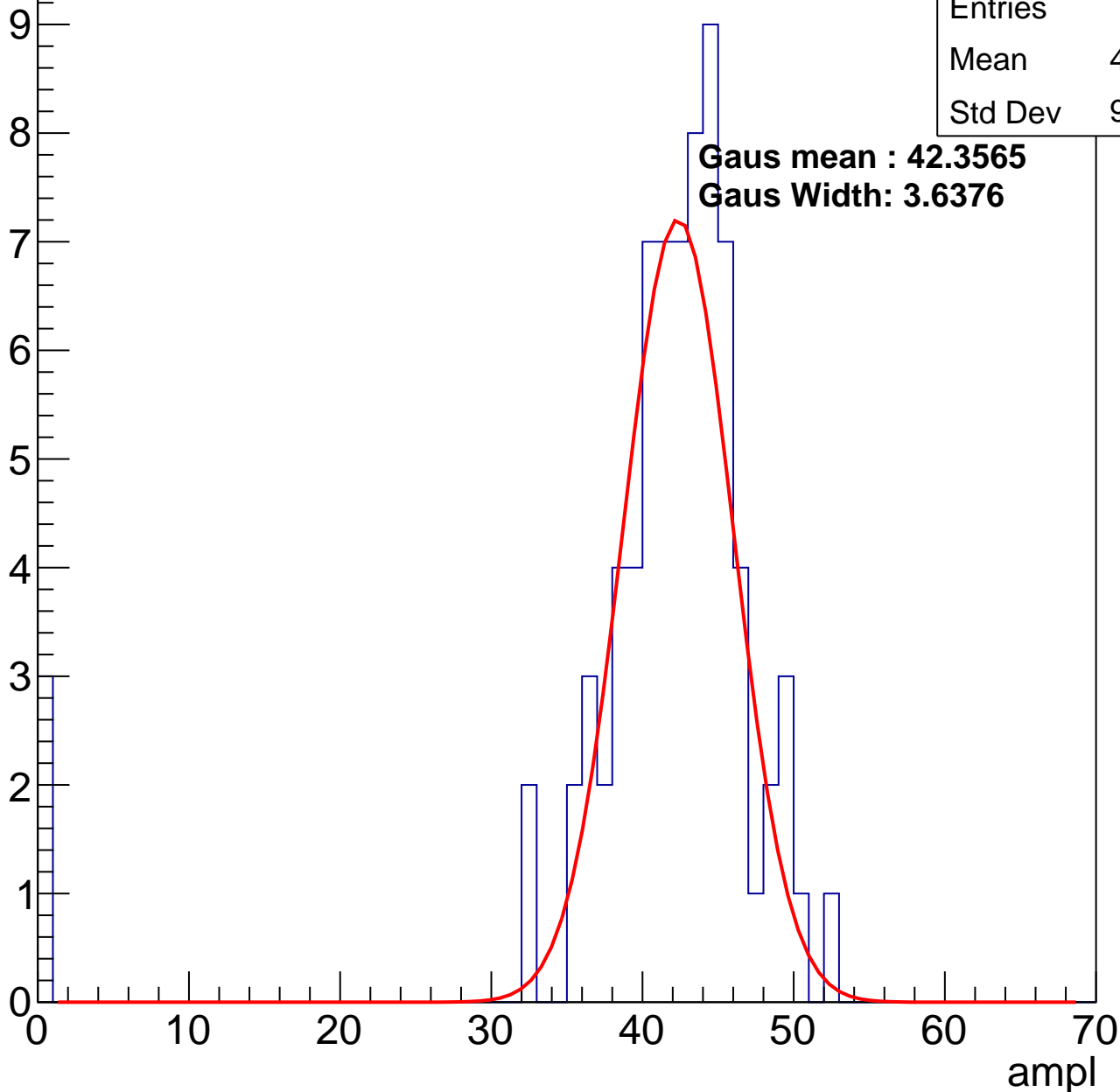
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	77
Mean	40.47
Std Dev	9.032

**Gaus mean : 42.3565**

**Gaus Width: 3.6376**

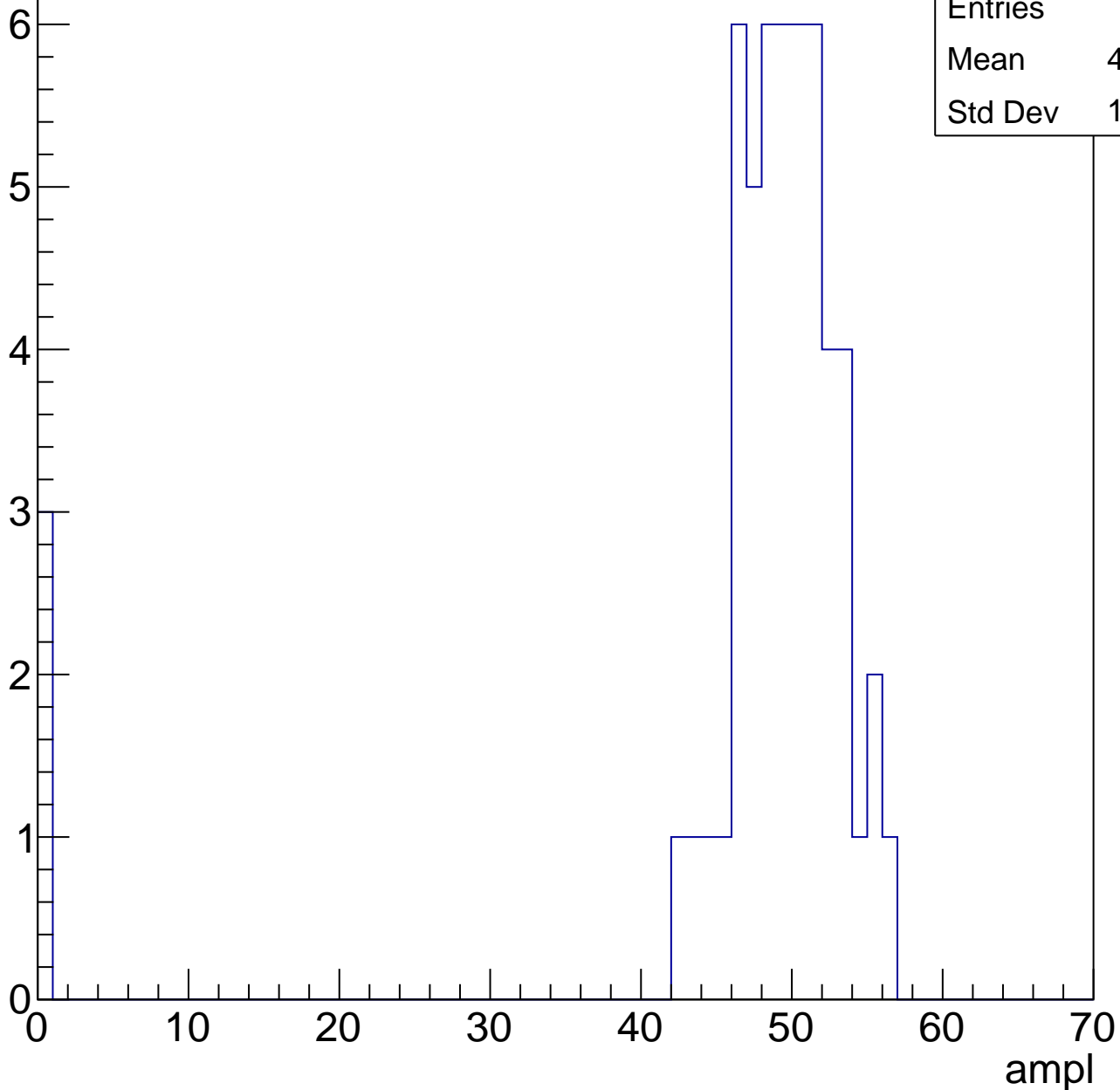


# B1L103S, U22-ch89, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	46.54
Std Dev	11.67

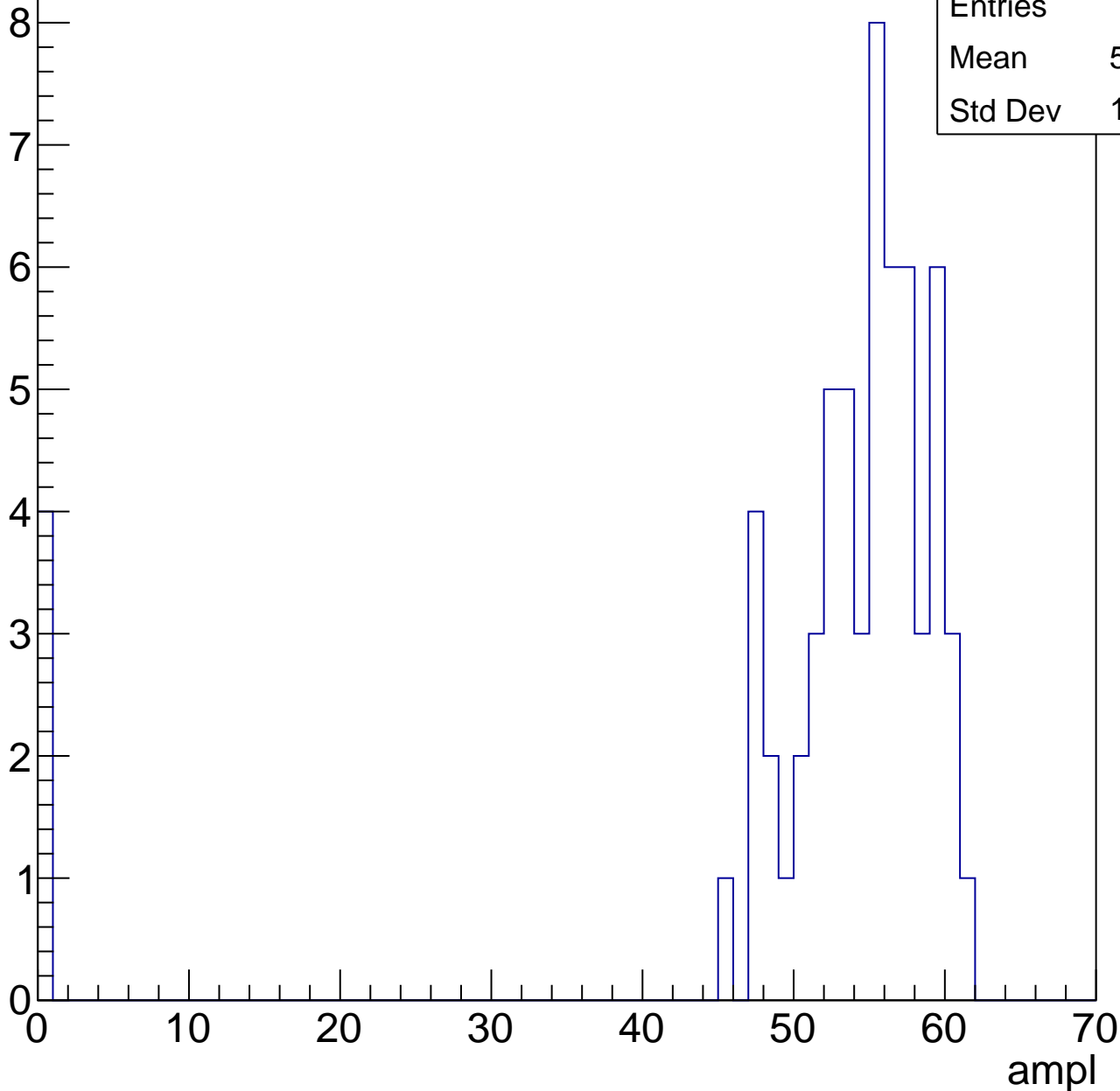


# B1L103S, U22-ch89, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	50.87
Std Dev	13.77

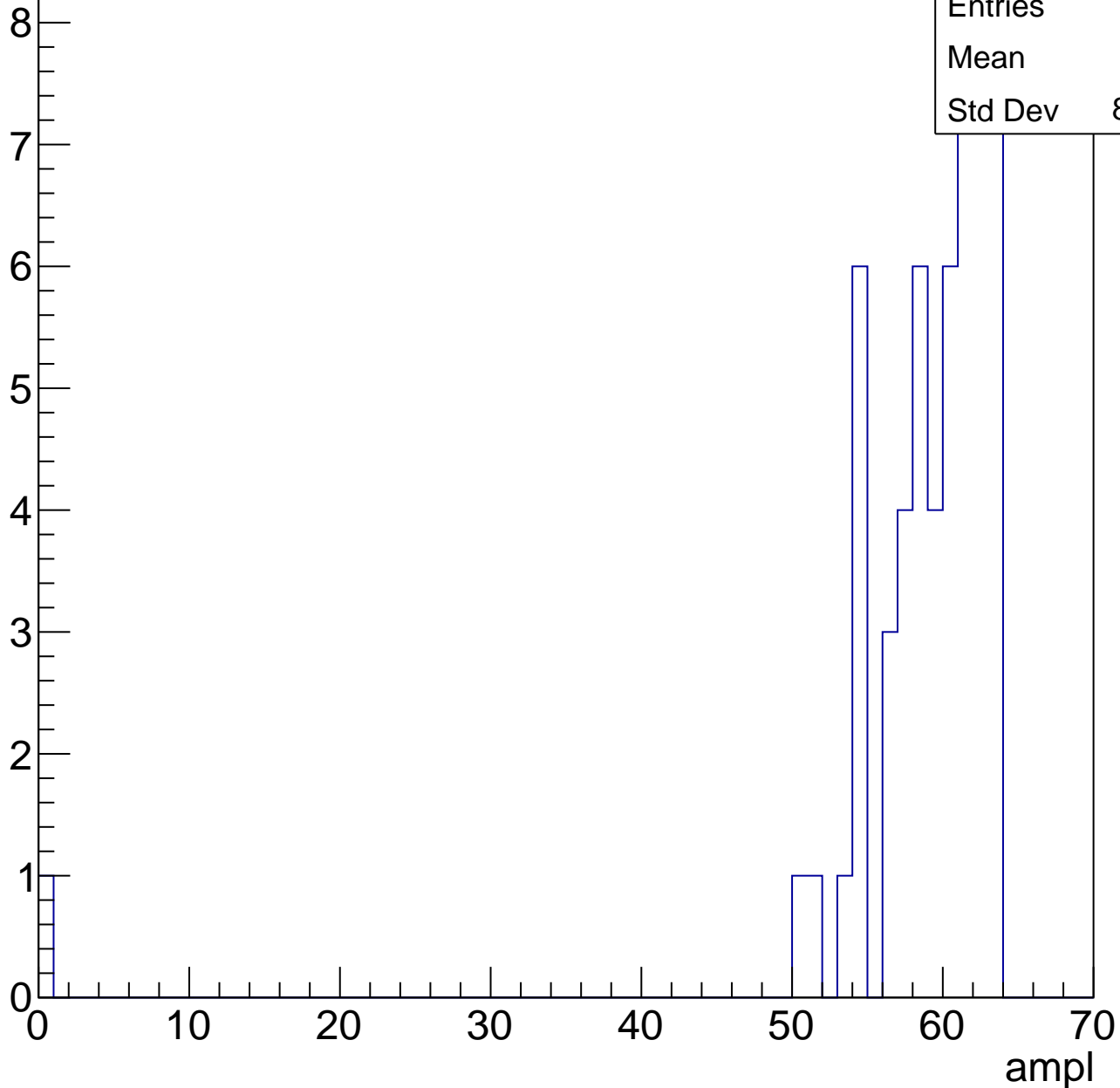


# B1L103S, U22-ch89, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

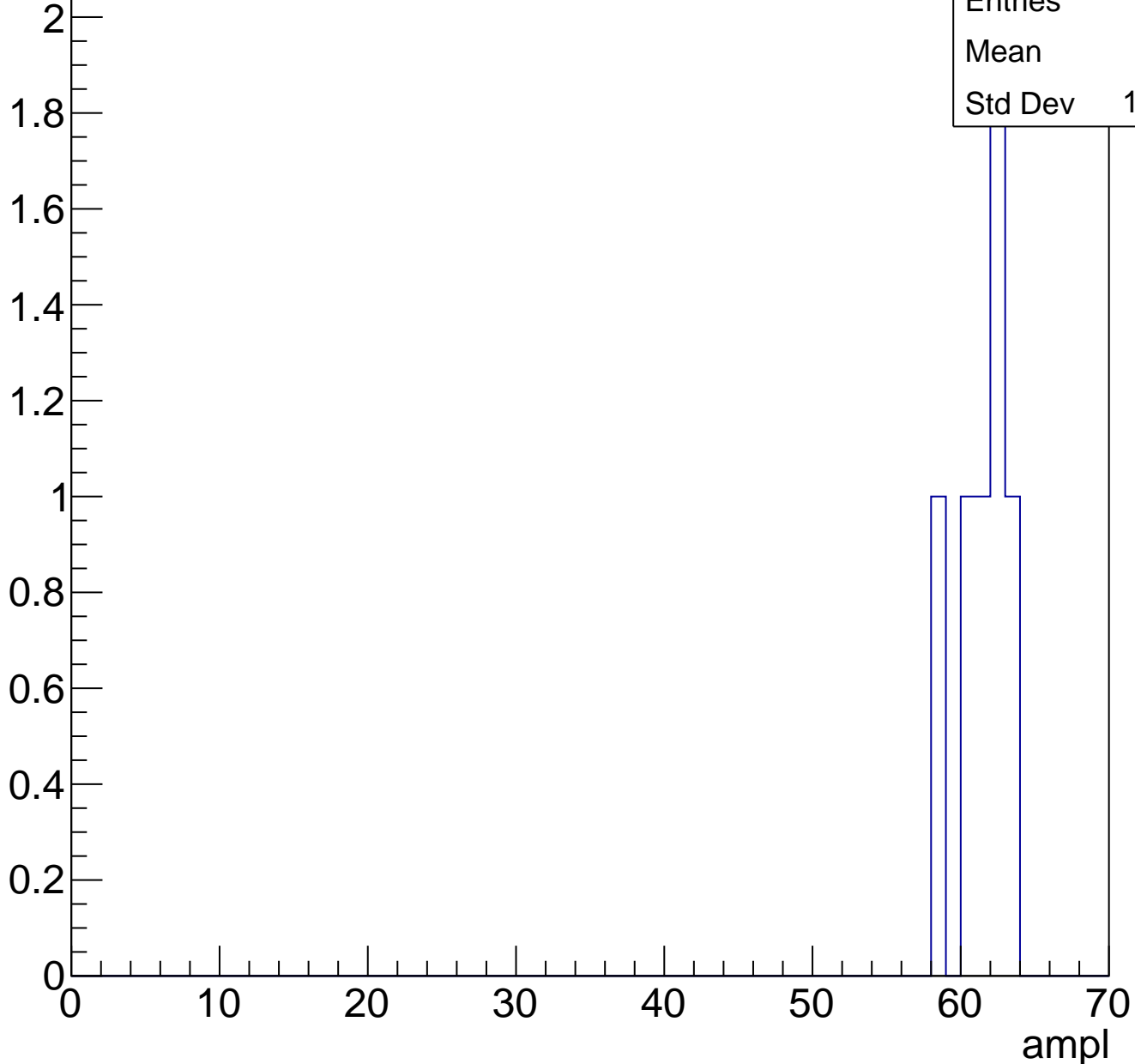
Entries	57
Mean	58
Std Dev	8.421



# B1L103S, U22-ch89, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



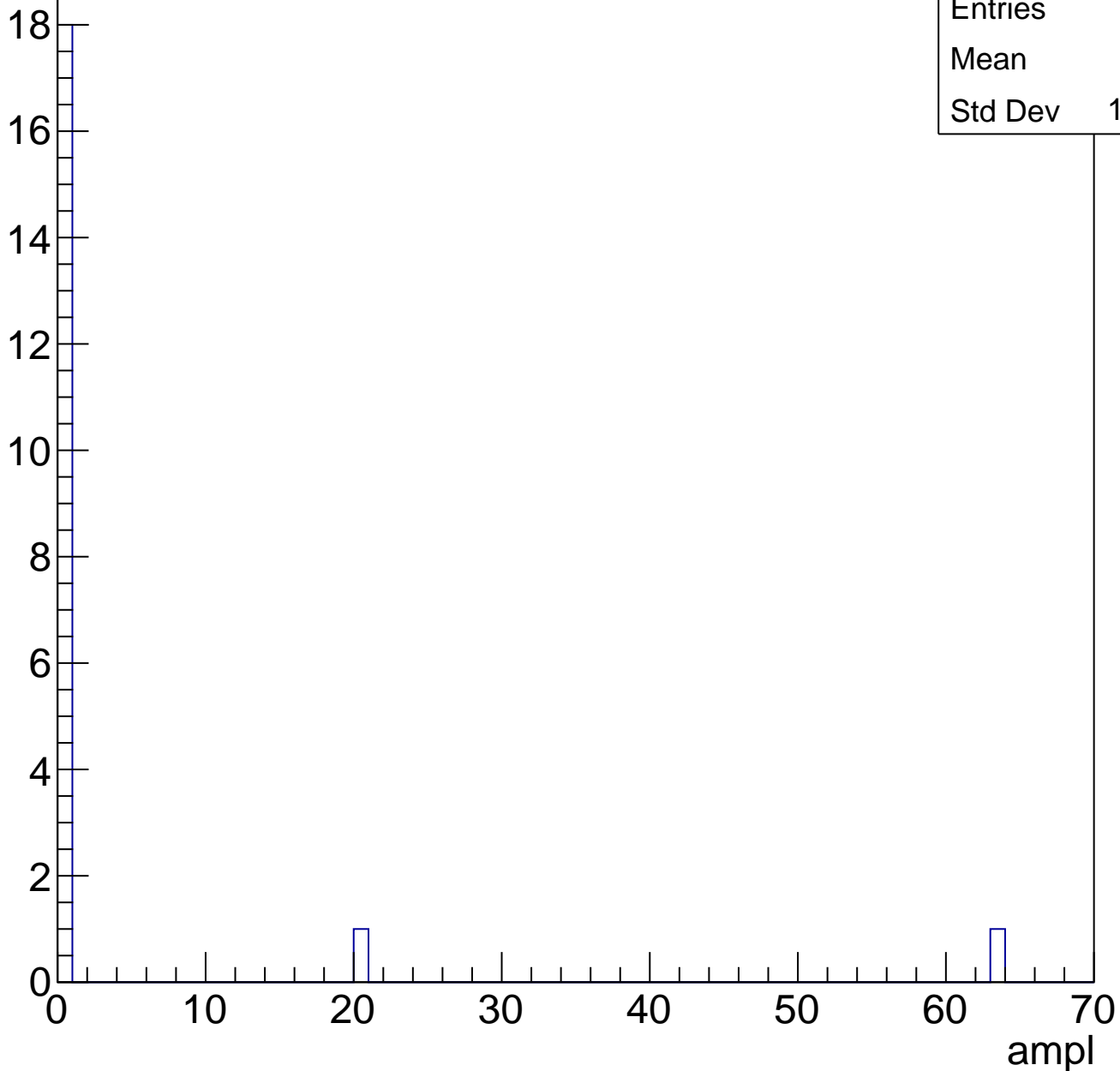


# B1L103S, U22-ch89, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	4.15
Std Dev	14.19

Entry



# B1L103S, U22-ch90, adc0

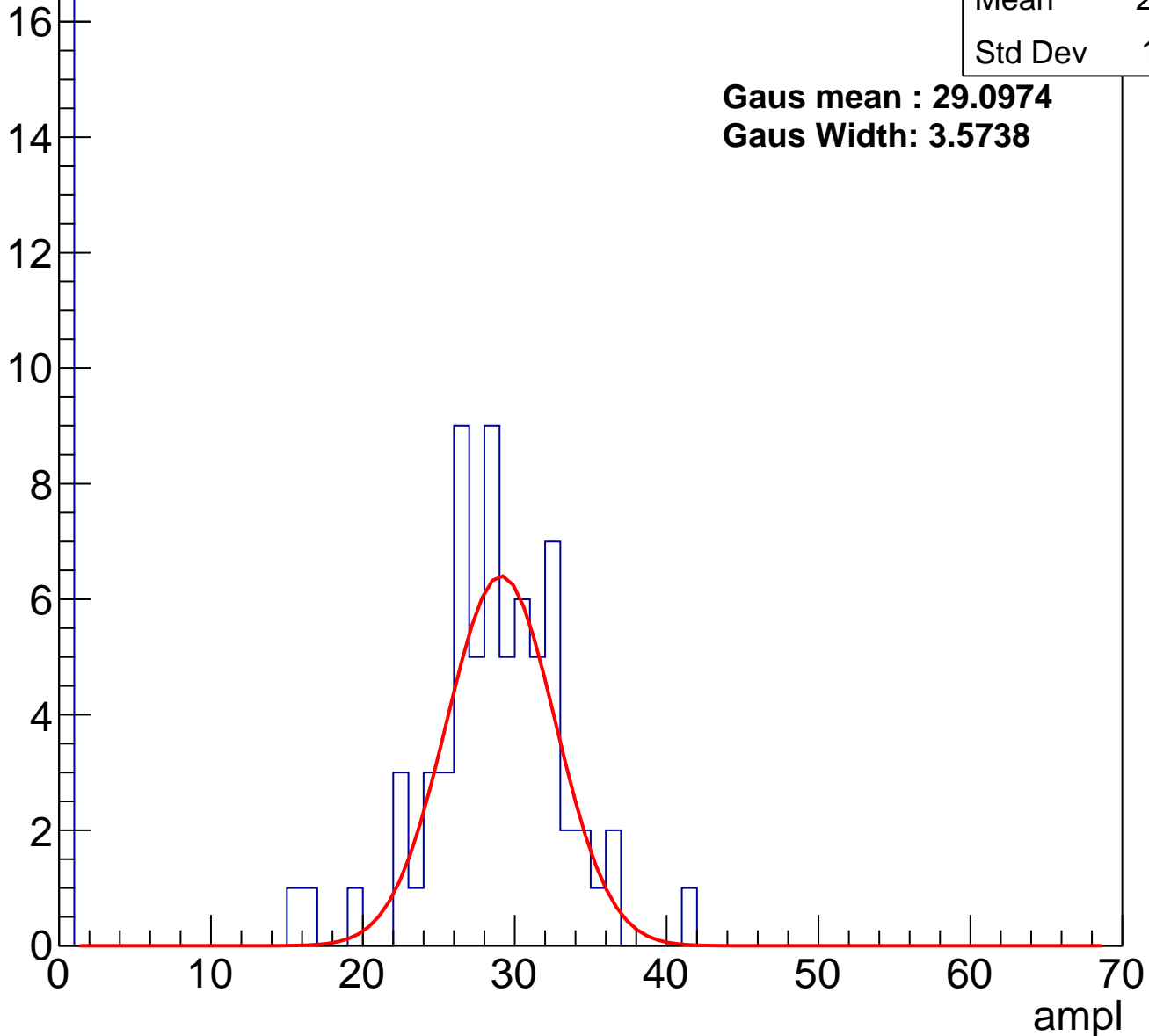
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	22.54
Std Dev	12.01

**Gaus mean : 29.0974**

**Gaus Width: 3.5738**

Entry



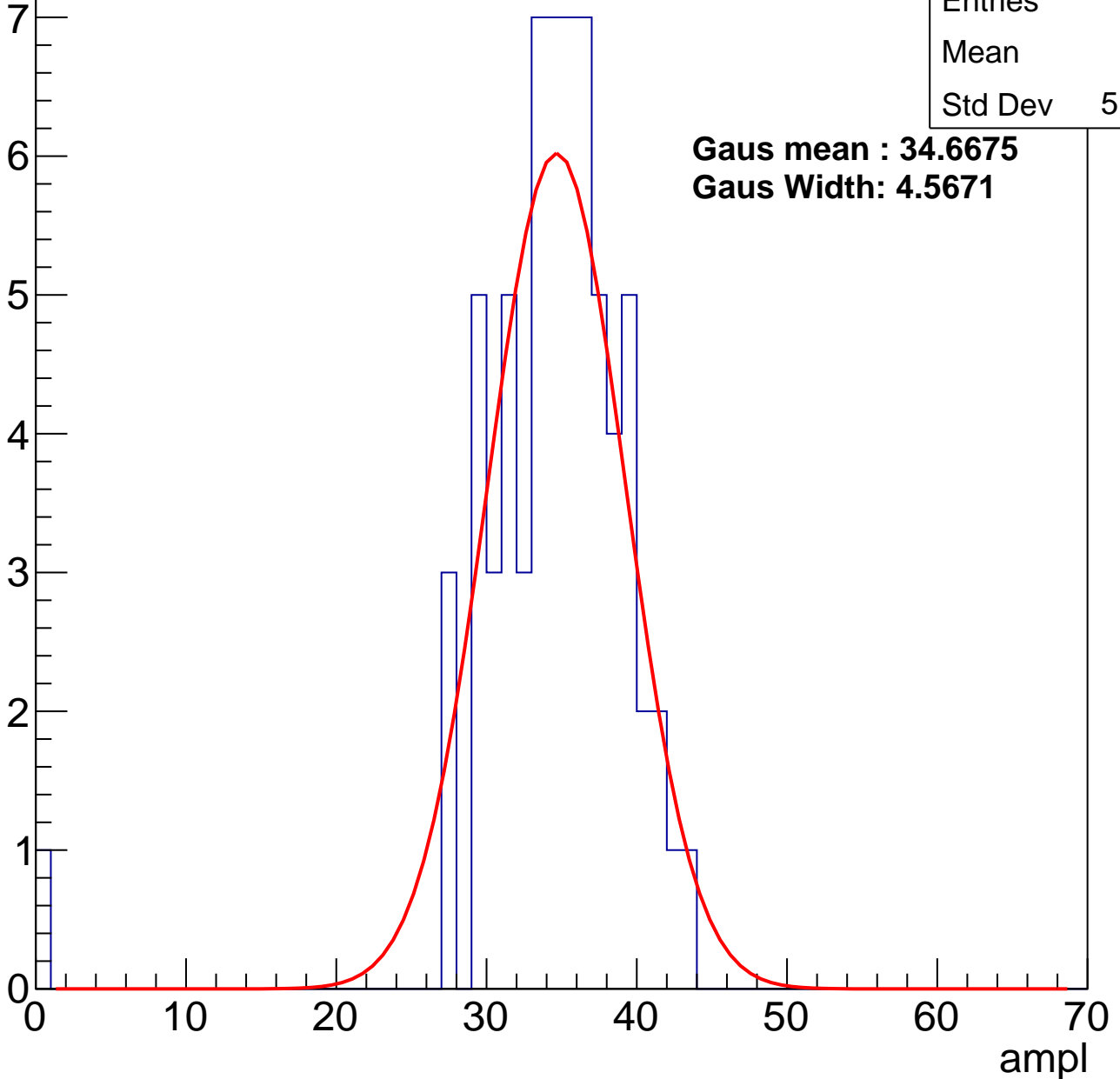
# B1L103S, U22-ch90, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34
Std Dev	5.589

**Gaus mean : 34.6675**  
**Gaus Width: 4.5671**



# B1L103S, U22-ch90, adc2

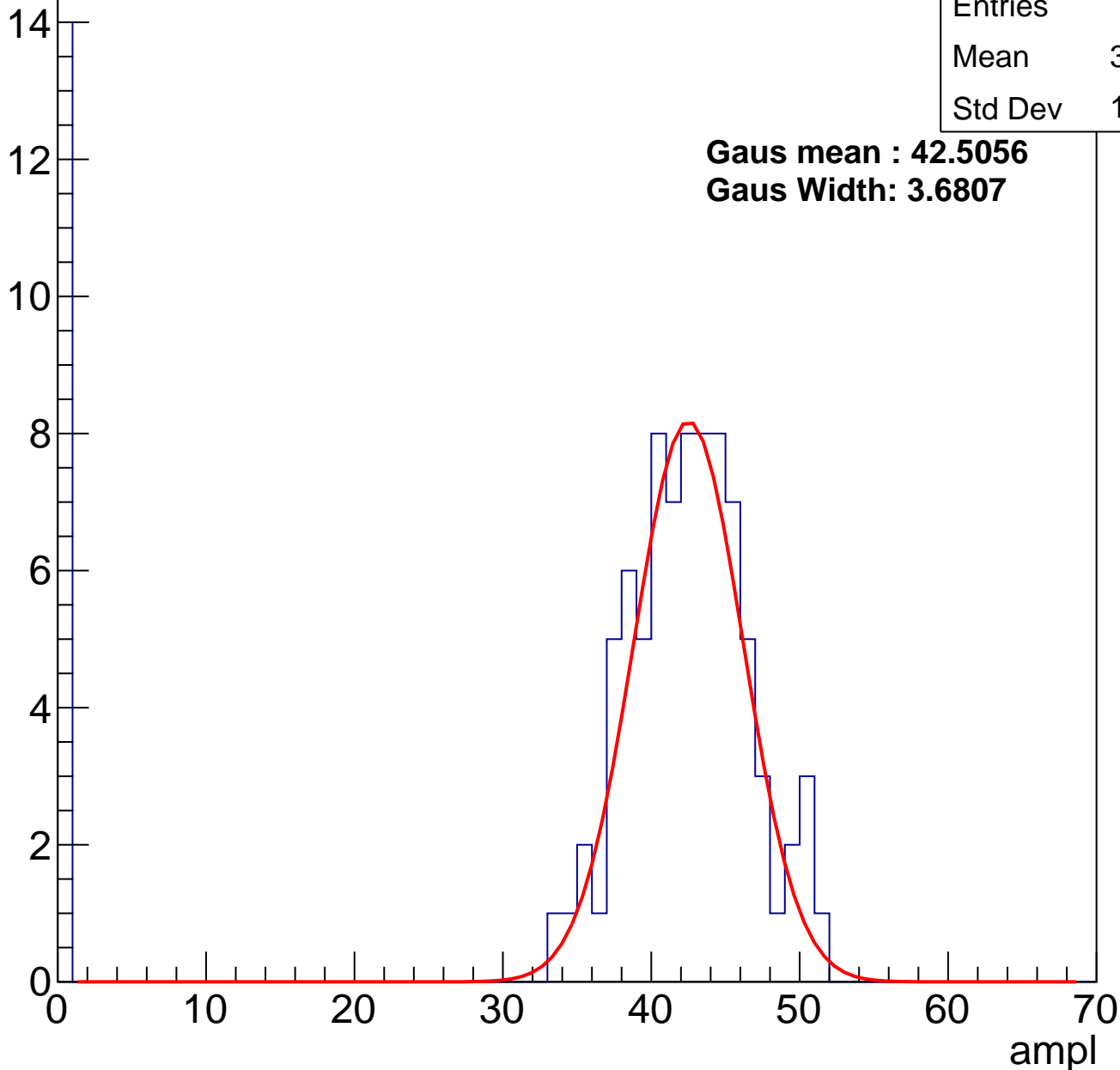
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	96
Mean	35.97
Std Dev	15.29

**Gaus mean : 42.5056**

**Gaus Width: 3.6807**

Entry

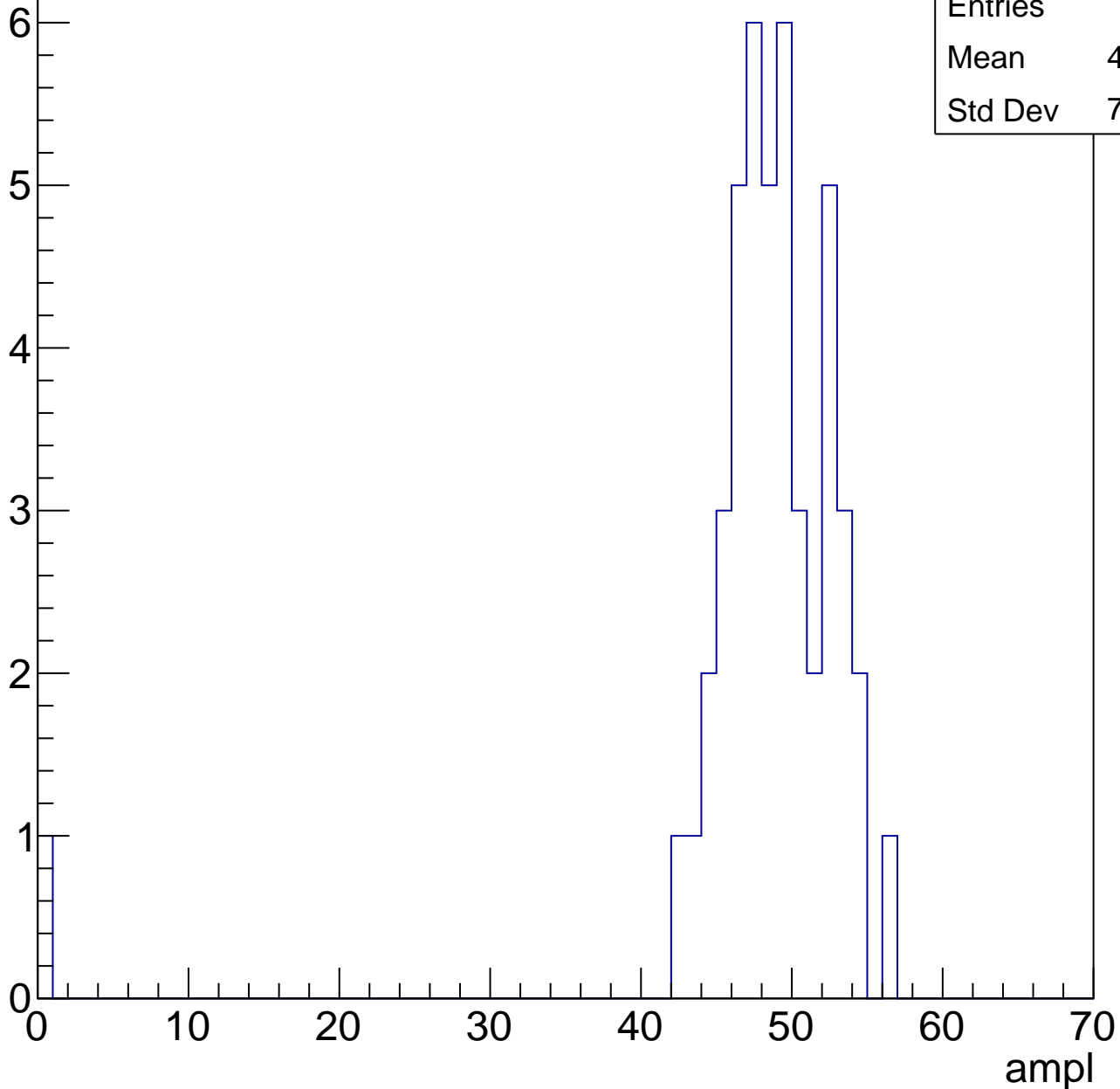


# B1L103S, U22-ch90, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	47.59
Std Dev	7.759

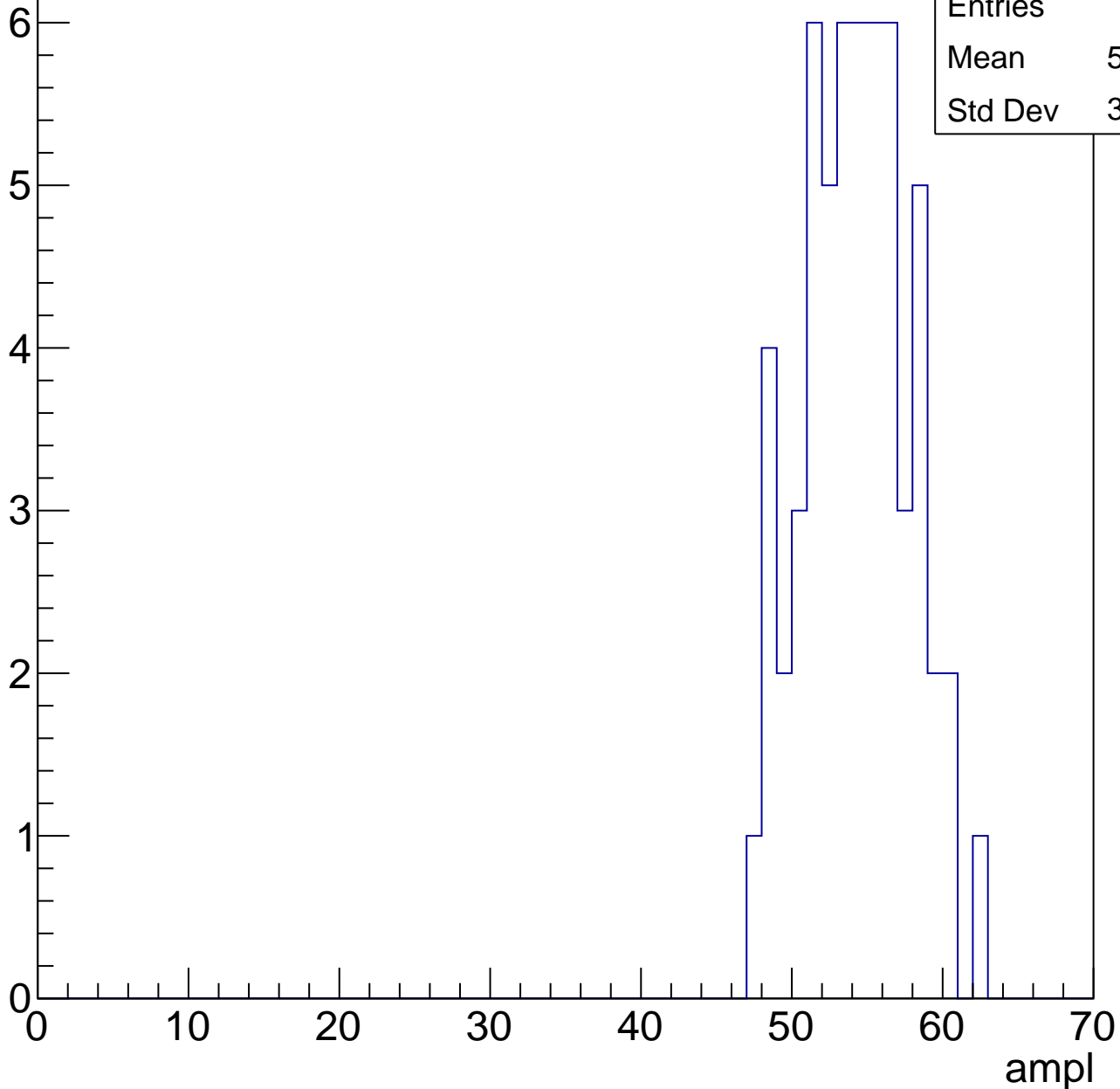


# B1L103S, U22-ch90, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	53.83
Std Dev	3.455

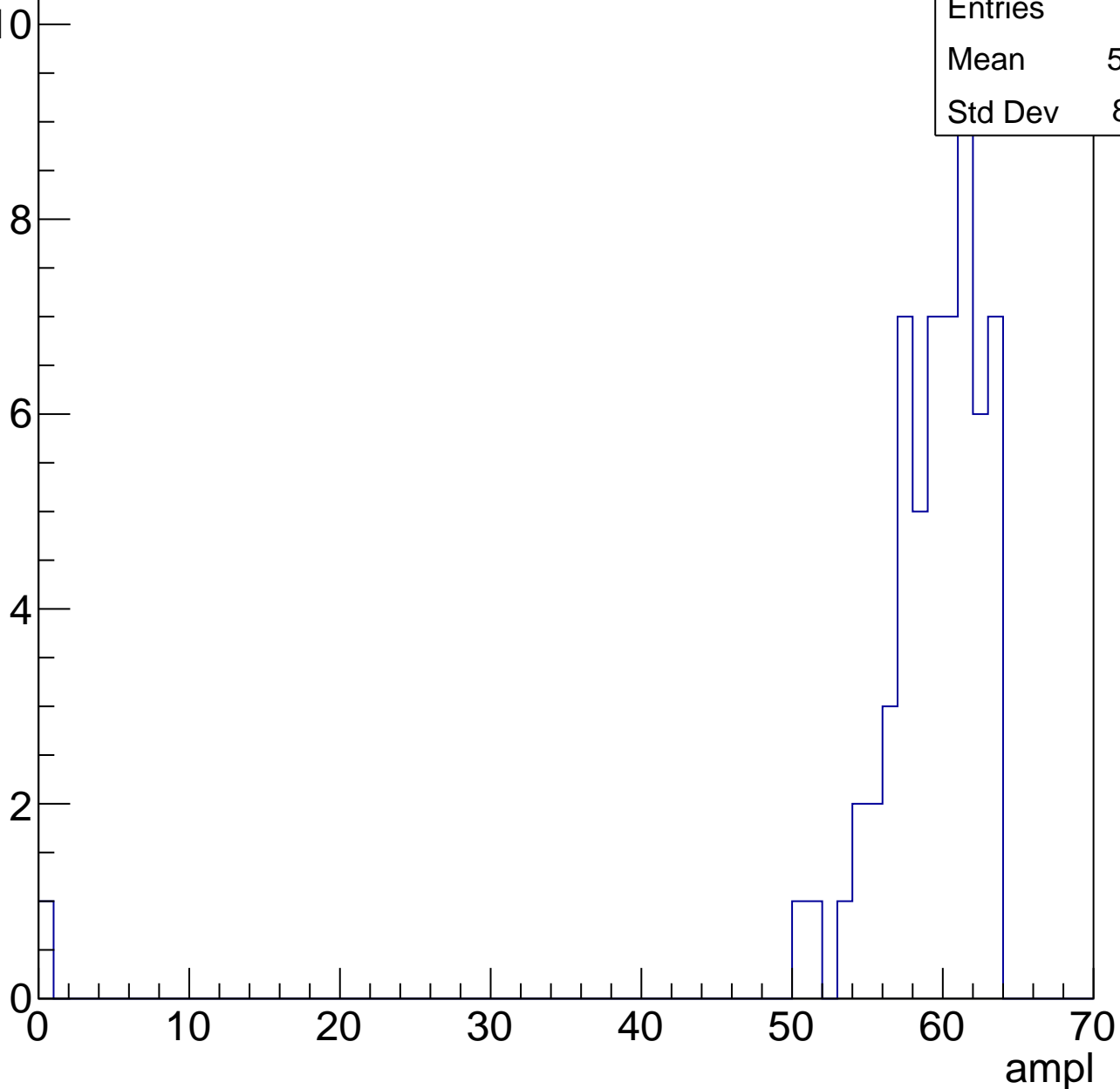


# B1L103S, U22-ch90, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

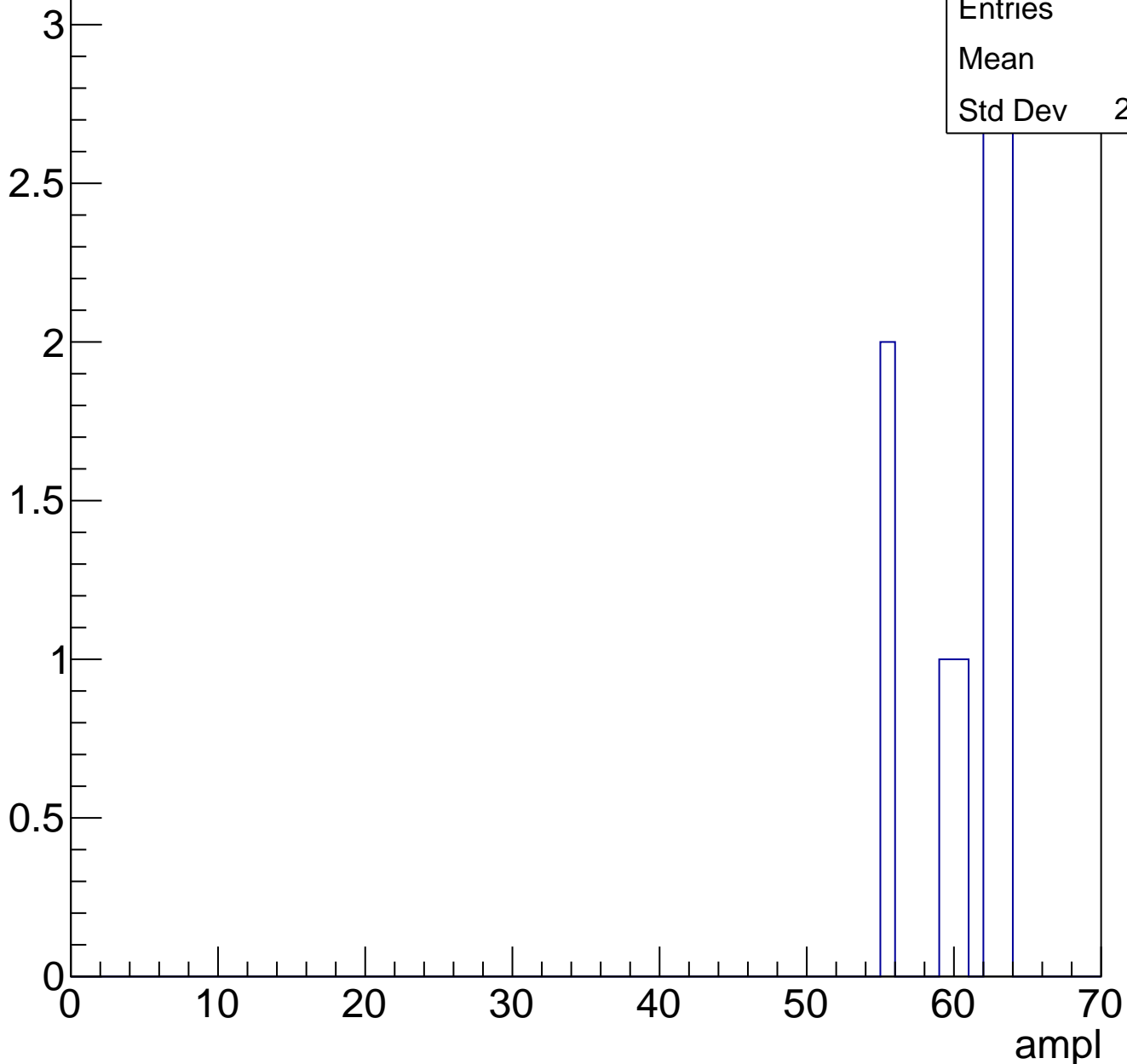
Entries	60
Mean	58.08
Std Dev	8.131



# B1L103S, U22-ch90, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



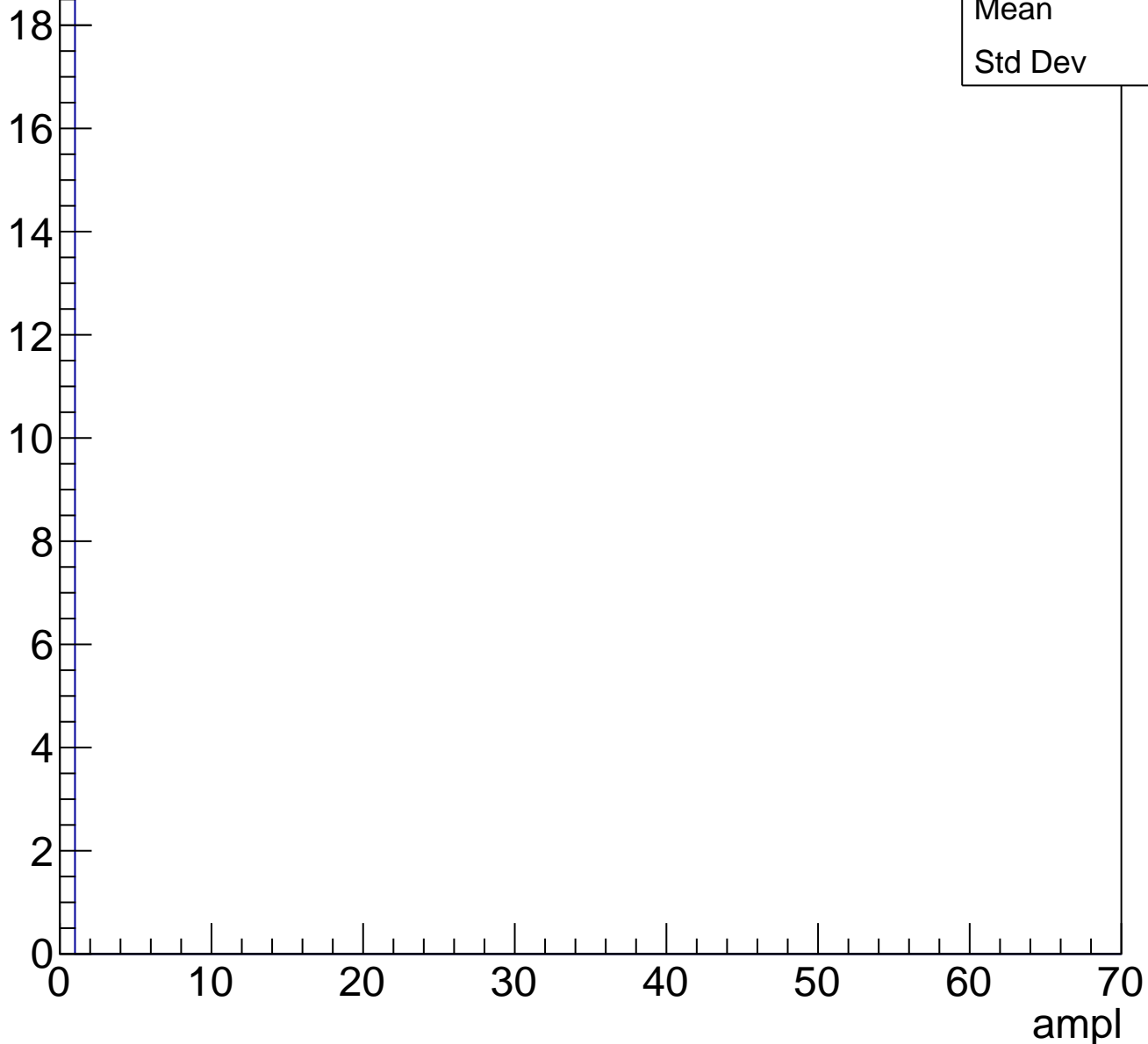
Entries	10
Mean	60.4
Std Dev	2.973



# B1L103S, U22-ch90, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch91, adc0

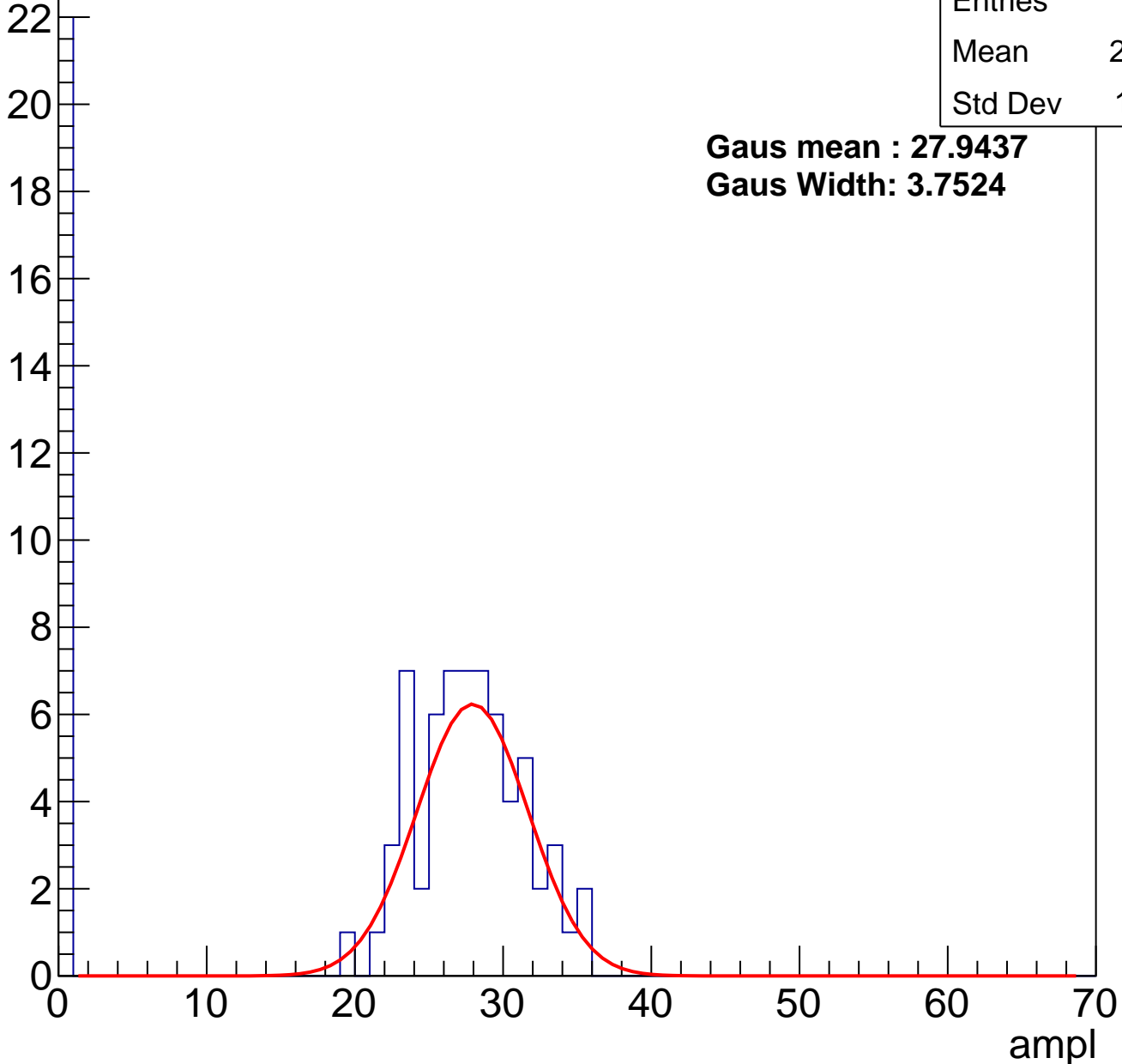
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	86
Mean	20.33
Std Dev	12.31

**Gaus mean : 27.9437**

**Gaus Width: 3.7524**

Entry



# B1L103S, U22-ch91, adc1

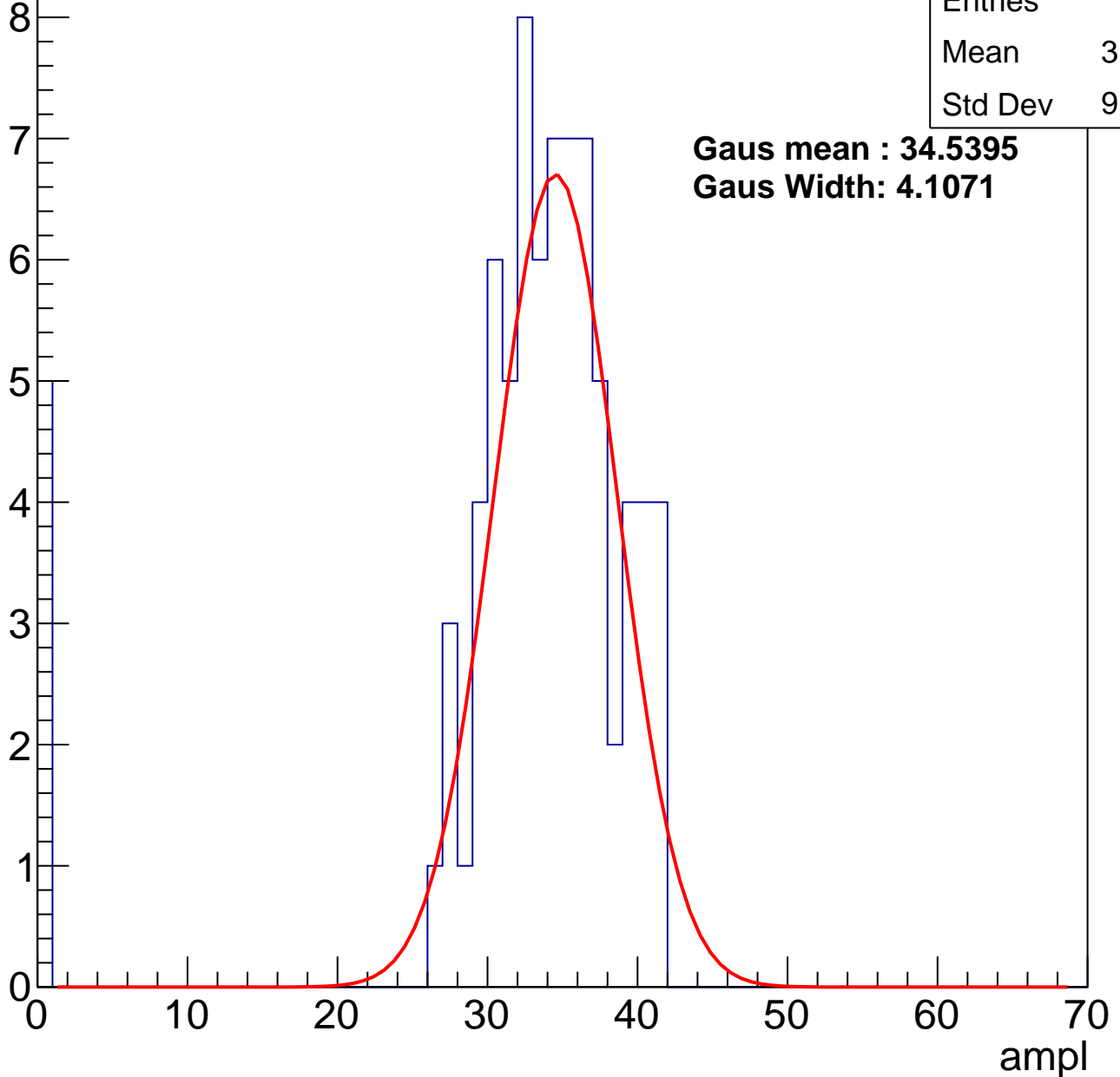
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	31.85
Std Dev	9.065

**Gaus mean : 34.5395**

**Gaus Width: 4.1071**



# B1L103S, U22-ch91, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	70
Mean	34.47
Std Dev	15.28

**Gaus mean : 40.7776**

**Gaus Width: 4.3566**

Entry

10

8

6

4

2

0

0

10

20

30

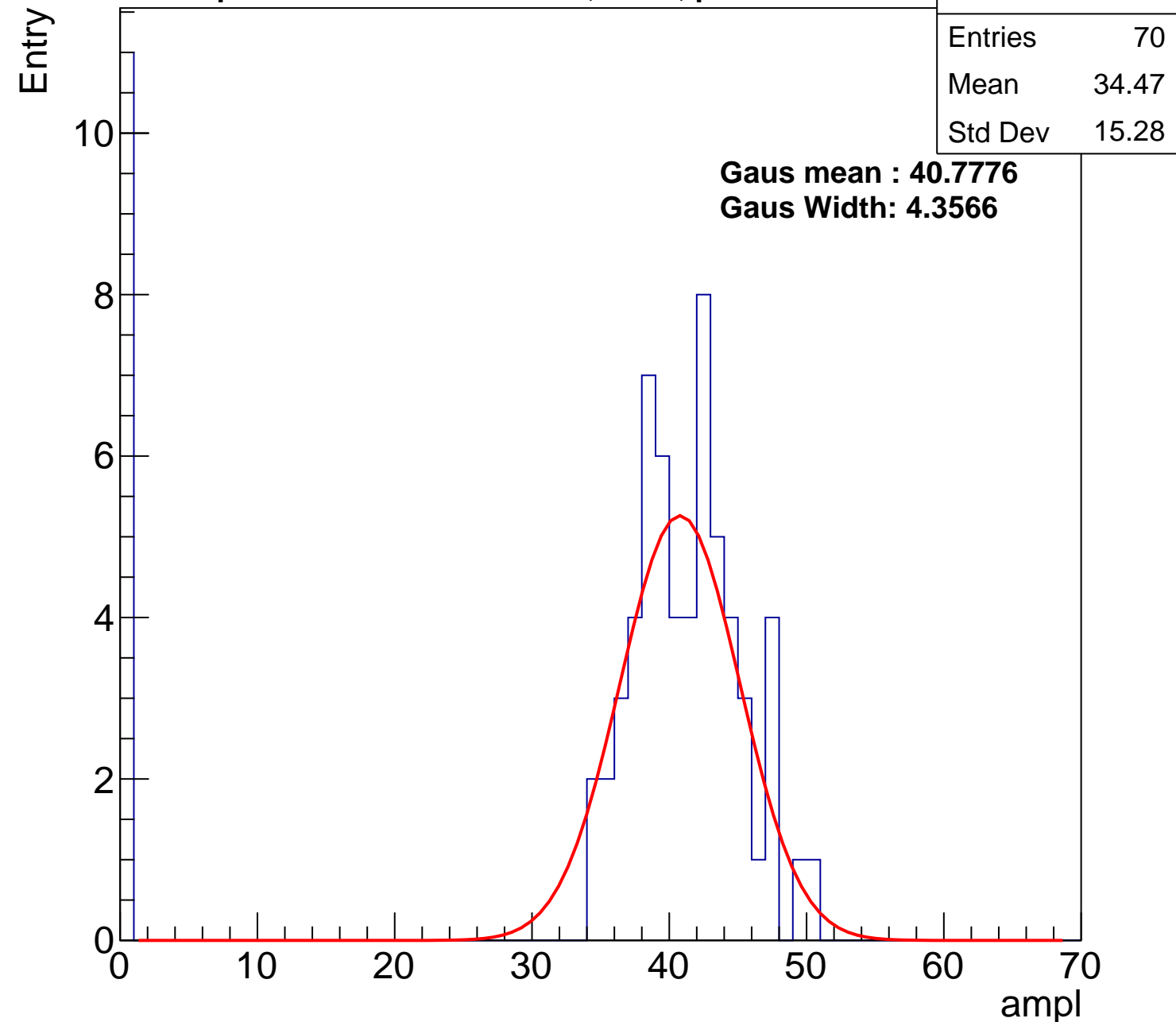
40

50

60

70

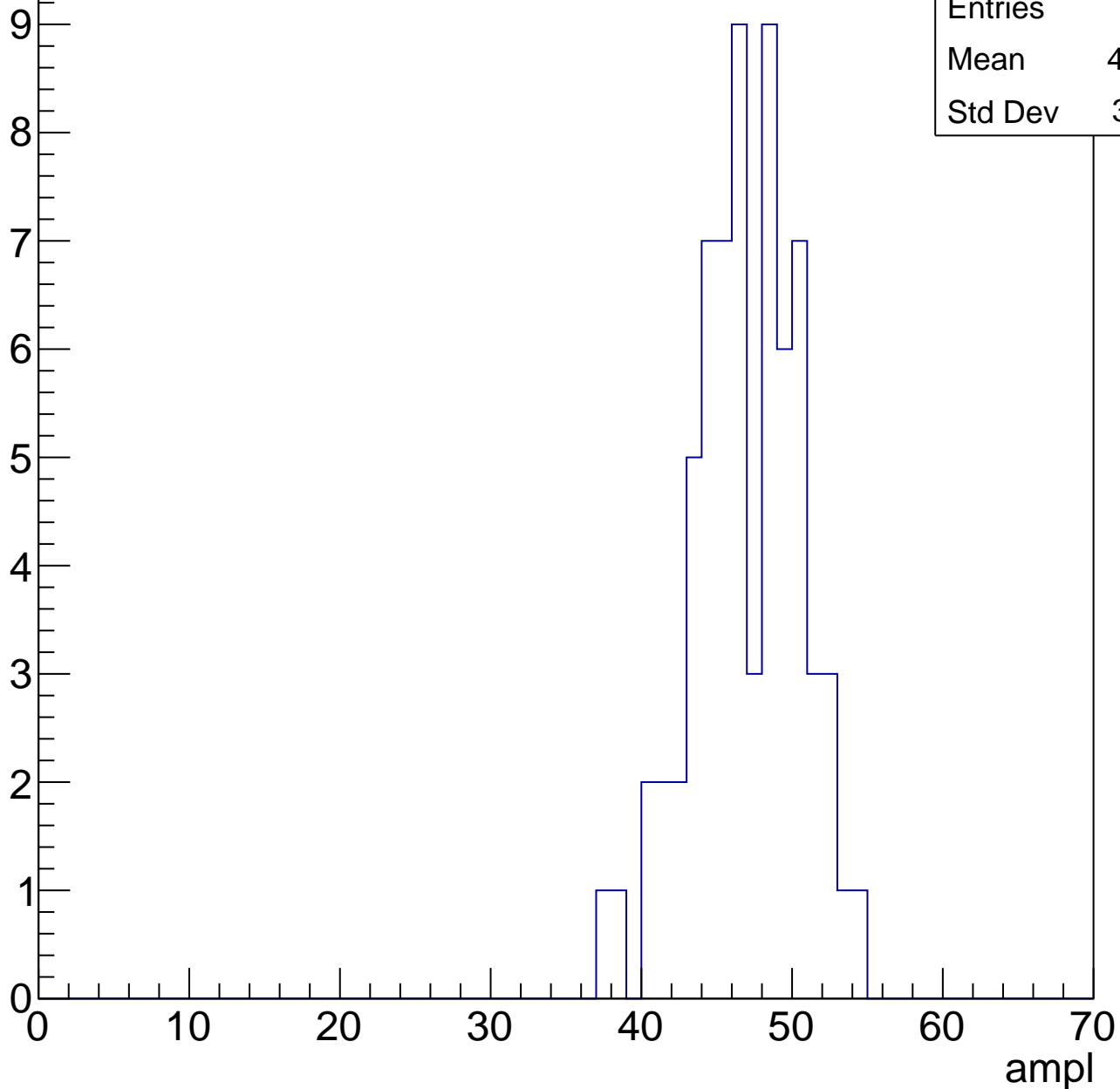
ampl



# B1L103S, U22-ch91, adc3

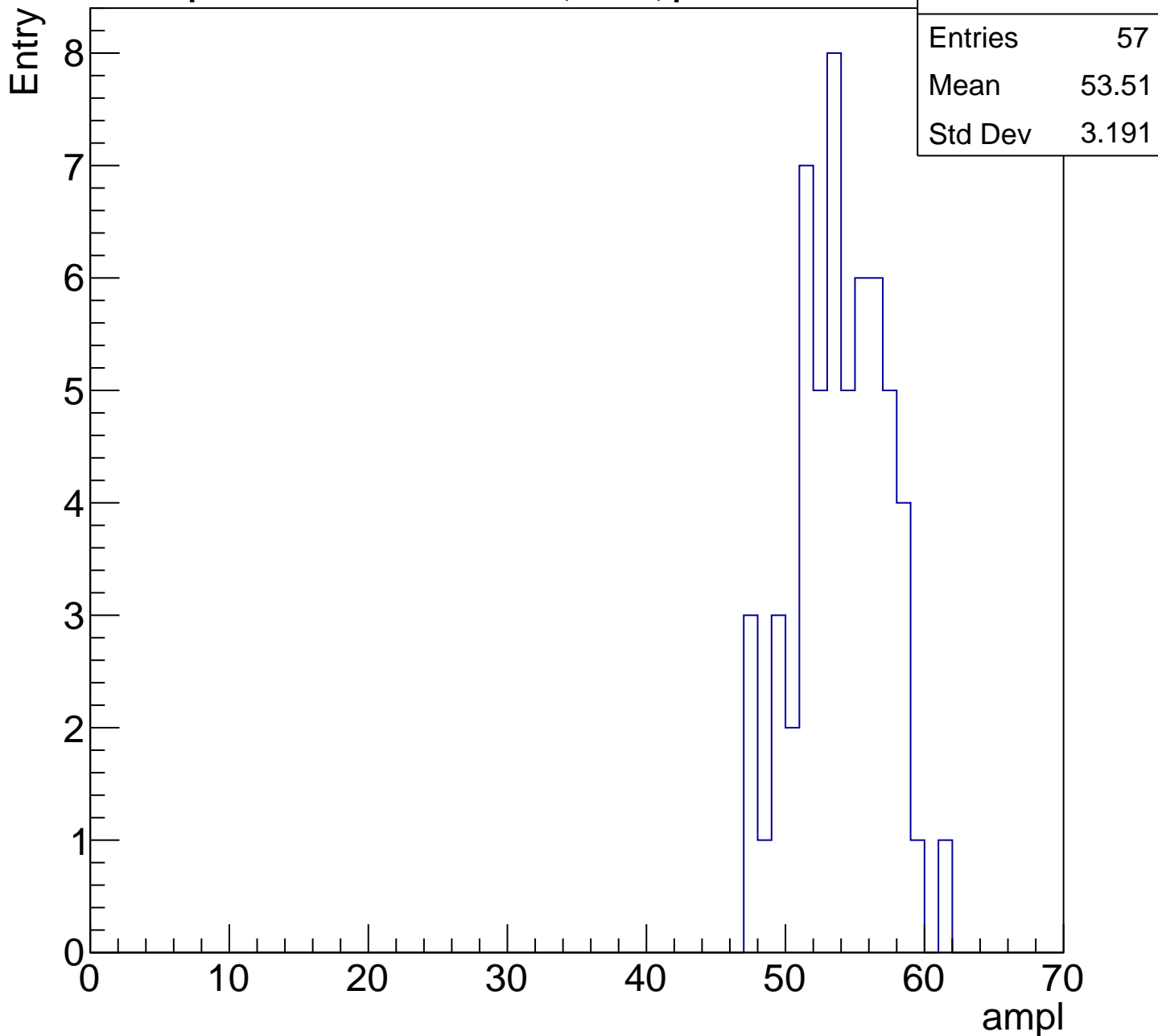
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch91, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

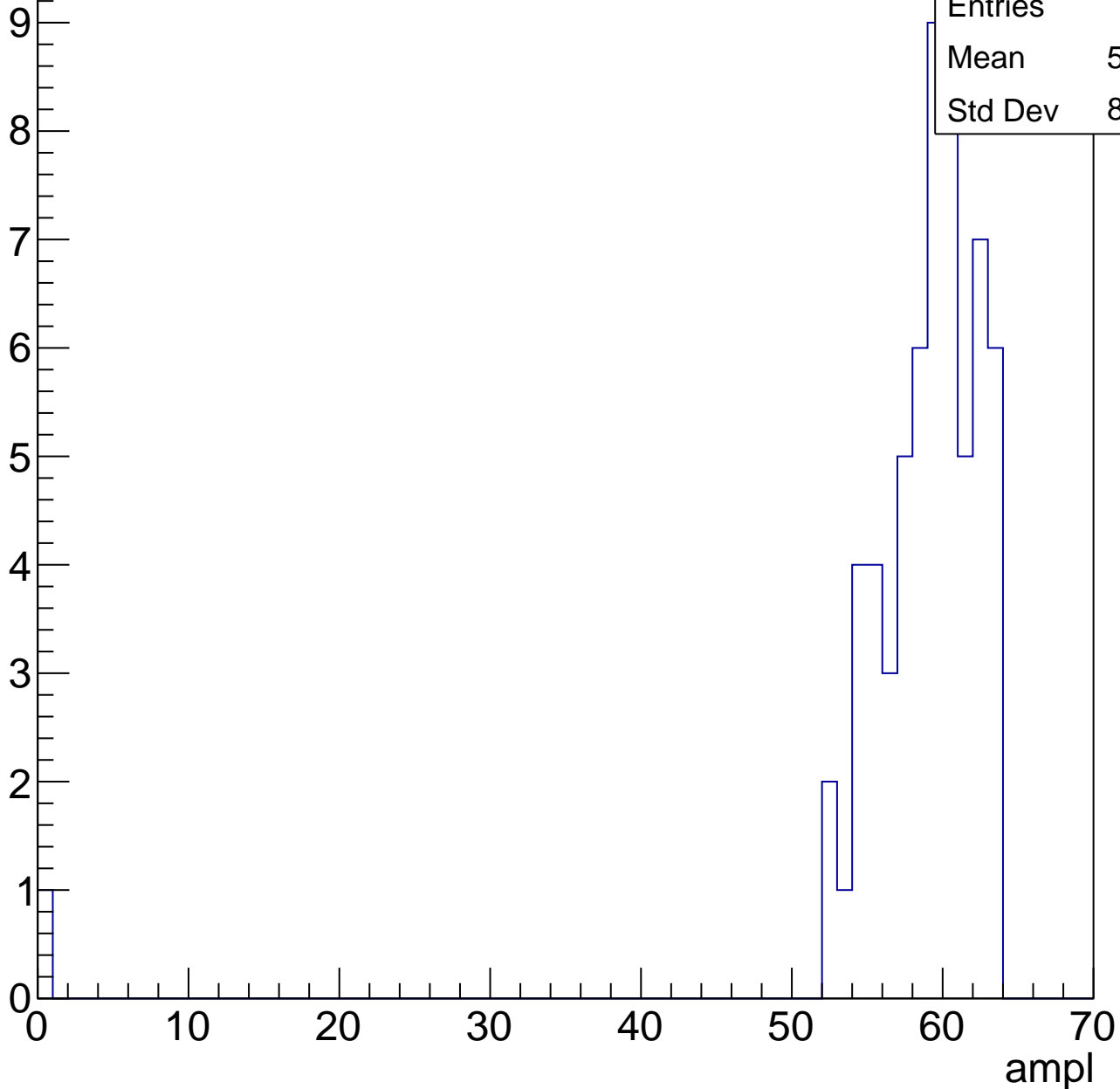


# B1L103S, U22-ch91, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

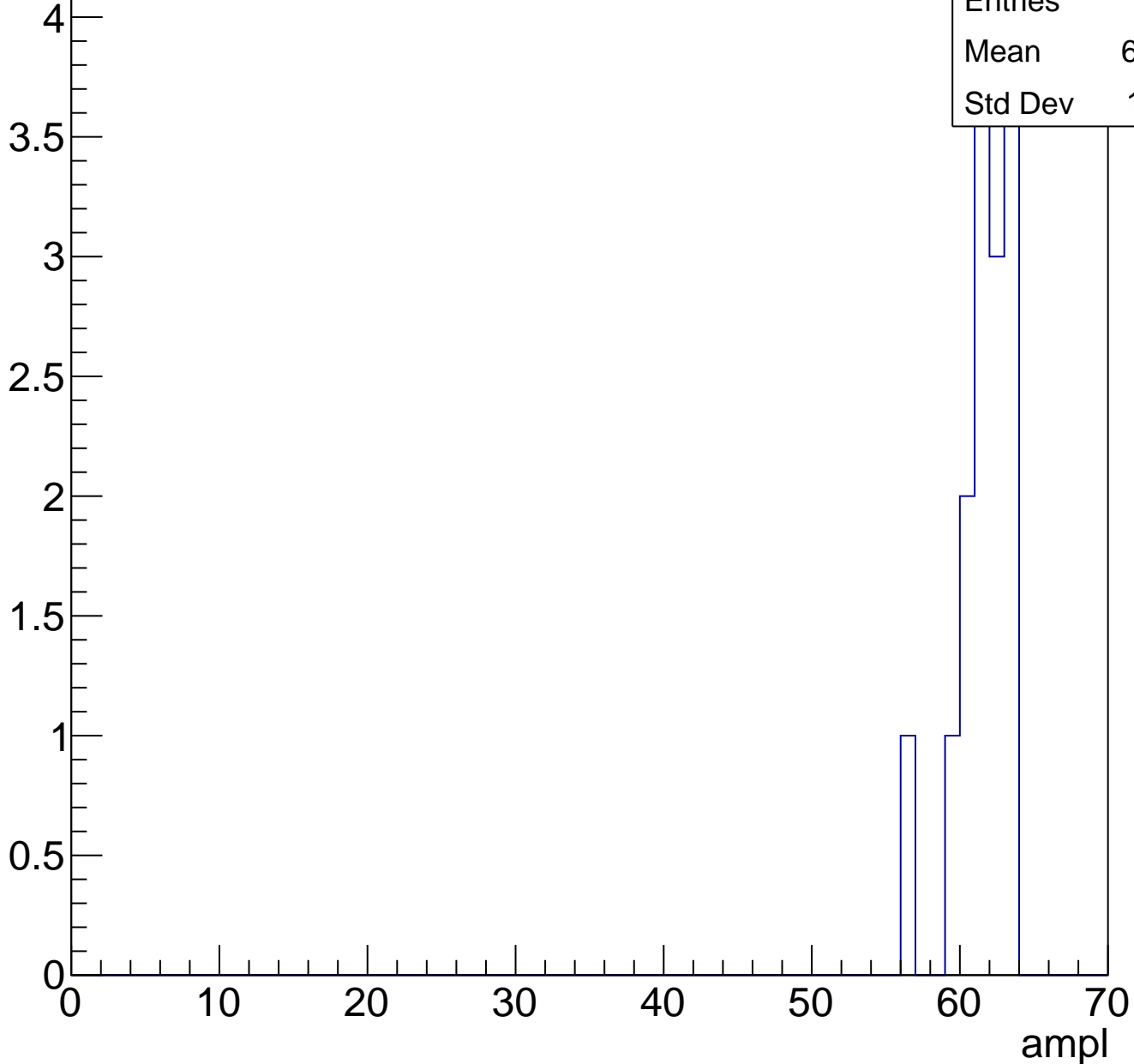
Entries	61
Mean	57.74
Std Dev	8.018



# B1L103S, U22-ch91, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	15
Mean	61.13
Std Dev	1.821



# B1L103S, U22-ch91, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

Entries	19
Mean	0
Std Dev	0

# B1L103S, U22-ch92, adc0

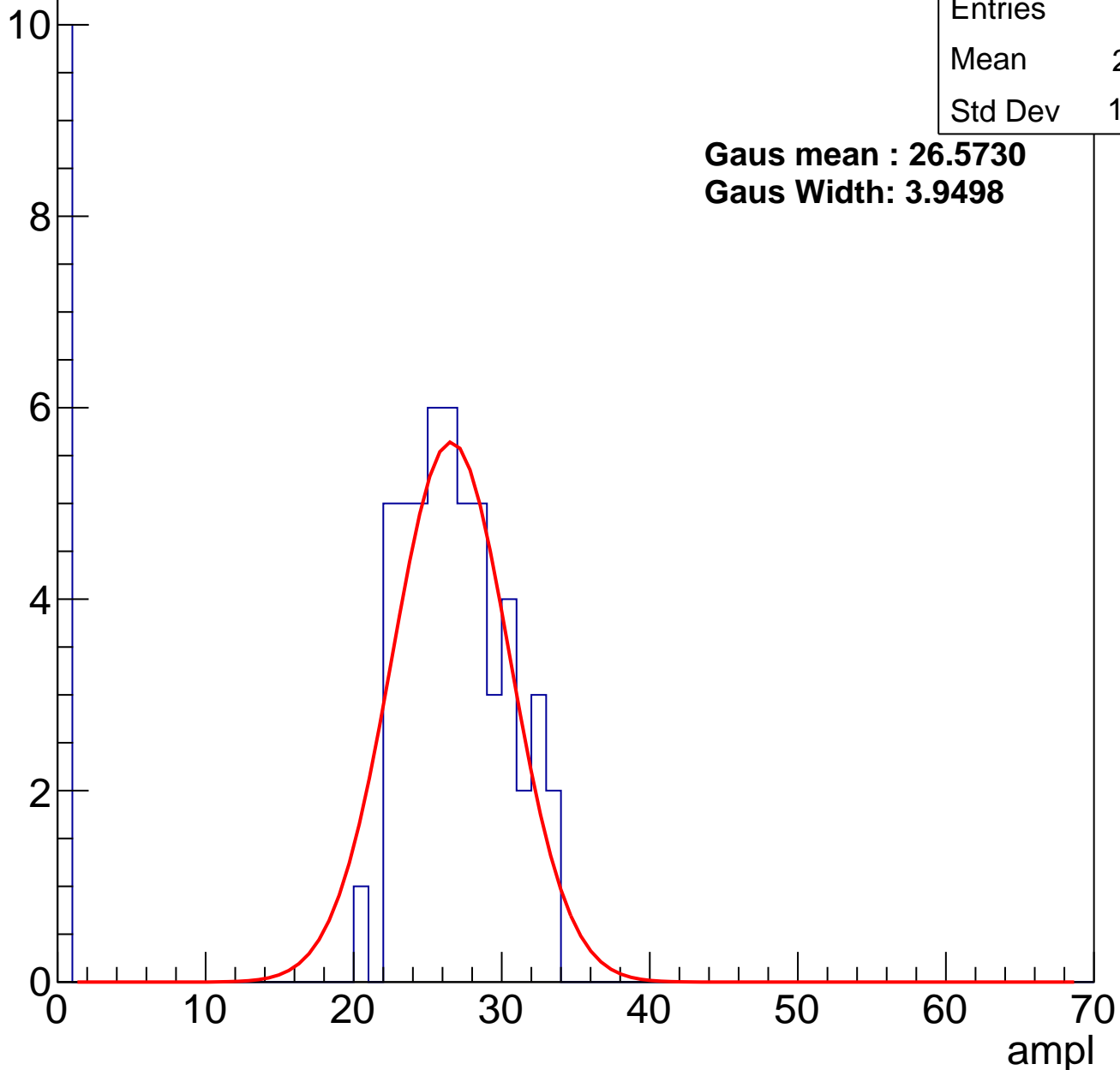
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	62
Mean	22.21
Std Dev	10.18

**Gaus mean : 26.5730**

**Gaus Width: 3.9498**

Entry



# B1L103S, U22-ch92, adc1

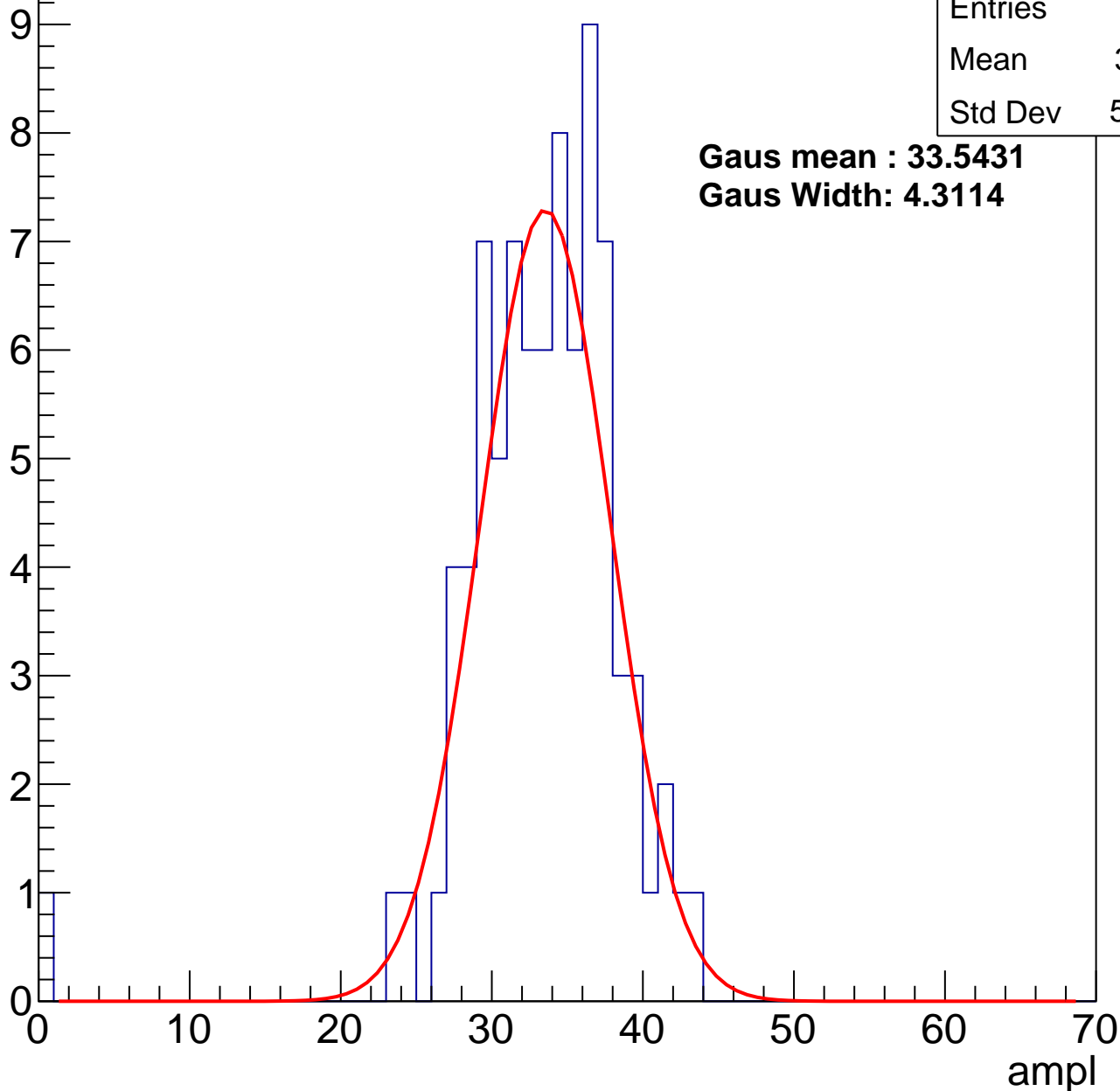
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	32.81
Std Dev	5.474

**Gaus mean : 33.5431**

**Gaus Width: 4.3114**



# B1L103S, U22-ch92, adc2

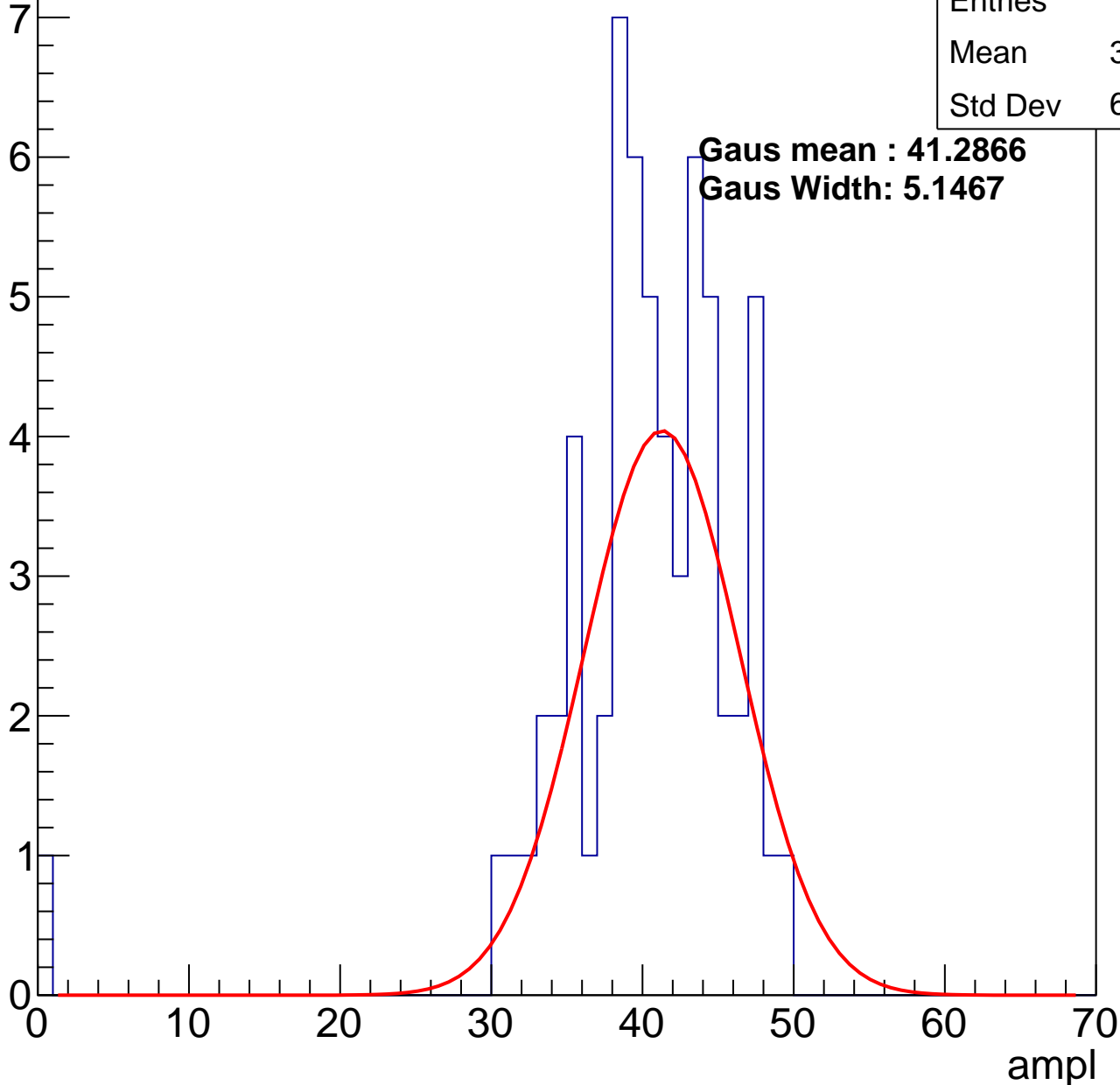
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	39.66
Std Dev	6.766

**Gaus mean : 41.2866**

**Gaus Width: 5.1467**

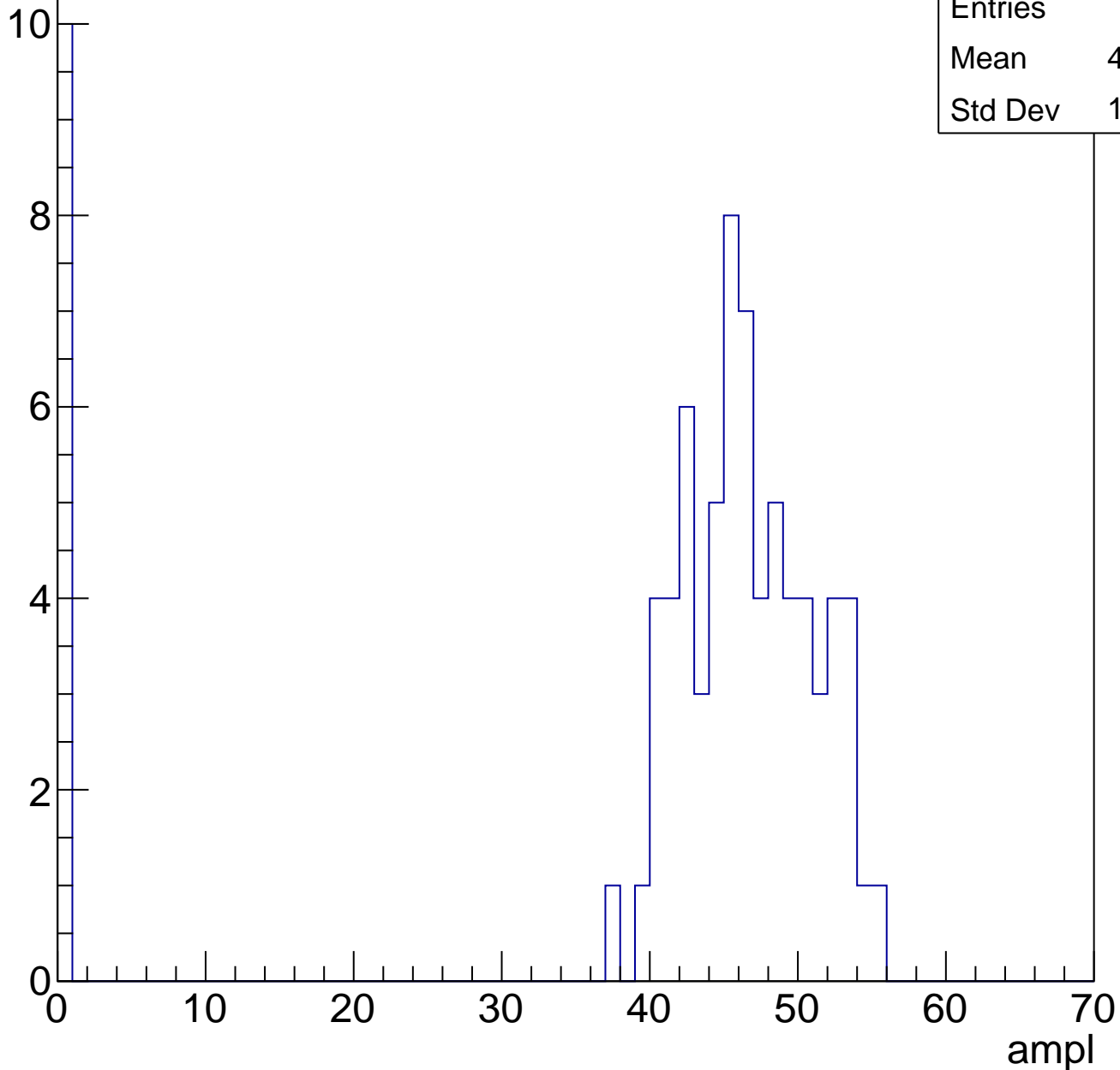


# B1L103S, U22-ch92, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	40.37
Std Dev	15.86

Entry

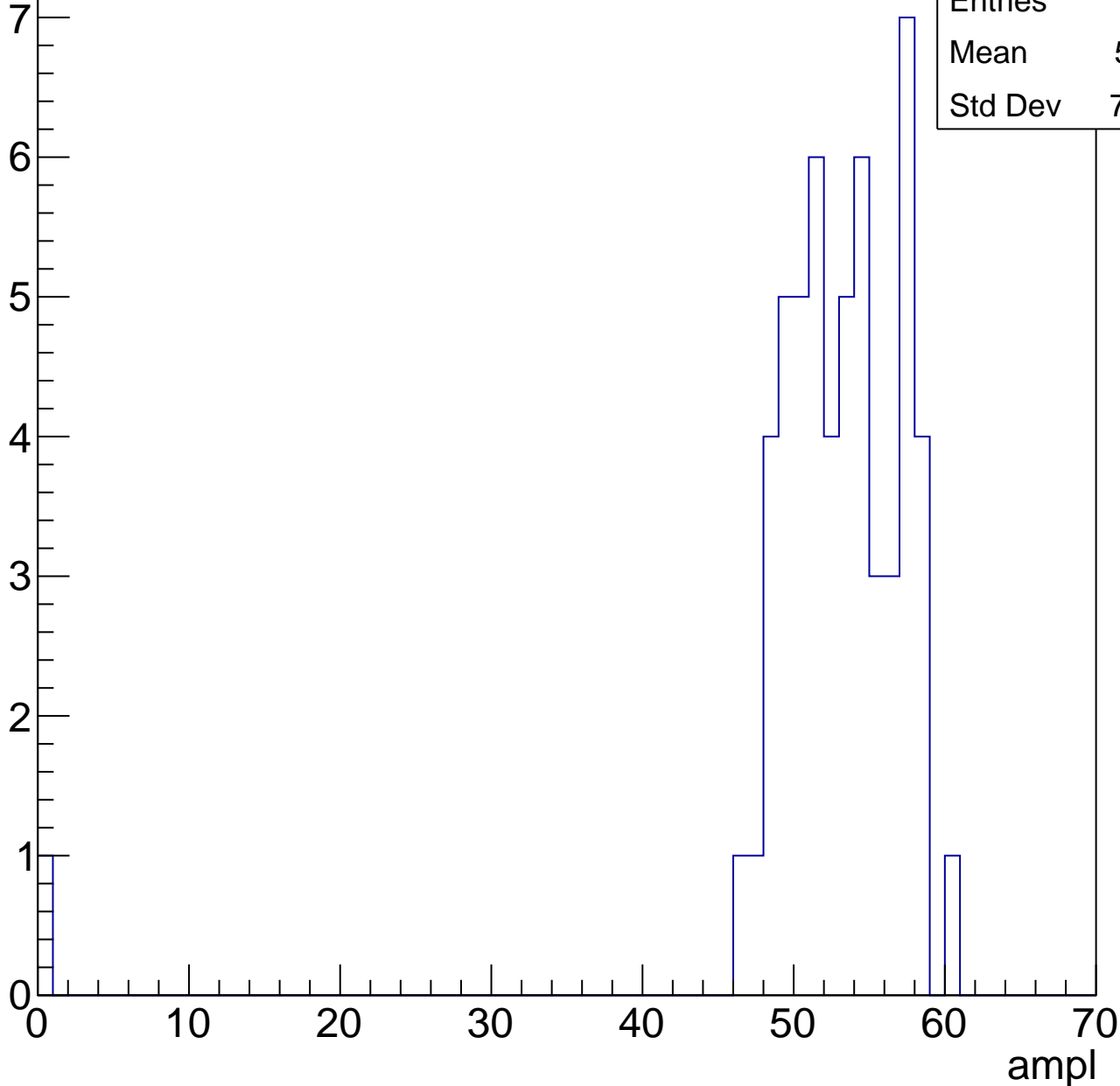


# B1L103S, U22-ch92, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	51.91
Std Dev	7.777

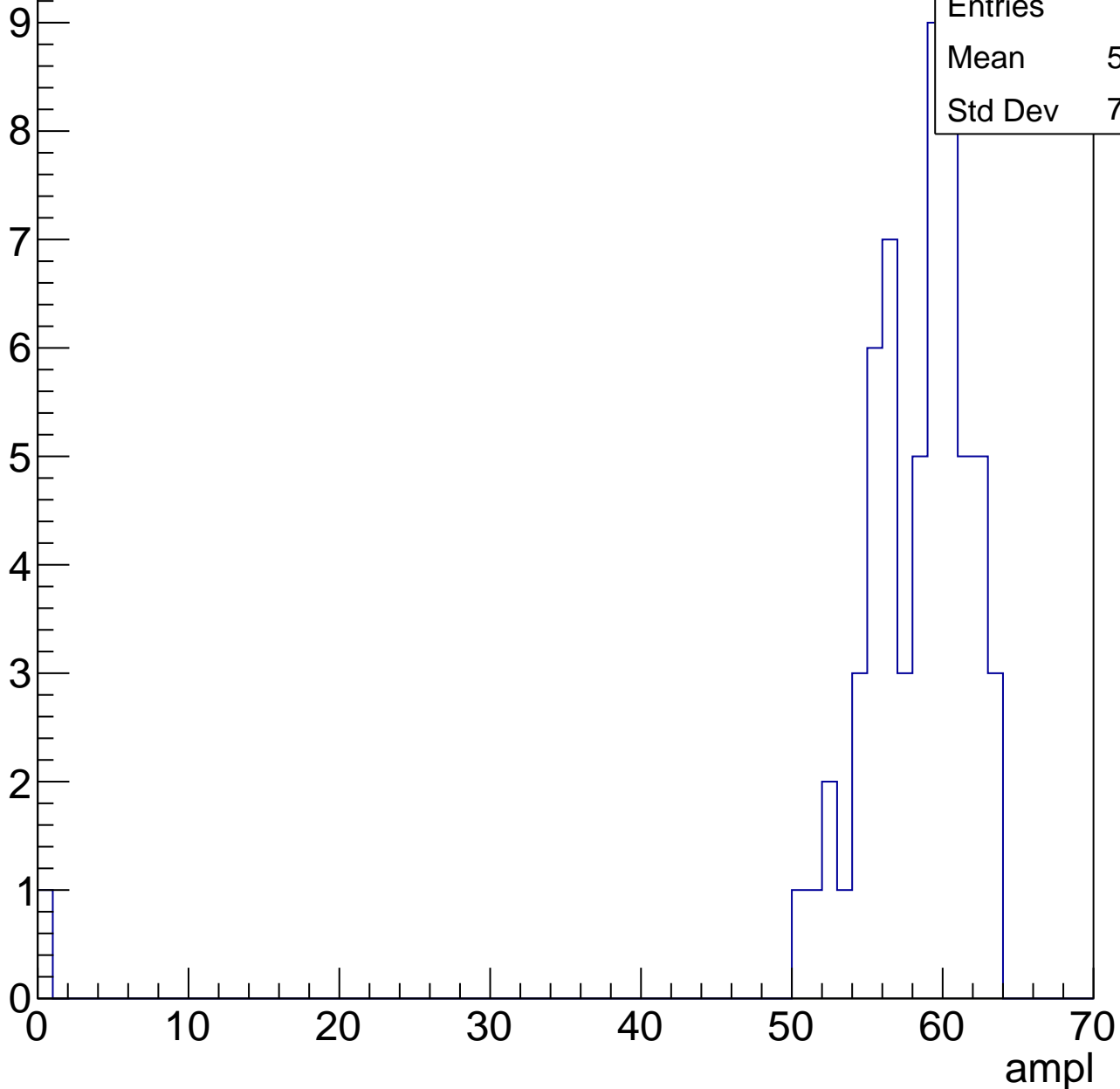


# B1L103S, U22-ch92, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	57.02
Std Dev	7.989

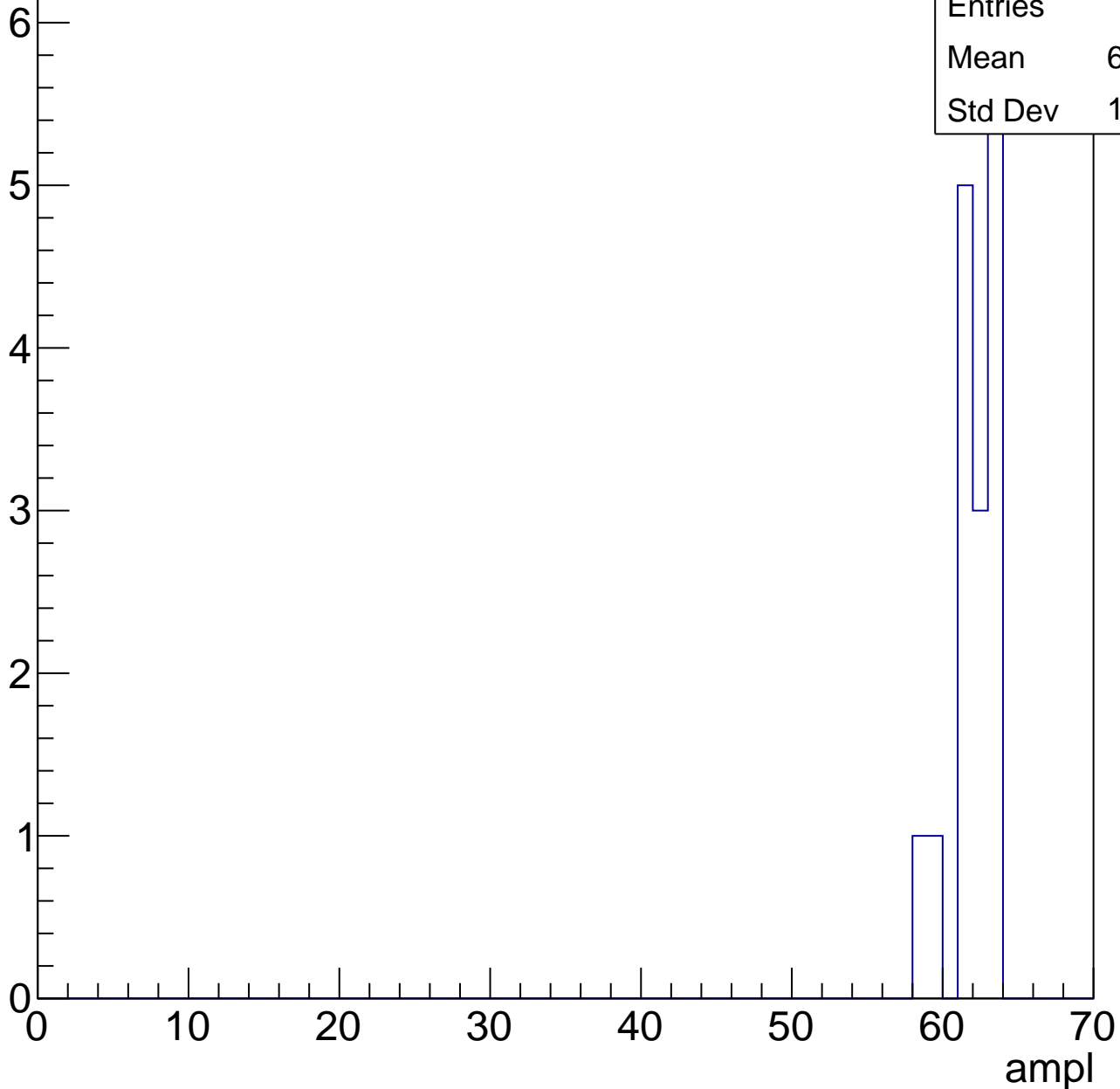


# B1L103S, U22-ch92, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.62
Std Dev	1.452



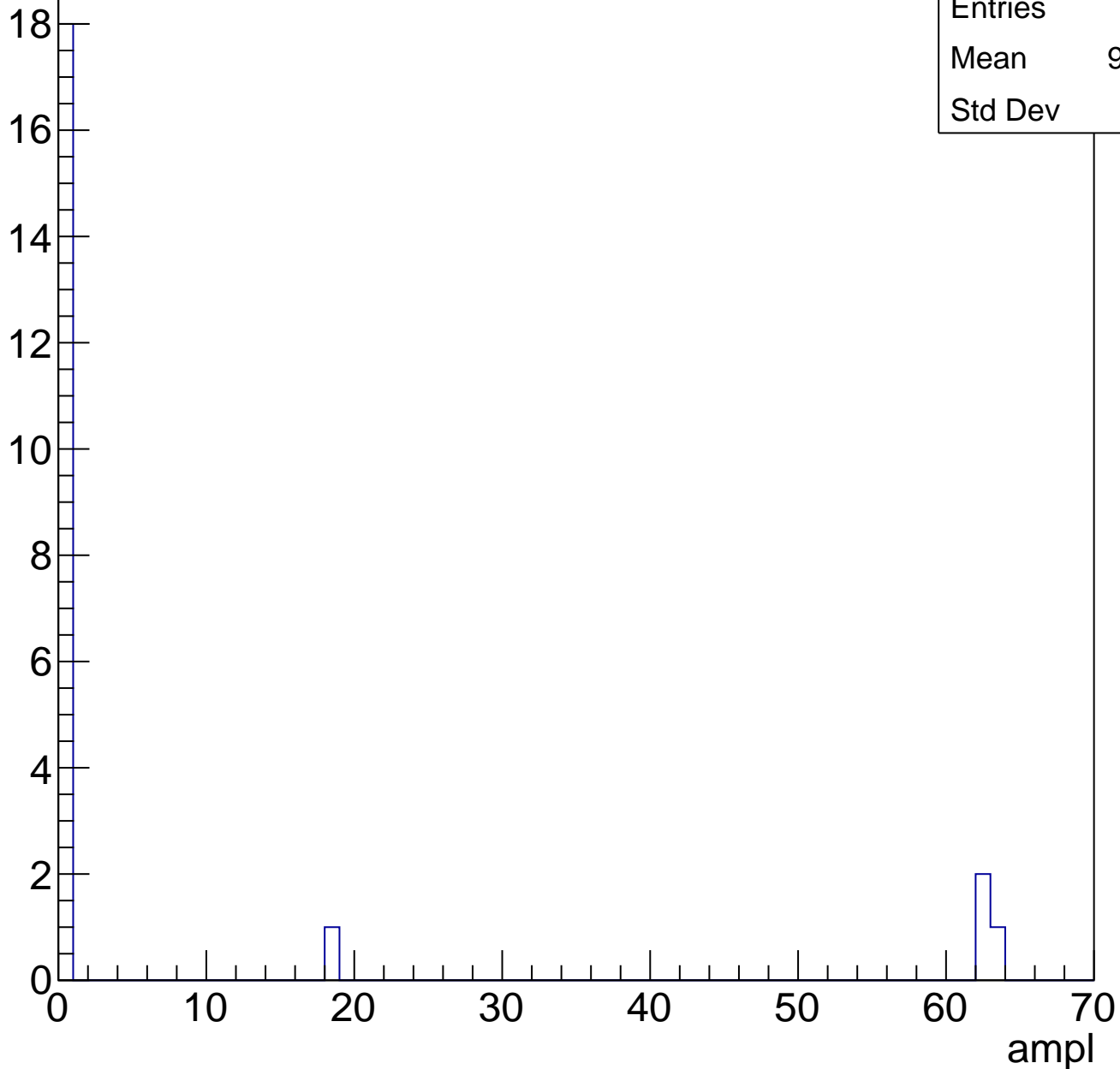


# B1L103S, U22-ch92, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	22
Mean	9.318
Std Dev	21.4

Entry



# B1L103S, U22-ch93, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	72
Mean	23.42
Std Dev	11.39

**Gaus mean : 28.8405**

**Gaus Width: 3.6422**

Entry

12

10

8

6

4

2

0

0

10

20

30

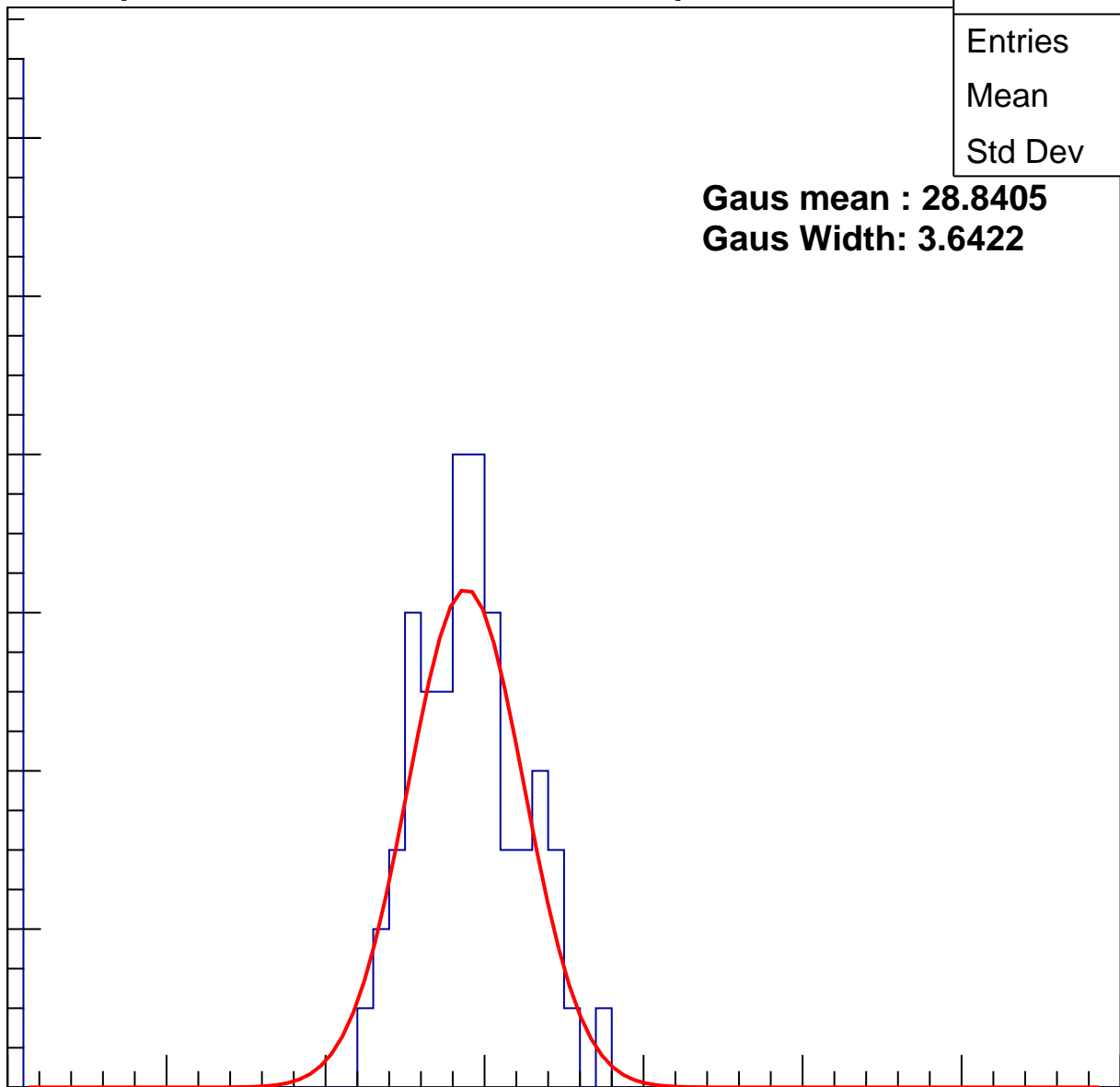
40

50

60

70

ampl



# B1L103S, U22-ch93, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	33.48
Std Dev	8.393

**Gaus mean : 35.5246**

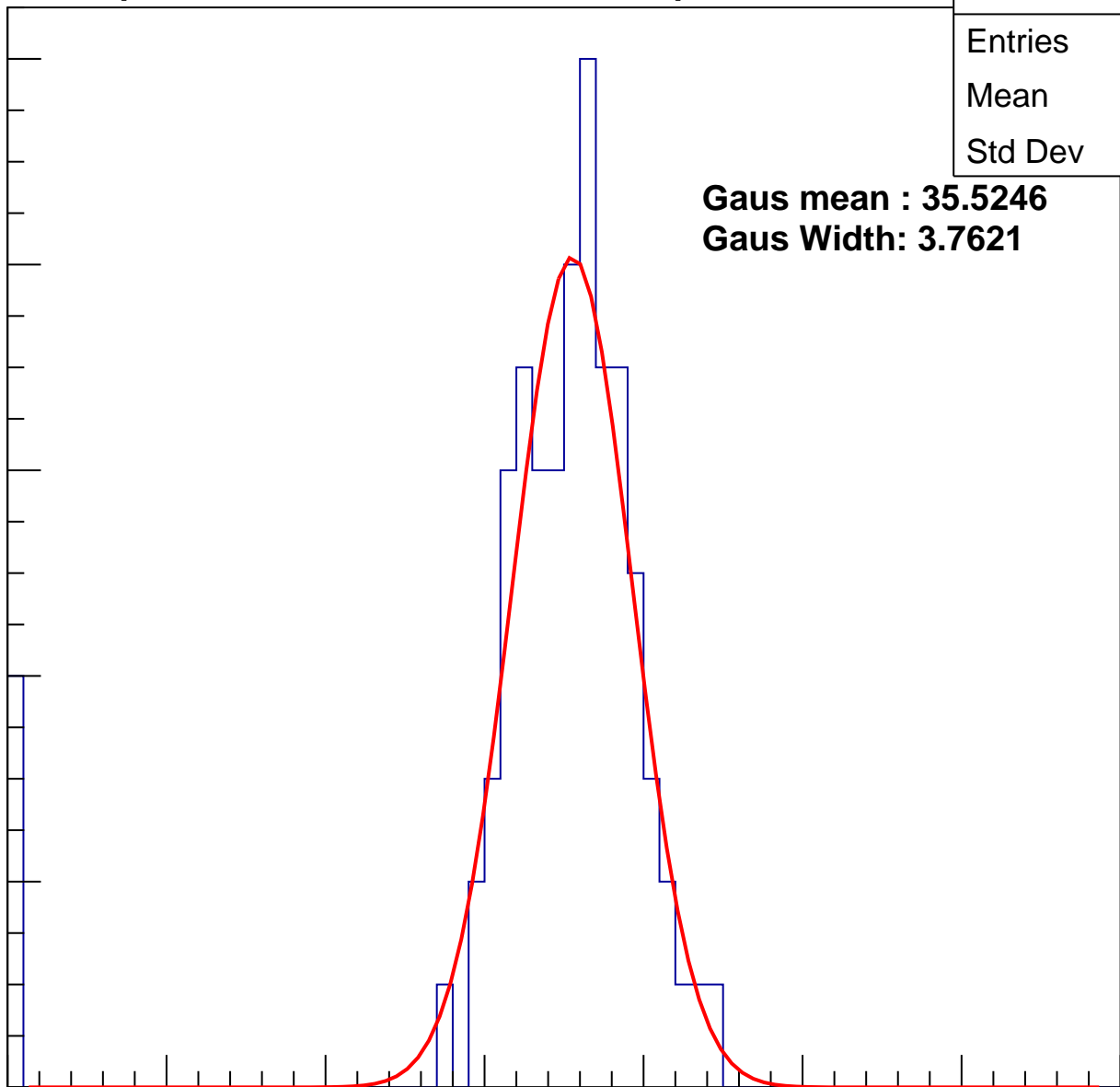
**Gaus Width: 3.7621**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch93, adc2

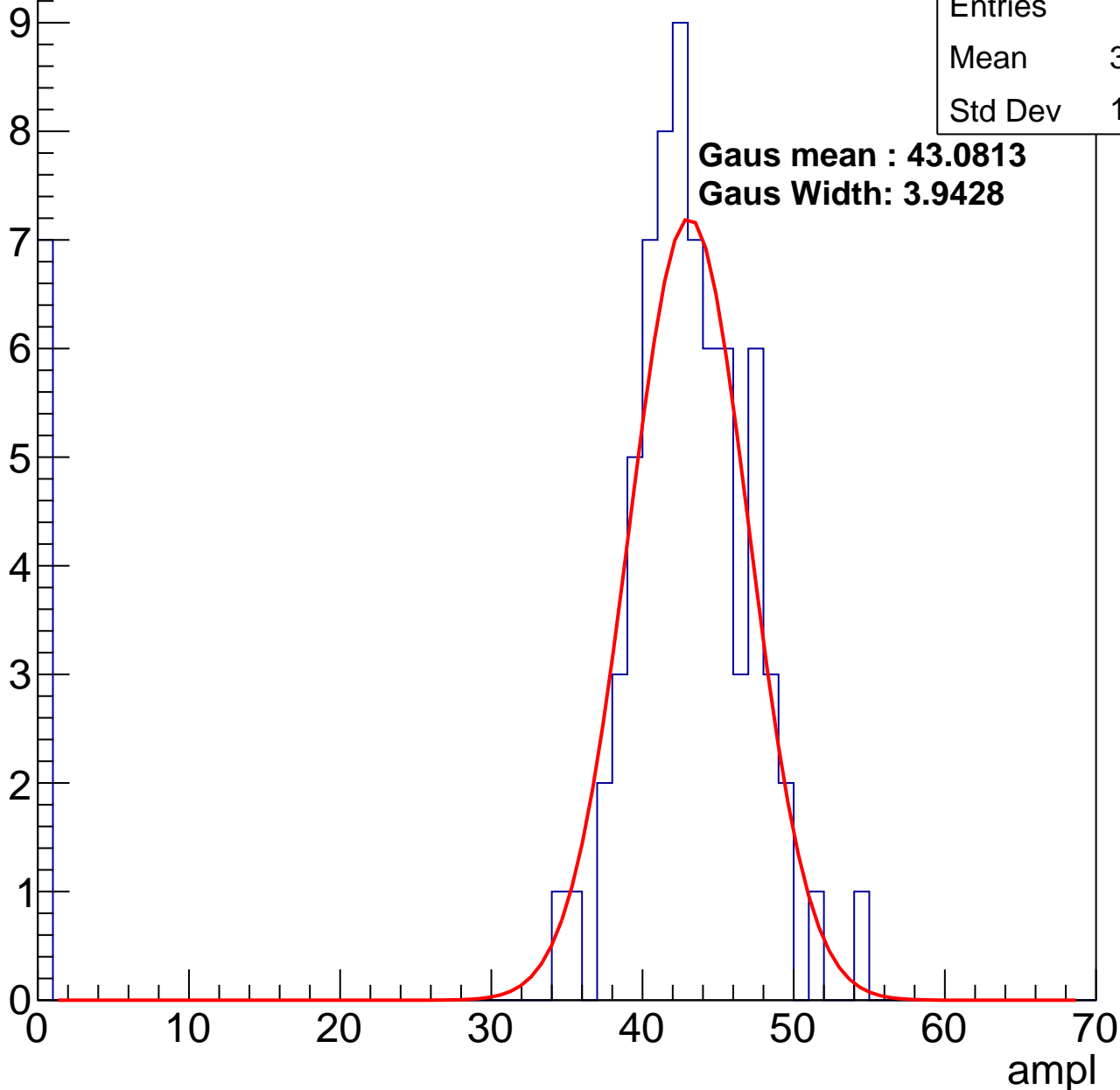
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	38.97
Std Dev	12.73

**Gaus mean : 43.0813**

**Gaus Width: 3.9428**

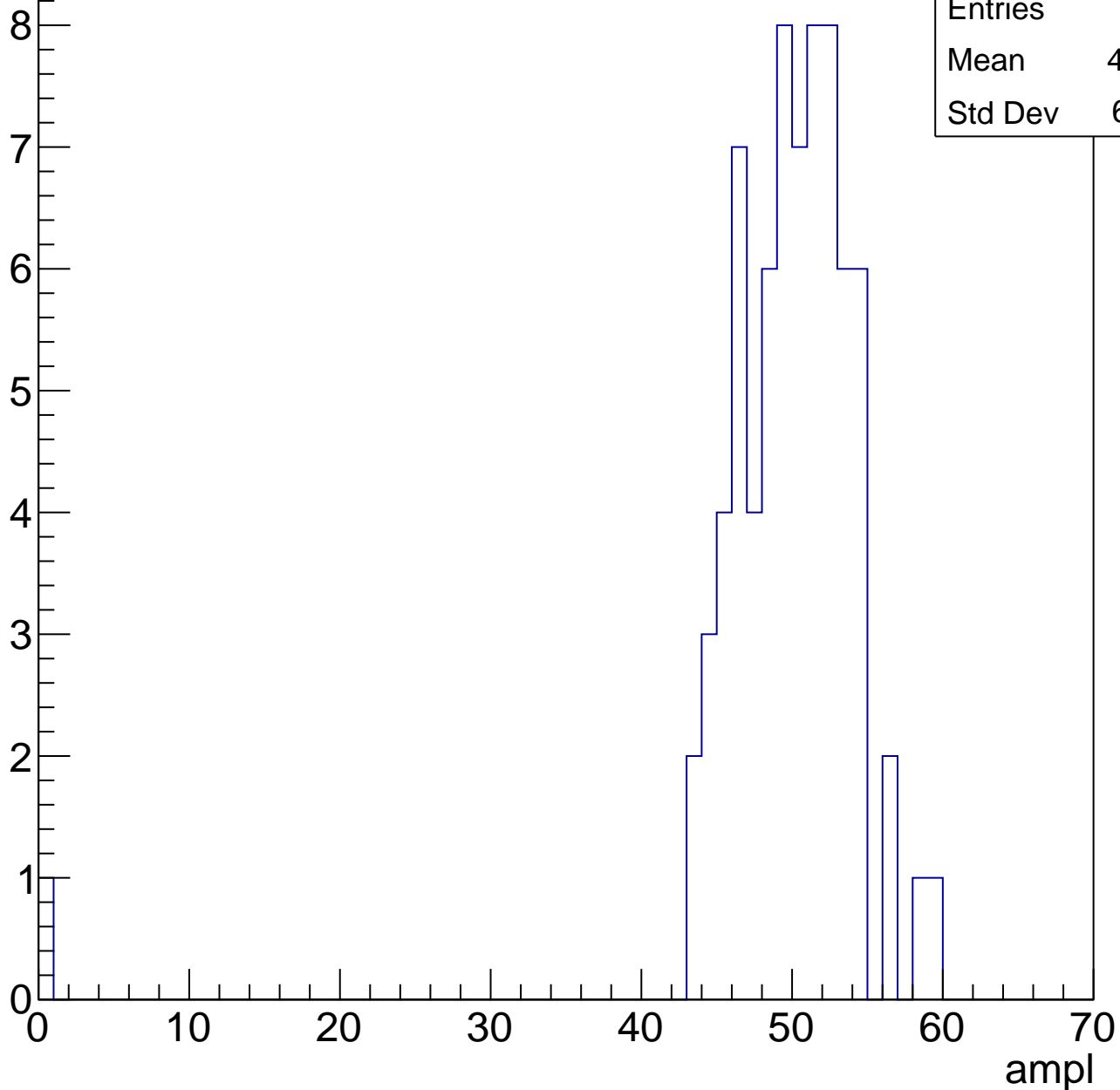


# B1L103S, U22-ch93, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	49.09
Std Dev	6.711

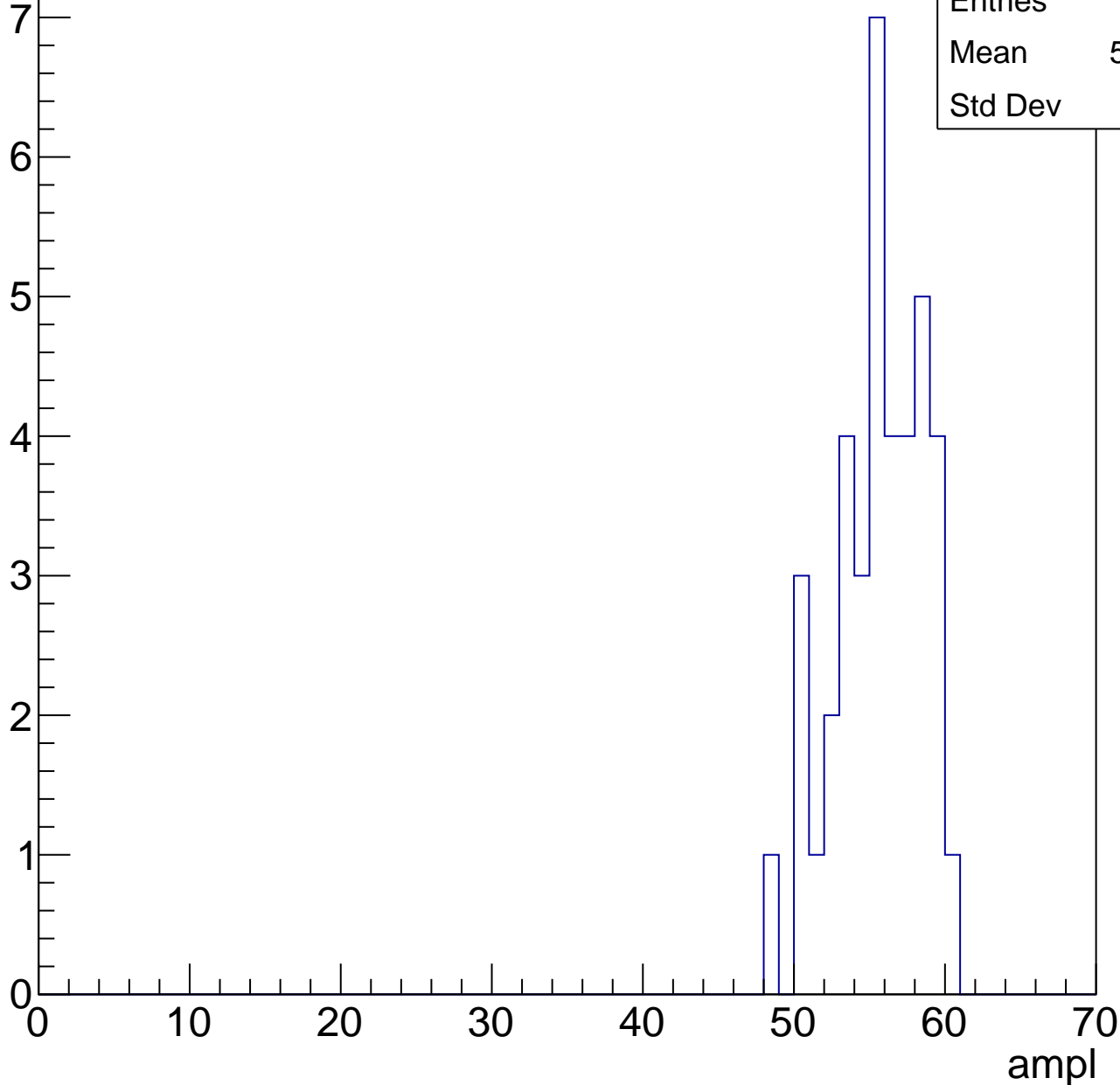


# B1L103S, U22-ch93, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

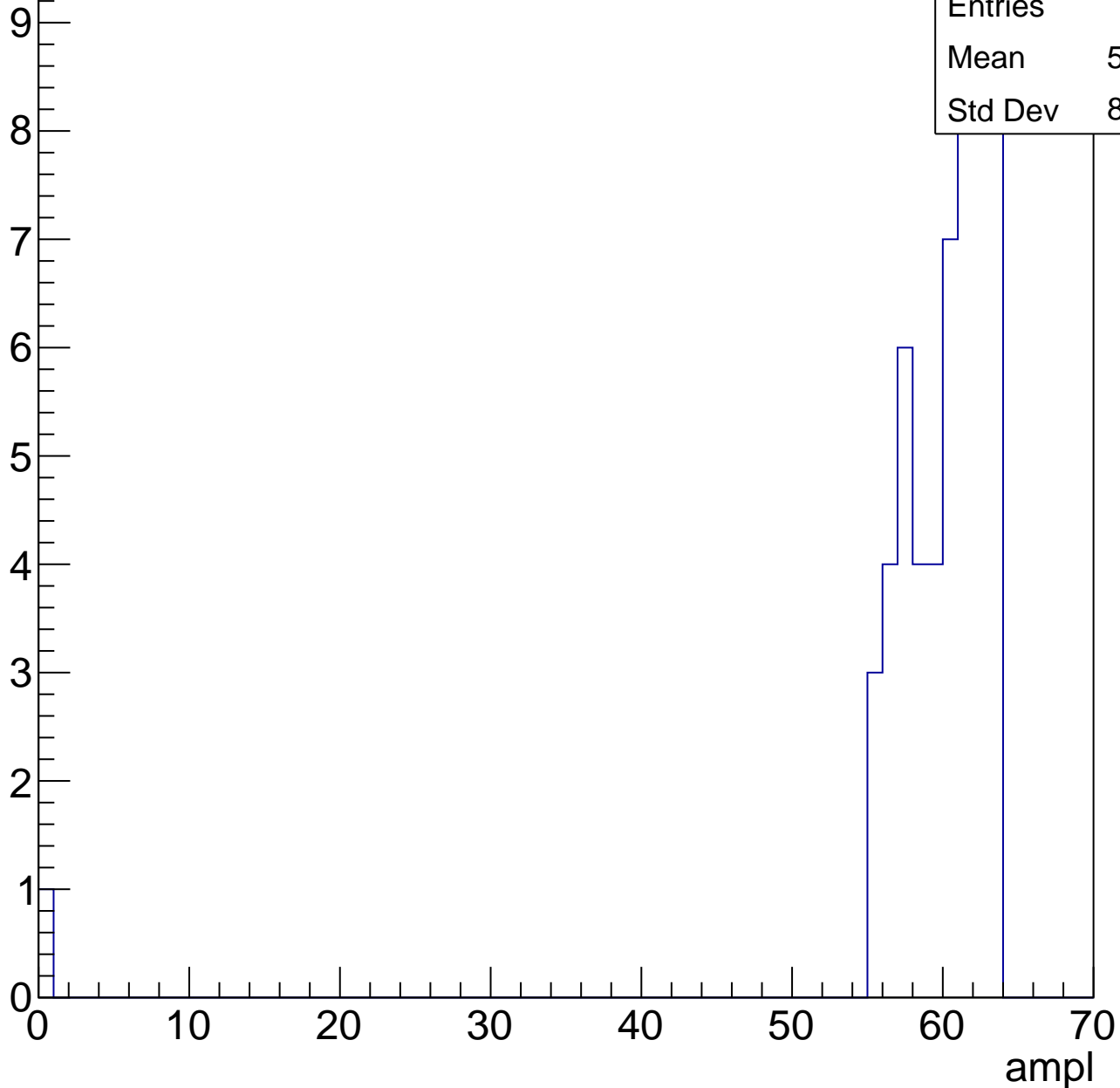
Entries	39
Mean	55.13
Std Dev	2.91



# B1L103S, U22-ch93, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch93, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

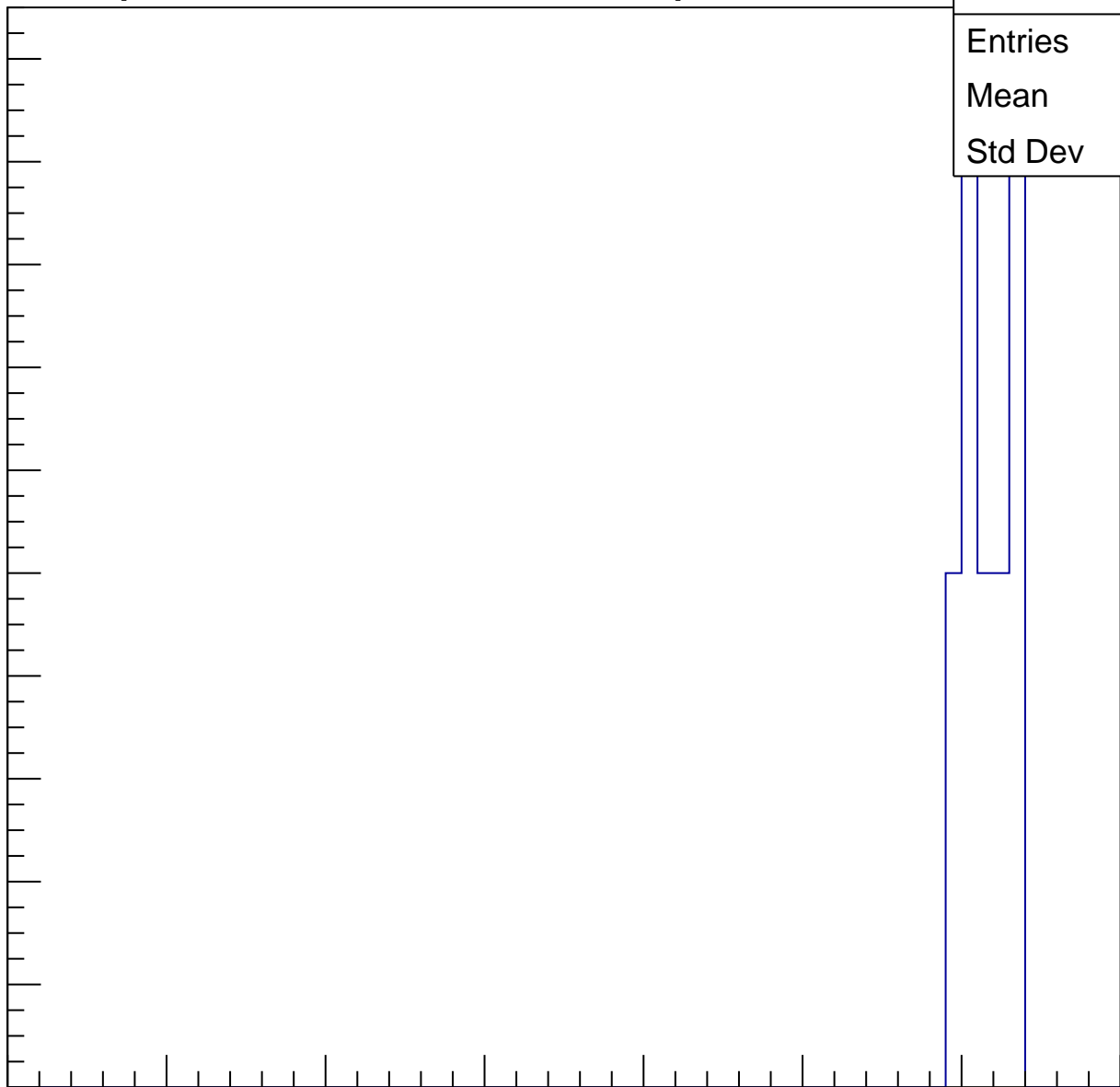
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.14
Std Dev	1.457

0 10 20 30 40 50 60 70

ampl





# B1L103S, U22-ch93, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	18
Mean	0
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B1L103S, U22-ch94, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	18.64
Std Dev	10.95

**Gaus mean : 24.3239**

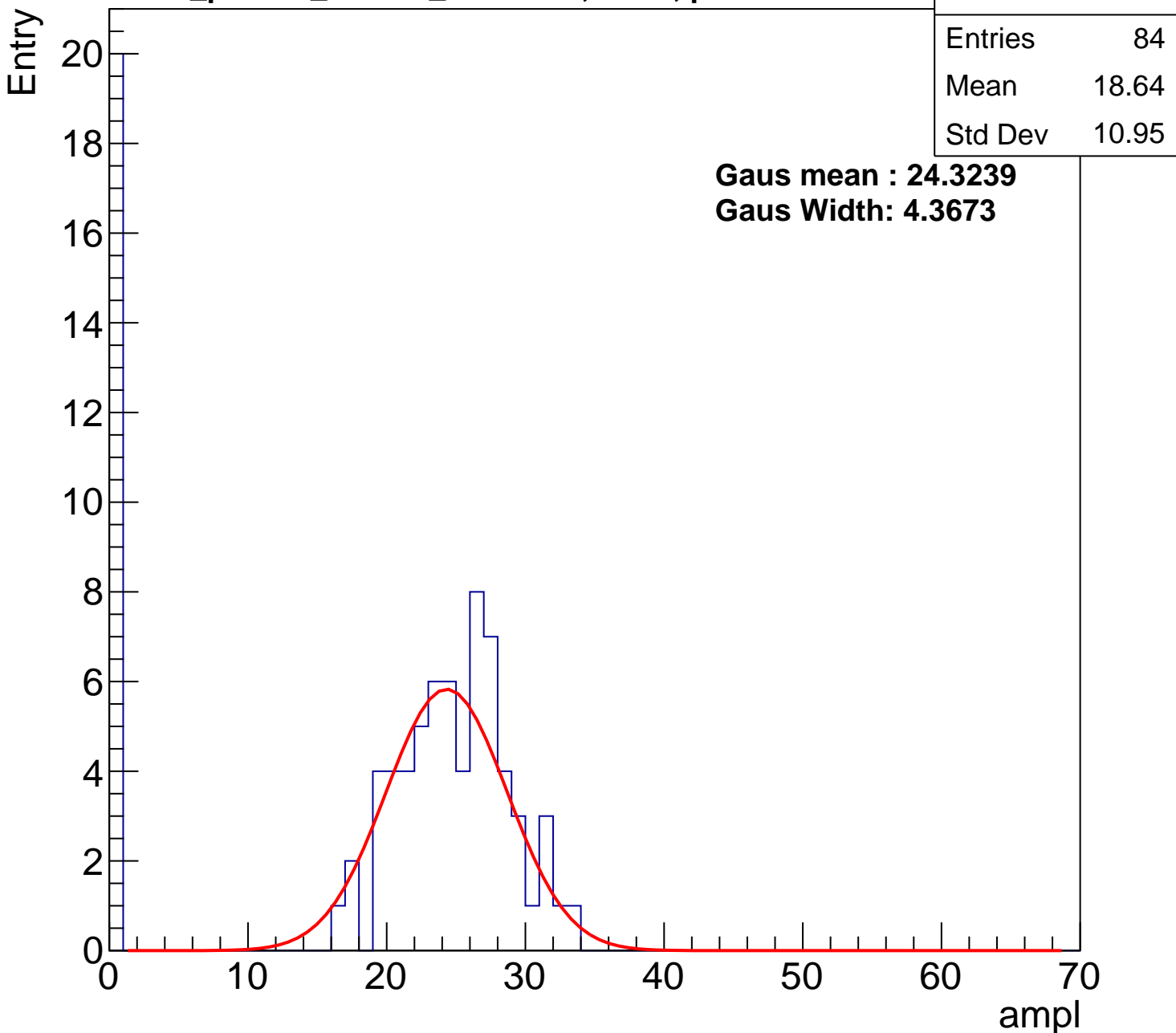
**Gaus Width: 4.3673**

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch94, adc1

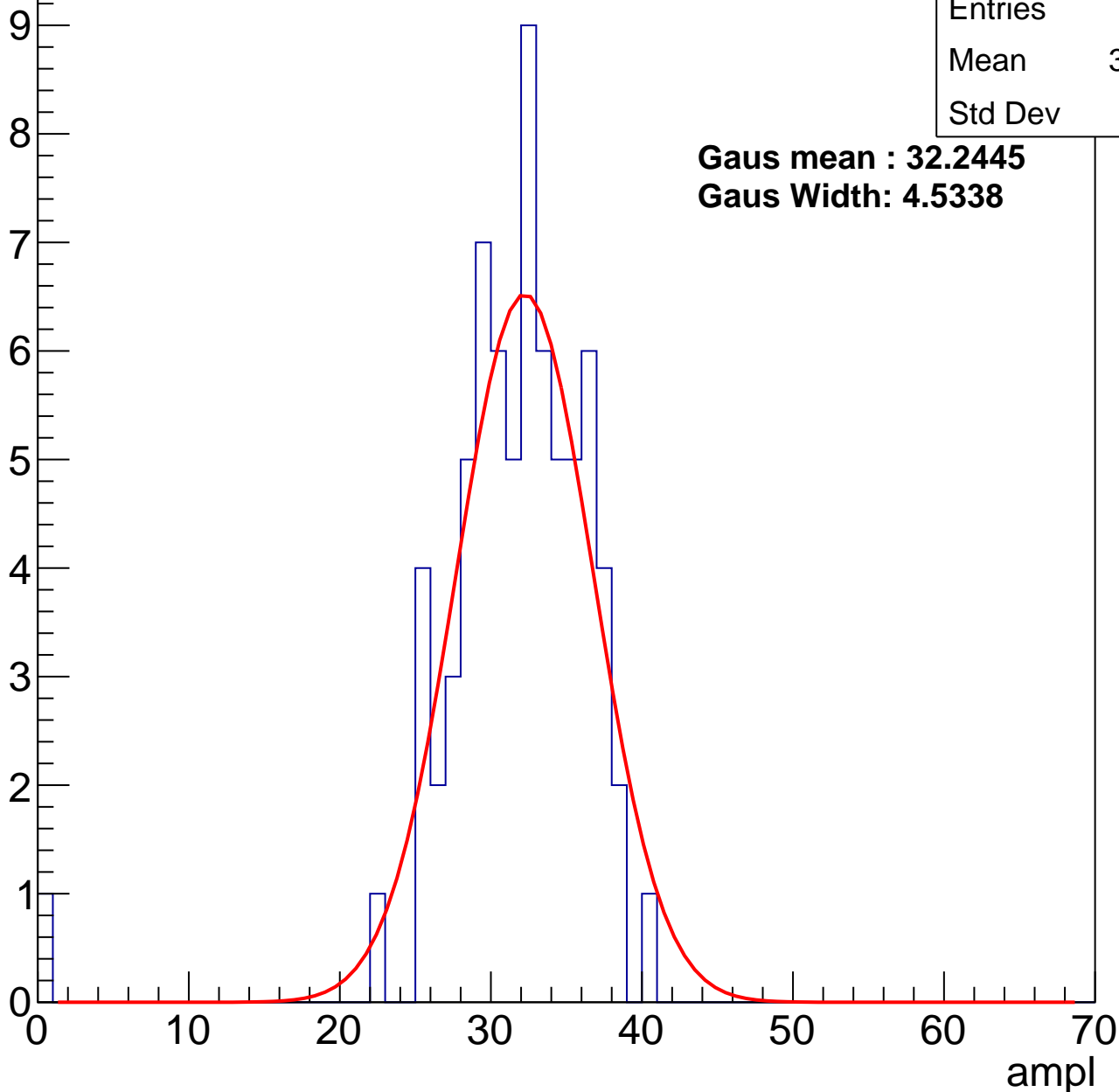
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	31.17
Std Dev	5.26

**Gaus mean : 32.2445**

**Gaus Width: 4.5338**



# B1L103S, U22-ch94, adc2

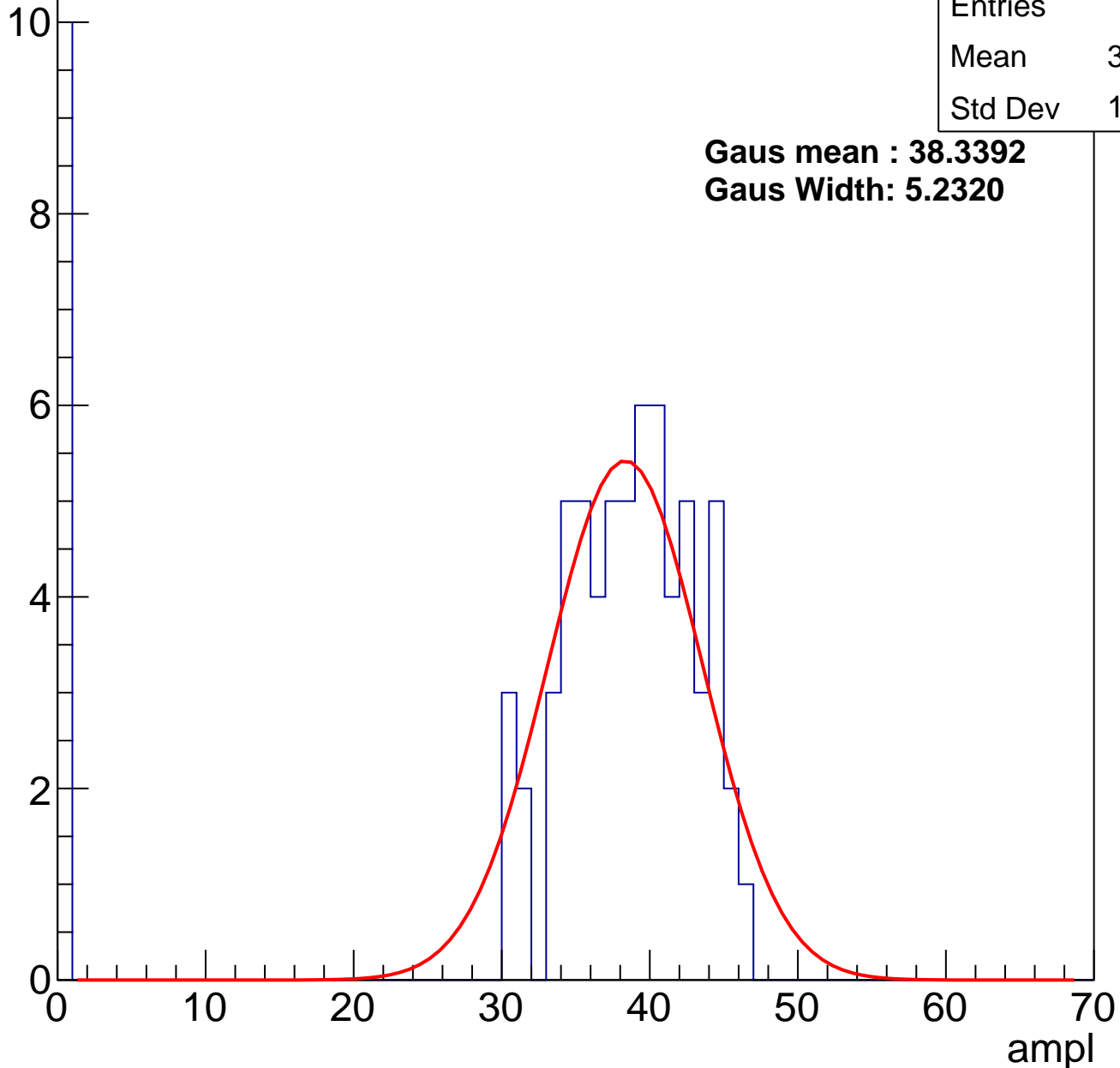
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	74
Mean	33.08
Std Dev	13.62

**Gaus mean : 38.3392**

**Gaus Width: 5.2320**

Entry

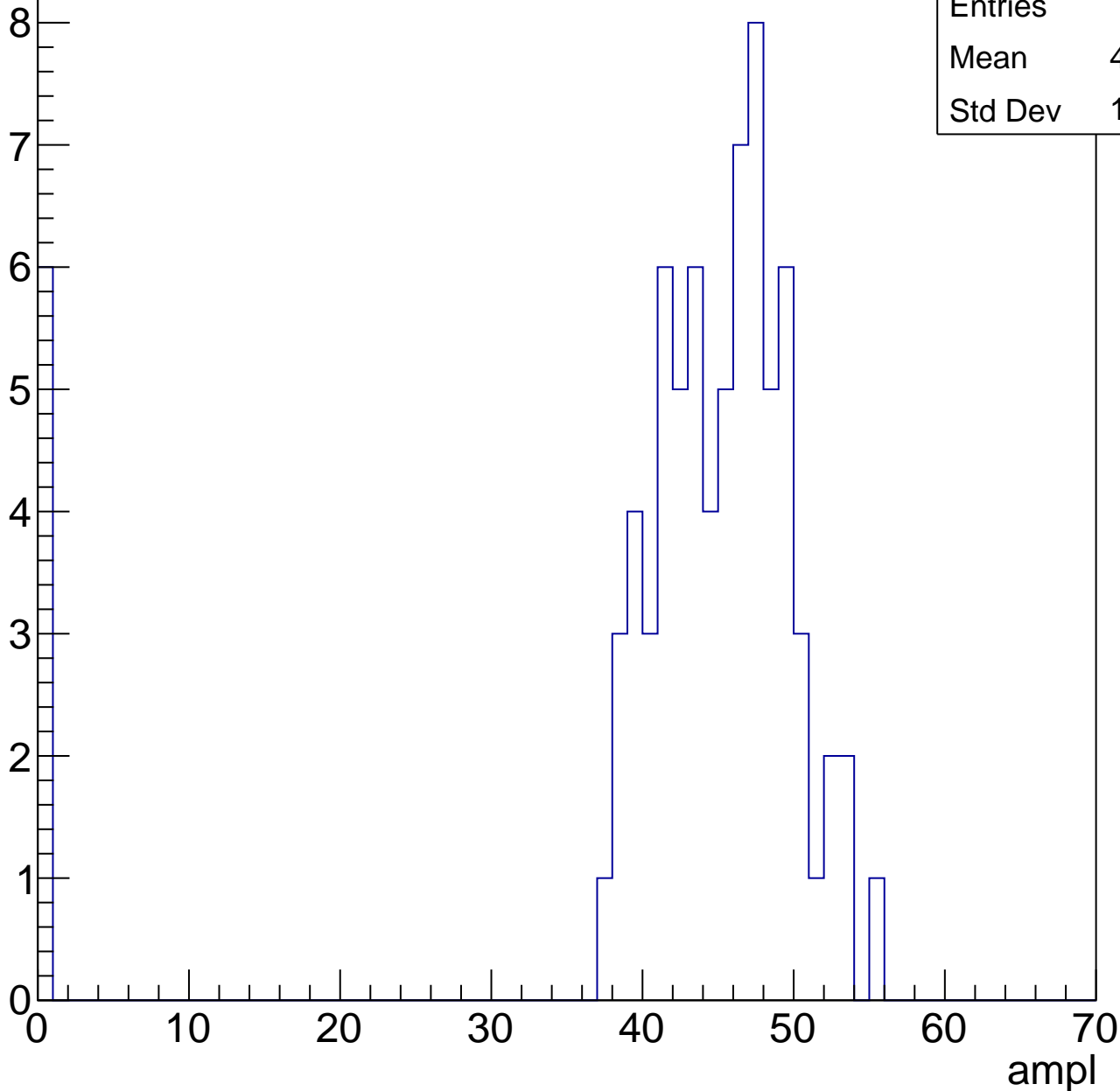


# B1L103S, U22-ch94, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	41.54
Std Dev	12.62

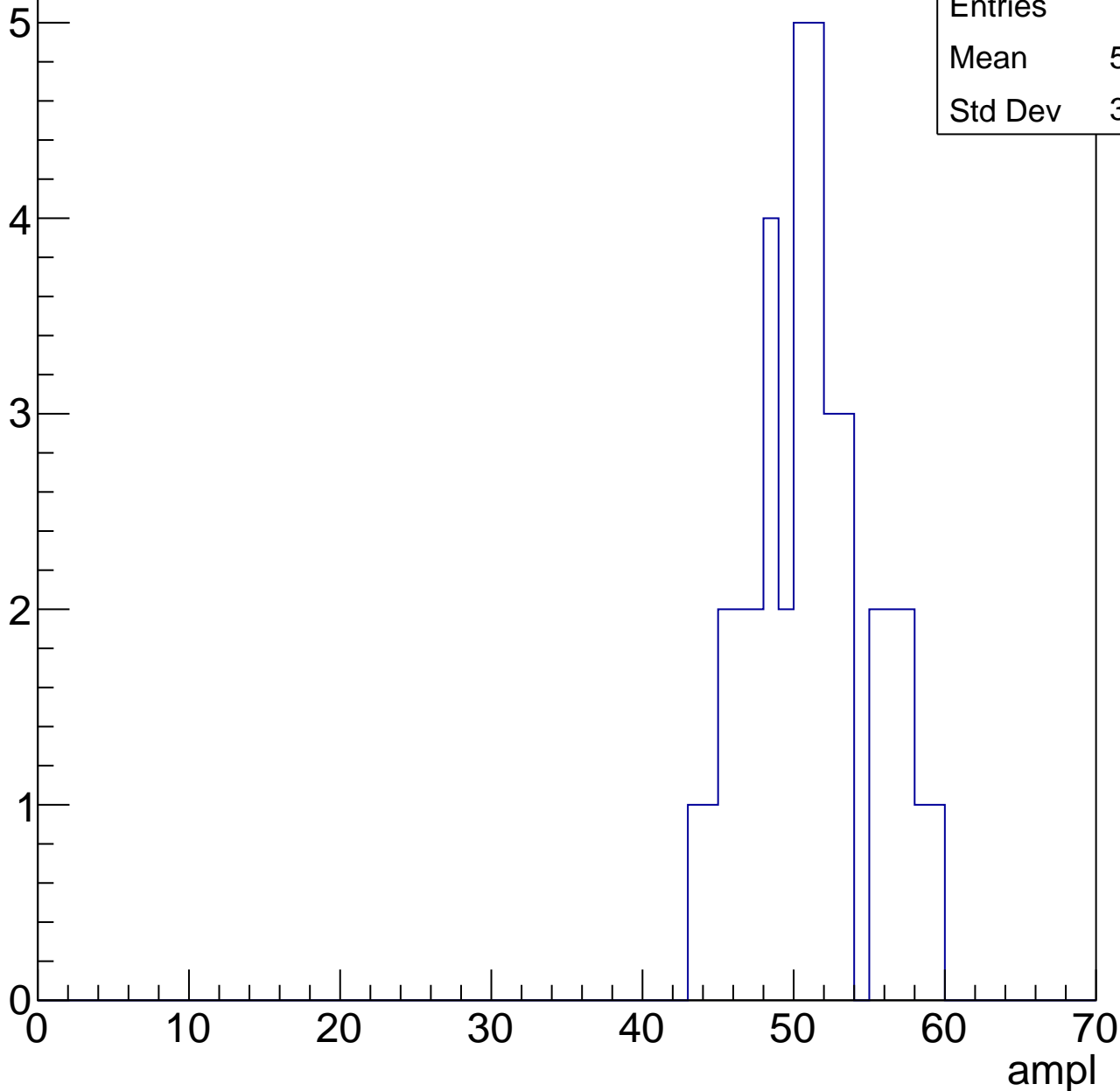


# B1L103S, U22-ch94, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	38
Mean	50.68
Std Dev	3.954

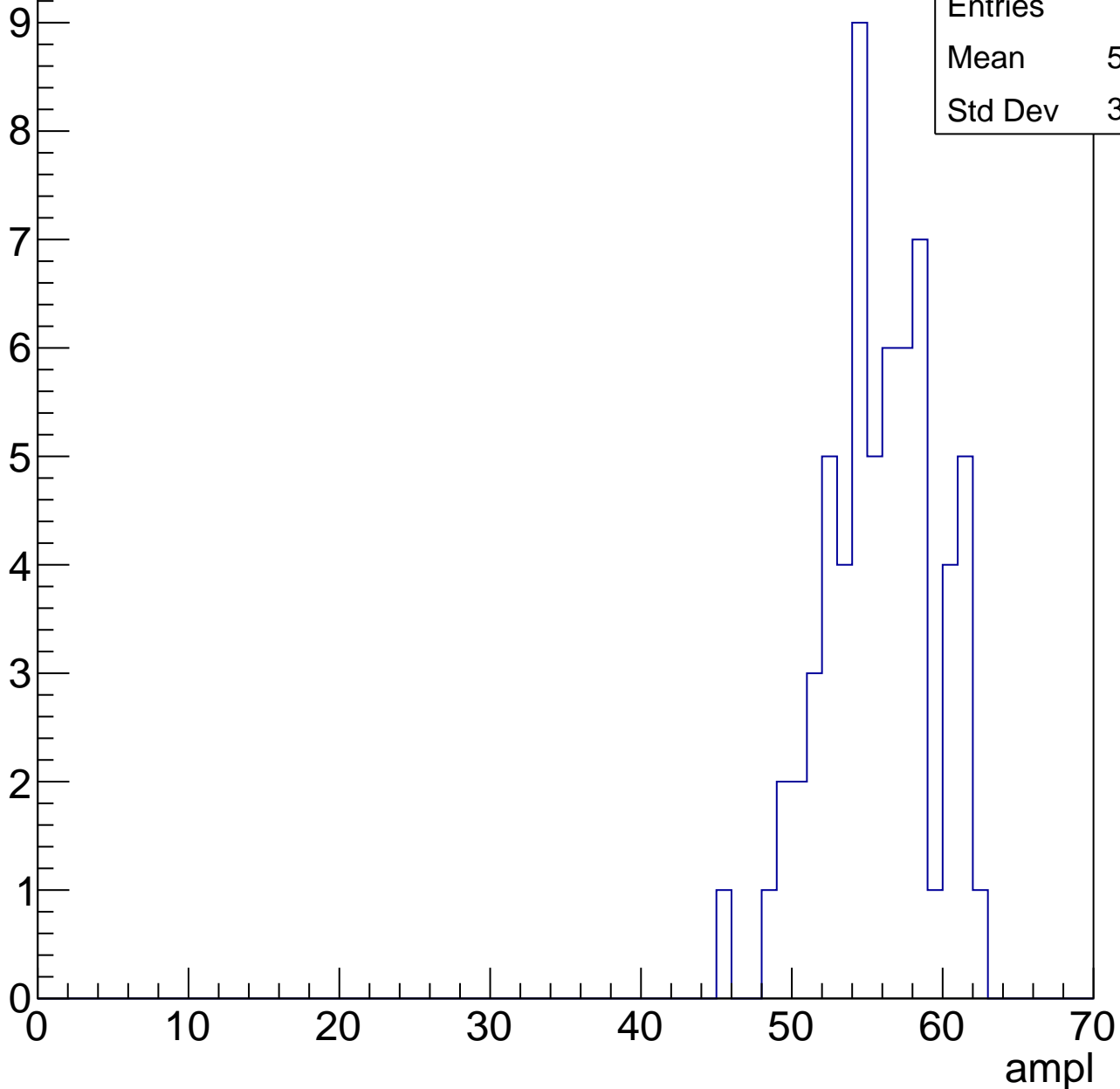


# B1L103S, U22-ch94, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

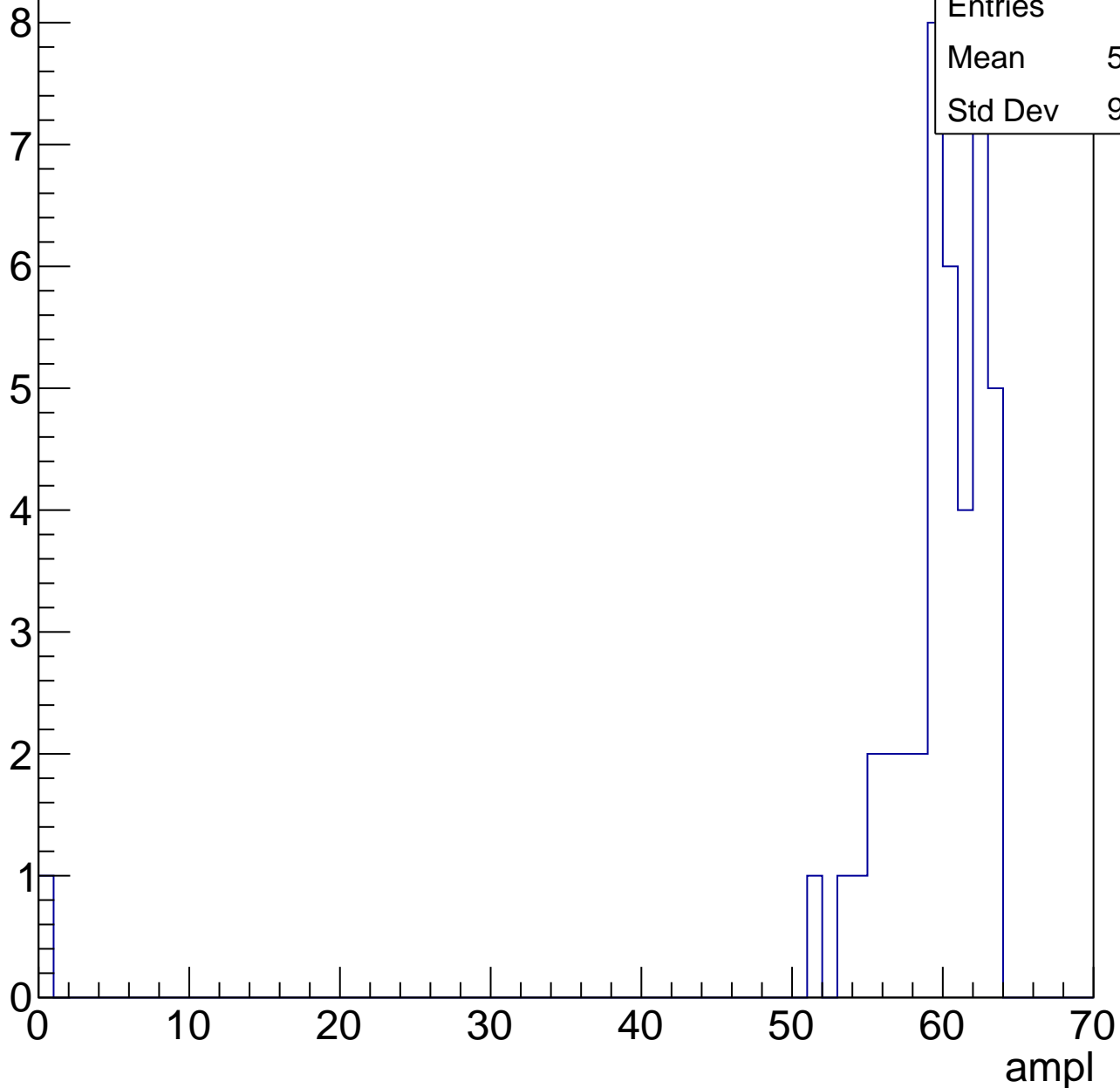
Entries	62
Mean	55.27
Std Dev	3.642



# B1L103S, U22-ch94, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch94, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

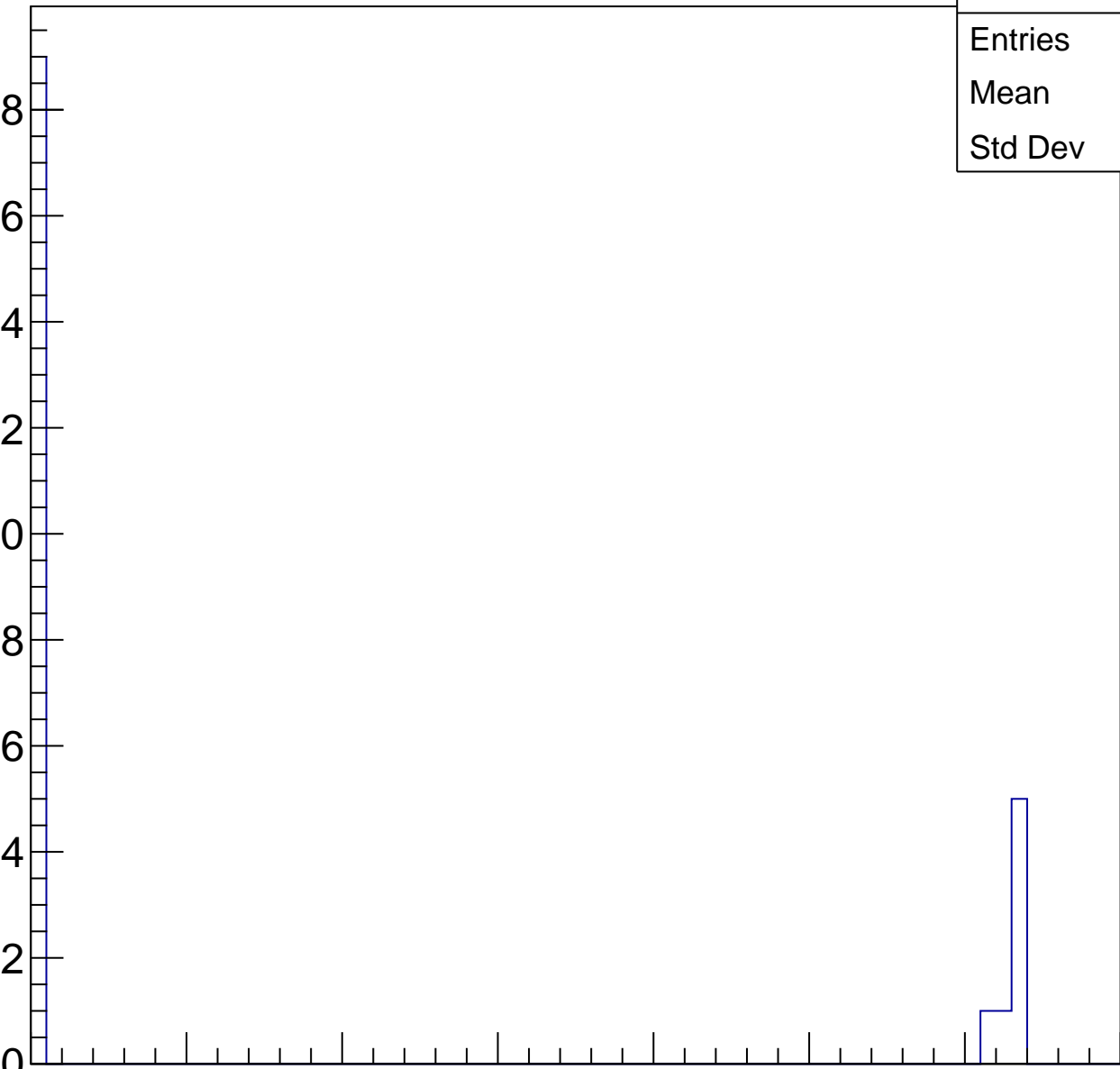
Entries	26
Mean	16.85
Std Dev	27.76

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L103S, U22-ch95, adc0

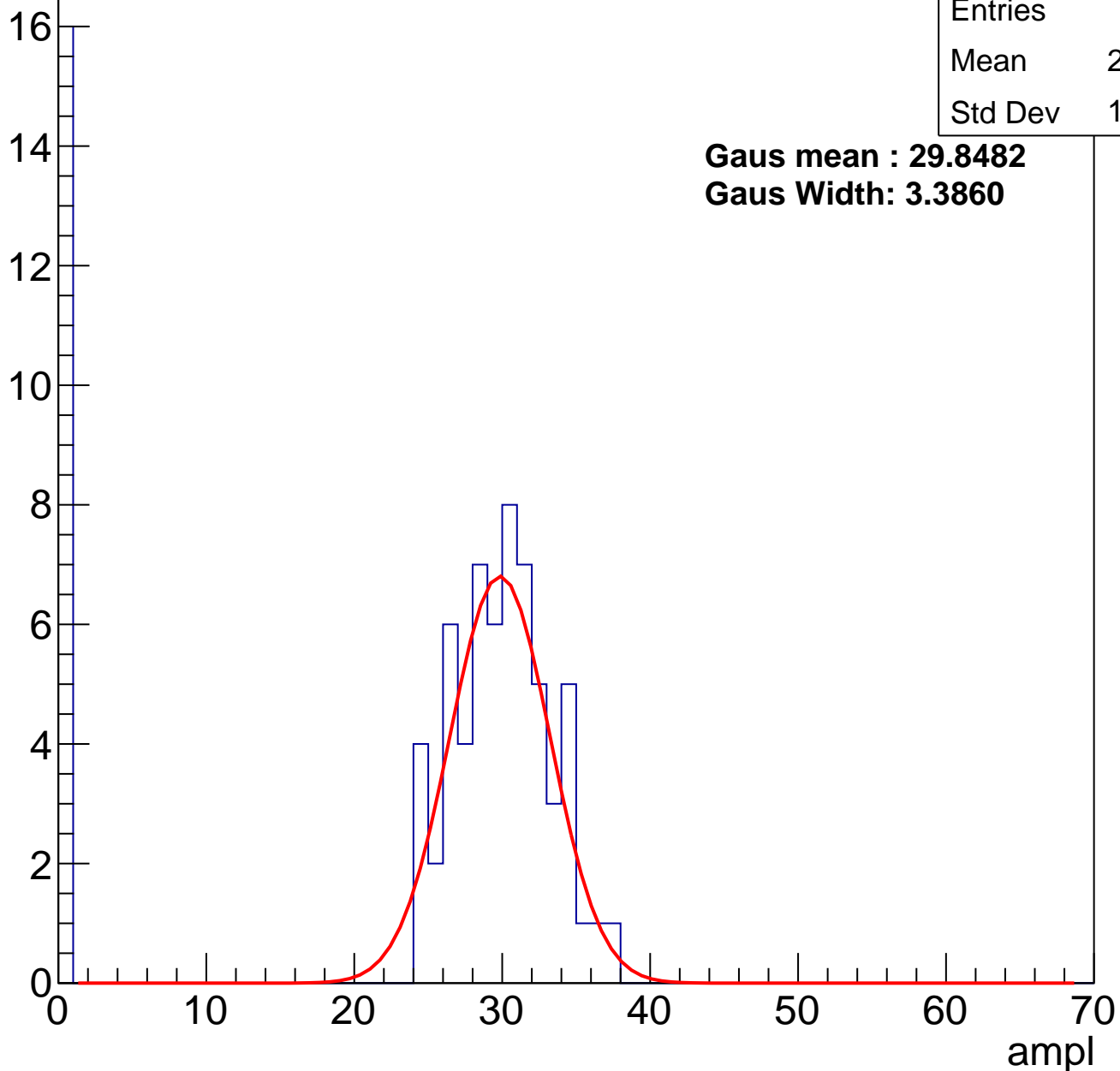
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	23.34
Std Dev	12.37

**Gaus mean : 29.8482**

**Gaus Width: 3.3860**

Entry



# B1L103S, U22-ch95, adc1

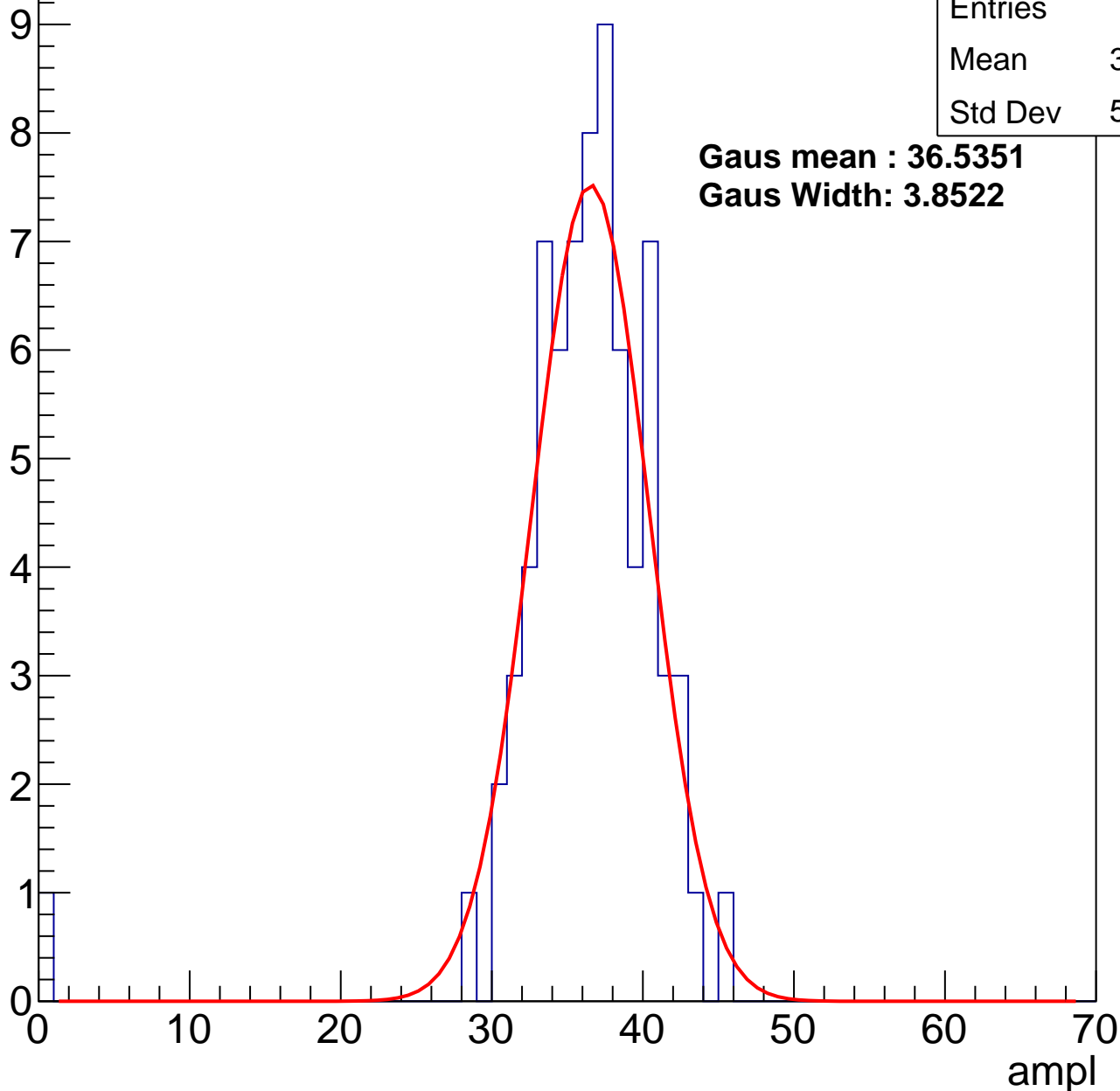
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	35.77
Std Dev	5.433

**Gaus mean : 36.5351**

**Gaus Width: 3.8522**



# B1L103S, U22-ch95, adc2

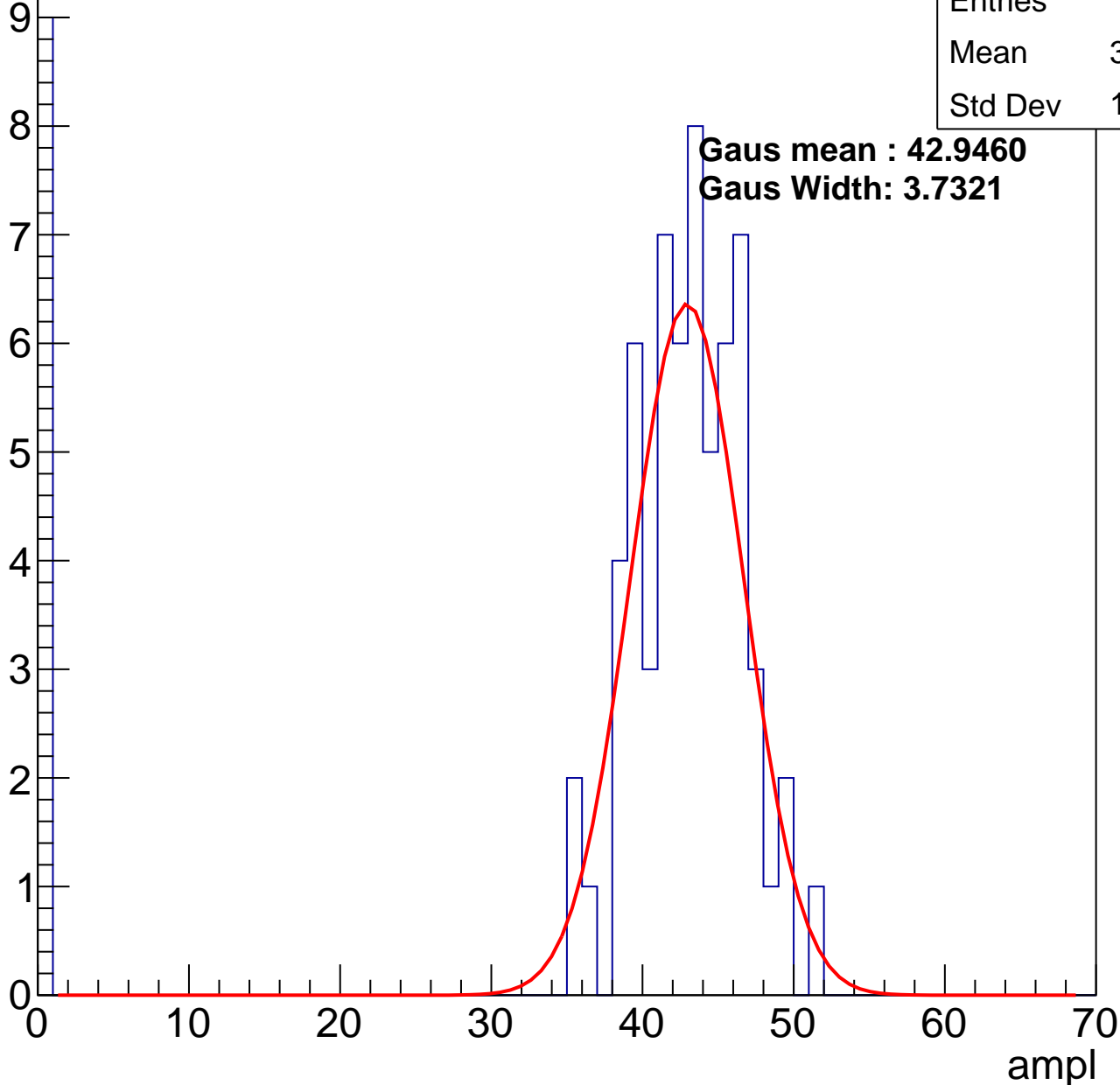
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	37.25
Std Dev	14.55

**Gaus mean : 42.9460**

**Gaus Width: 3.7321**

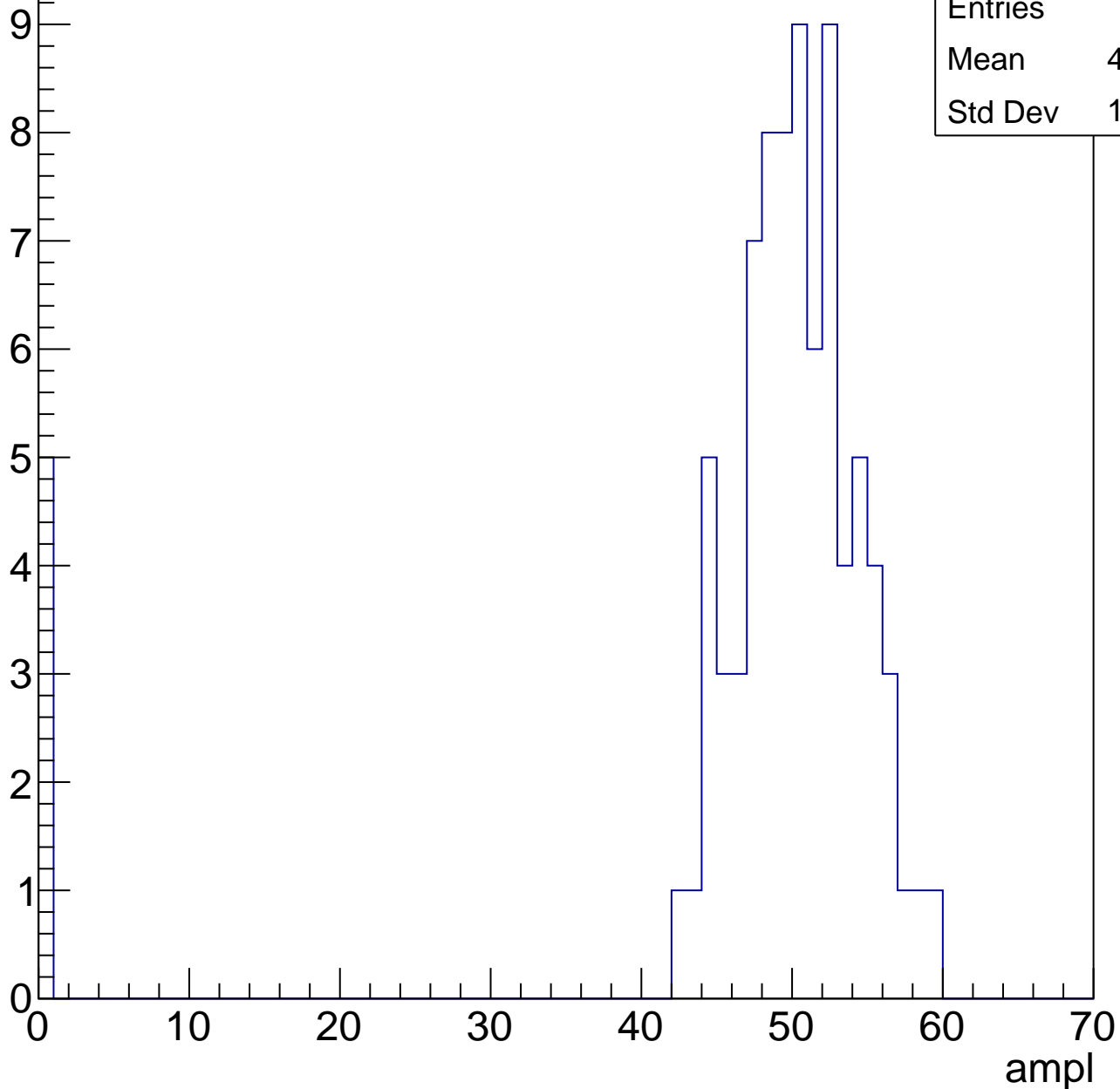


# B1L103S, U22-ch95, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	47.04
Std Dev	12.36

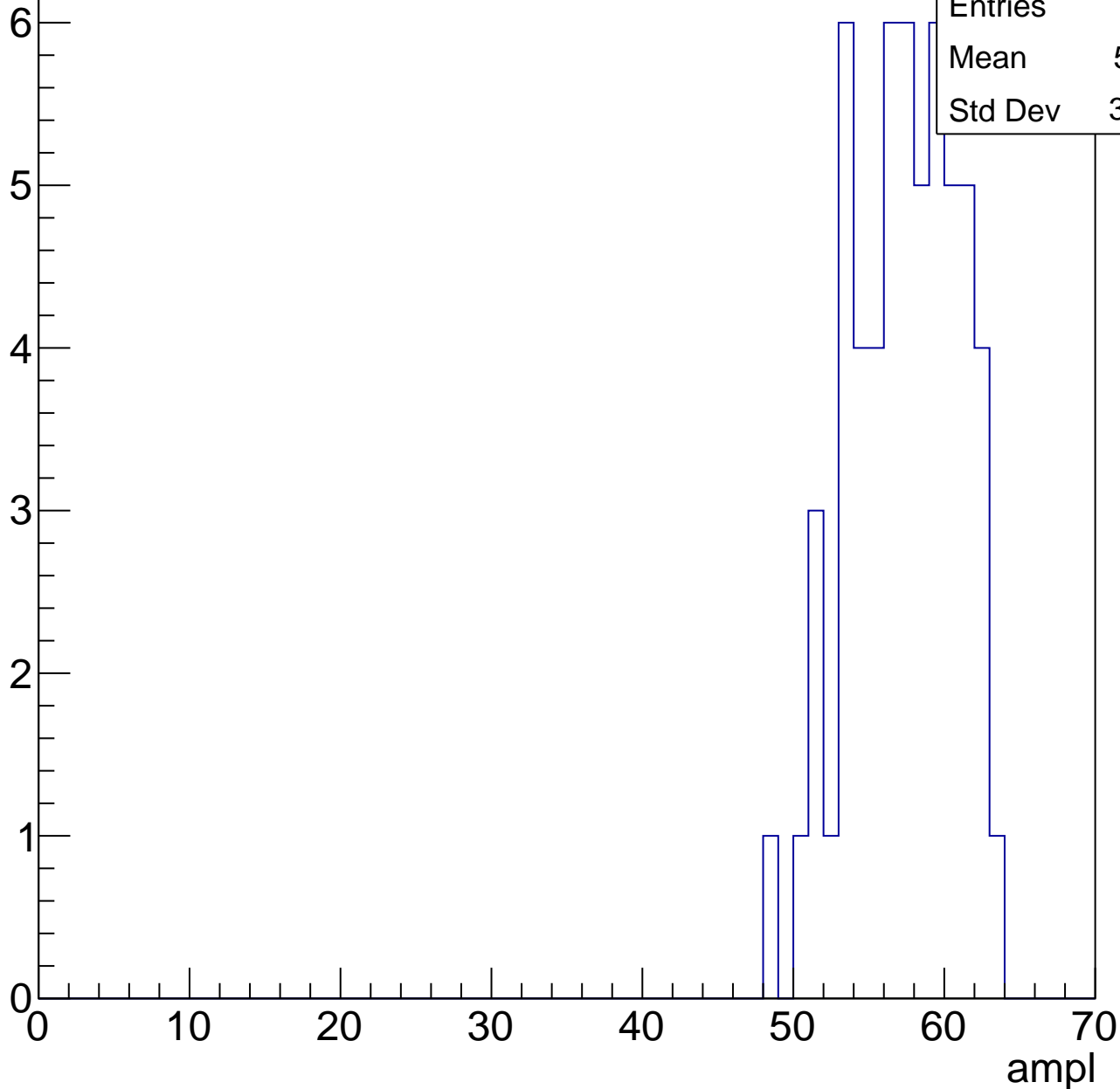


# B1L103S, U22-ch95, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	56.81
Std Dev	3.486

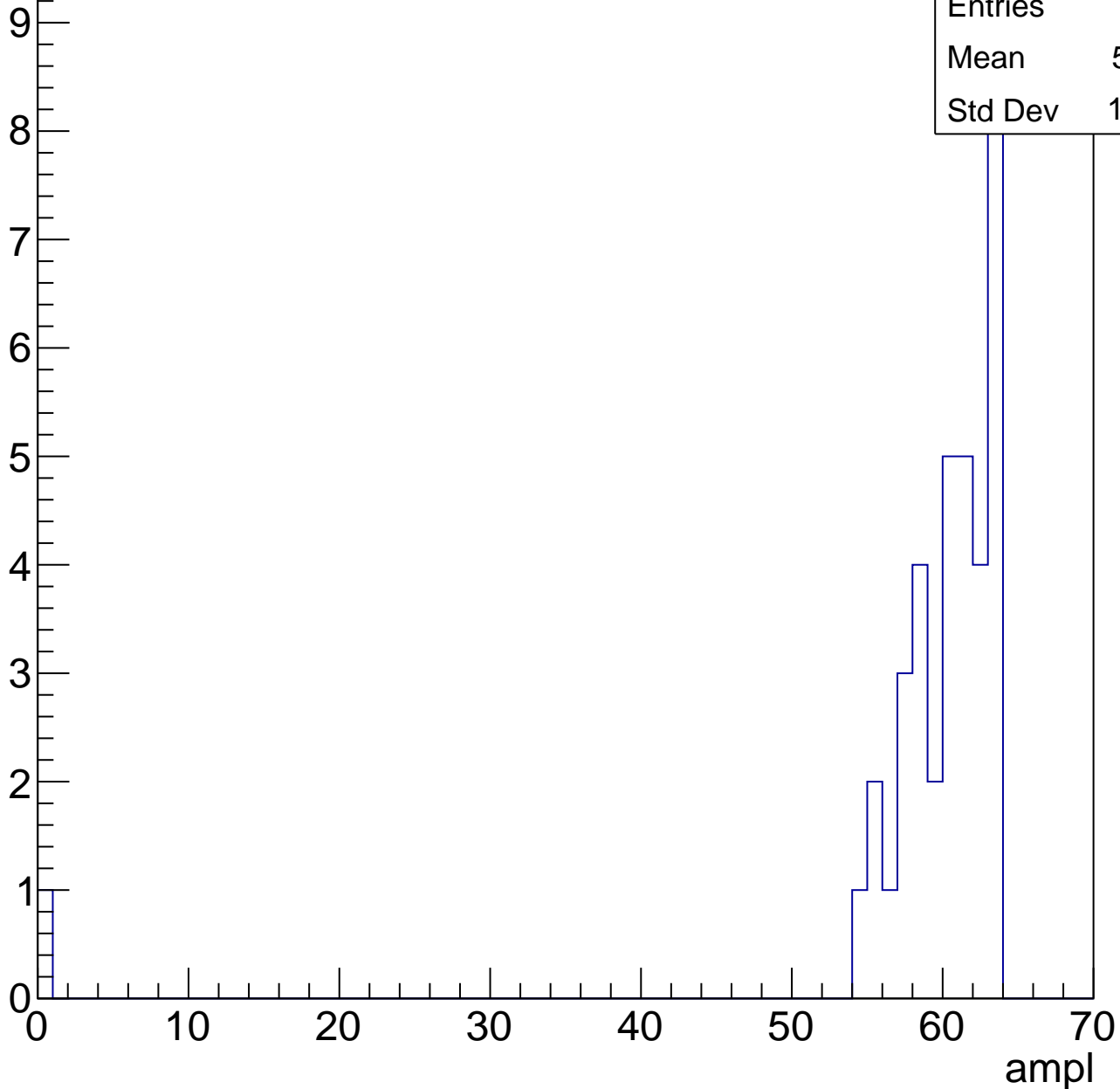


# B1L103S, U22-ch95, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	37
Mean	58.41
Std Dev	10.07



# B1L103S, U22-ch95, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch95, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	1.158
Std Dev	4.913

Entry



# B1L103S, U22-ch96, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	21.9
Std Dev	10.16

**Gaus mean : 26.7361**

**Gaus Width: 4.1834**

Entry

12

10

8

6

4

2

0

0

10

20

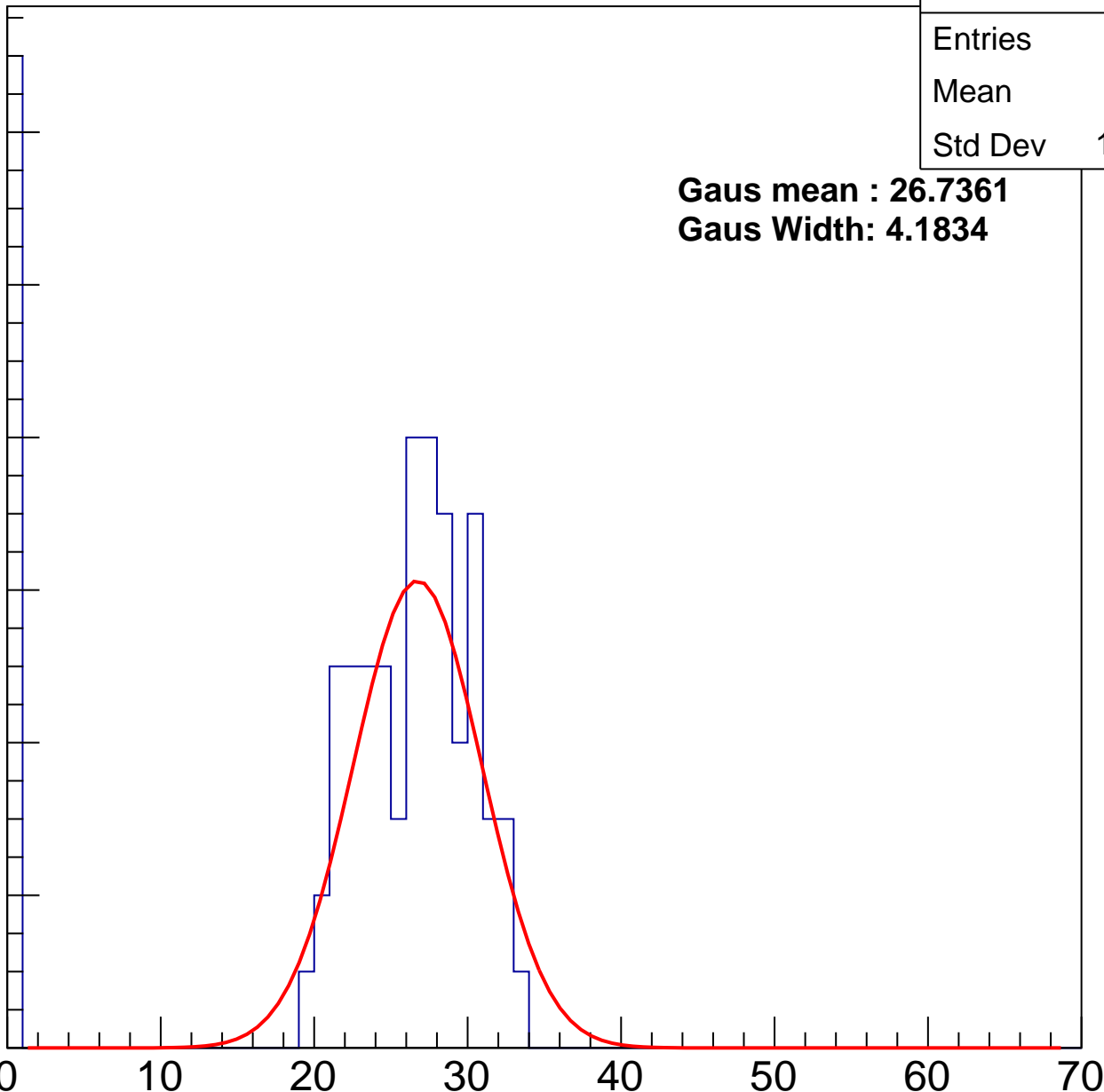
30

40

50

60

ampl



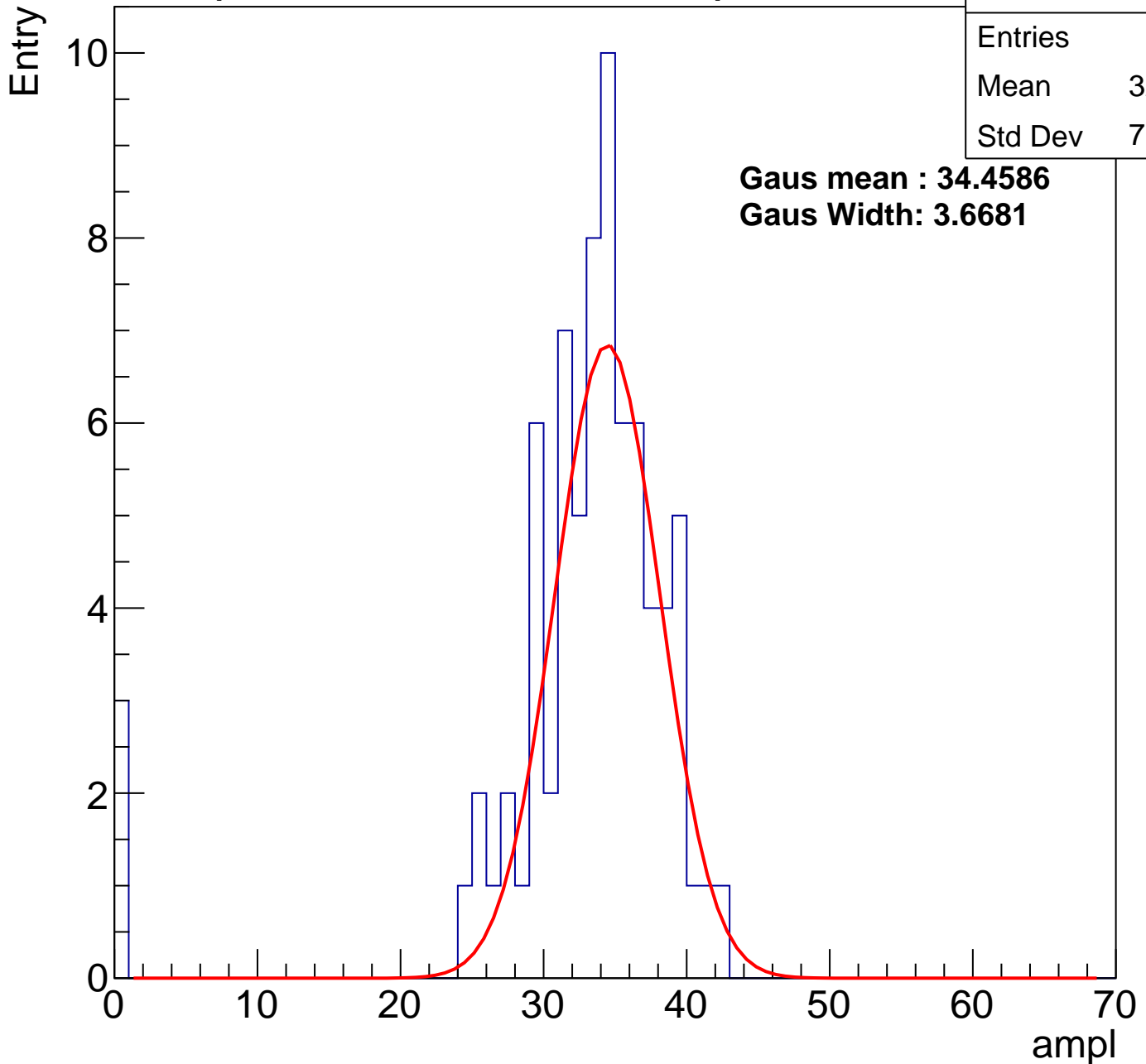
# B1L103S, U22-ch96, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	76
Mean	32.12
Std Dev	7.553

**Gaus mean : 34.4586**

**Gaus Width: 3.6681**



# B1L103S, U22-ch96, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	34.38
Std Dev	15.18

**Gaus mean : 40.4153**

**Gaus Width: 4.9414**

Entry

12

10

8

6

4

2

0

0

10

20

30

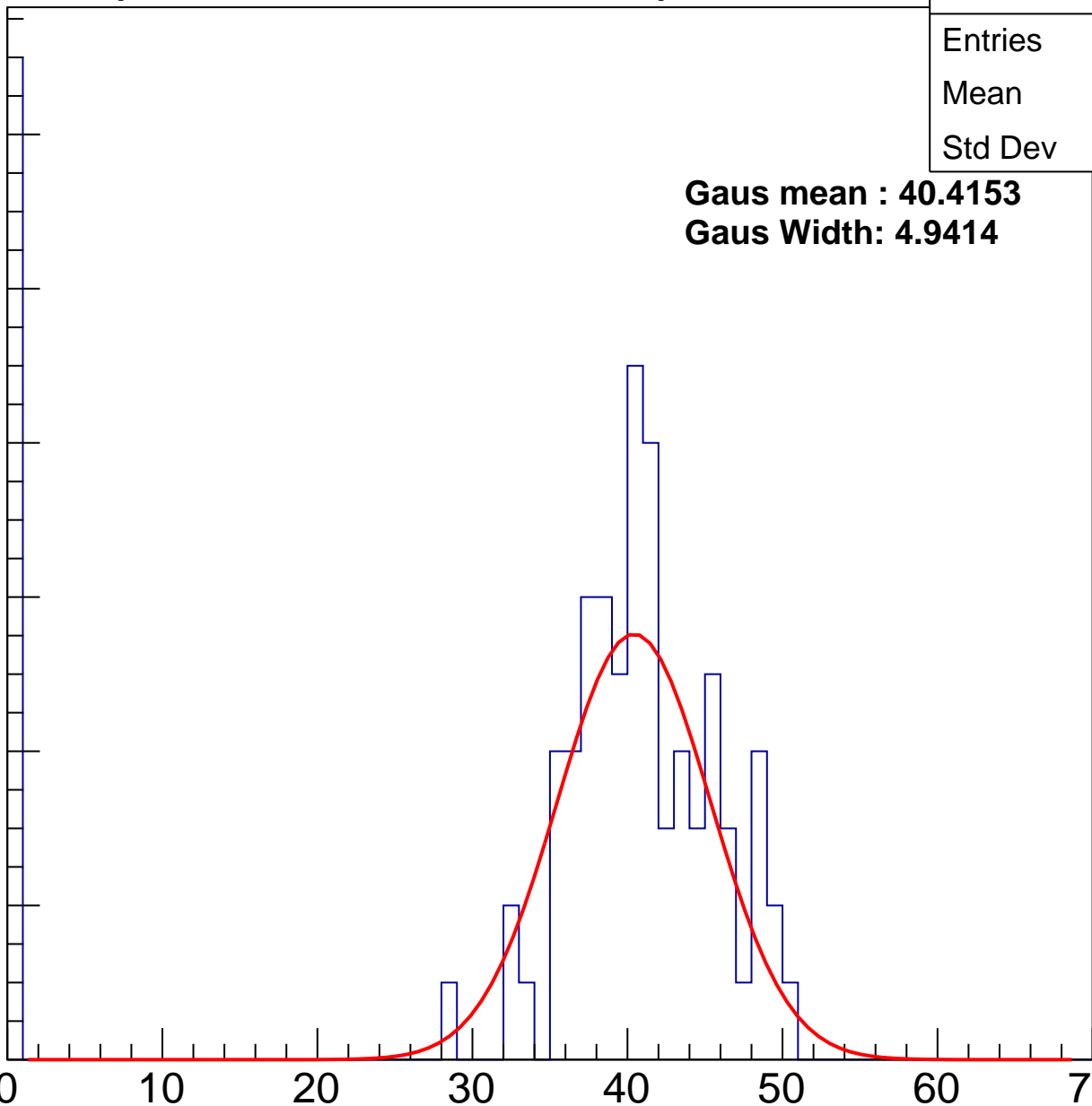
40

50

60

70

ampl

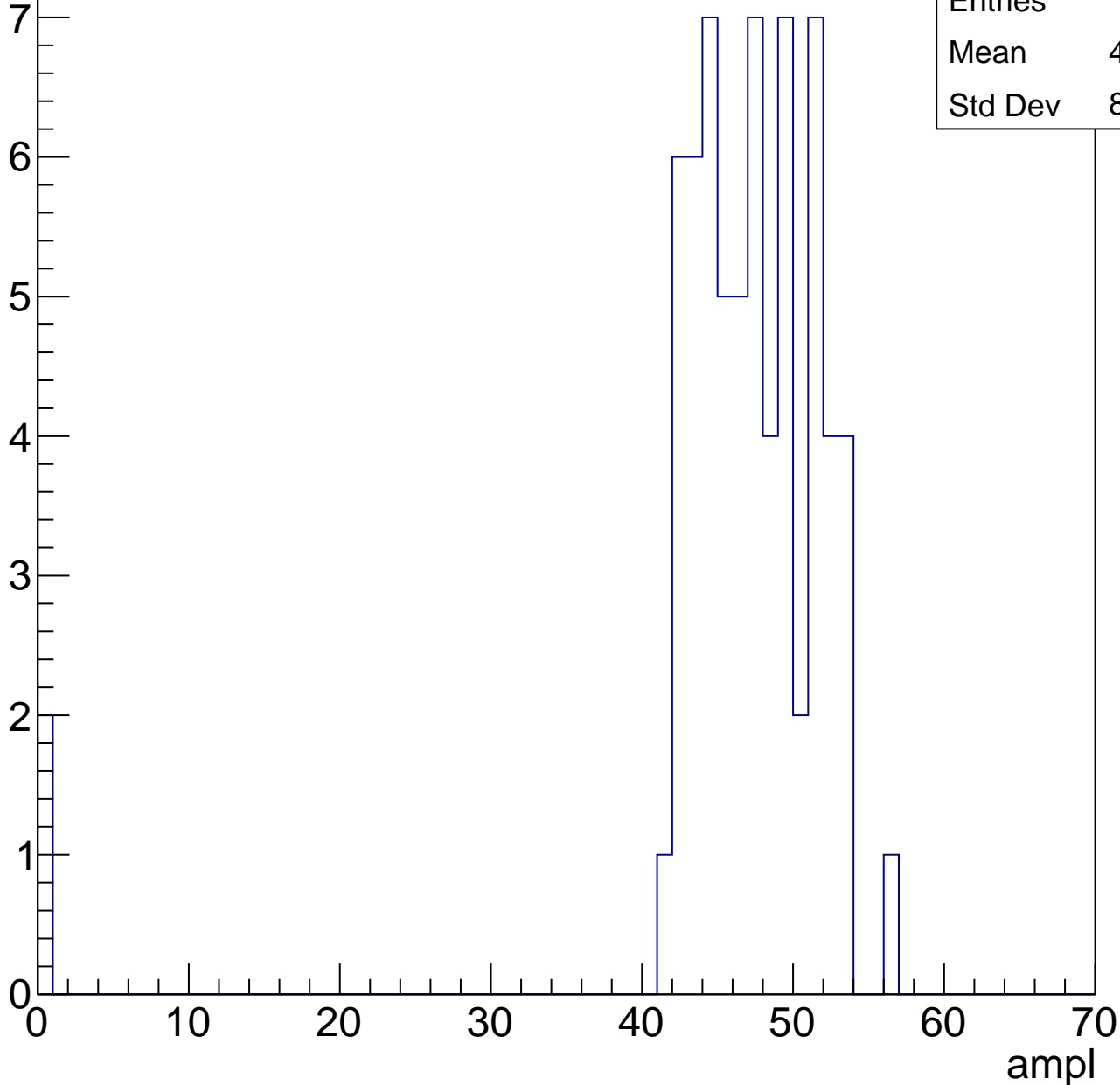


# B1L103S, U22-ch96, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	45.75
Std Dev	8.718

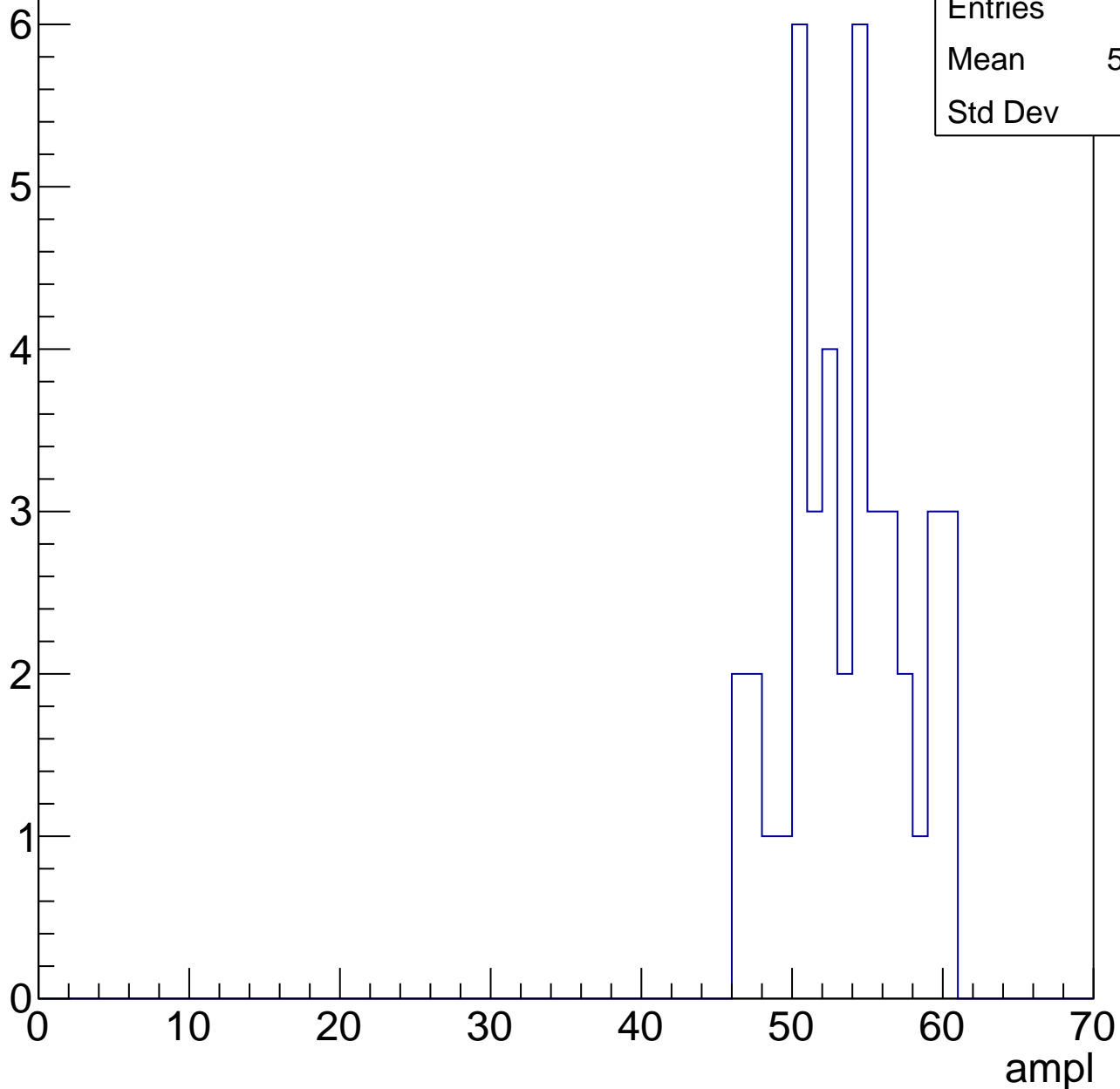


# B1L103S, U22-ch96, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

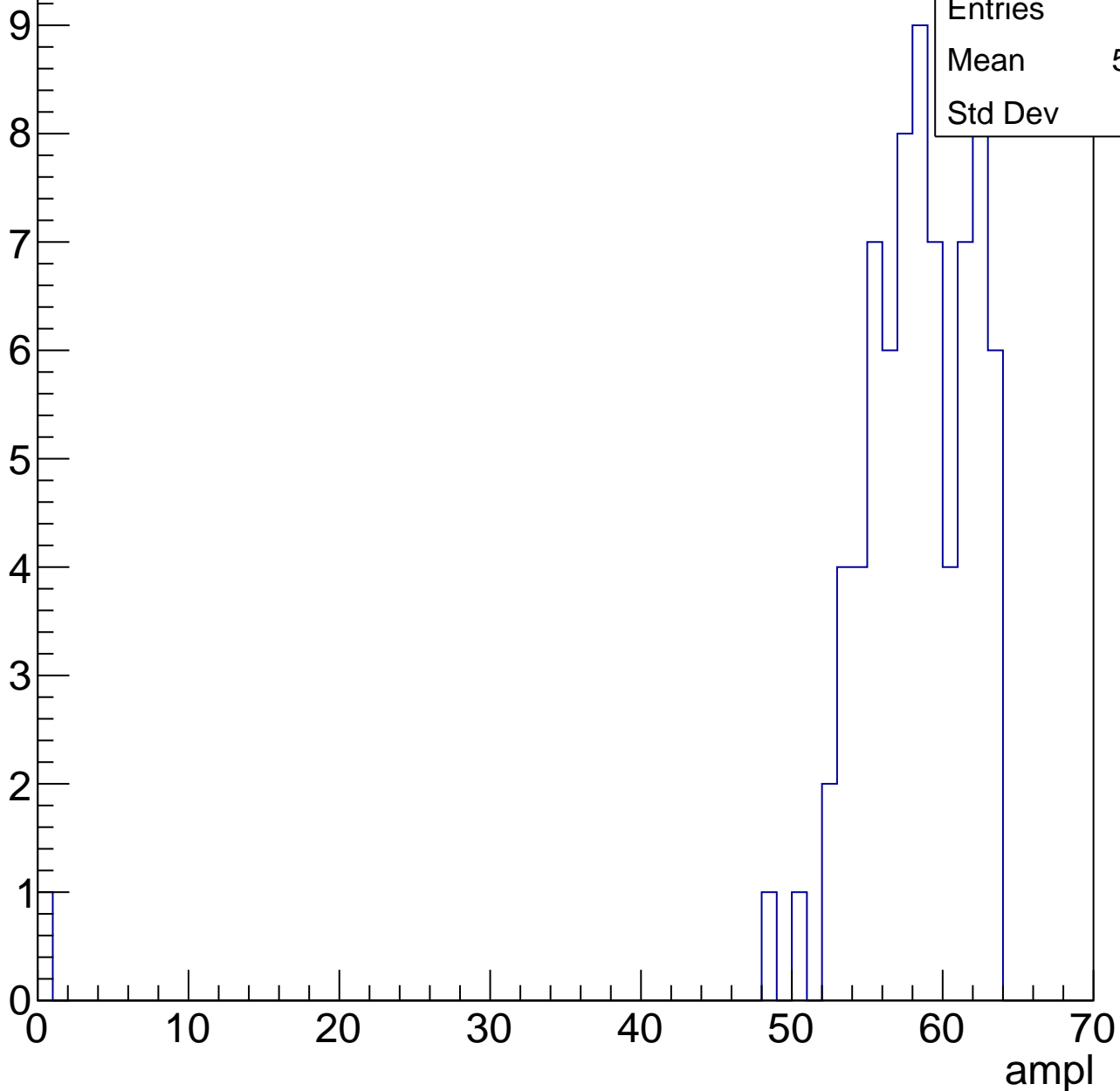
Entries	42
Mean	53.24
Std Dev	3.89



# B1L103S, U22-ch96, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch96, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.3

Std Dev

1.1

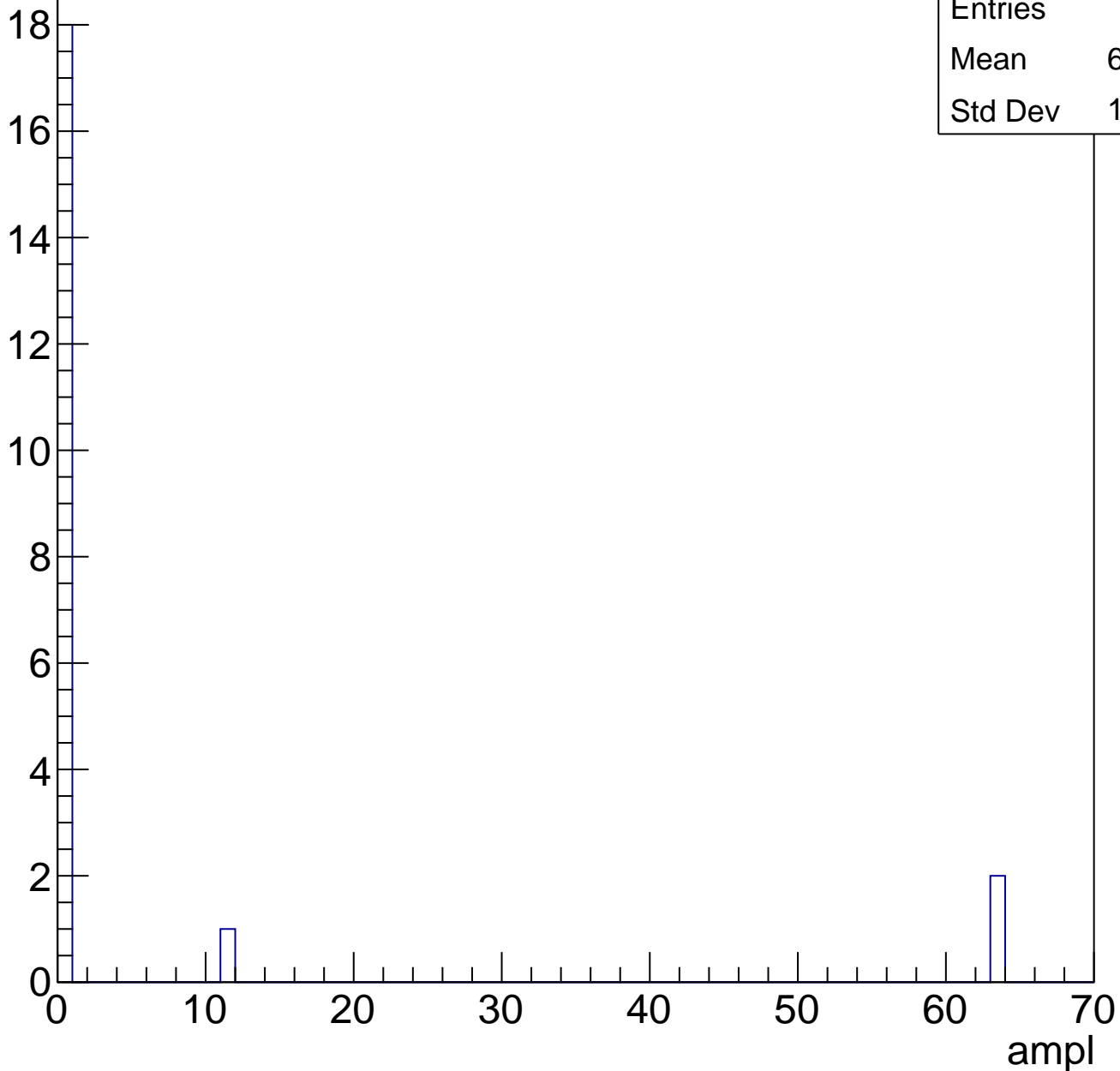


# B1L103S, U22-ch96, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	6.524
Std Dev	18.47

Entry



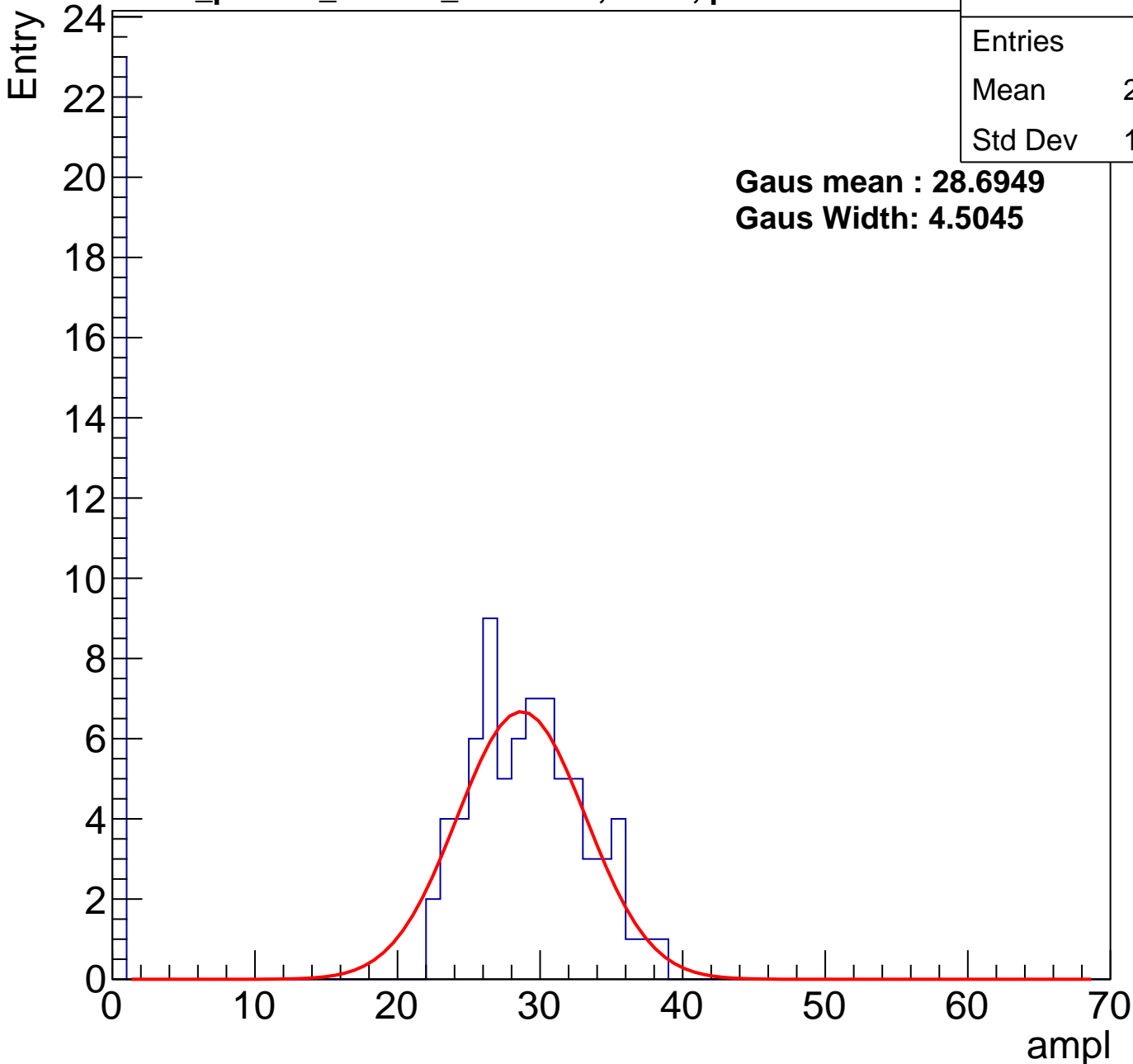
# B1L103S, U22-ch97, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	96
Mean	21.86
Std Dev	12.72

**Gaus mean : 28.6949**

**Gaus Width: 4.5045**



# B1L103S, U22-ch97, adc1

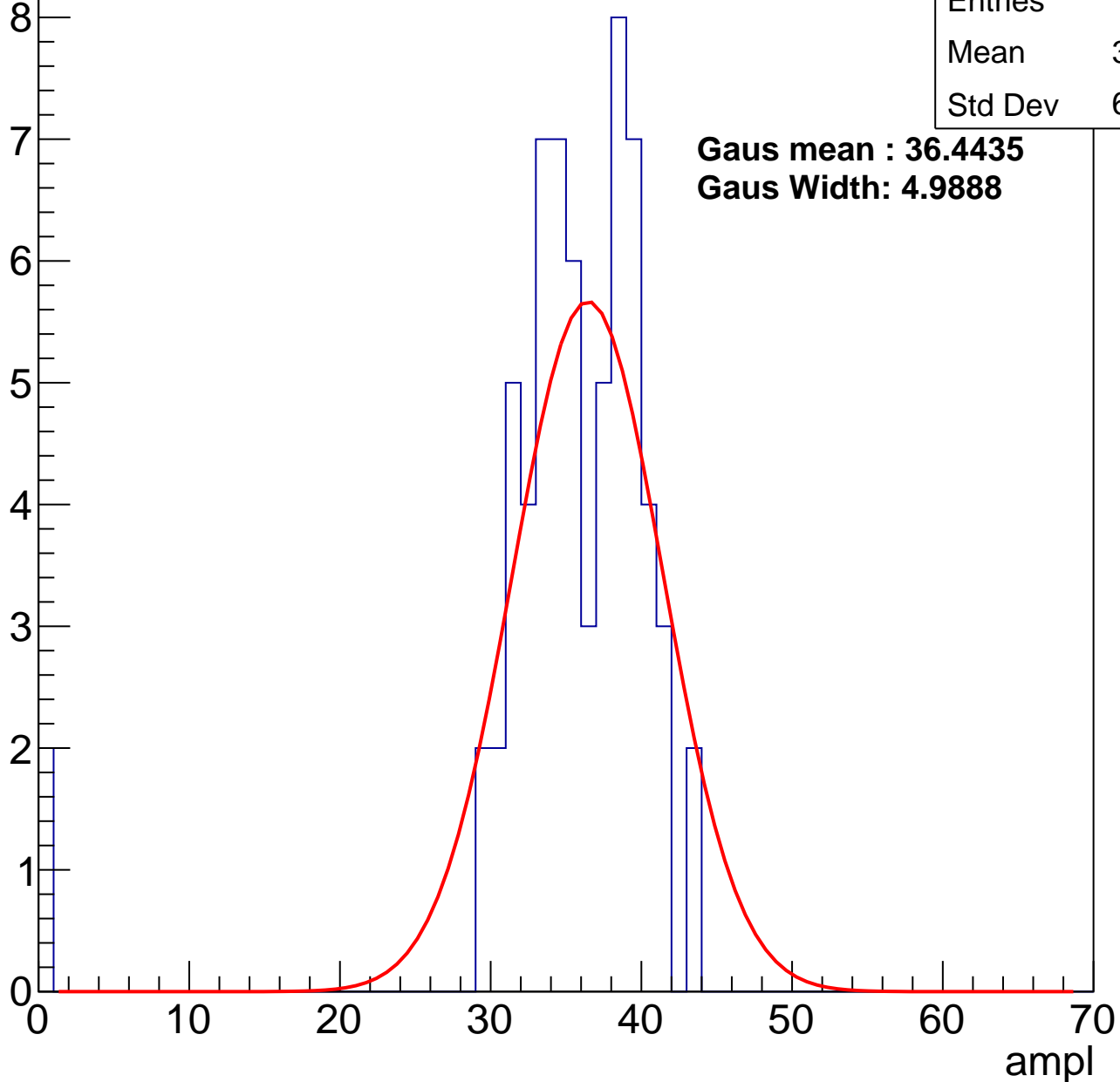
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	34.61
Std Dev	6.971

**Gaus mean : 36.4435**

**Gaus Width: 4.9888**



# B1L103S, U22-ch97, adc2

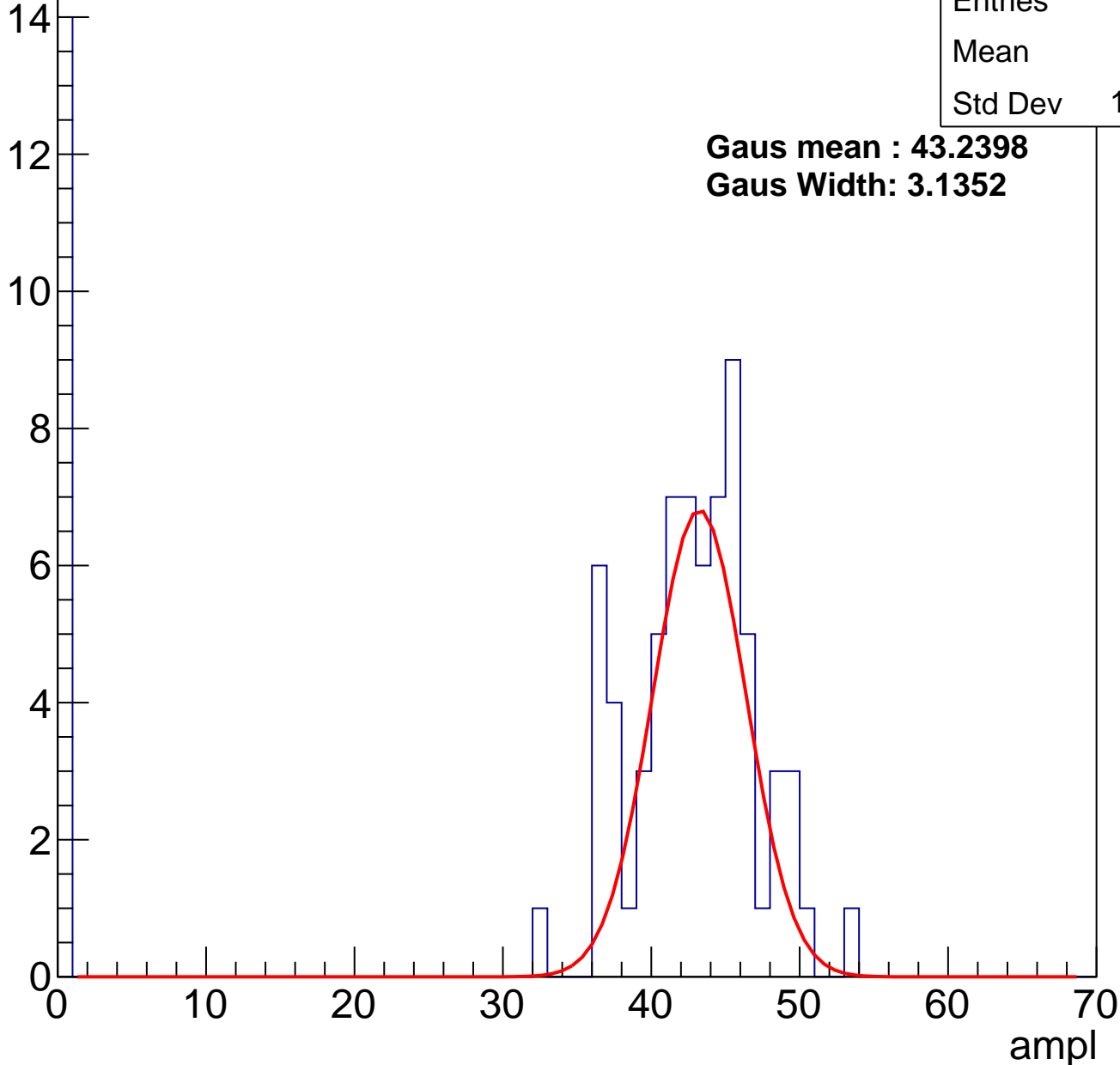
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	35.4
Std Dev	16.25

**Gaus mean : 43.2398**

**Gaus Width: 3.1352**

Entry

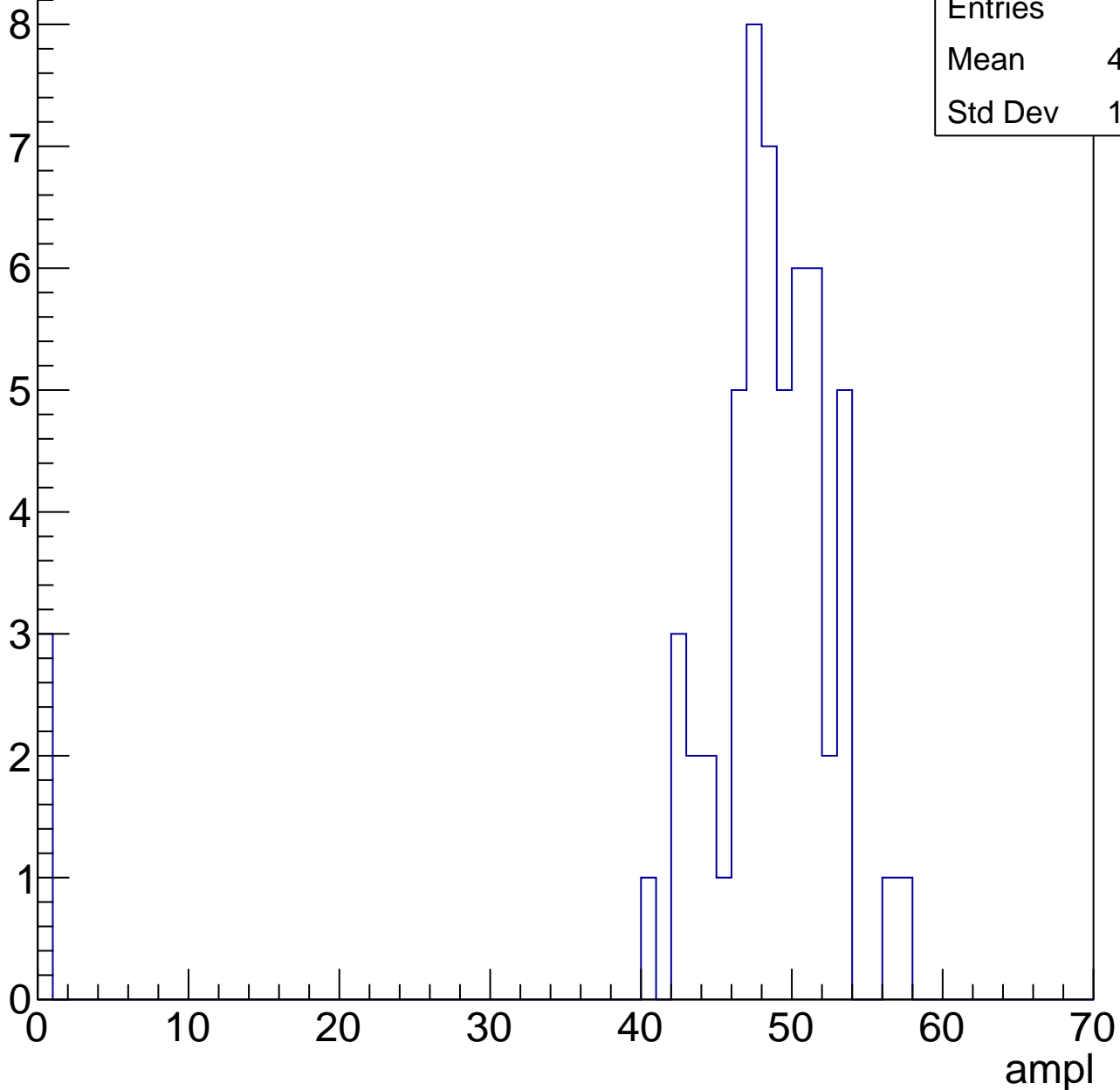


# B1L103S, U22-ch97, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	45.86
Std Dev	11.24

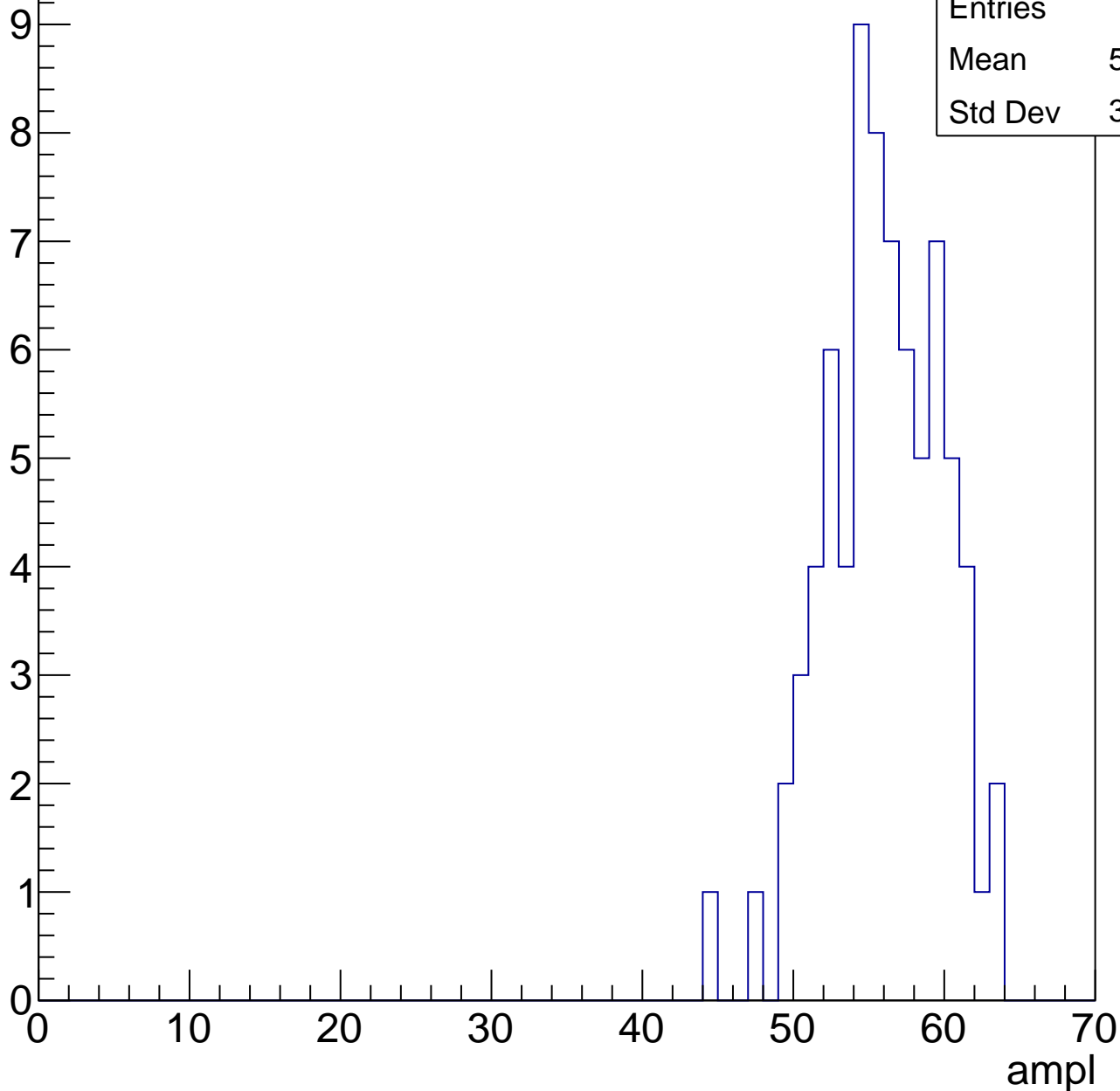


# B1L103S, U22-ch97, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	55.49
Std Dev	3.814

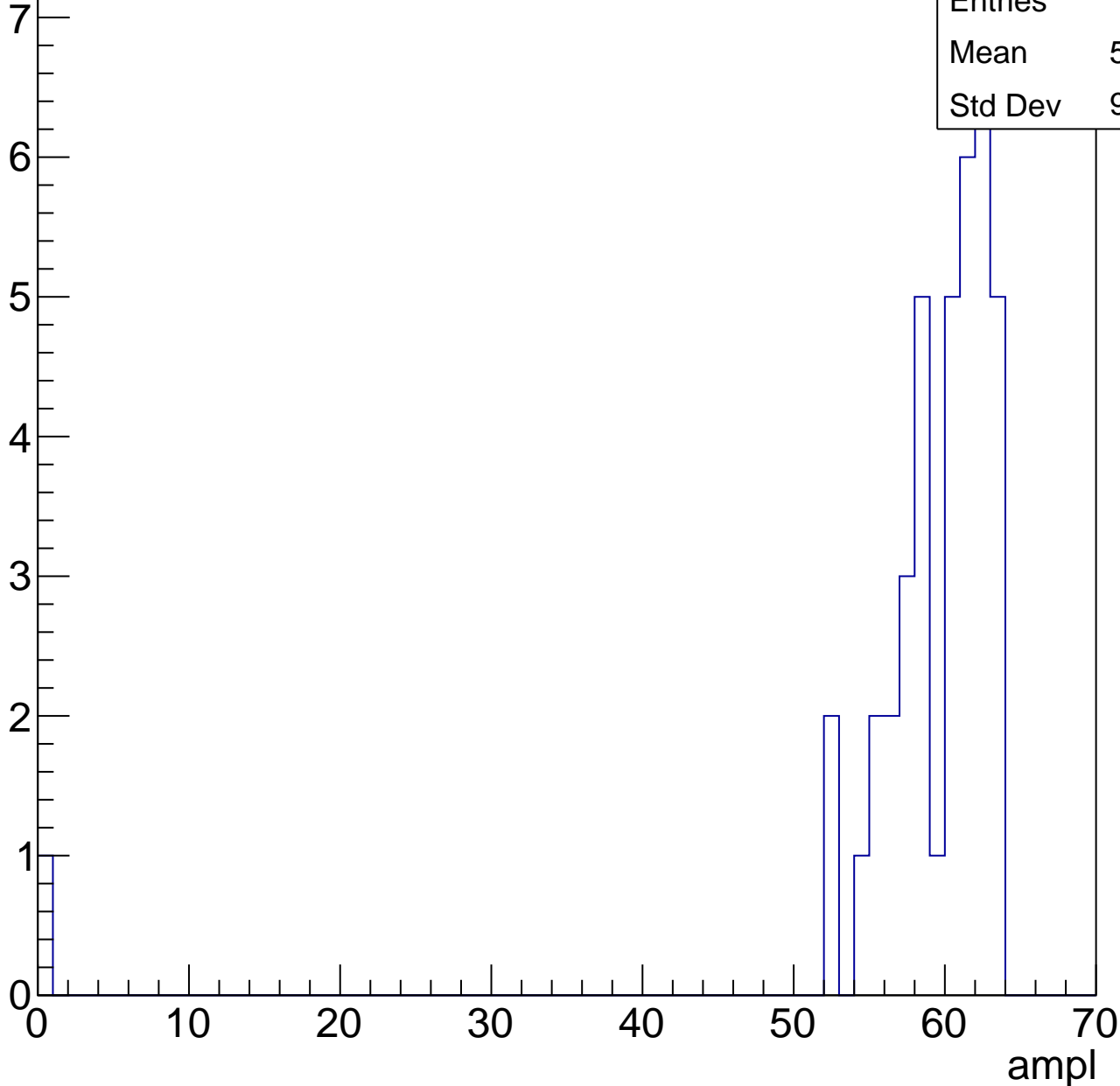


# B1L103S, U22-ch97, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

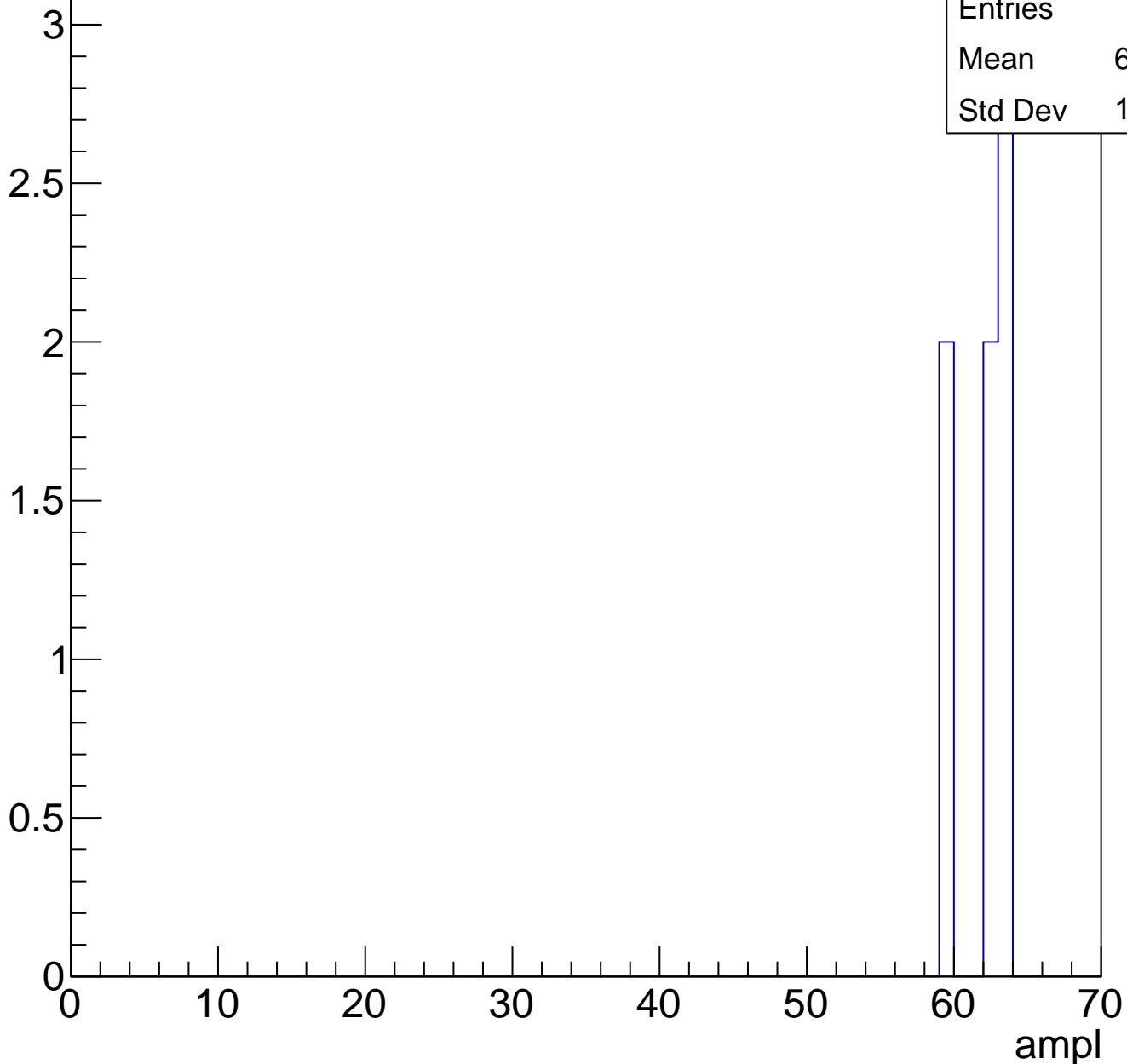
Entries	40
Mean	57.88
Std Dev	9.737



# B1L103S, U22-ch97, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



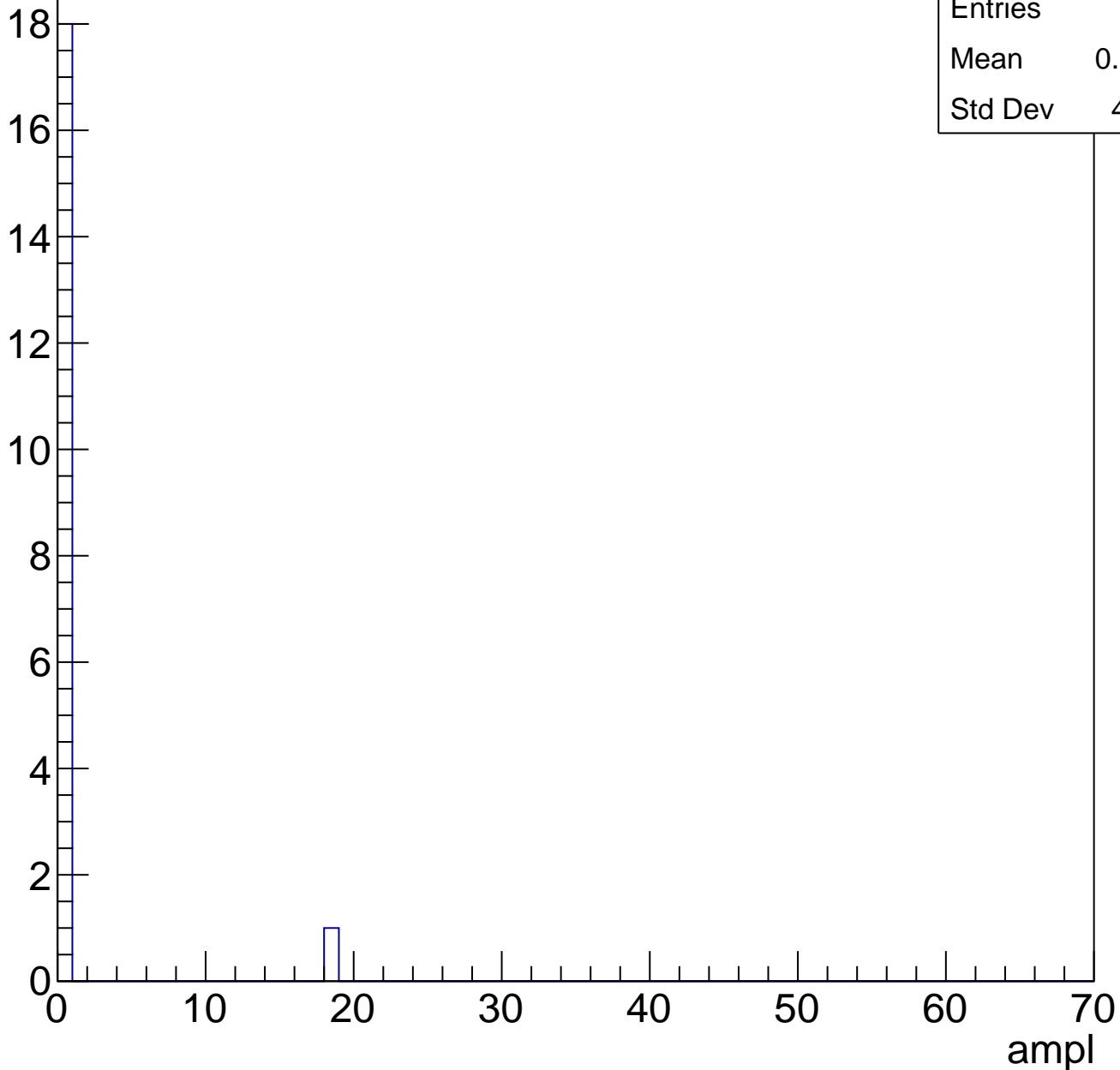


# B1L103S, U22-ch97, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	0.9474
Std Dev	4.019

Entry



# B1L103S, U22-ch98, adc0

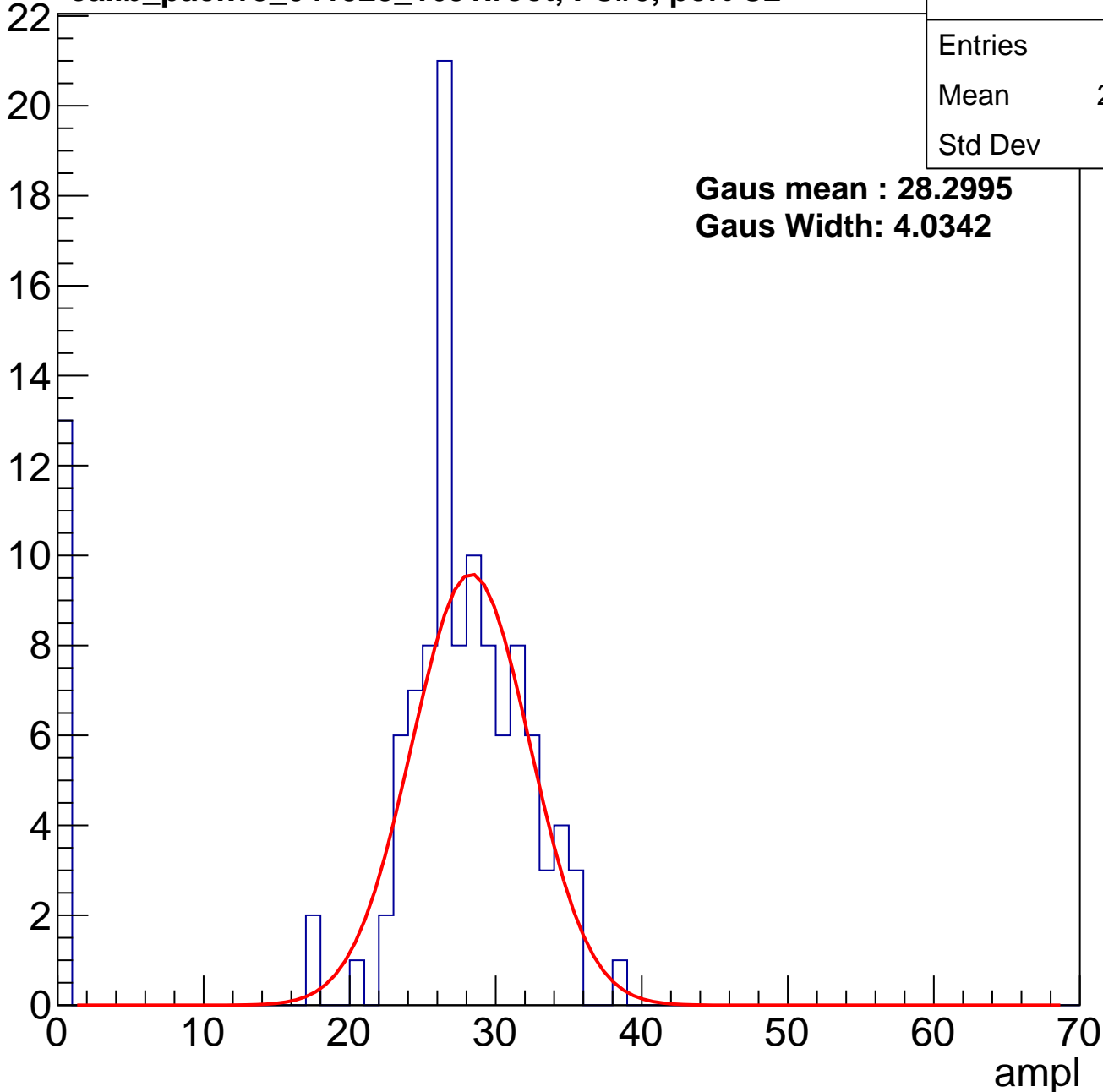
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	117
Mean	24.58
Std Dev	9.38

**Gaus mean : 28.2995**

**Gaus Width: 4.0342**

Entry



# B1L103S, U22-ch98, adc1

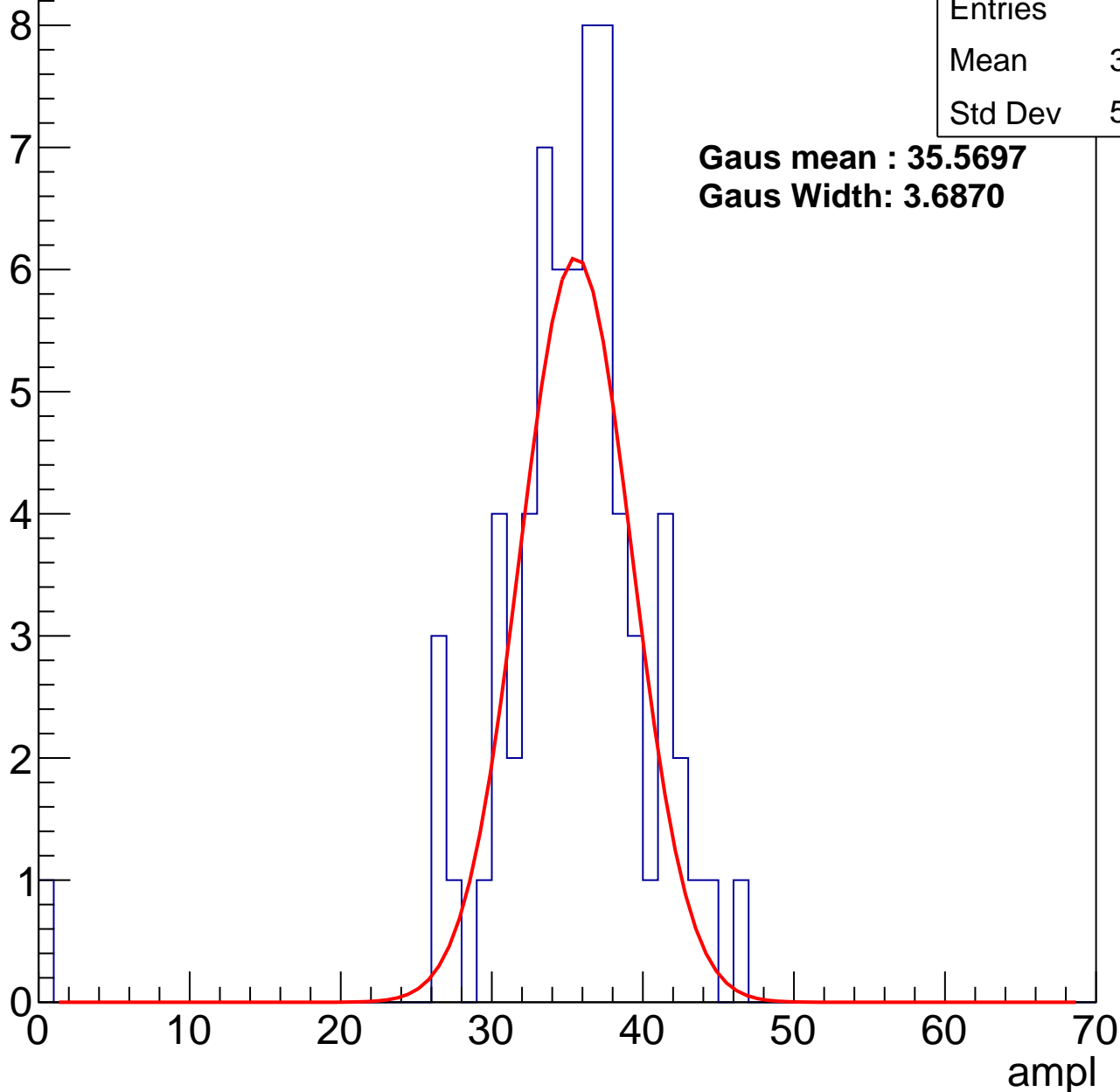
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	34.75
Std Dev	5.969

**Gaus mean : 35.5697**

**Gaus Width: 3.6870**



# B1L103S, U22-ch98, adc2

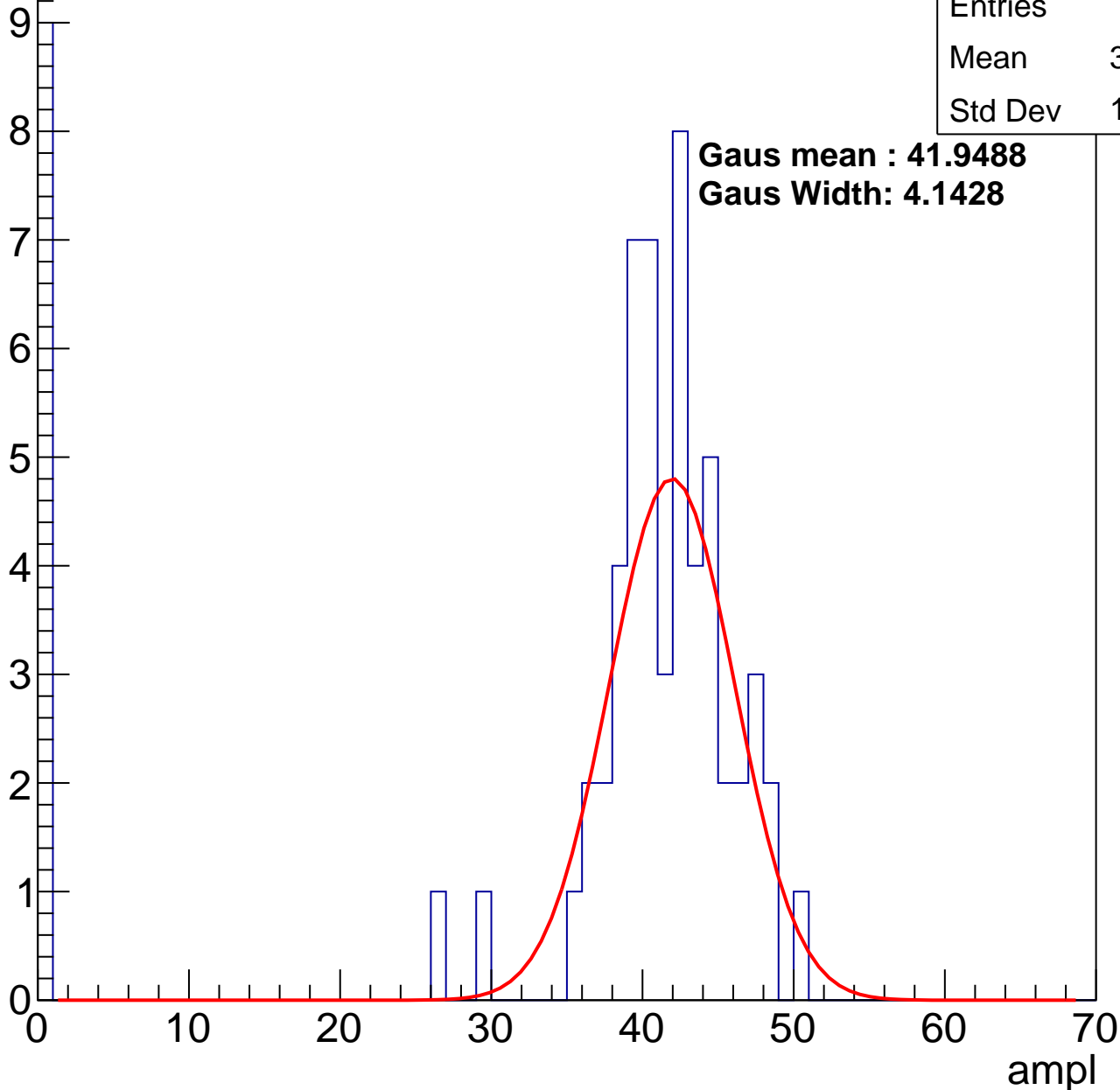
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	35.33
Std Dev	14.83

**Gaus mean : 41.9488**

**Gaus Width: 4.1428**

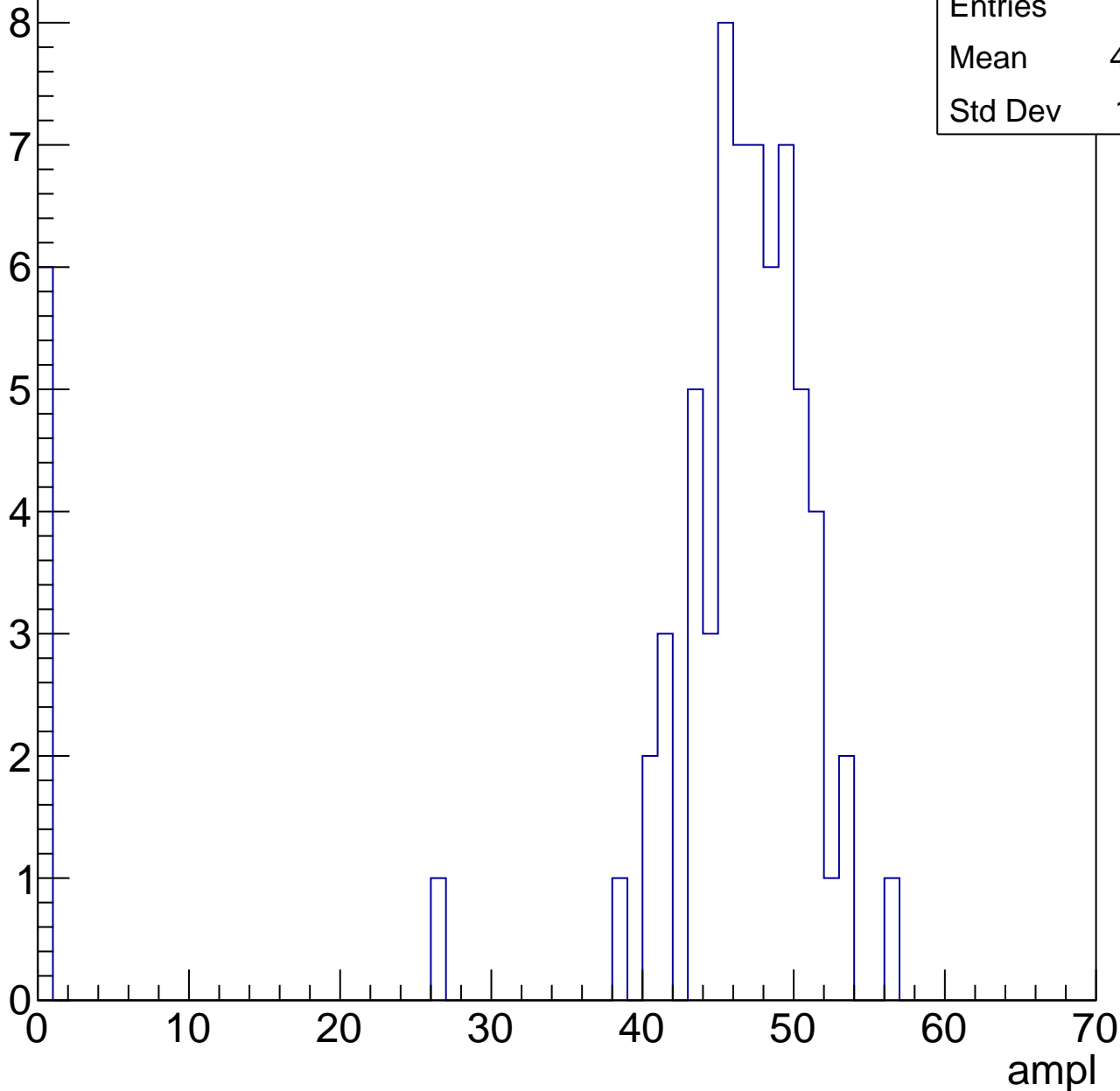


# B1L103S, U22-ch98, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	42.38
Std Dev	13.71

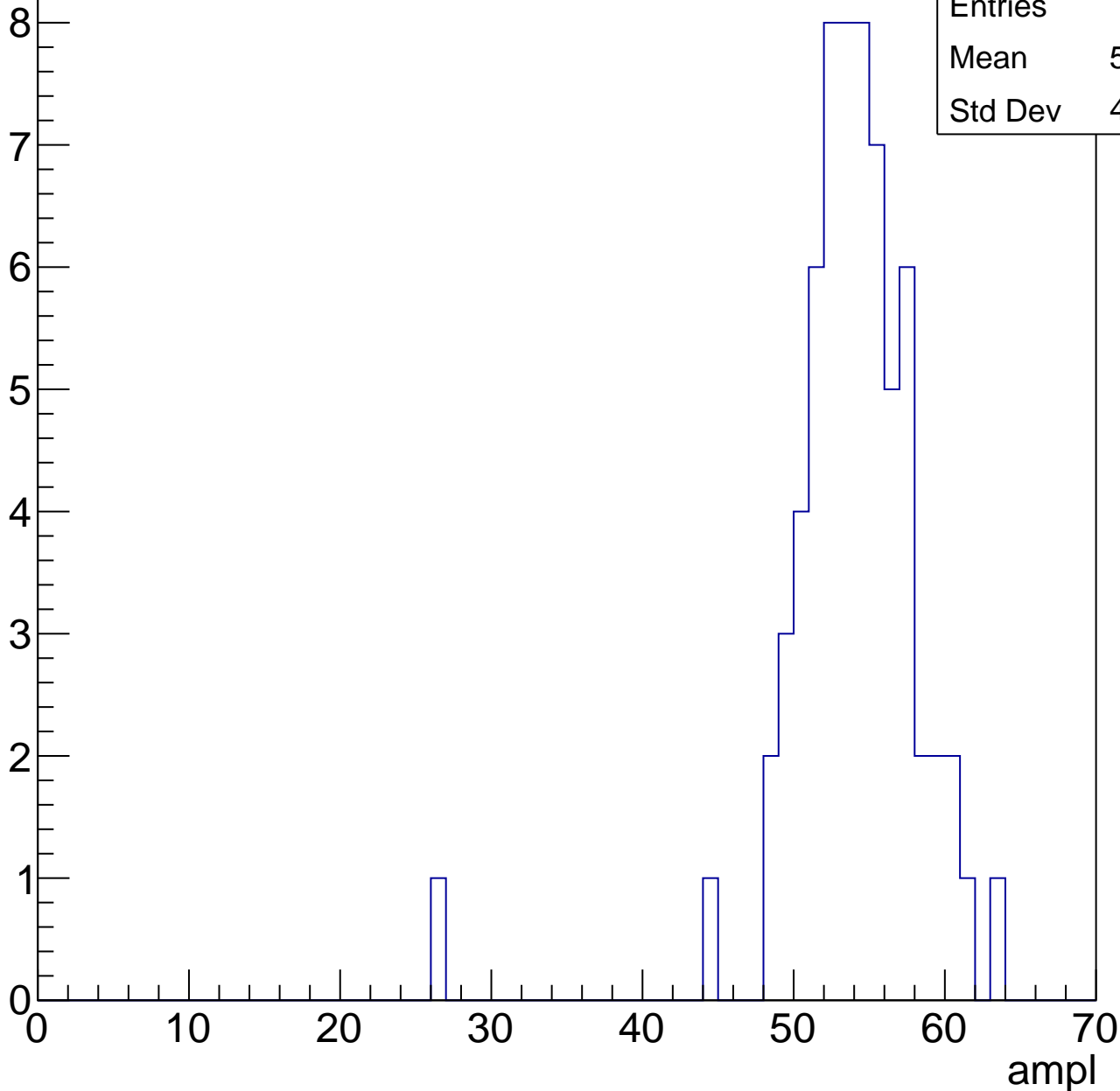


# B1L103S, U22-ch98, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	53.37
Std Dev	4.775

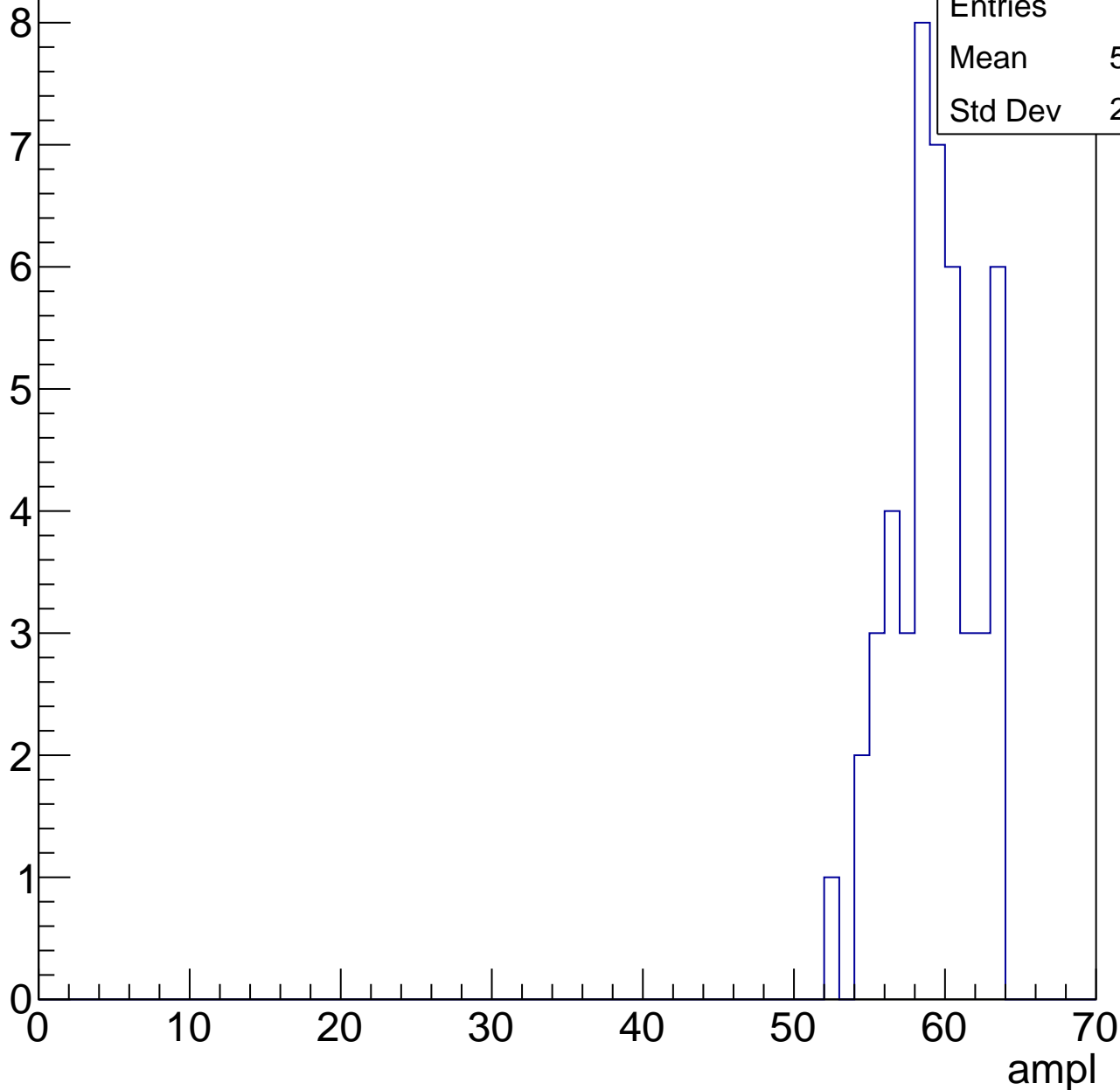


# B1L103S, U22-ch98, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	46
Mean	58.78
Std Dev	2.726

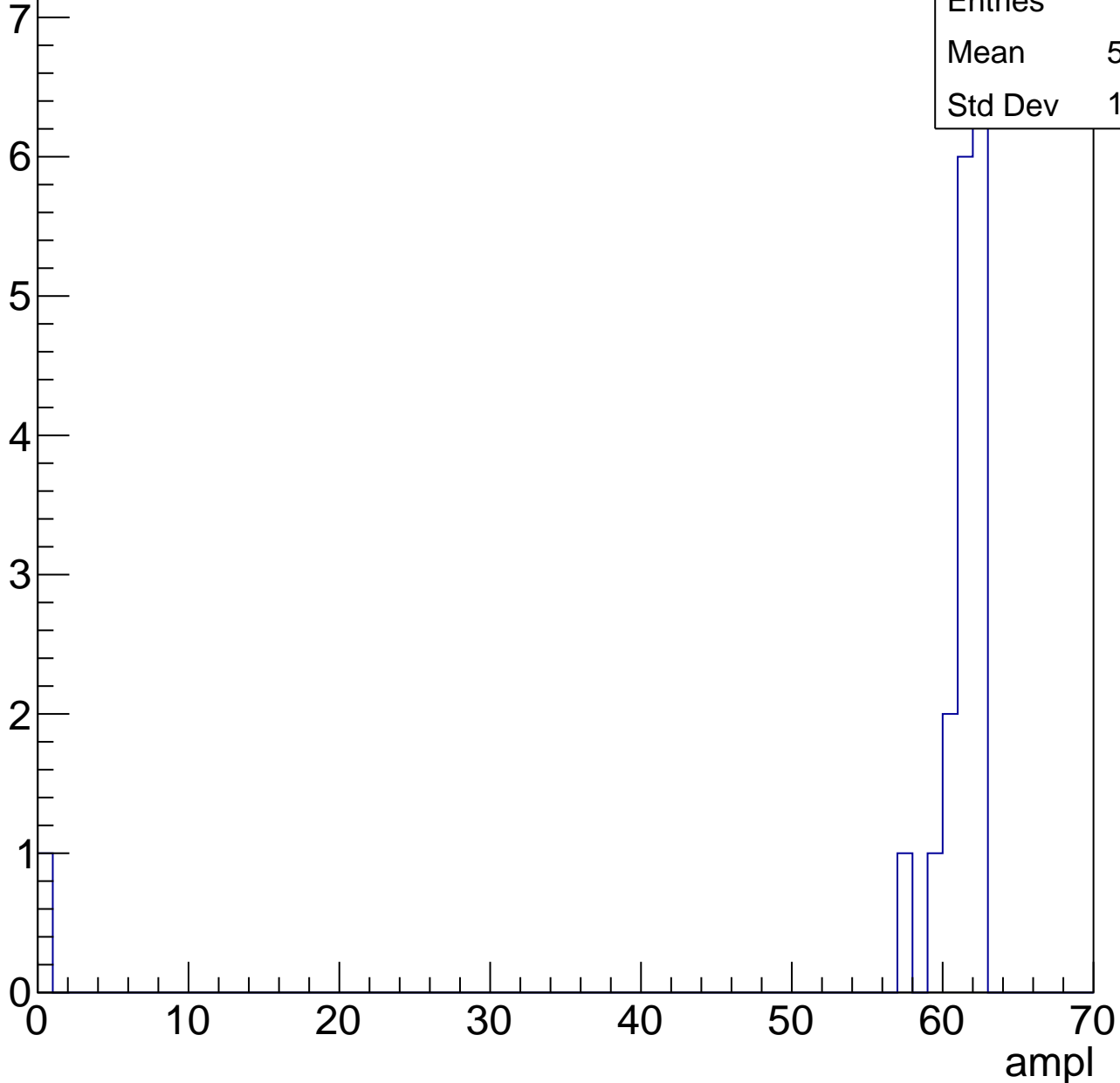


# B1L103S, U22-ch98, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.56
Std Dev	14.02



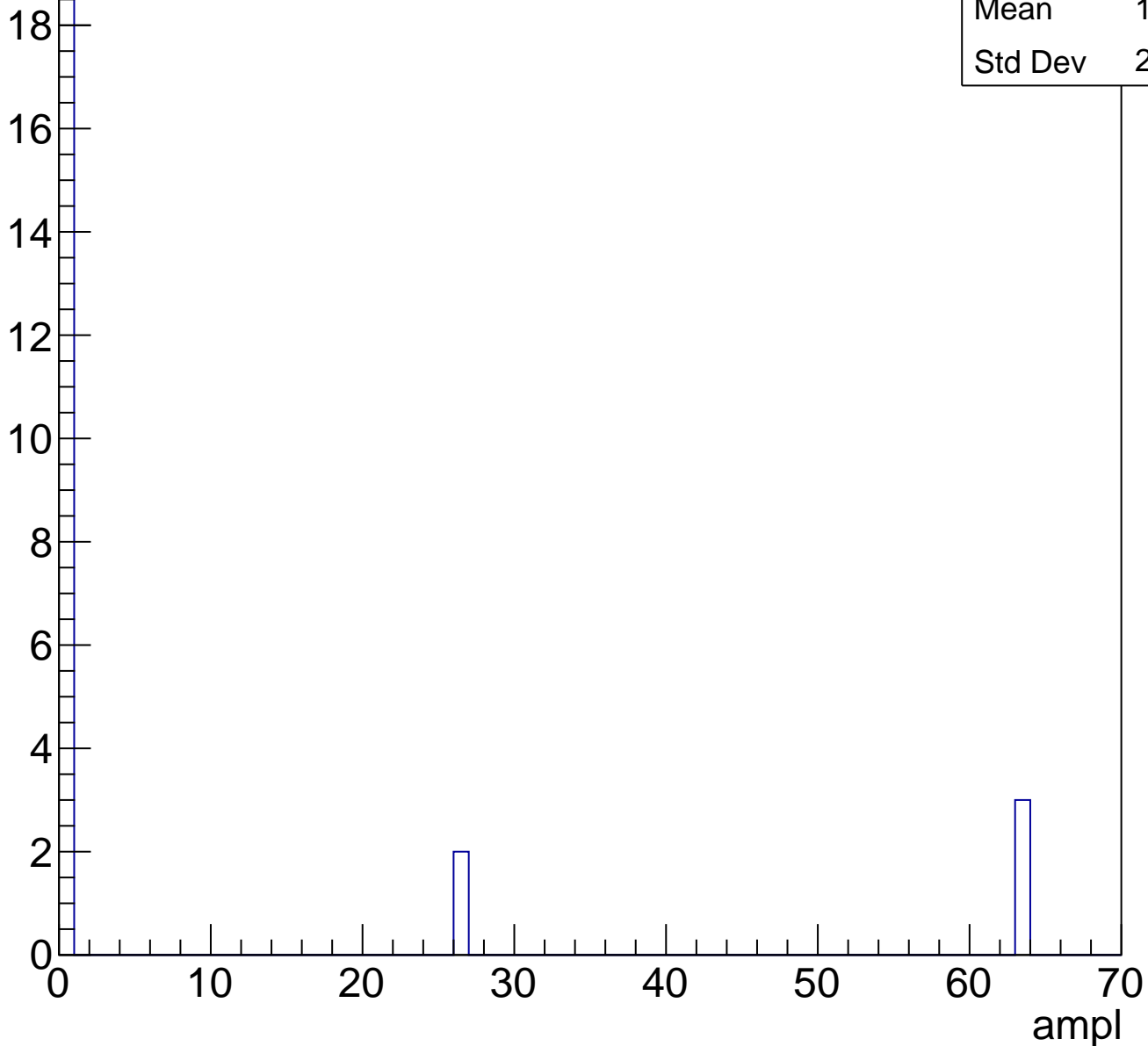


# B1L103S, U22-ch98, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	24
Mean	10.04
Std Dev	21.25

Entry



# B1L103S, U22-ch99, adc0

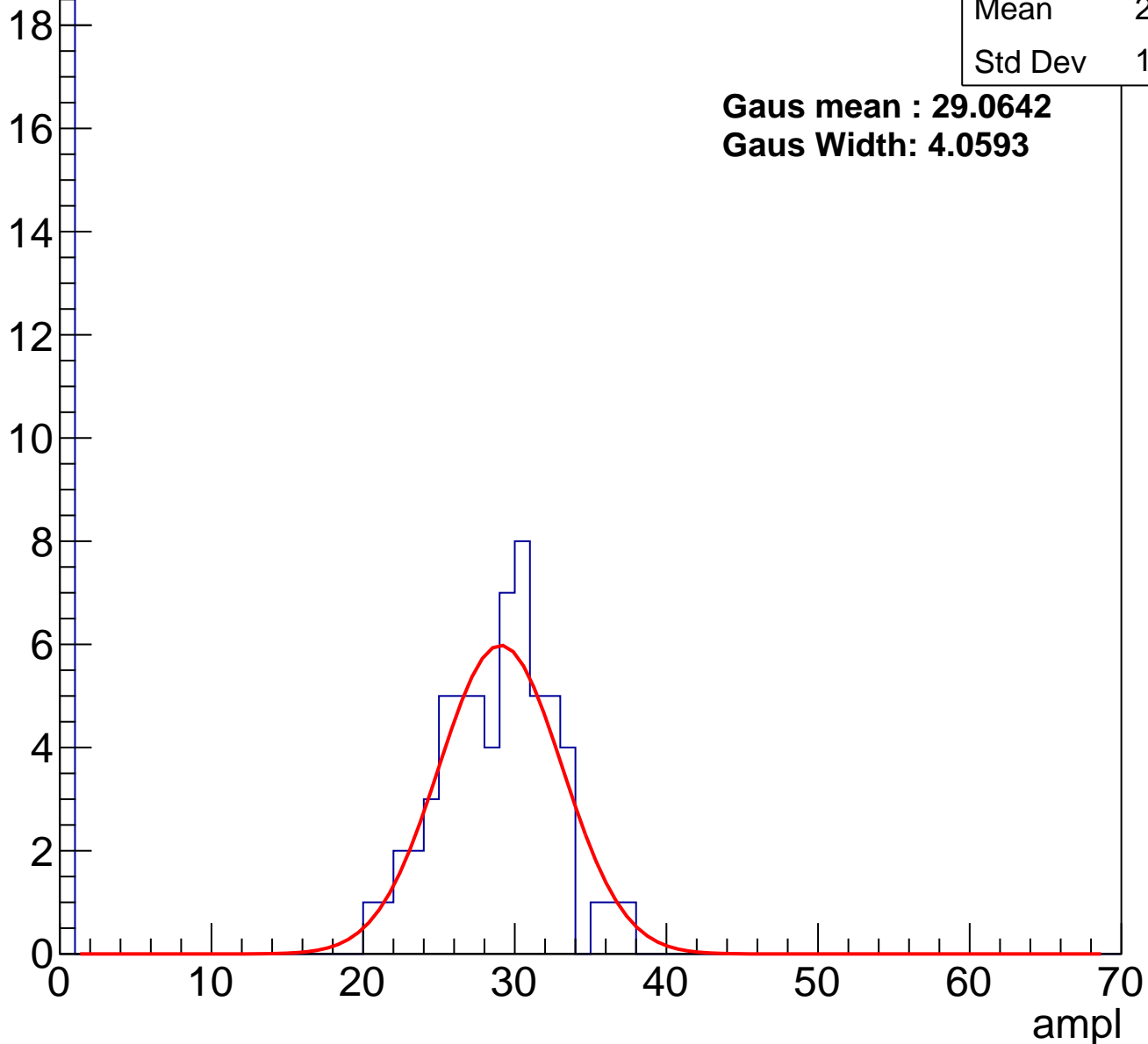
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	21.56
Std Dev	12.54

**Gaus mean : 29.0642**

**Gaus Width: 4.0593**

Entry



# B1L103S, U22-ch99, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	73
Mean	29.19
Std Dev	12.8

**Gaus mean : 36.1316**

**Gaus Width: 4.6017**

Entry

10

8

6

4

2

0

0

10

20

30

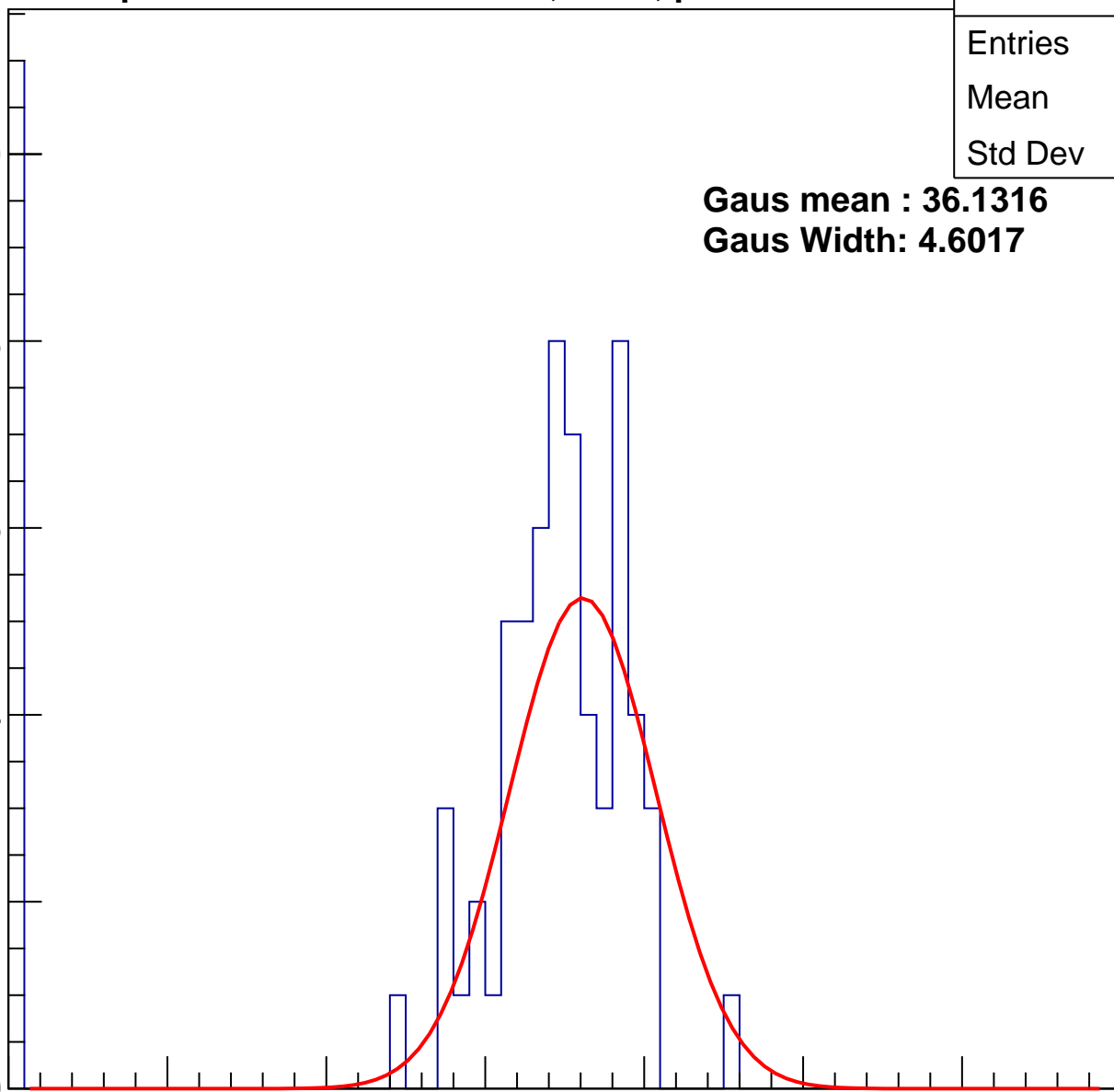
40

50

60

70

ampl



# B1L103S, U22-ch99, adc2

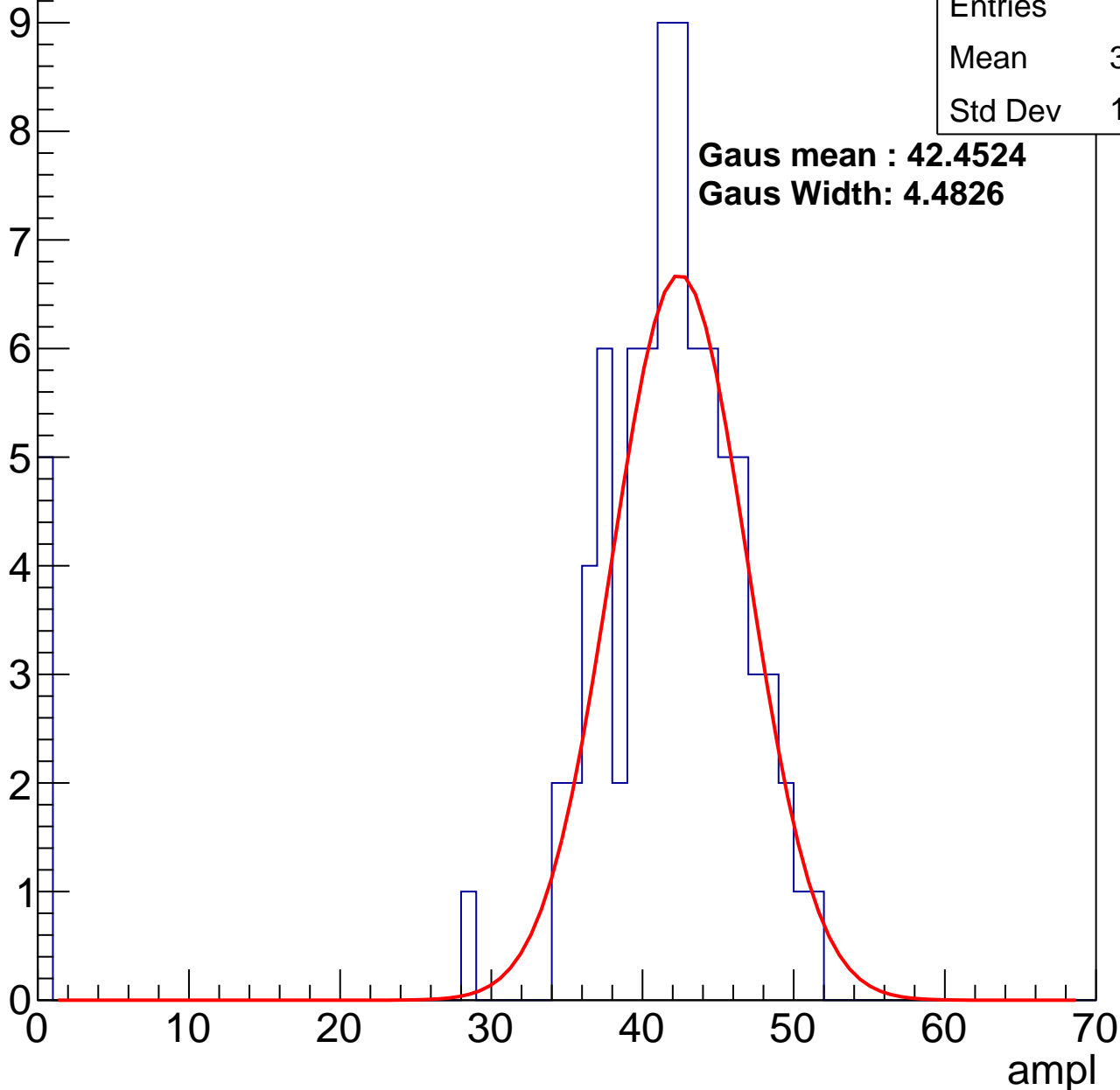
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	84
Mean	39.17
Std Dev	10.67

**Gaus mean : 42.4524**

**Gaus Width: 4.4826**

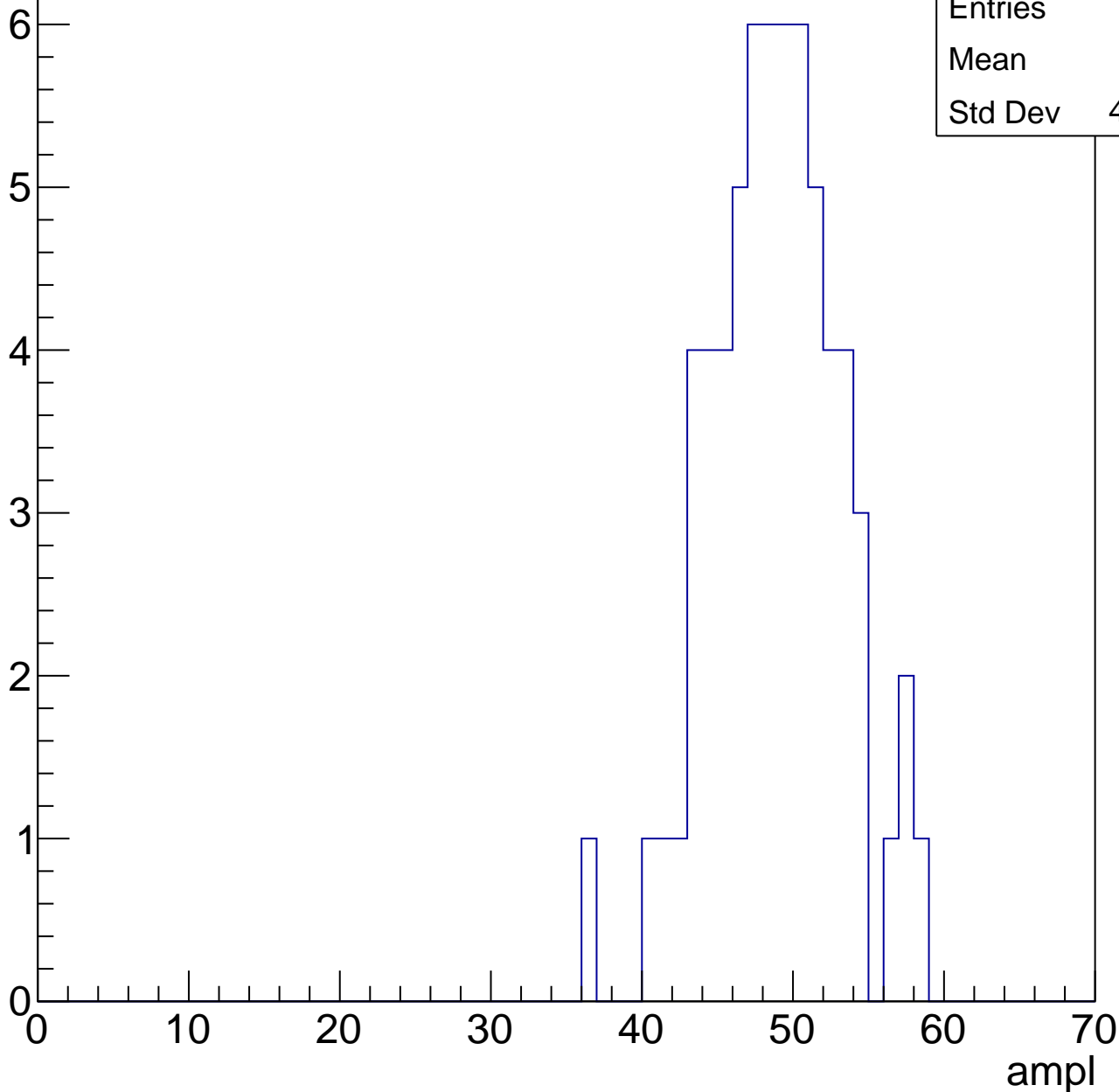


# B1L103S, U22-ch99, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	48.4
Std Dev	4.264

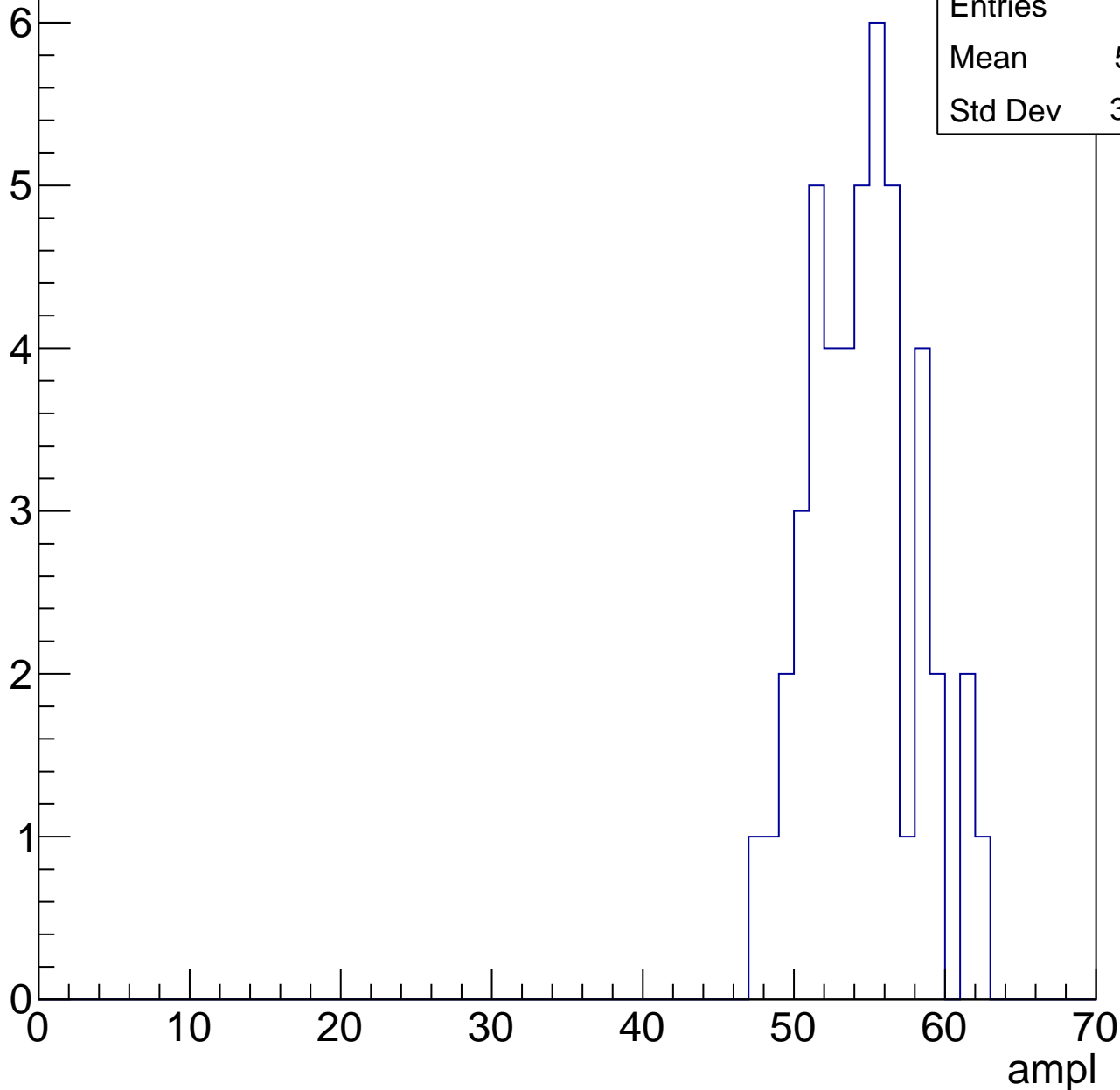


# B1L103S, U22-ch99, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

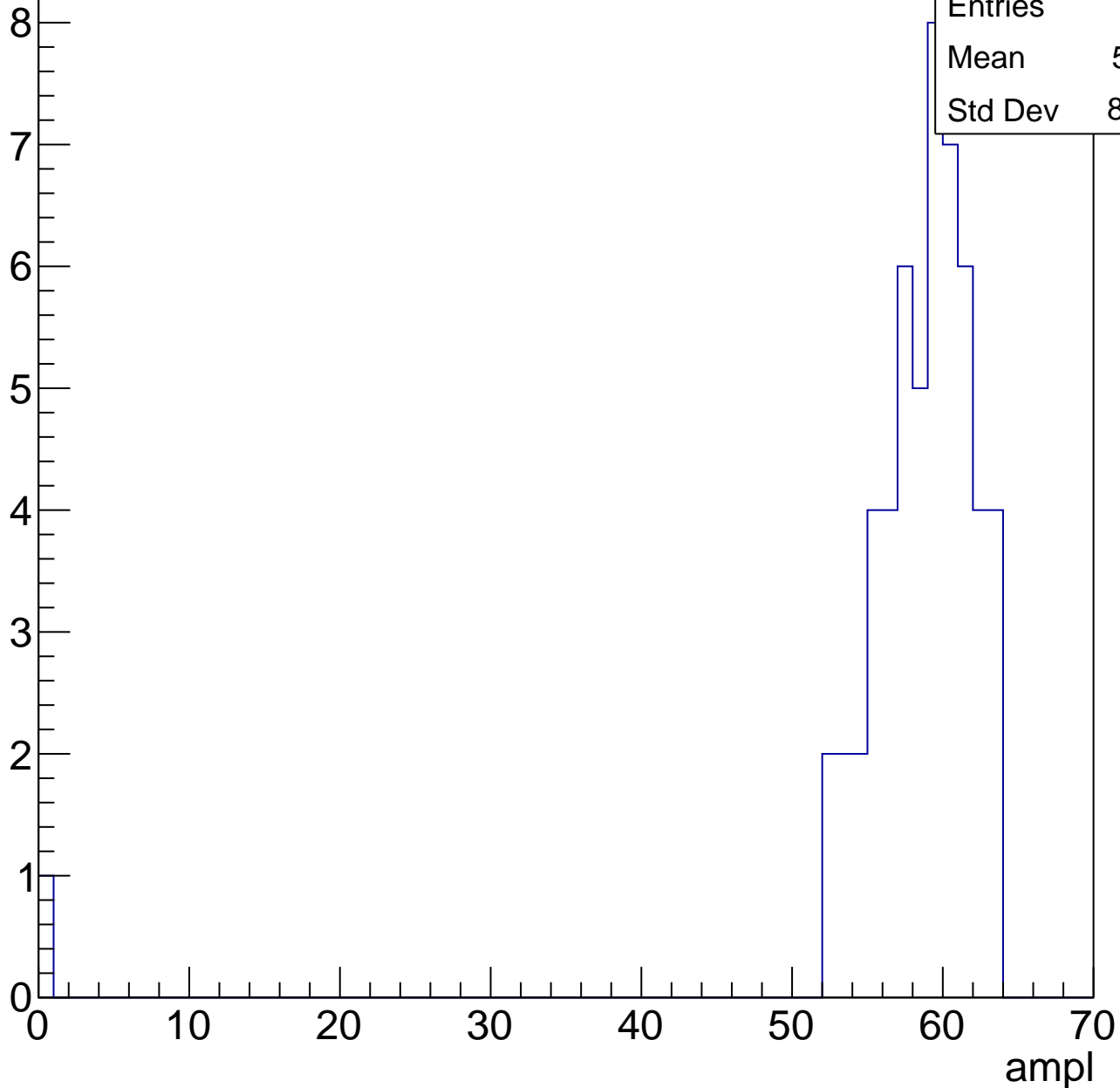
Entries	46
Mean	54.11
Std Dev	3.484



# B1L103S, U22-ch99, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

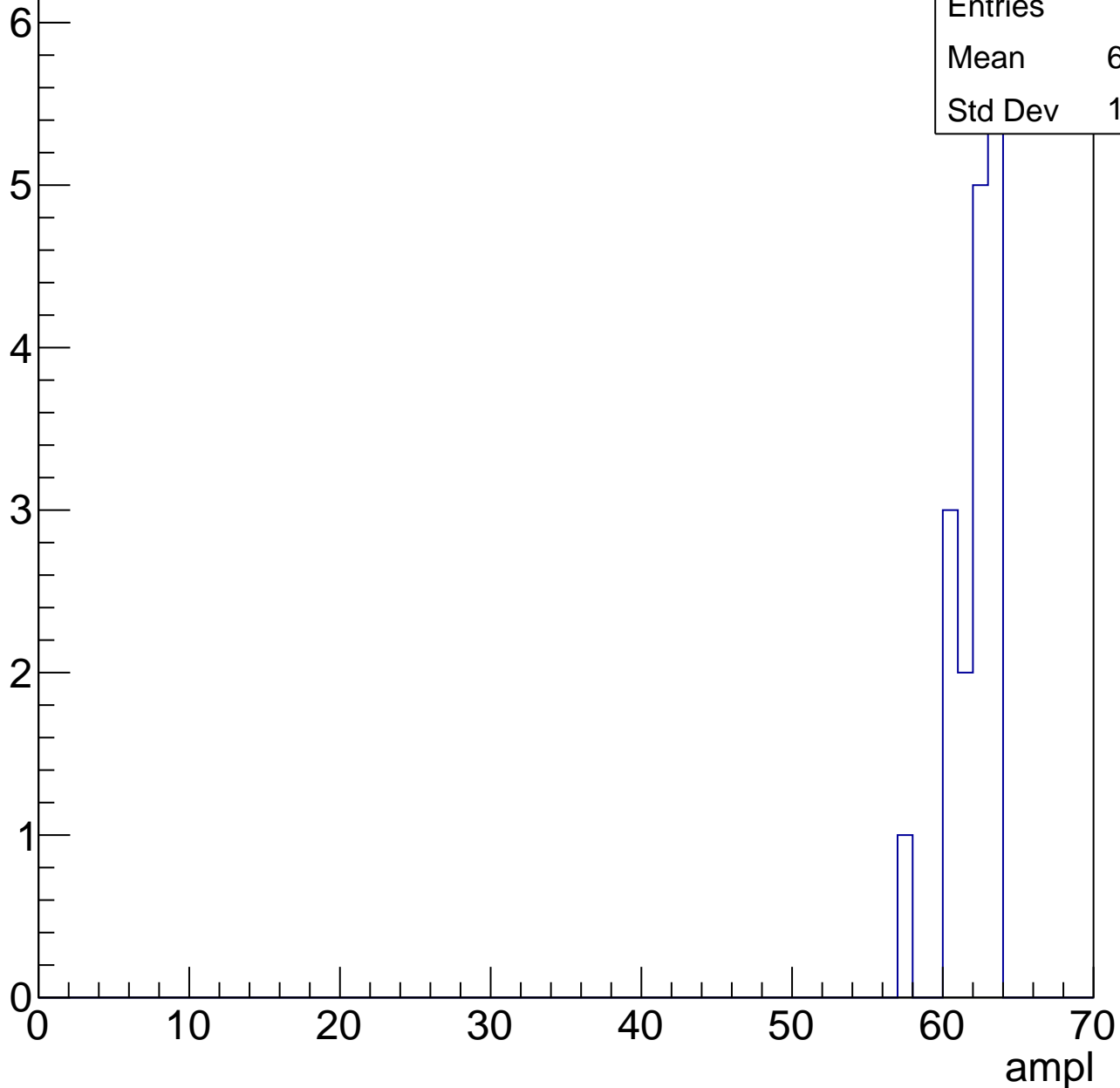


# B1L103S, U22-ch99, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.59
Std Dev	1.574





# B1L103S, U22-ch99, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch100, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	16.01
Std Dev	12.18

**Gaus mean : 24.9618**

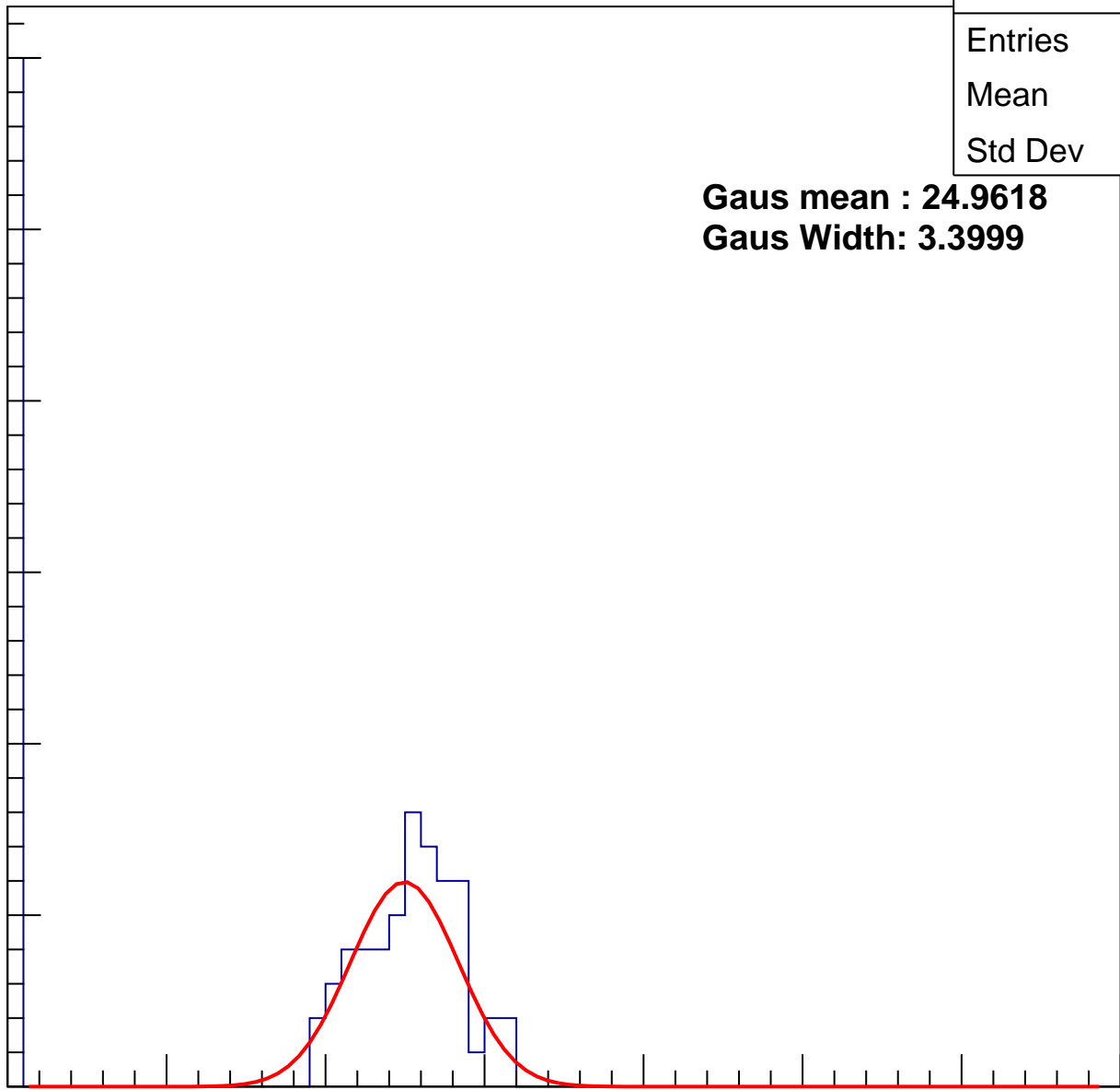
**Gaus Width: 3.3999**

Entry

30  
25  
20  
15  
10  
5  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch100, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	91
Mean	31.08
Std Dev	6.024

**Gaus mean : 32.3519**

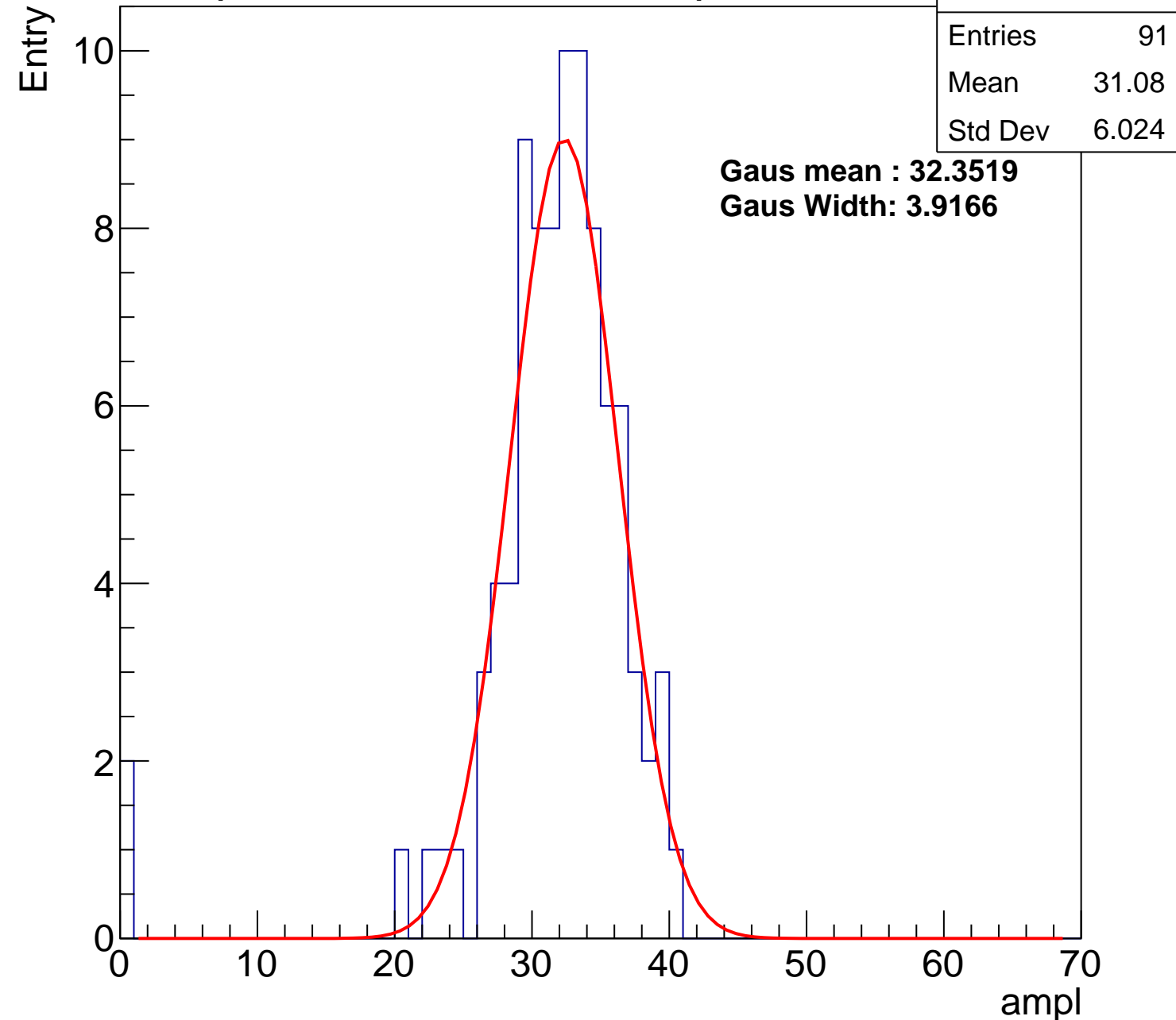
**Gaus Width: 3.9166**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch100, adc2

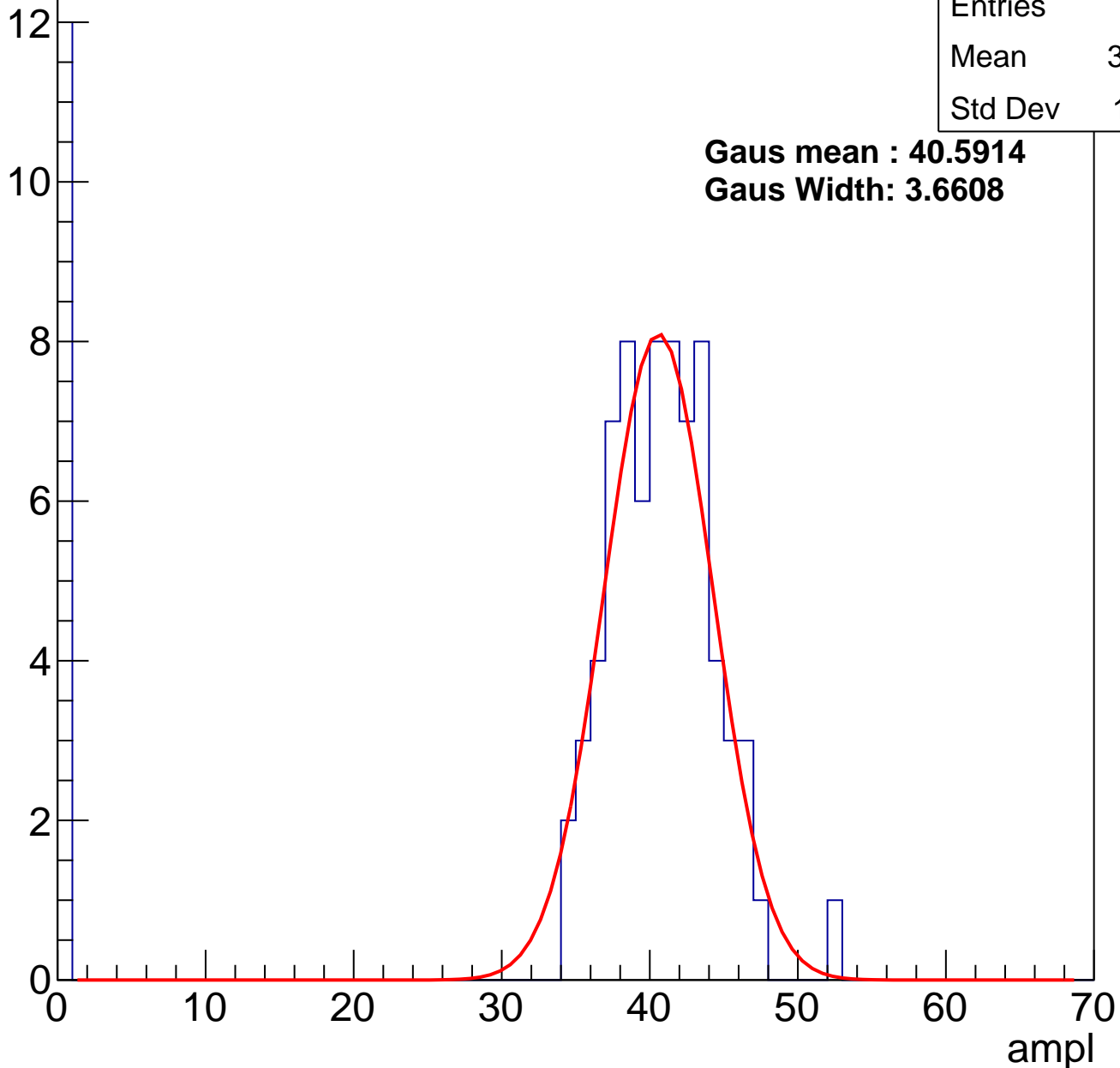
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	34.68
Std Dev	14.41

**Gaus mean : 40.5914**

**Gaus Width: 3.6608**

Entry

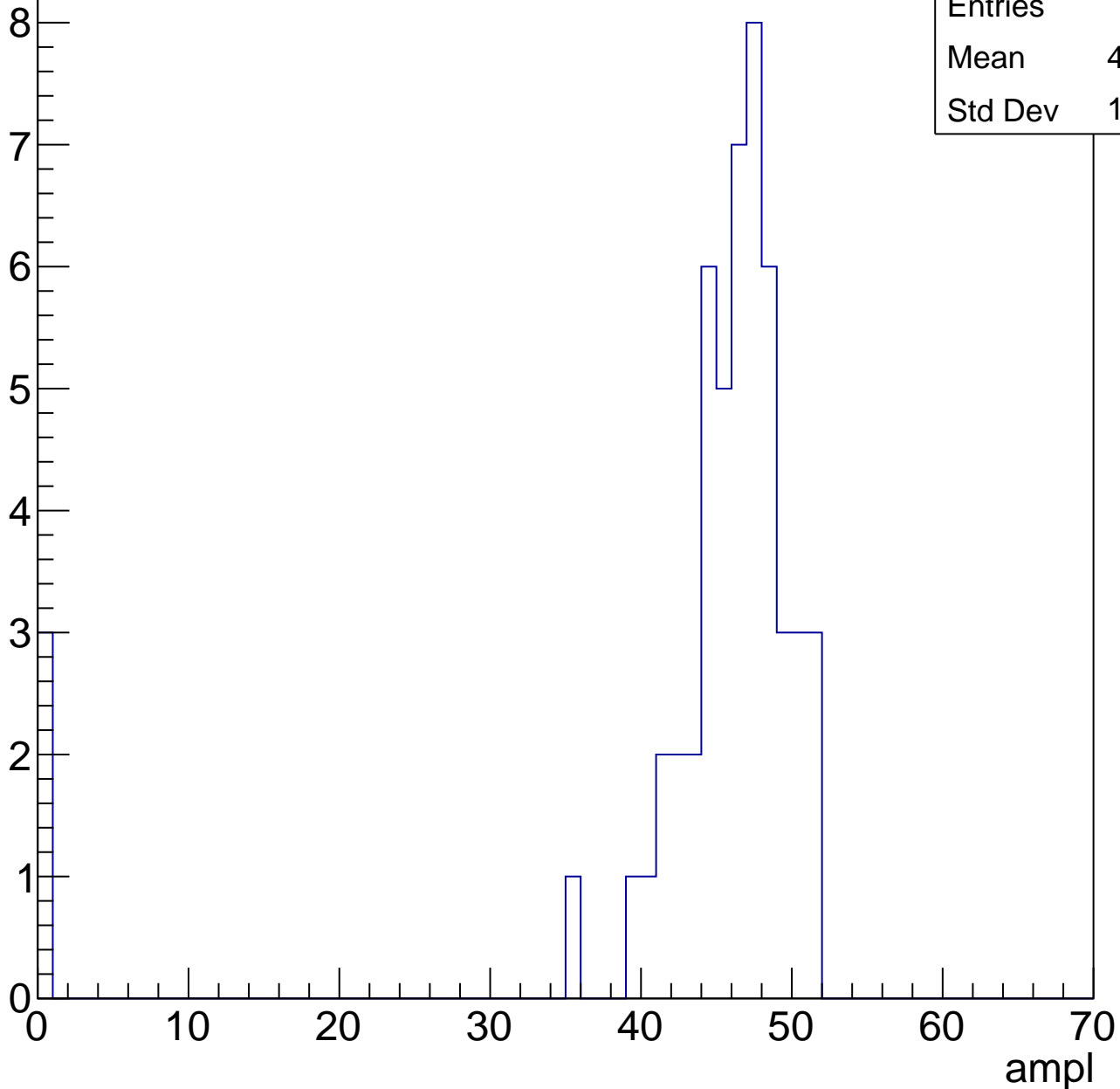


# B1L103S, U22-ch100, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	43.23
Std Dev	11.04

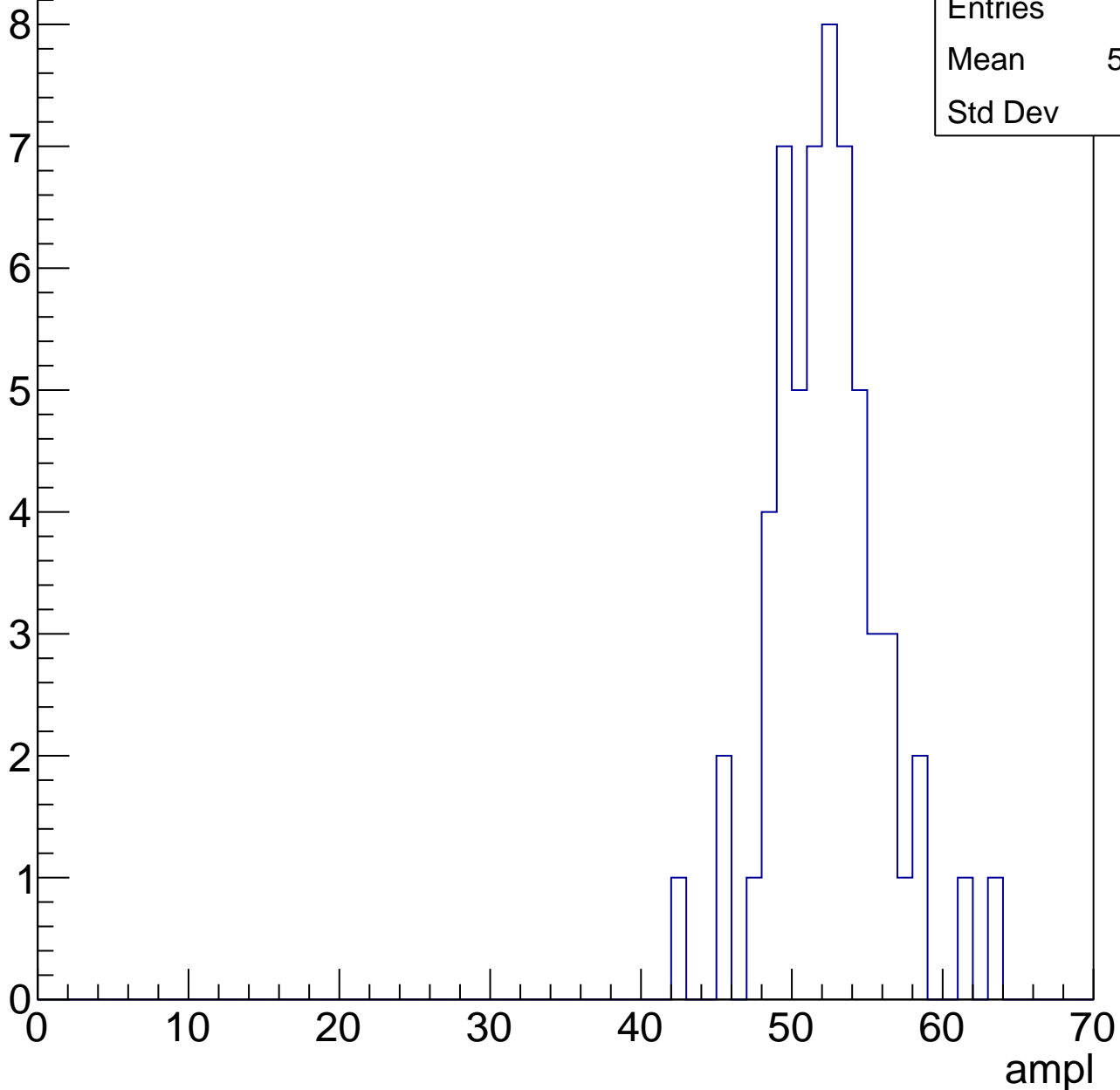


# B1L103S, U22-ch100, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	51.86
Std Dev	3.66

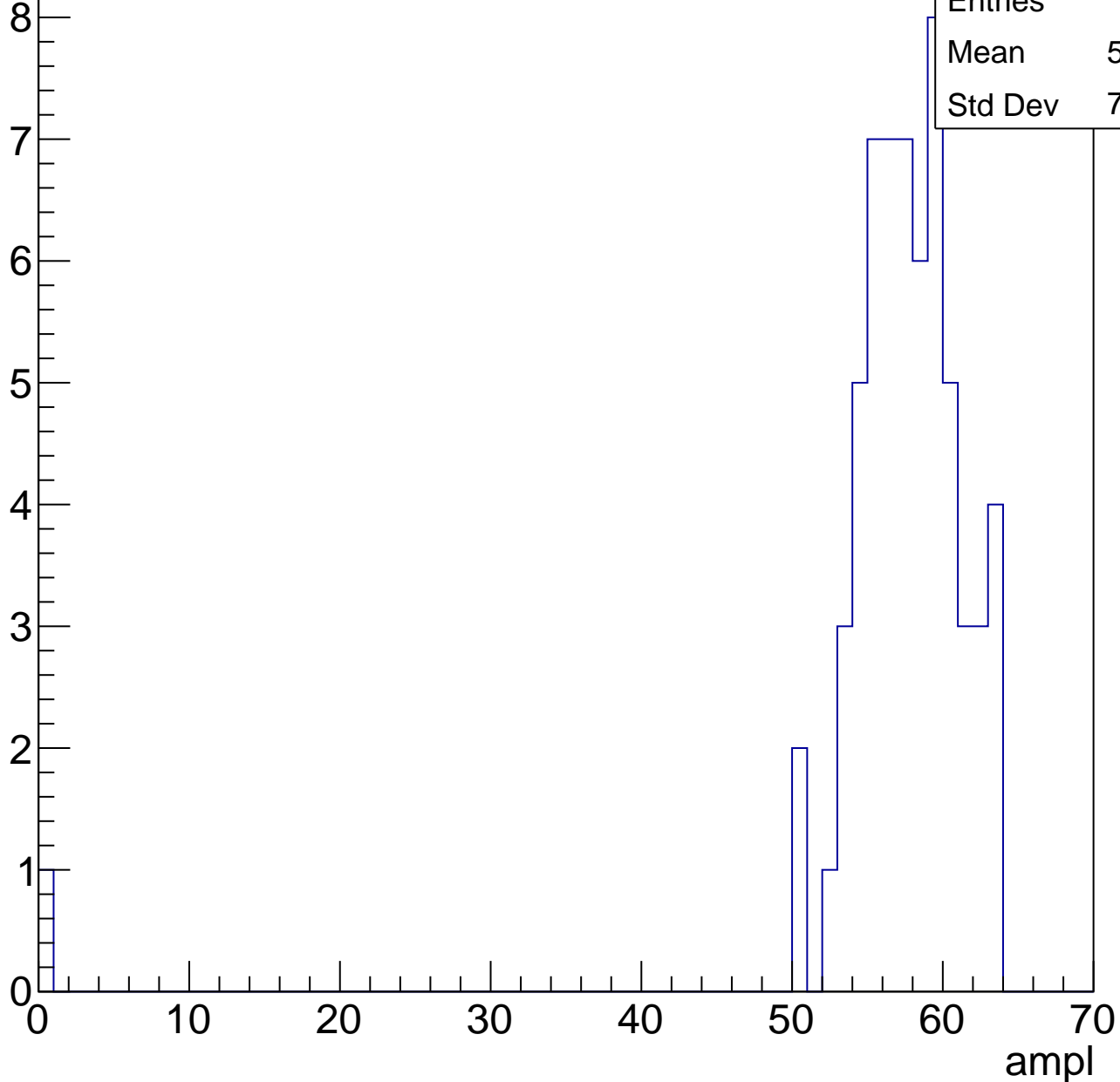


# B1L103S, U22-ch100, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	56.42
Std Dev	7.859

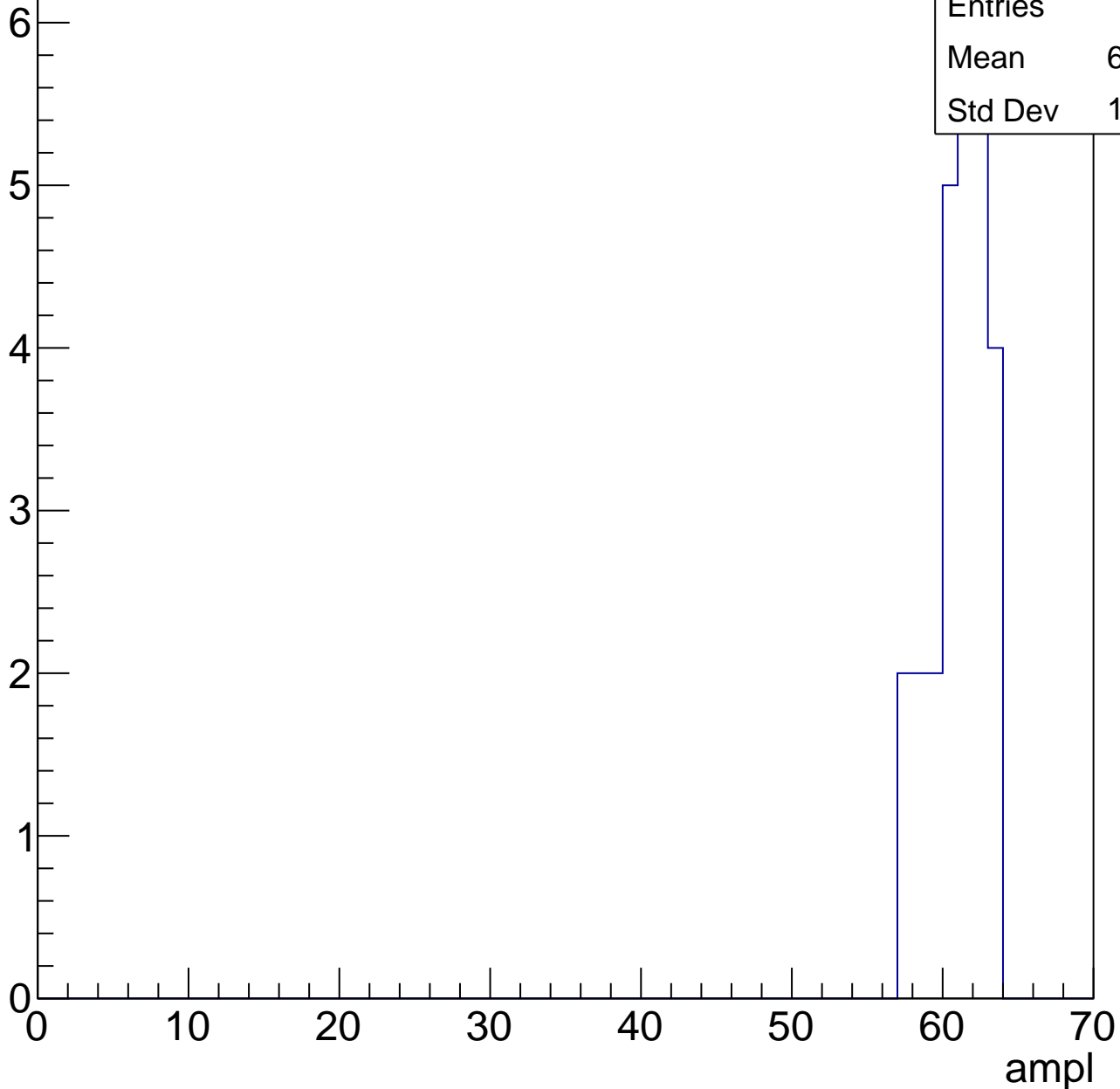


# B1L103S, U22-ch100, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	27
Mean	60.67
Std Dev	1.743

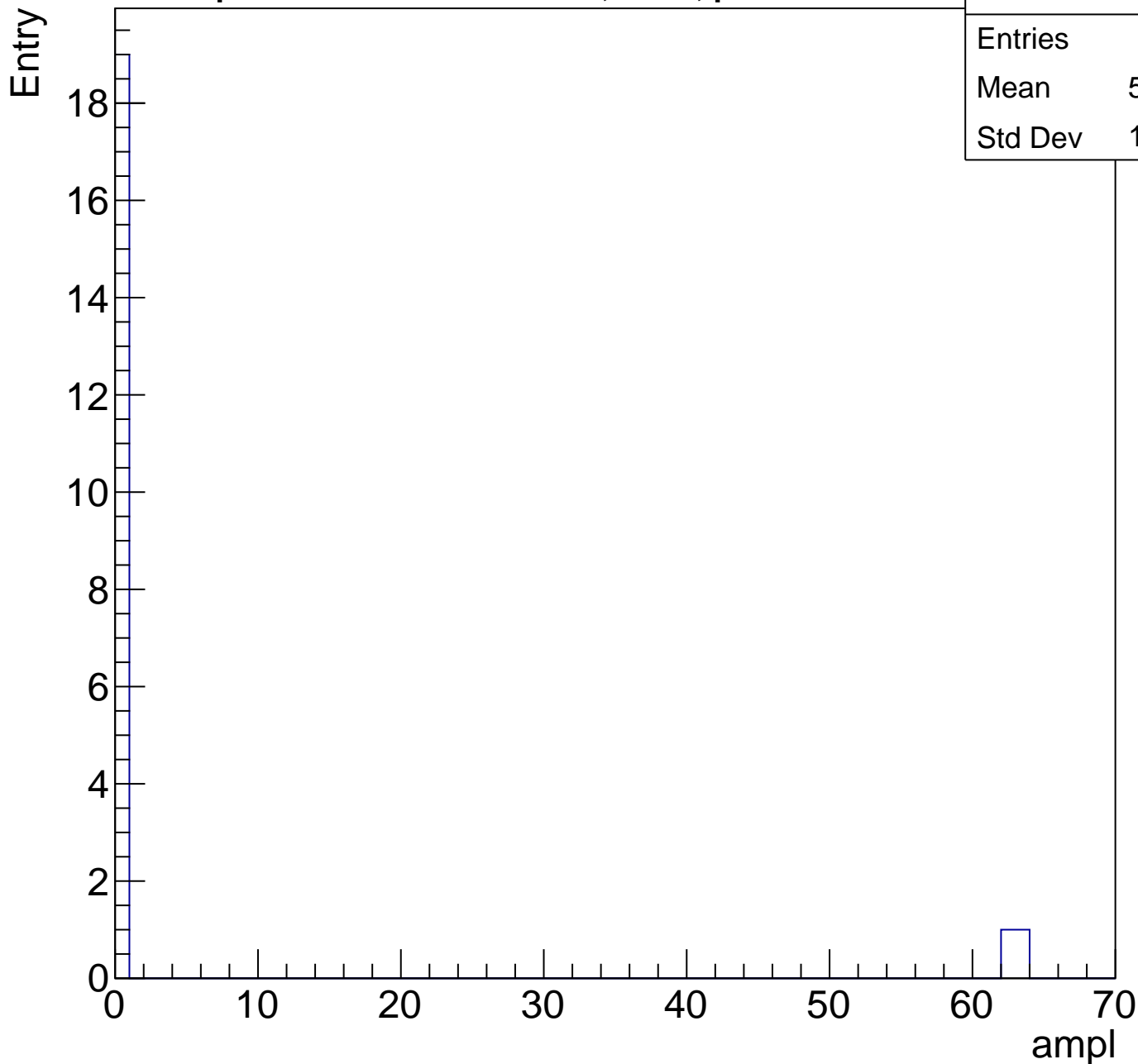




# B1L103S, U22-ch100, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



# B1L103S, U22-ch101, adc0

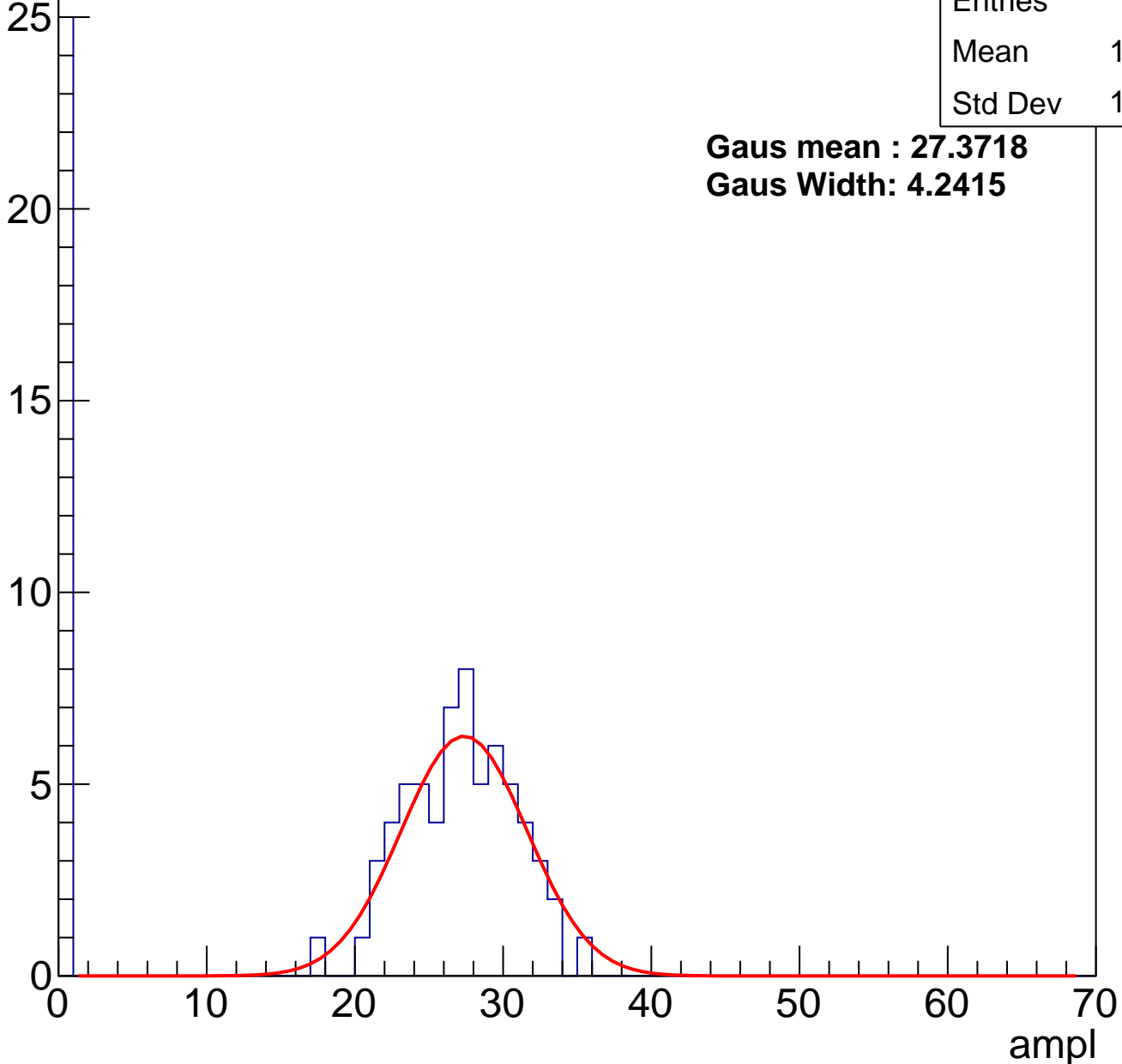
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	89
Mean	19.17
Std Dev	12.37

**Gaus mean : 27.3718**

**Gaus Width: 4.2415**

Entry



# B1L103S, U22-ch101, adc1

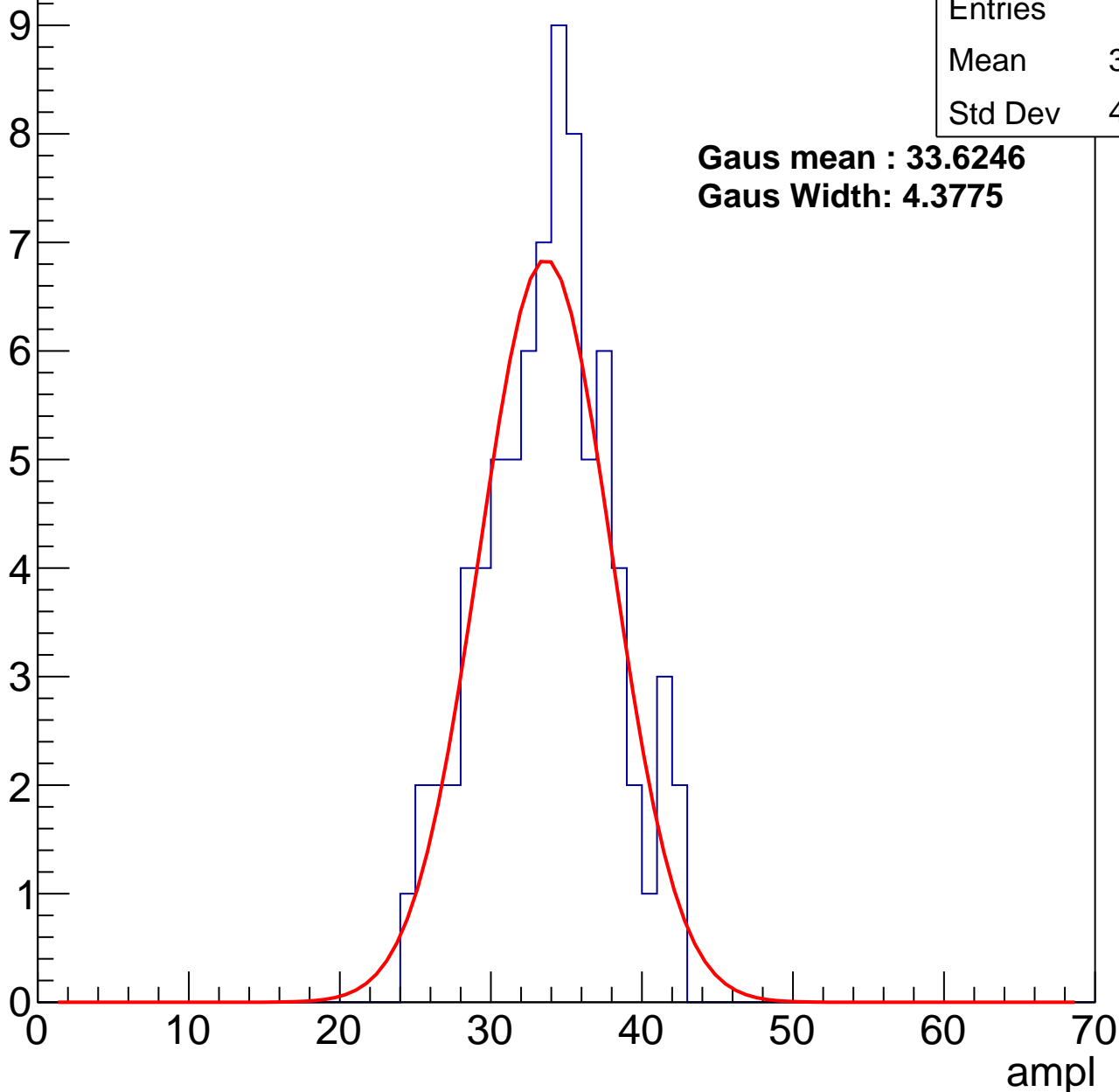
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	78
Mean	33.35
Std Dev	4.178

**Gaus mean : 33.6246**

**Gaus Width: 4.3775**



# B1L103S, U22-ch101, adc2

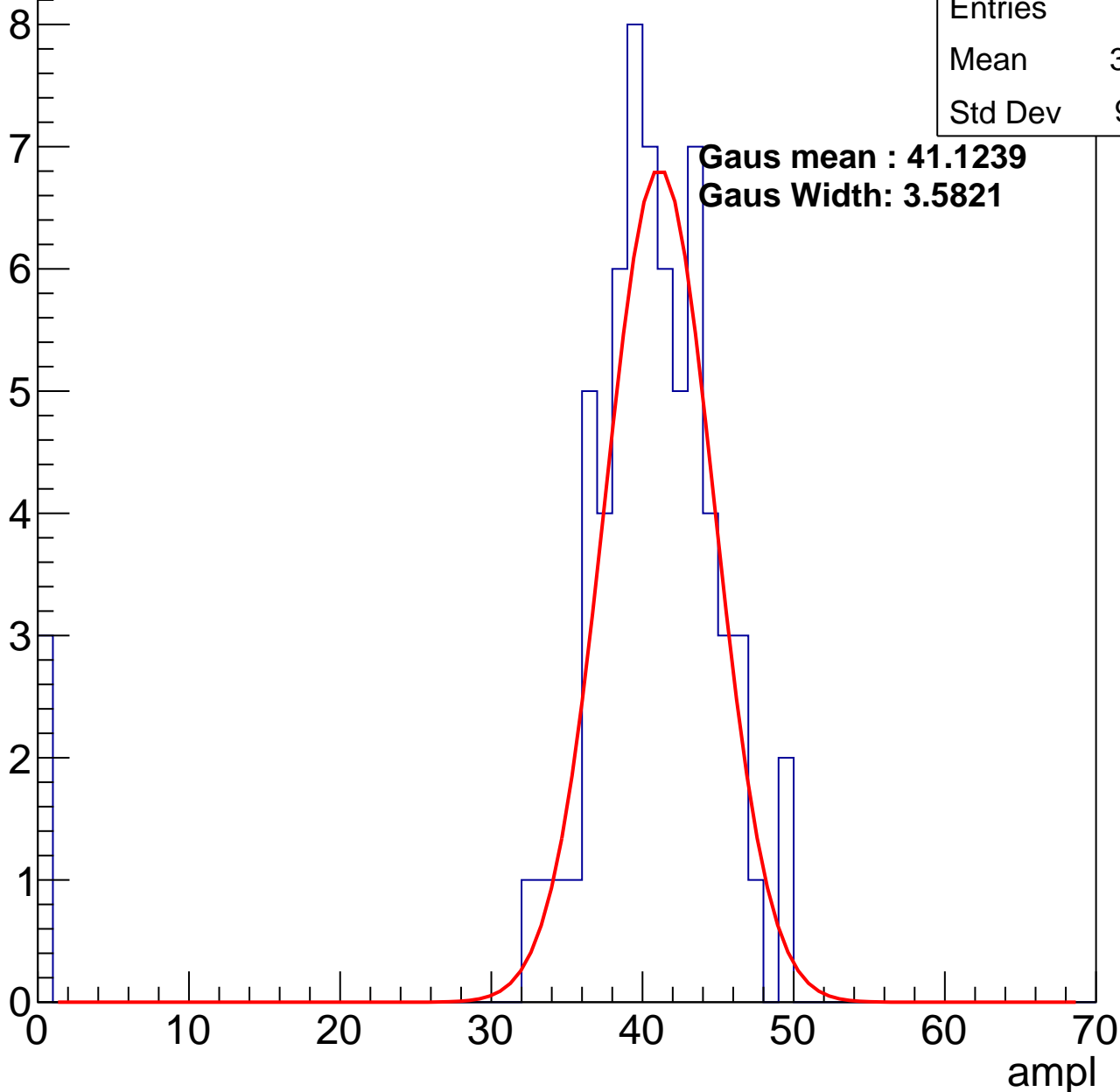
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.72
Std Dev	9.041

**Gaus mean : 41.1239**

**Gaus Width: 3.5821**

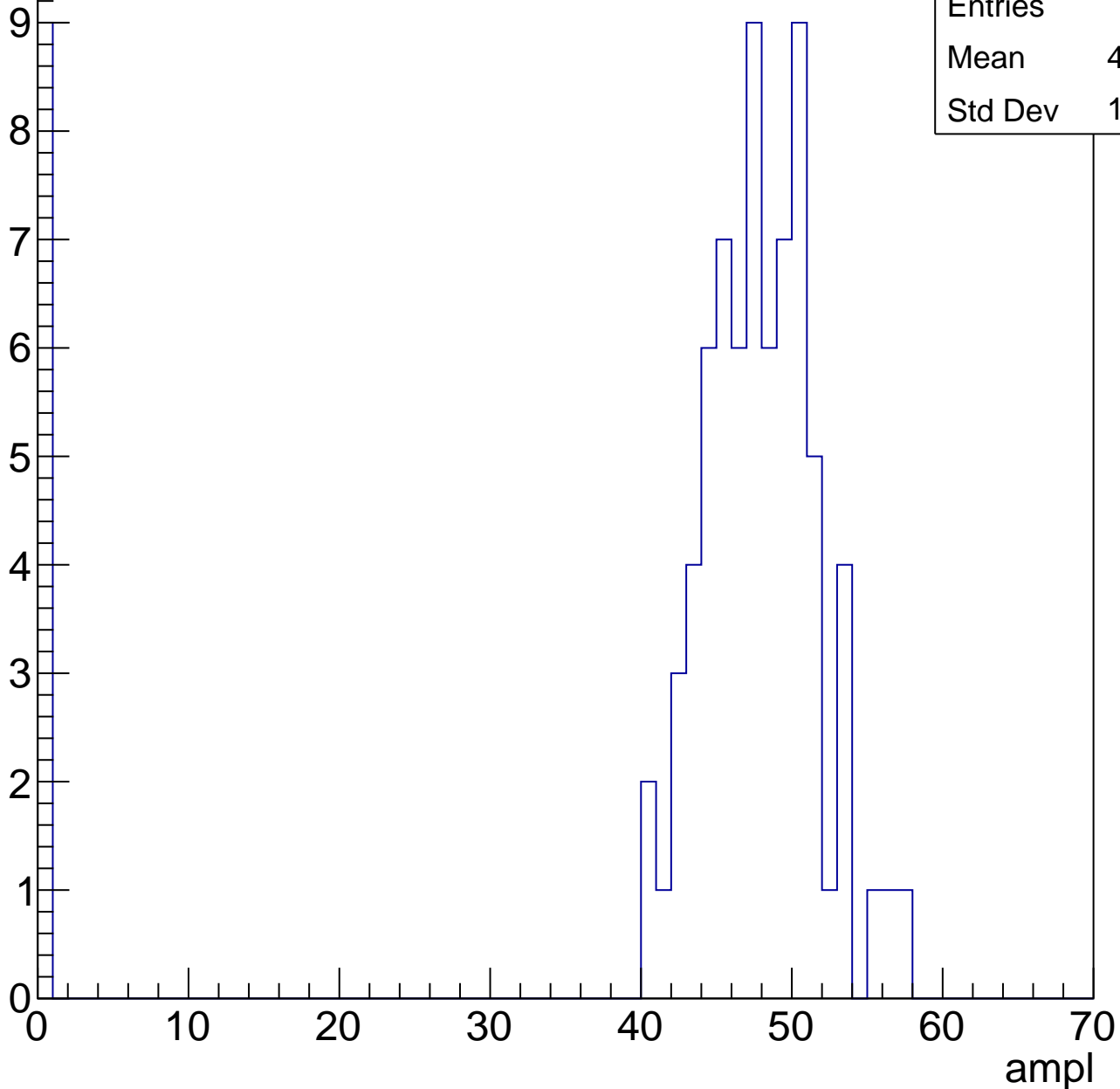


# B1L103S, U22-ch101, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	82
Mean	42.26
Std Dev	15.22

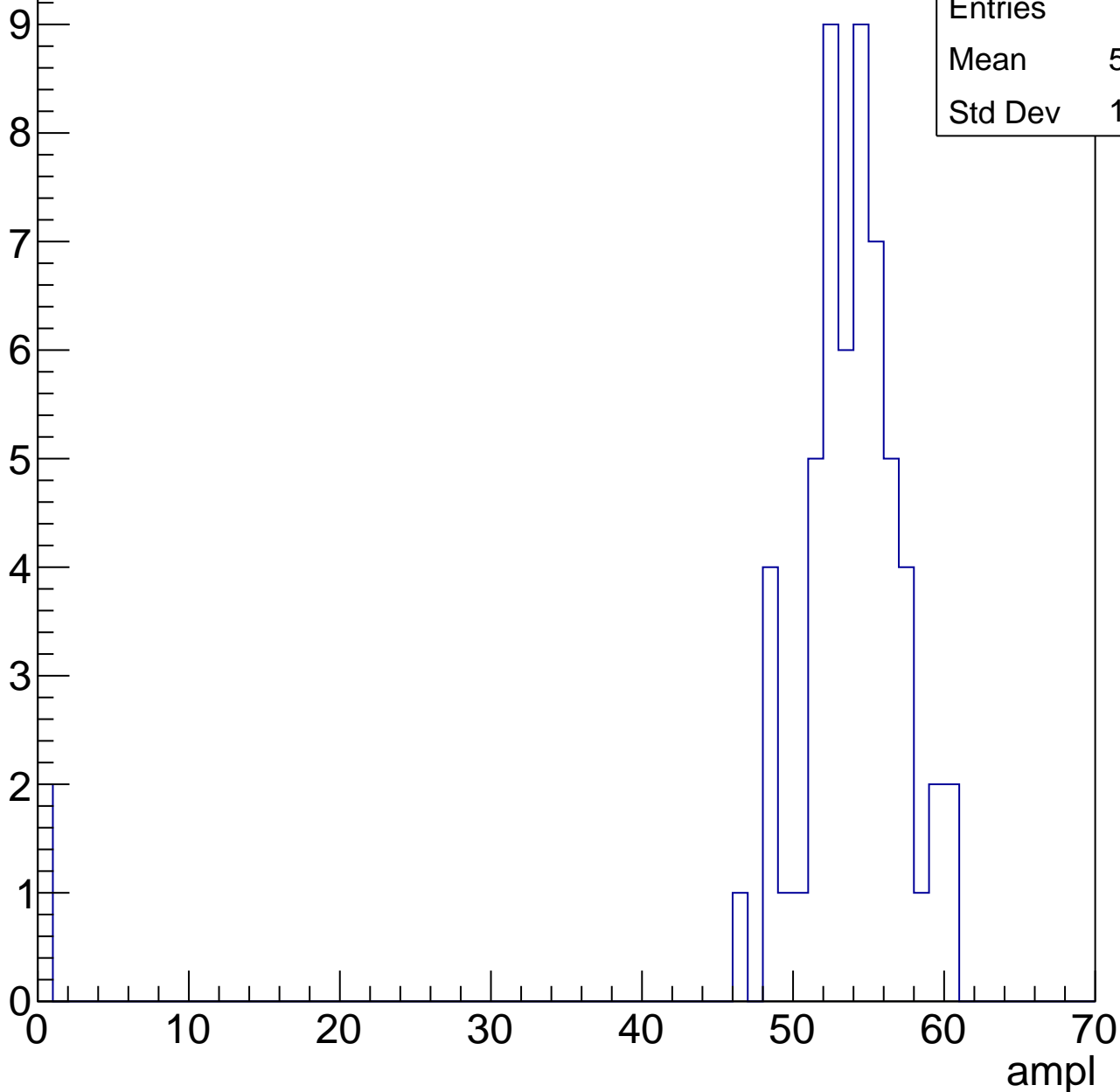


# B1L103S, U22-ch101, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	51.75
Std Dev	10.14

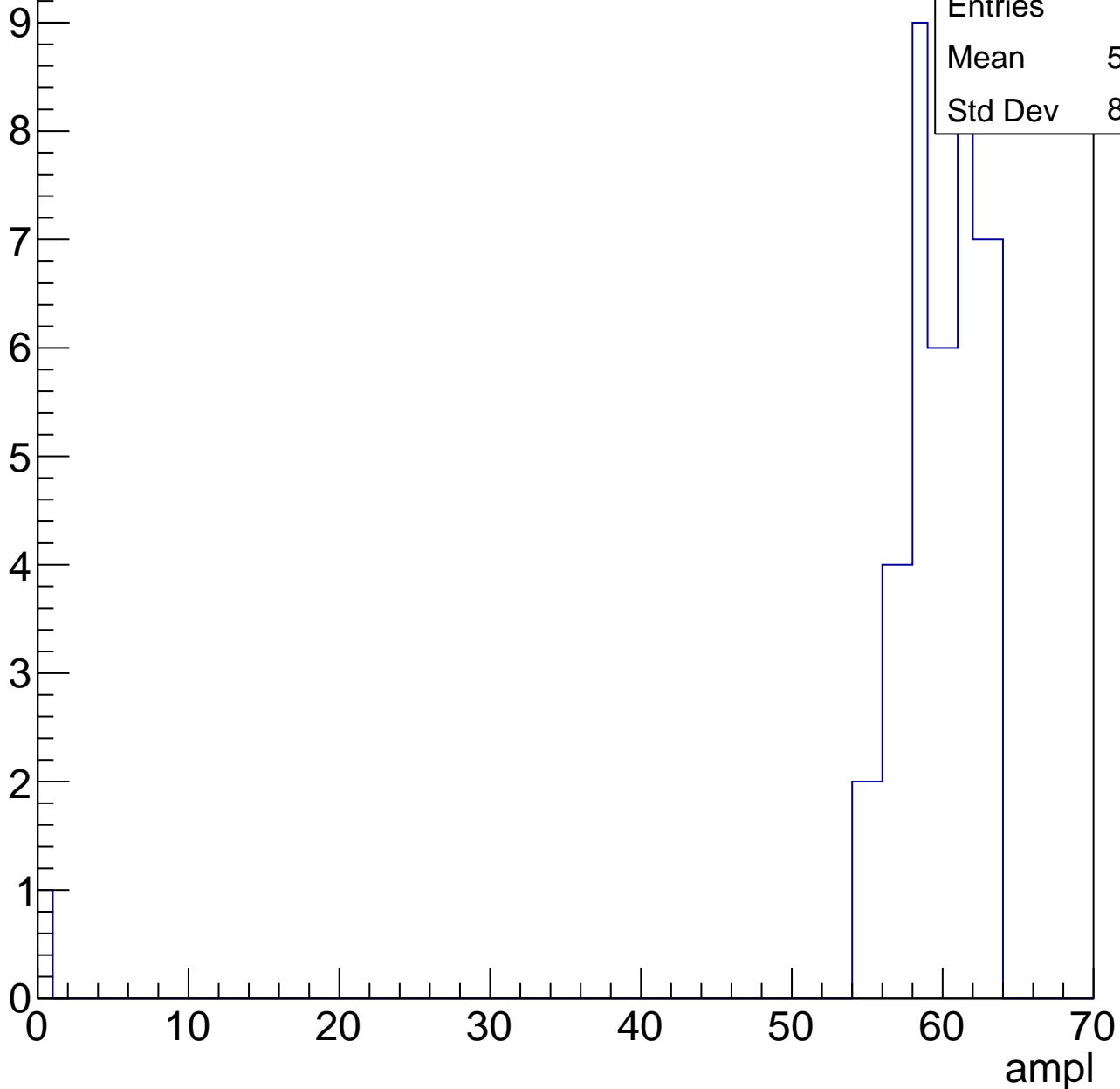


# B1L103S, U22-ch101, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.38
Std Dev	8.254



# B1L103S, U22-ch101, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

Mean

61

Std Dev

1.907

ampl

0 10 20 30 40 50 60 70

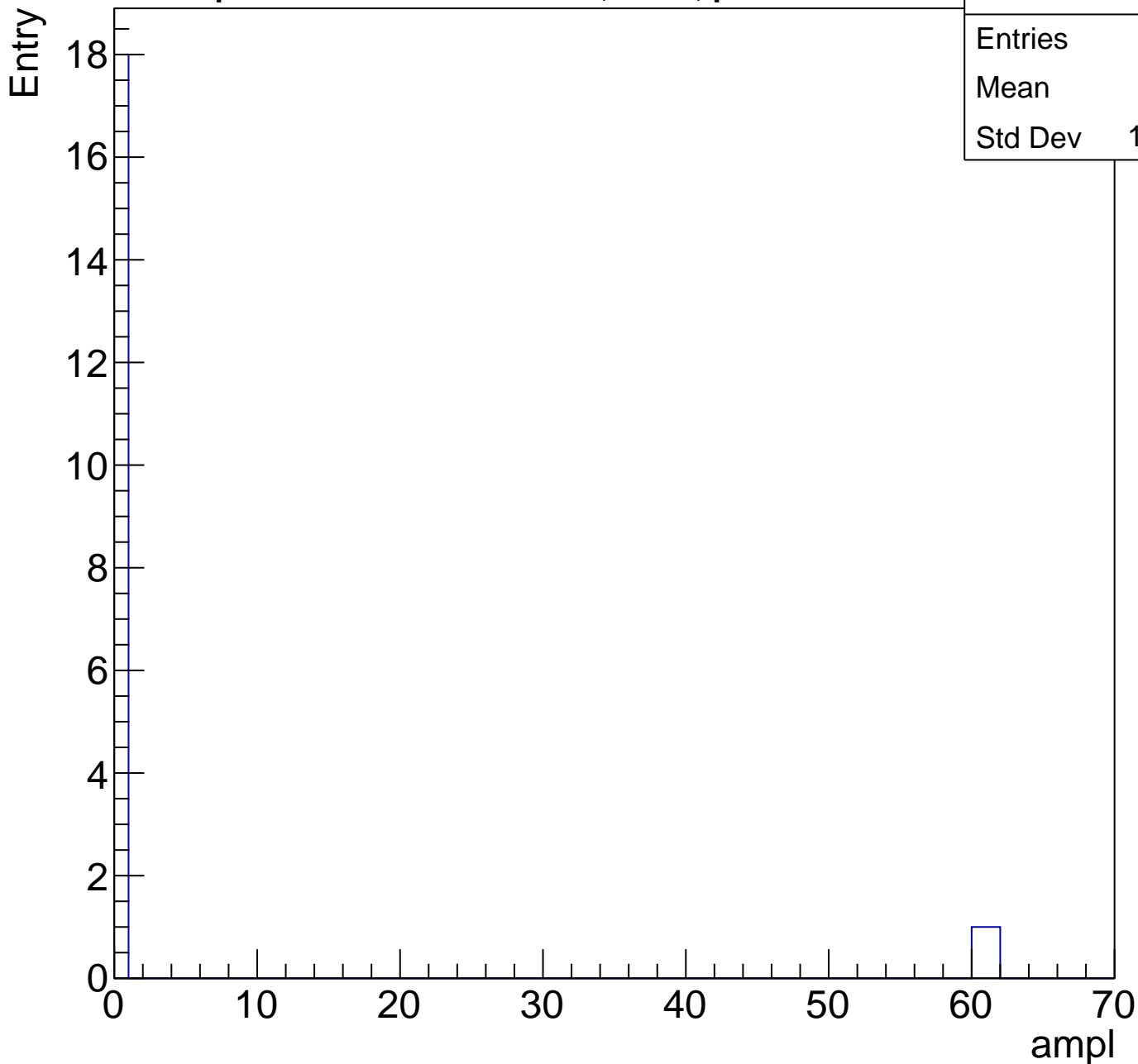
Entries	11
Mean	61
Std Dev	1.907



# B1L103S, U22-ch101, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	6.05
Std Dev	18.15



# B1L103S, U22-ch102, adc0

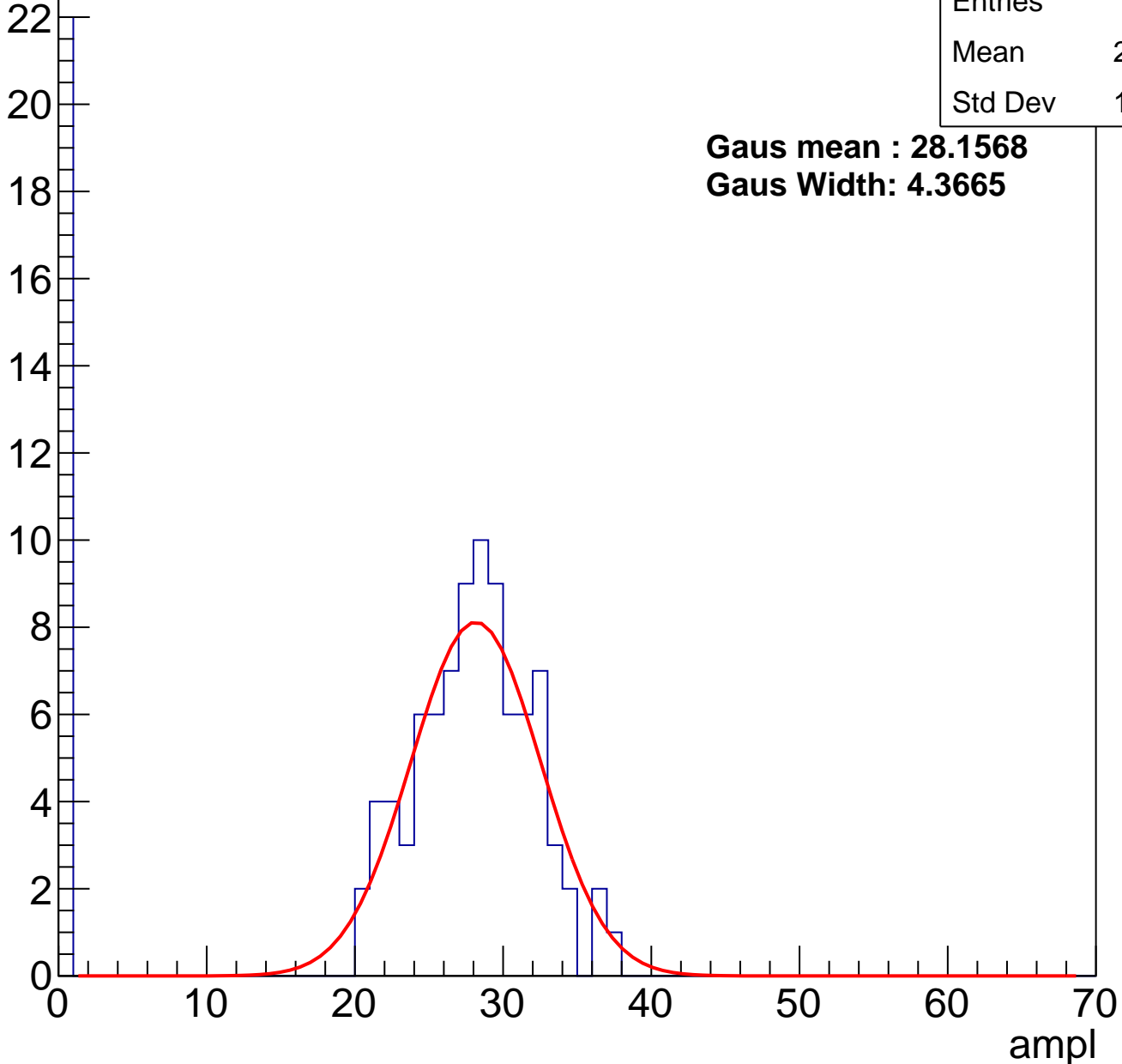
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	109
Mean	22.08
Std Dev	11.62

**Gaus mean : 28.1568**

**Gaus Width: 4.3665**

Entry



# B1L103S, U22-ch102, adc1

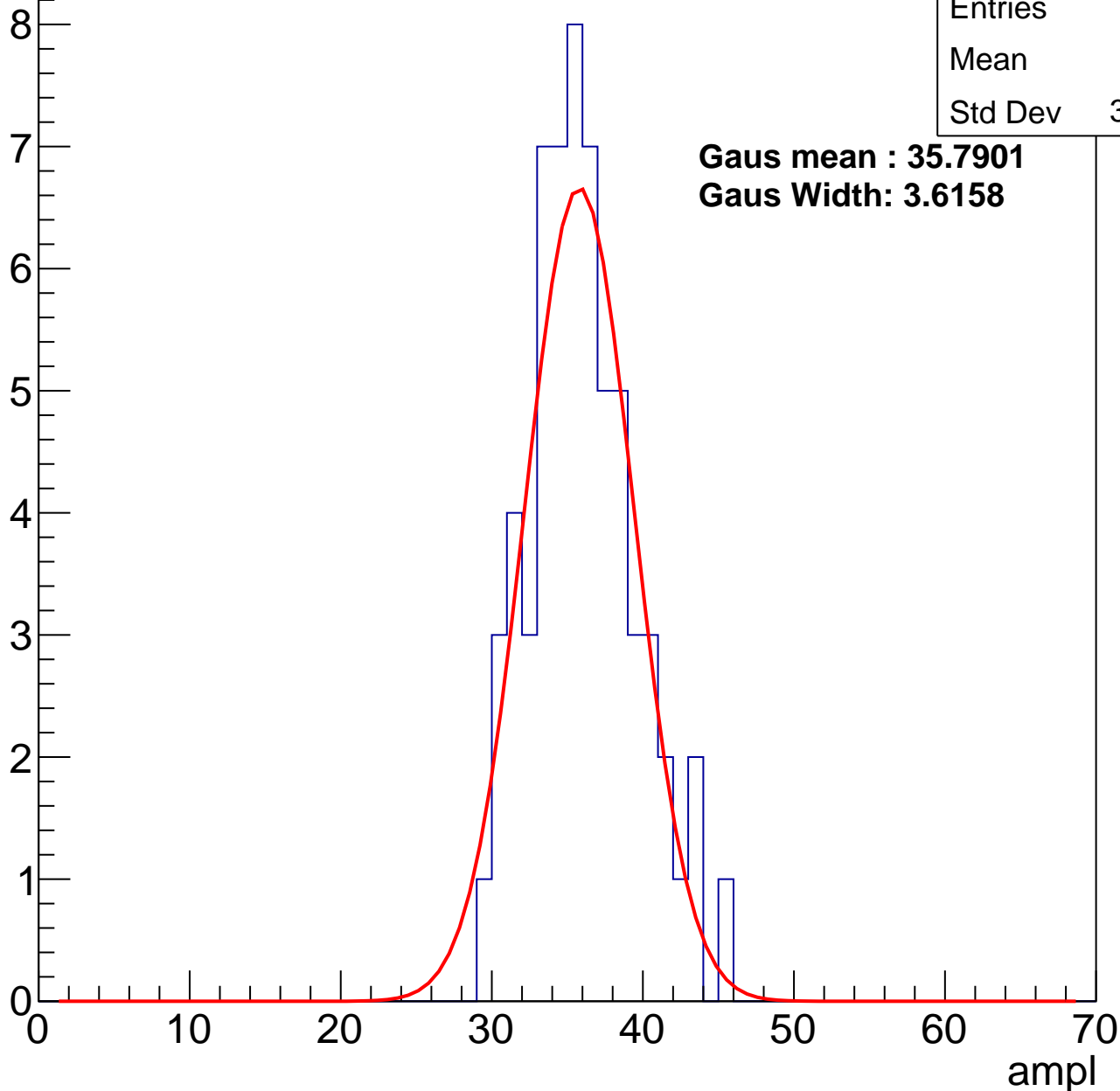
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	35.6
Std Dev	3.499

**Gaus mean : 35.7901**

**Gaus Width: 3.6158**



# B1L103S, U22-ch102, adc2

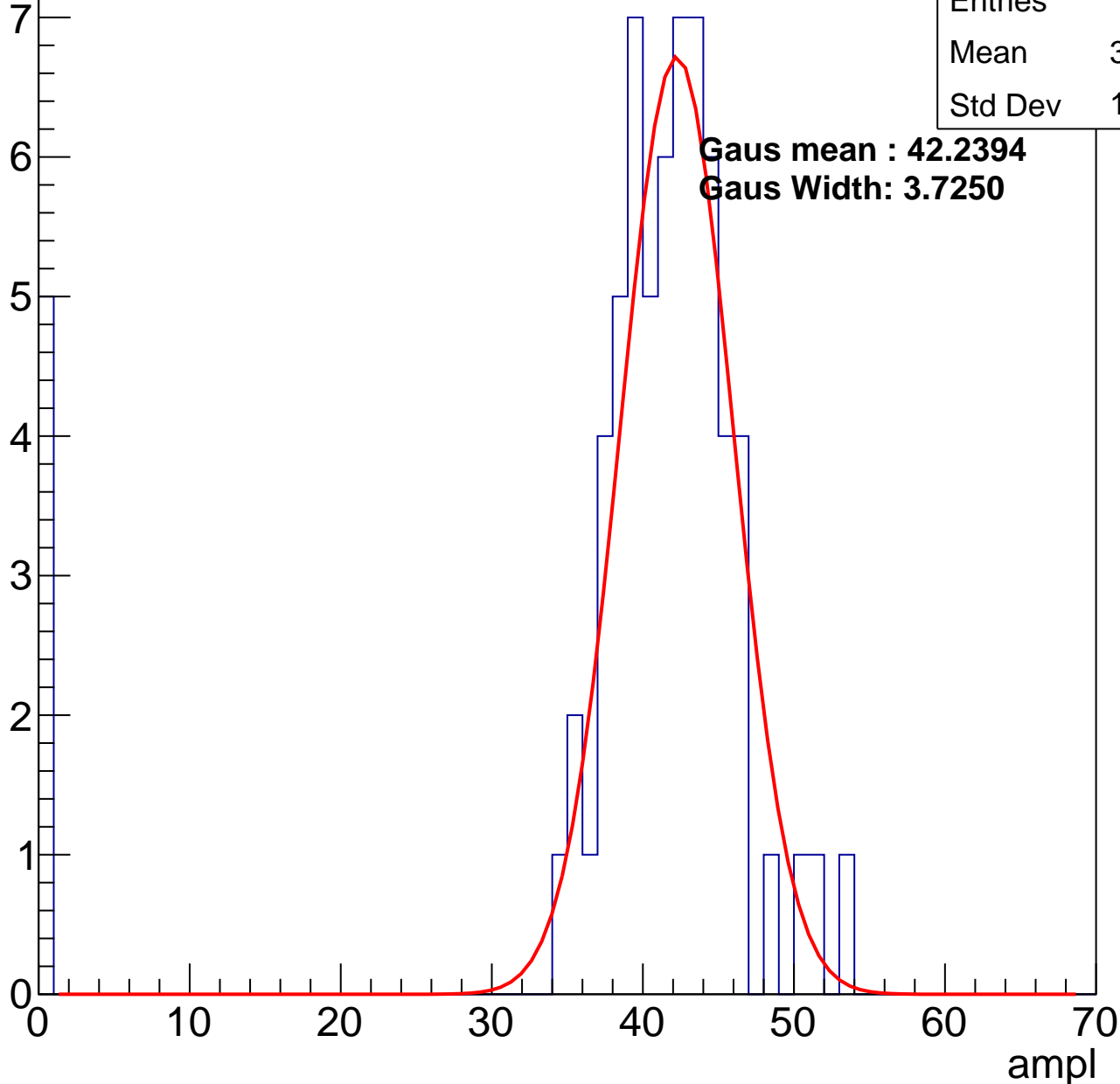
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	38.56
Std Dev	11.45

**Gaus mean : 42.2394**

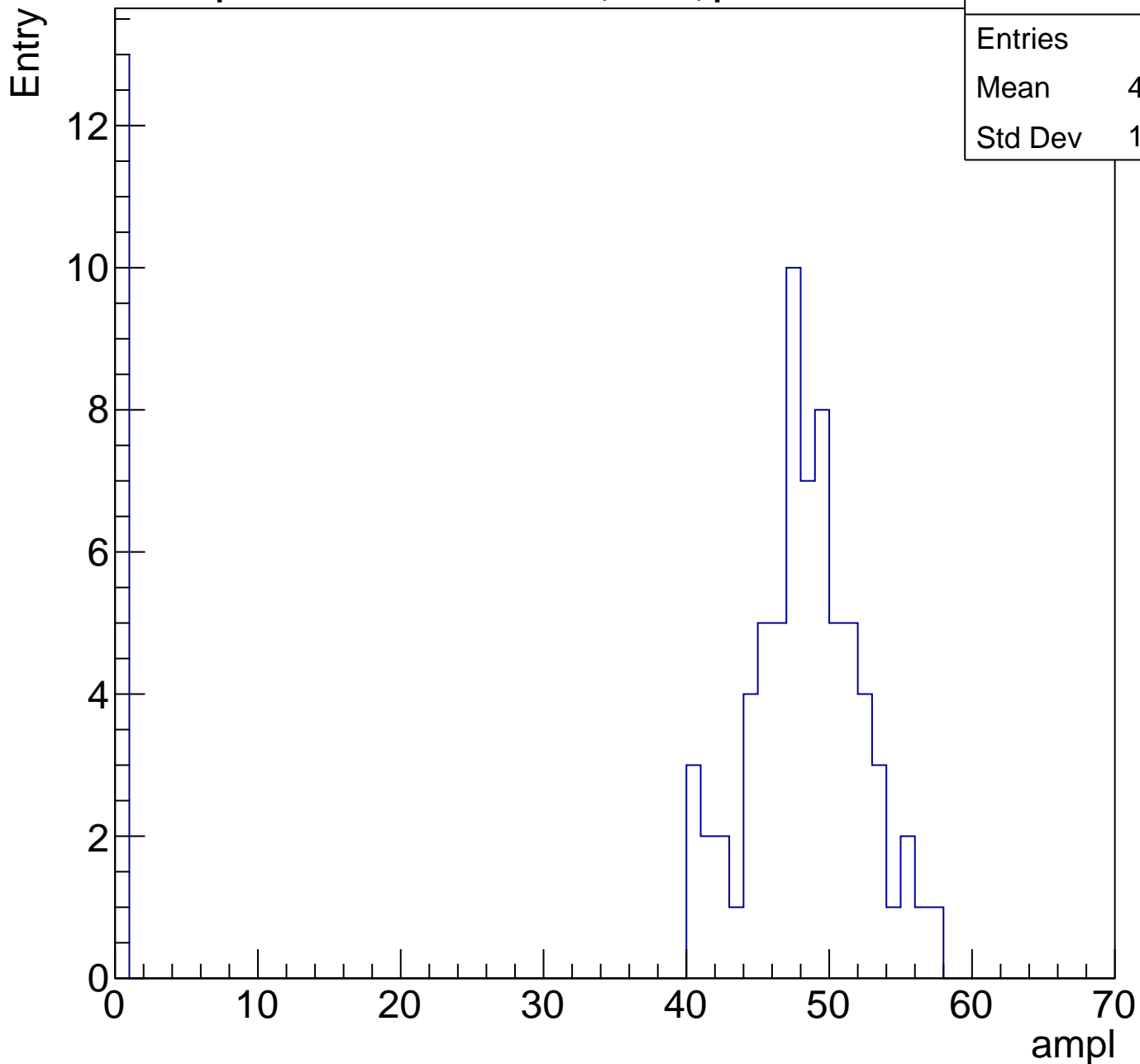
**Gaus Width: 3.7250**



# B1L103S, U22-ch102, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	40.33
Std Dev	17.86

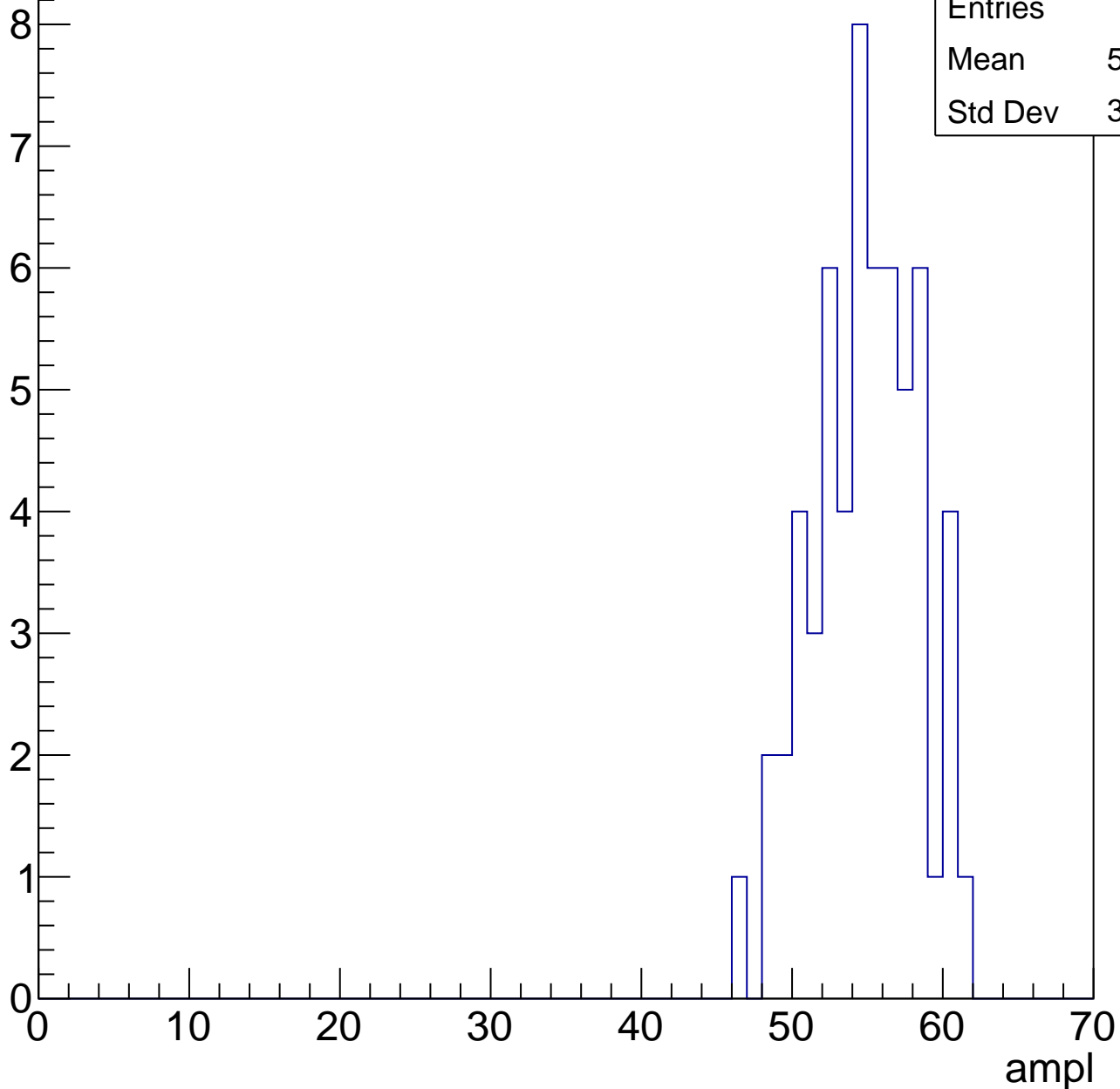


# B1L103S, U22-ch102, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.37
Std Dev	3.409

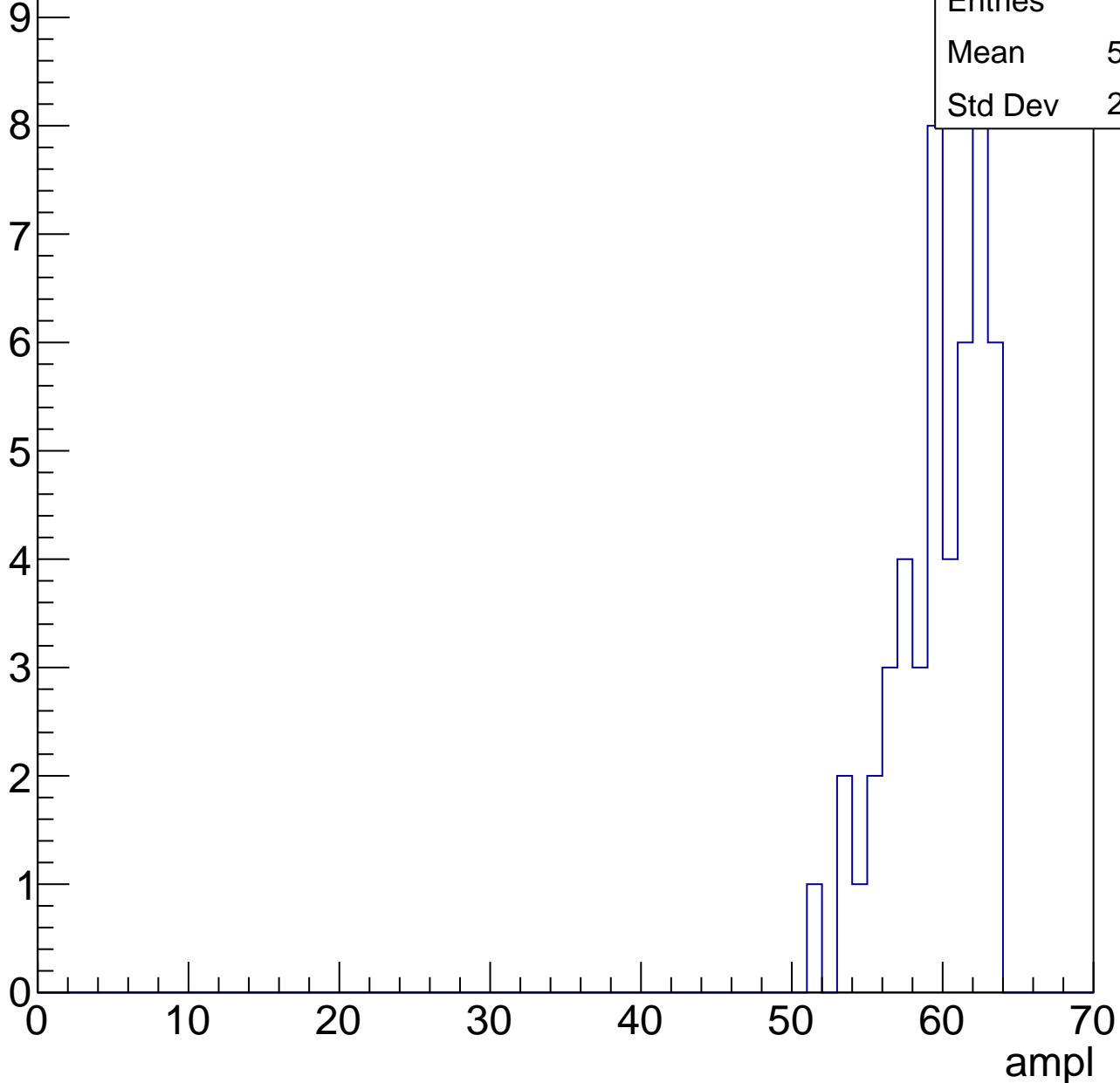


# B1L103S, U22-ch102, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	59.29
Std Dev	2.997



# B1L103S, U22-ch102, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

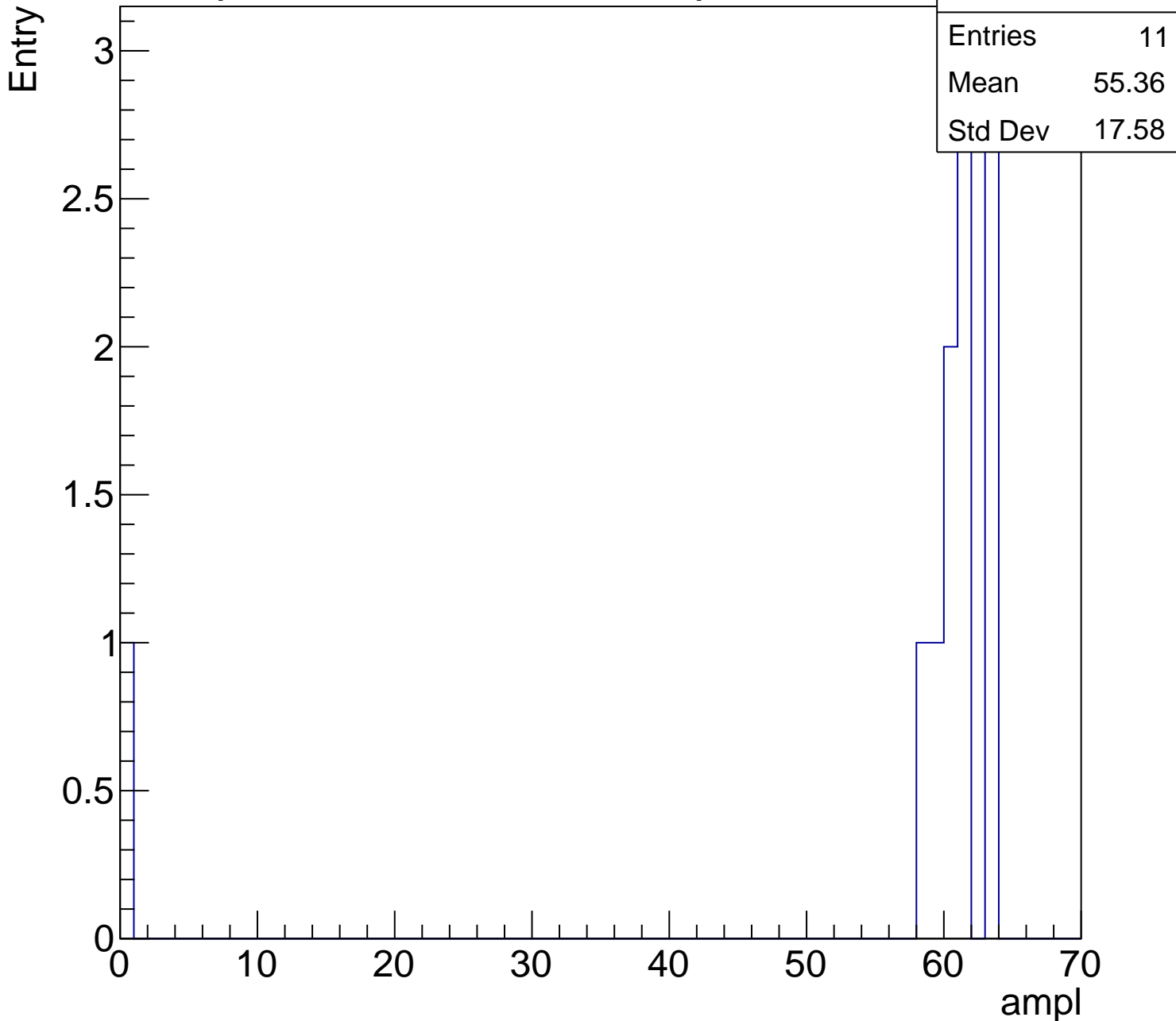
Mean

55.36

Std Dev

17.58

ampl



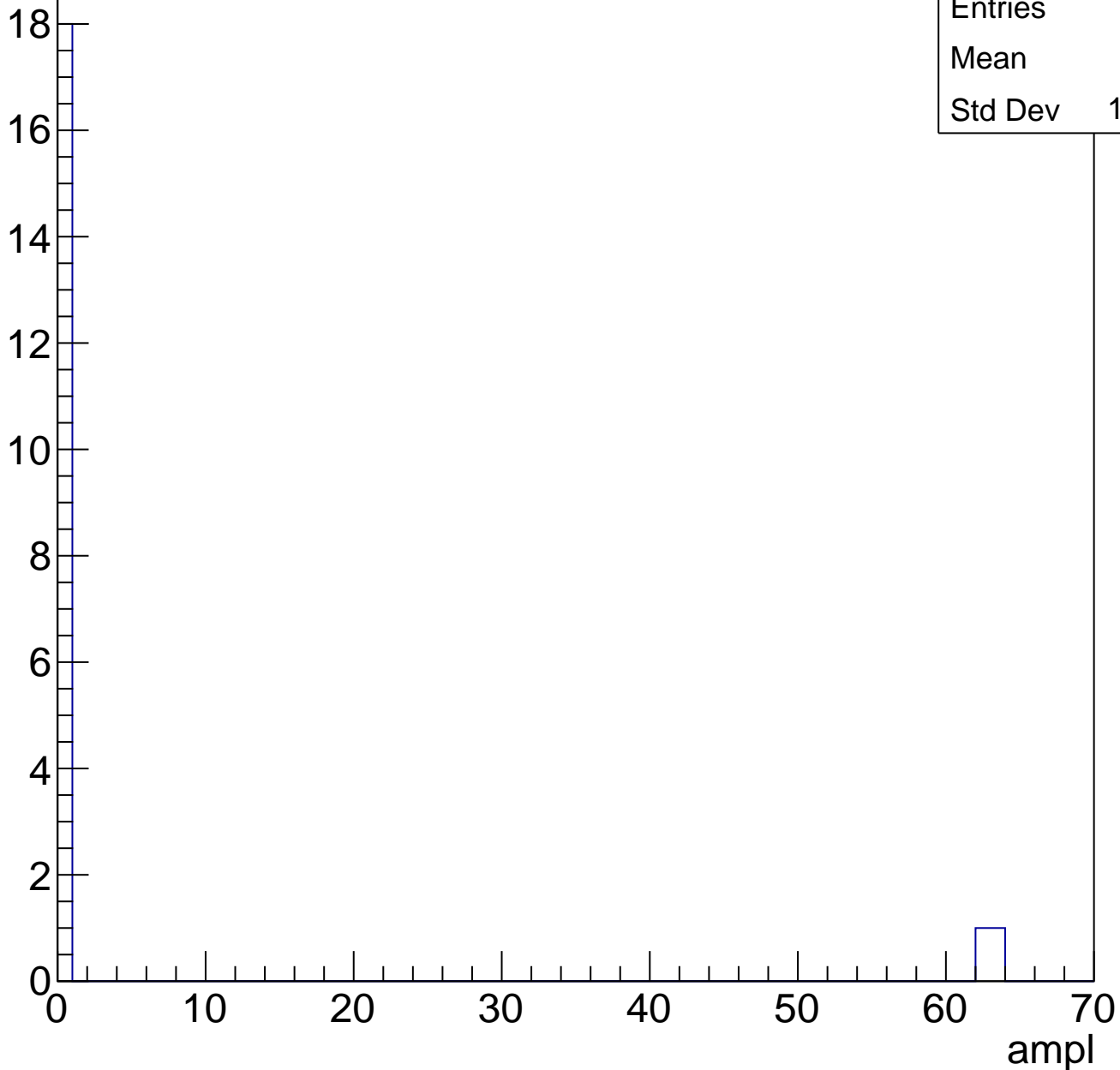


# B1L103S, U22-ch102, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	6.25
Std Dev	18.75

Entry



# B1L103S, U22-ch103, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	77
Mean	23.84
Std Dev	12.27

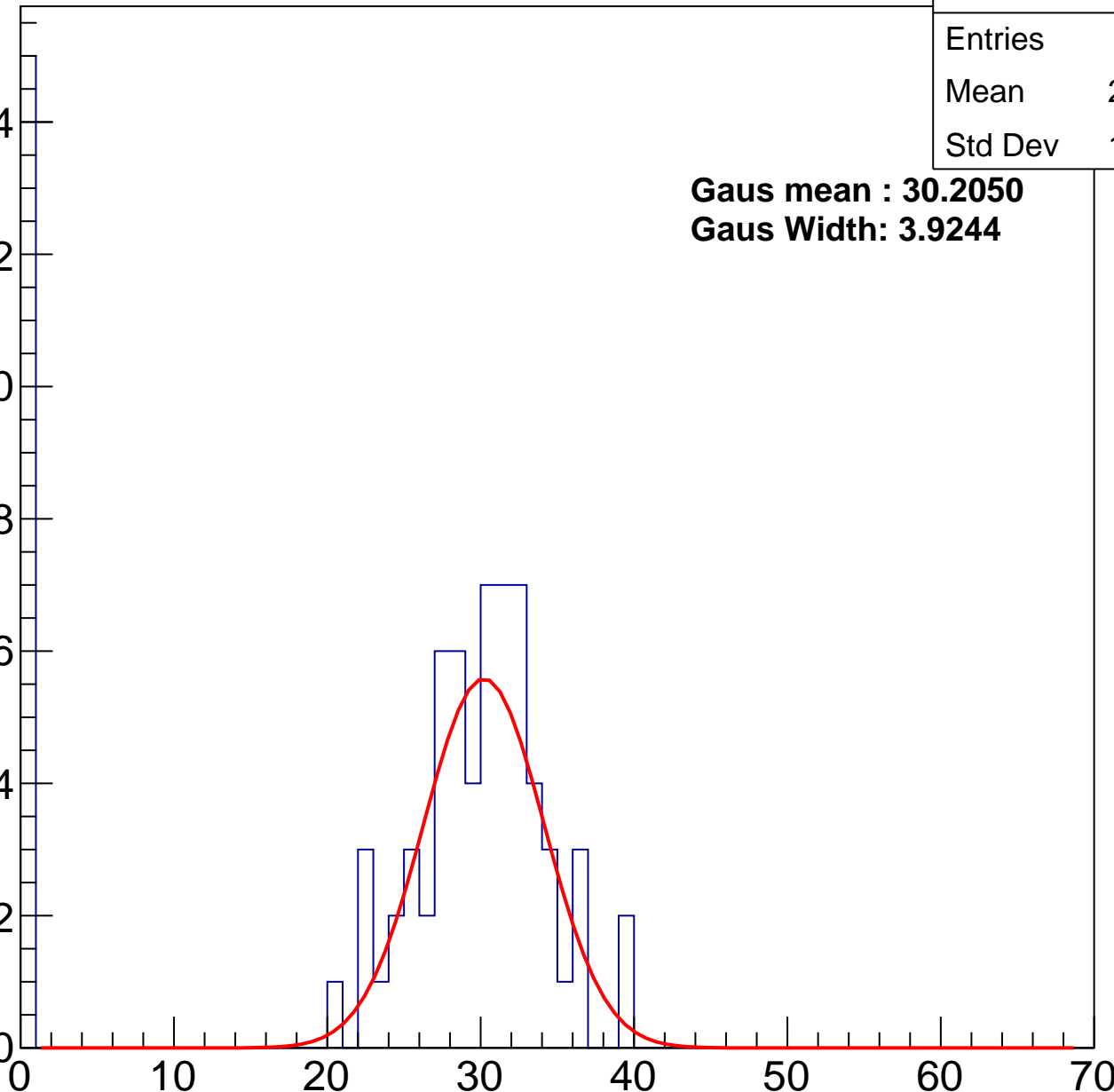
**Gaus mean : 30.2050**

**Gaus Width: 3.9244**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch103, adc1

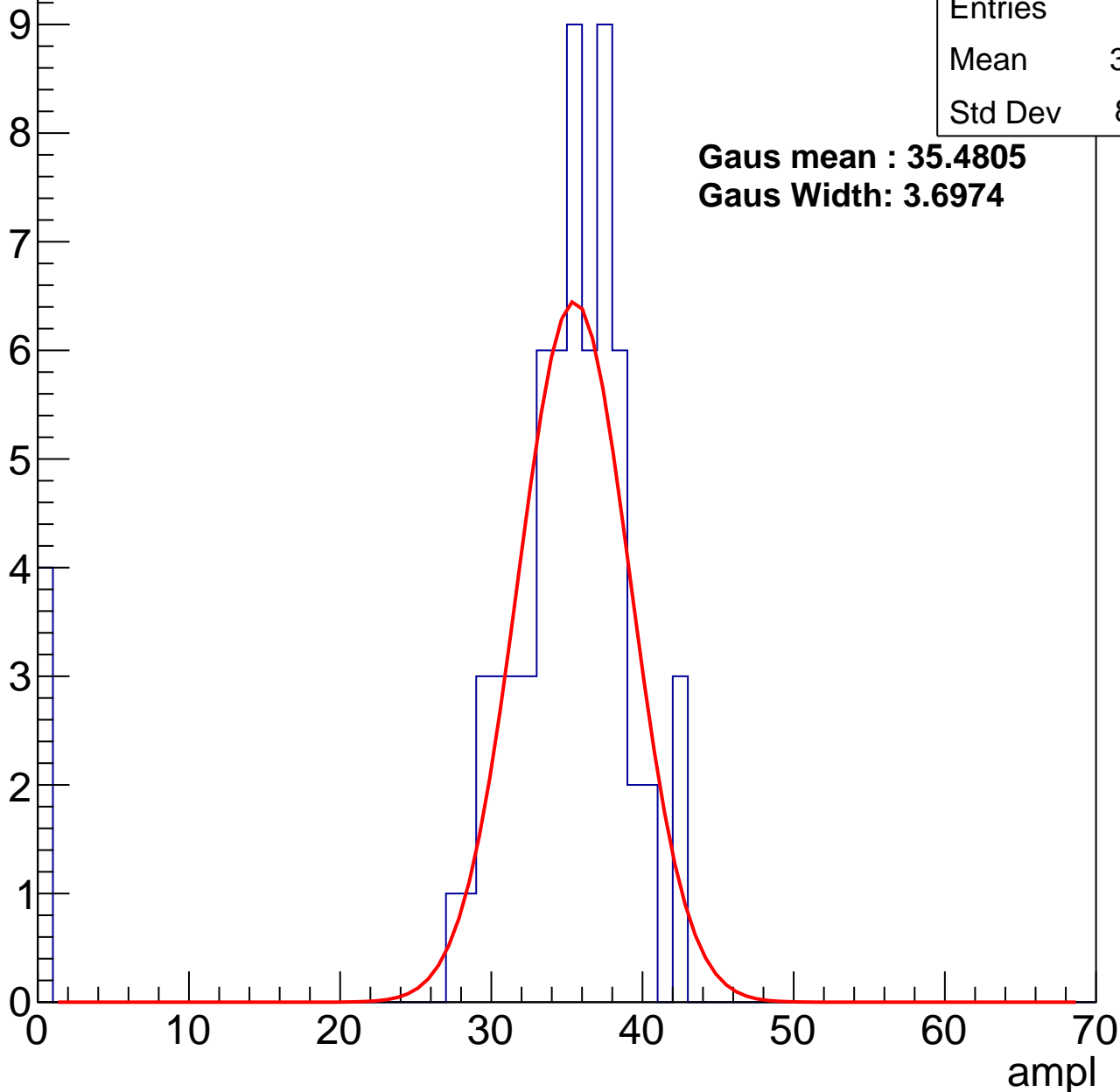
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	32.82
Std Dev	8.901

**Gaus mean : 35.4805**

**Gaus Width: 3.6974**



# B1L103S, U22-ch103, adc2

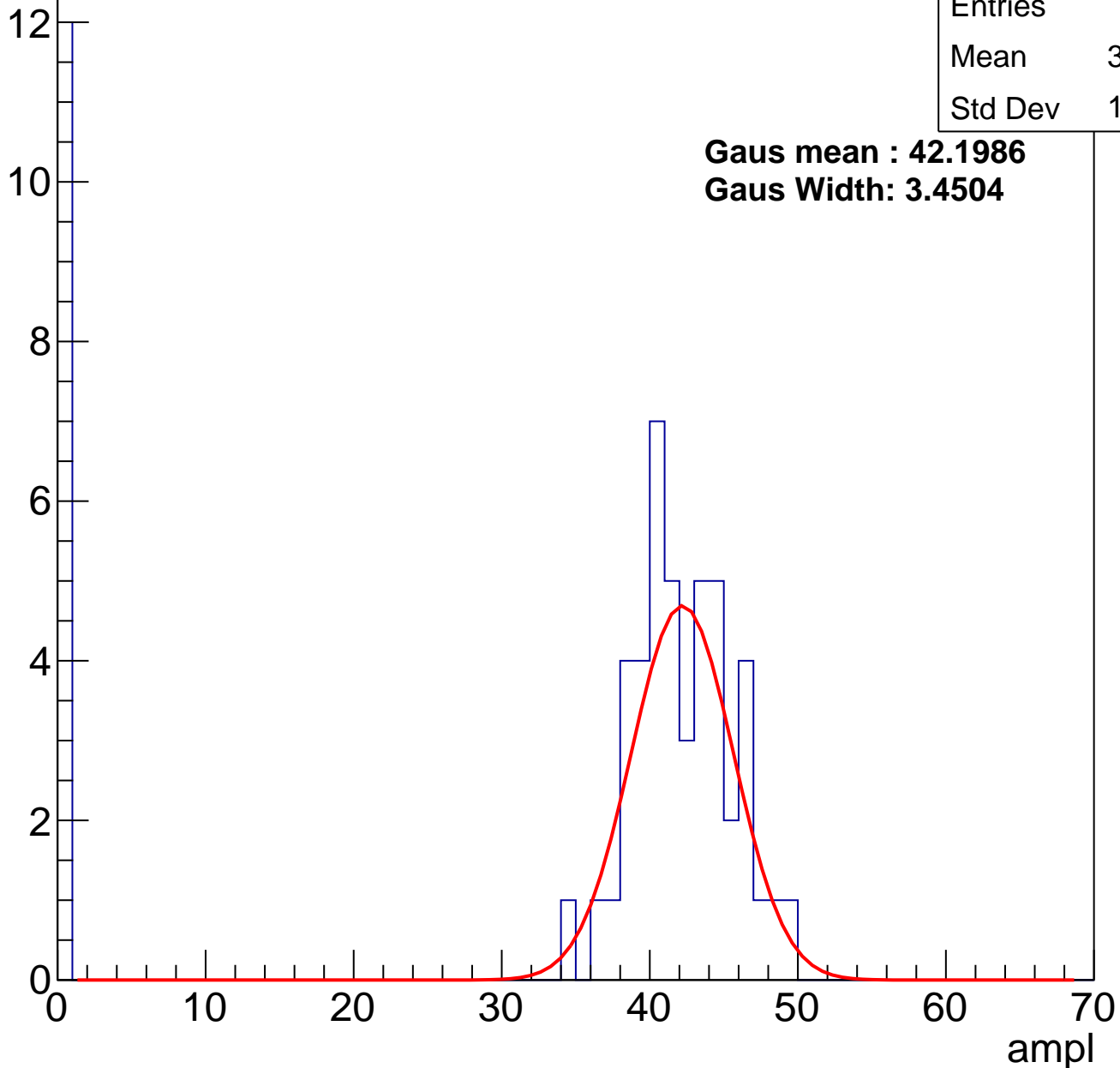
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	57
Mean	32.96
Std Dev	17.26

**Gaus mean : 42.1986**

**Gaus Width: 3.4504**

Entry

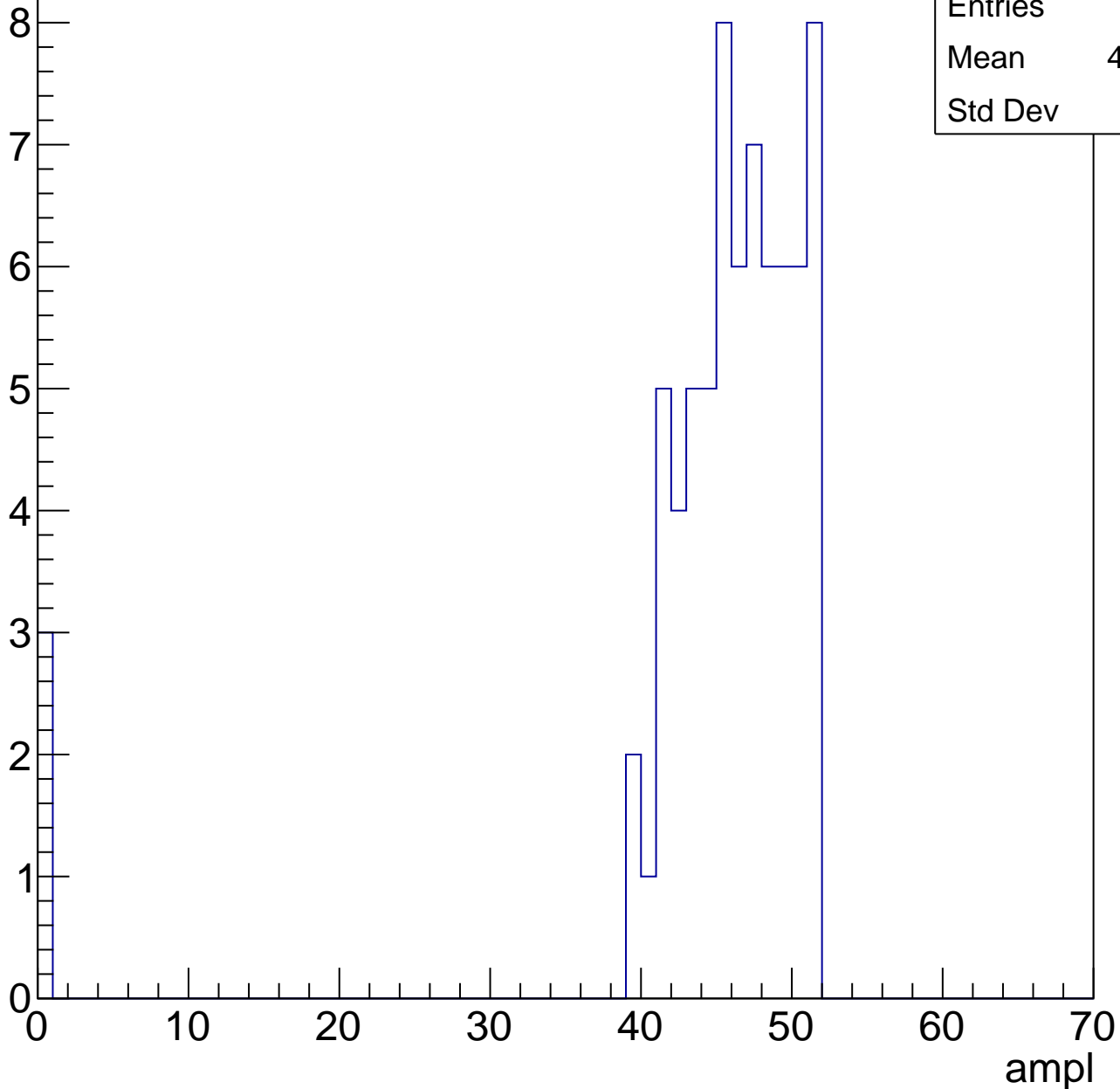


# B1L103S, U22-ch103, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	44.18
Std Dev	9.78



# B1L103S, U22-ch103, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	73
Mean	54.23
Std Dev	3.625

Entry

10

8

6

4

2

0

0

10

20

30

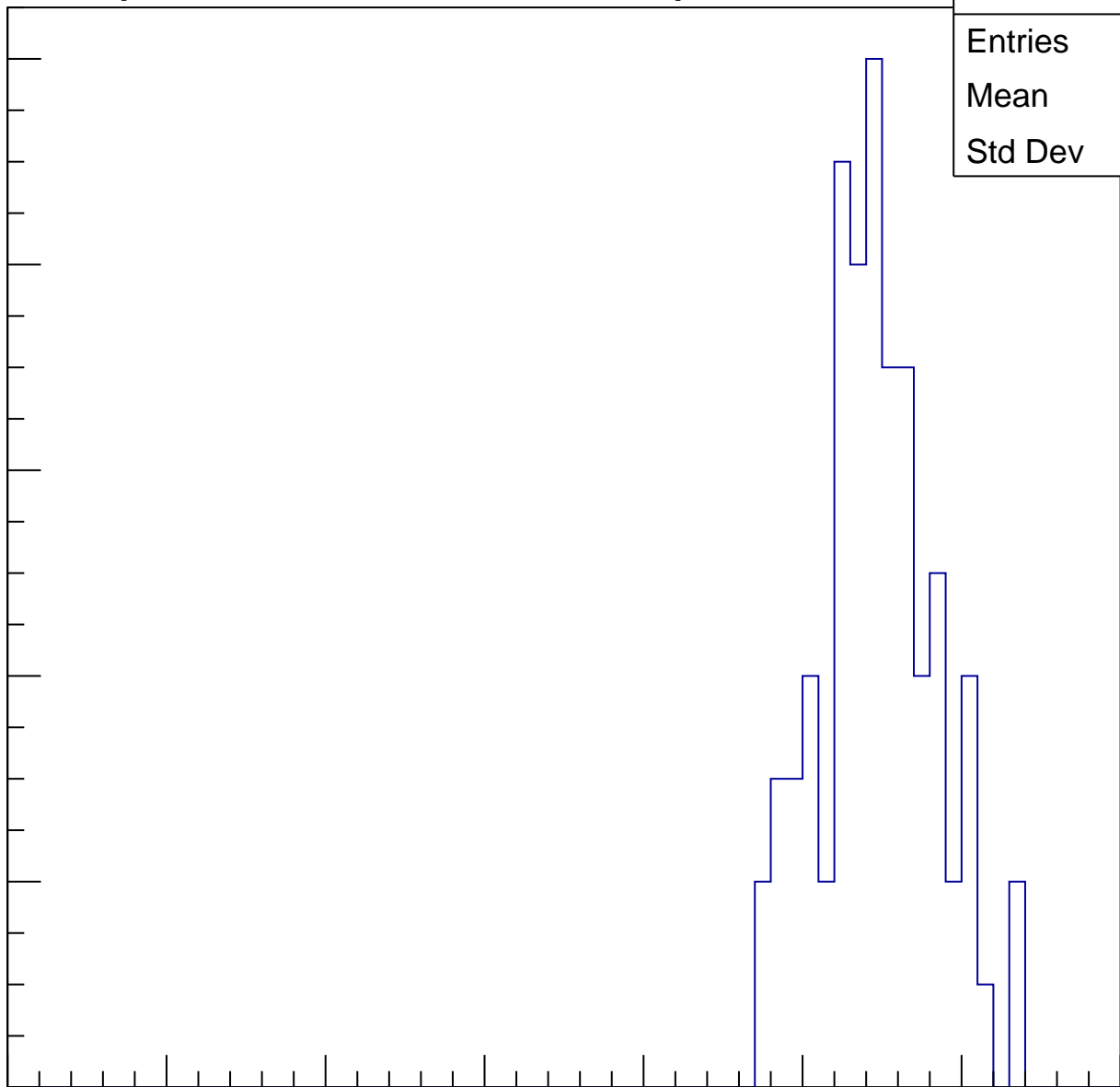
40

50

60

70

ampl

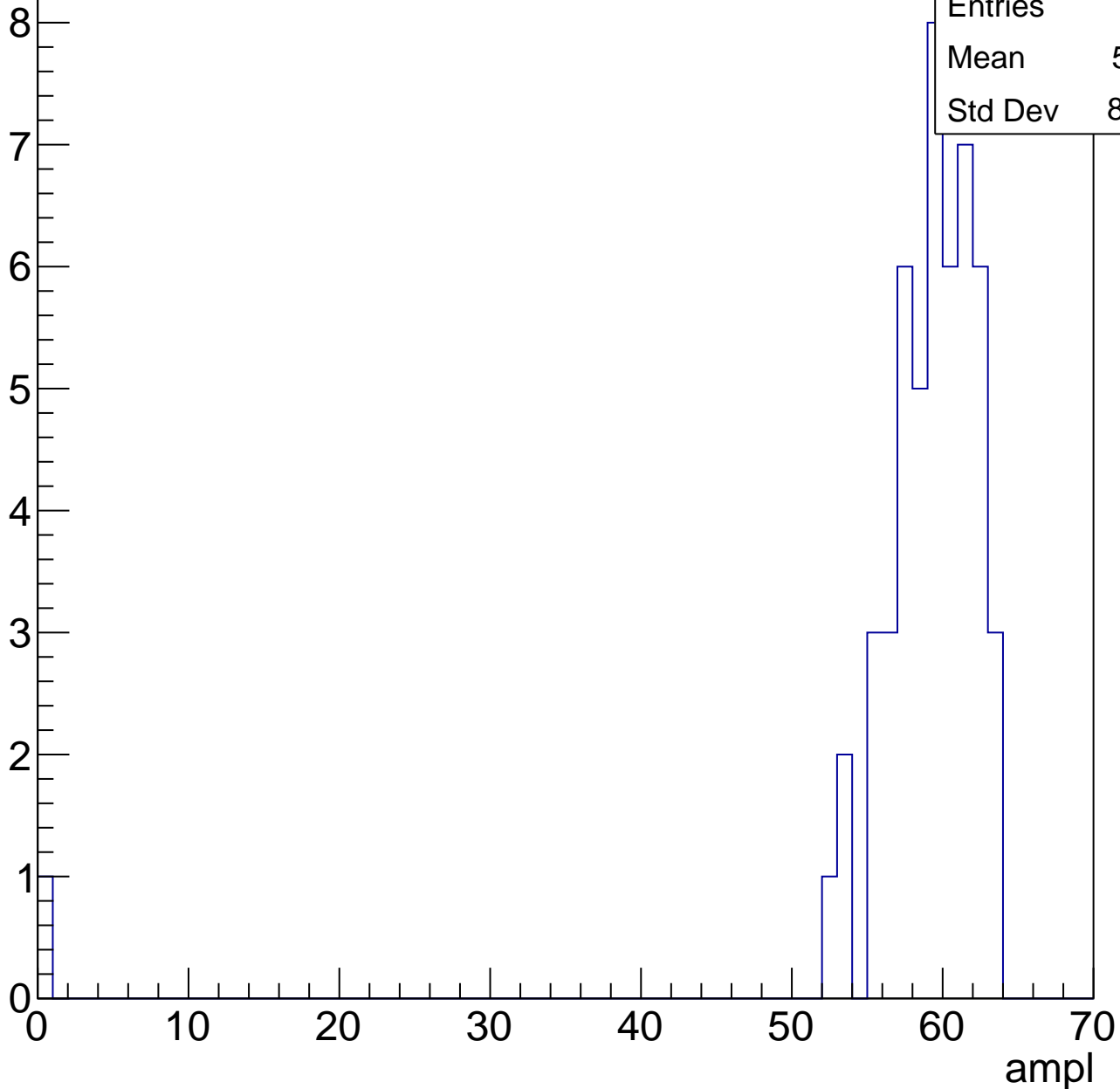


# B1L103S, U22-ch103, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	51
Mean	57.71
Std Dev	8.582

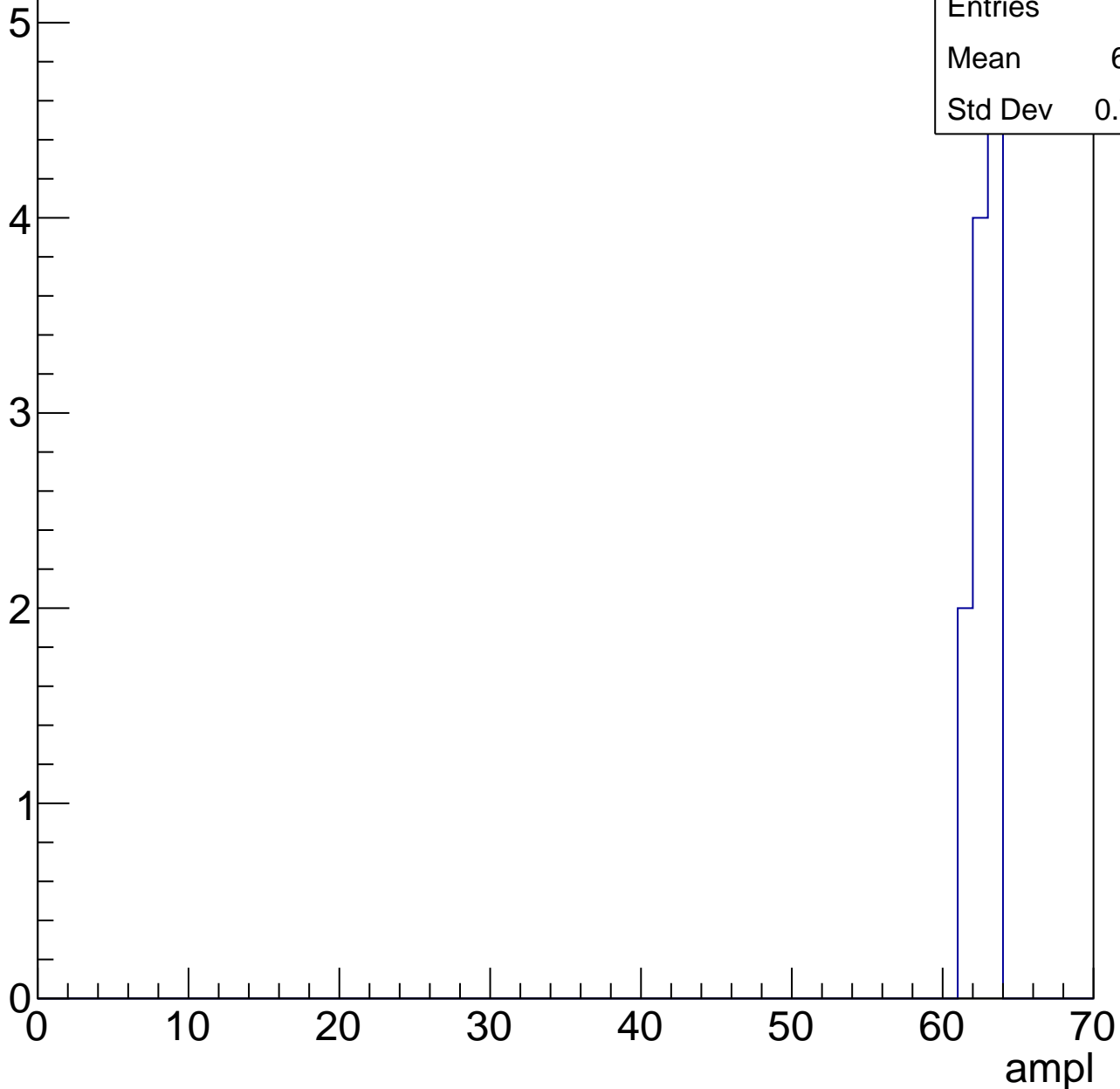


# B1L103S, U22-ch103, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	11
Mean	62.27
Std Dev	0.7497



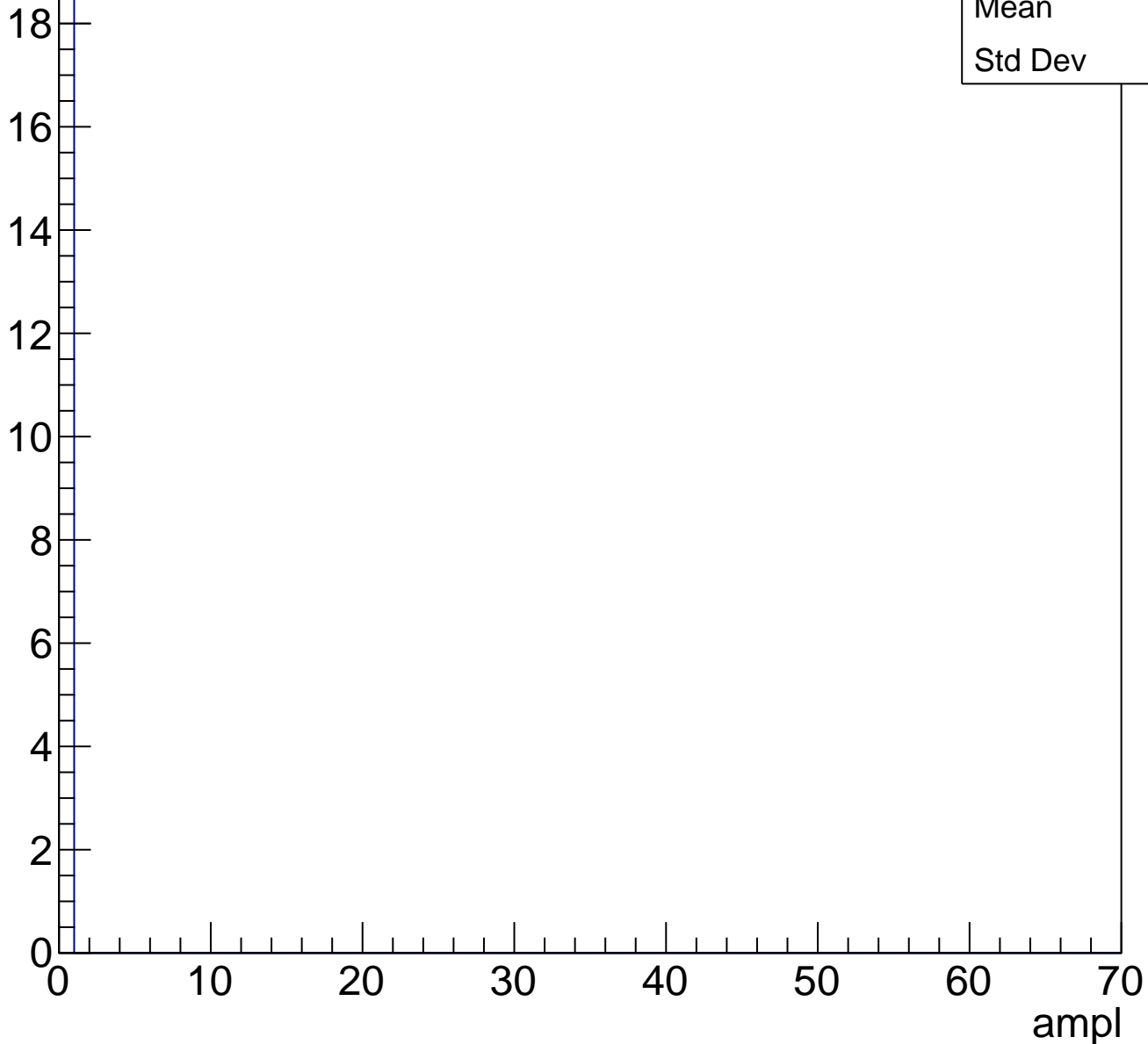


# B1L103S, U22-ch103, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



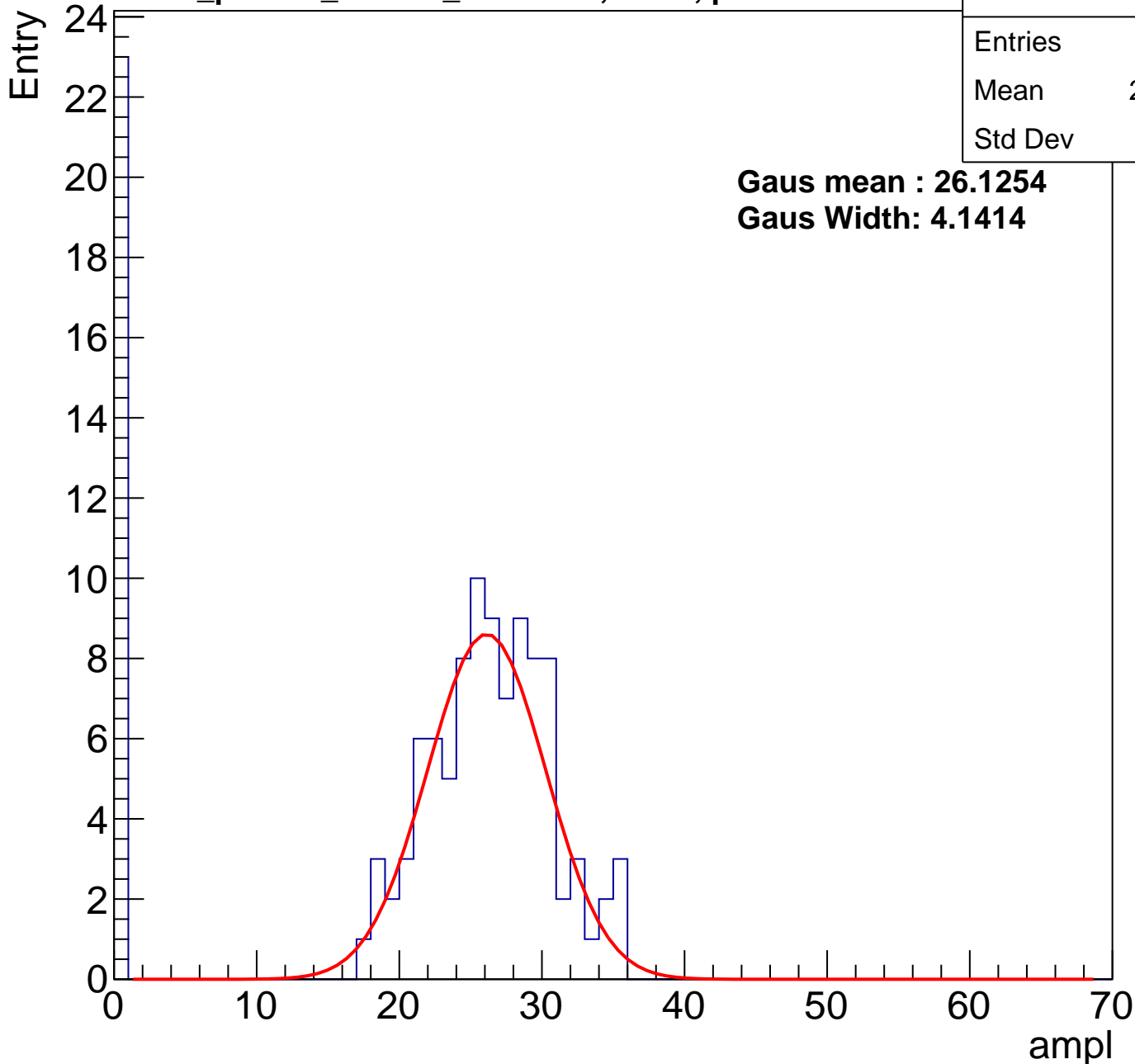
# B1L103S, U22-ch104, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	119
Mean	20.97
Std Dev	10.91

**Gaus mean : 26.1254**

**Gaus Width: 4.1414**



# B1L103S, U22-ch104, adc1

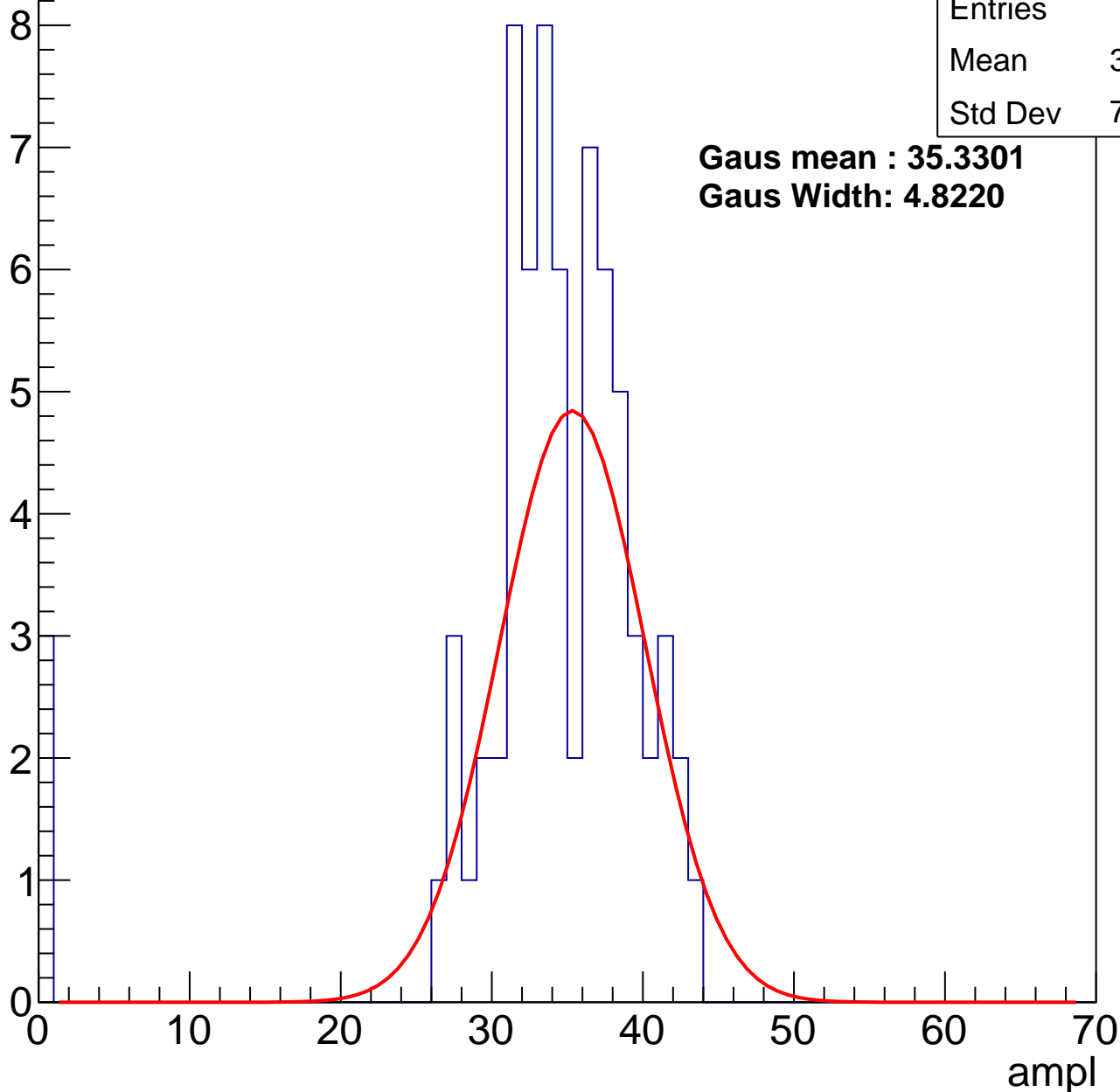
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	32.99
Std Dev	7.957

**Gaus mean : 35.3301**

**Gaus Width: 4.8220**



# B1L103S, U22-ch104, adc2

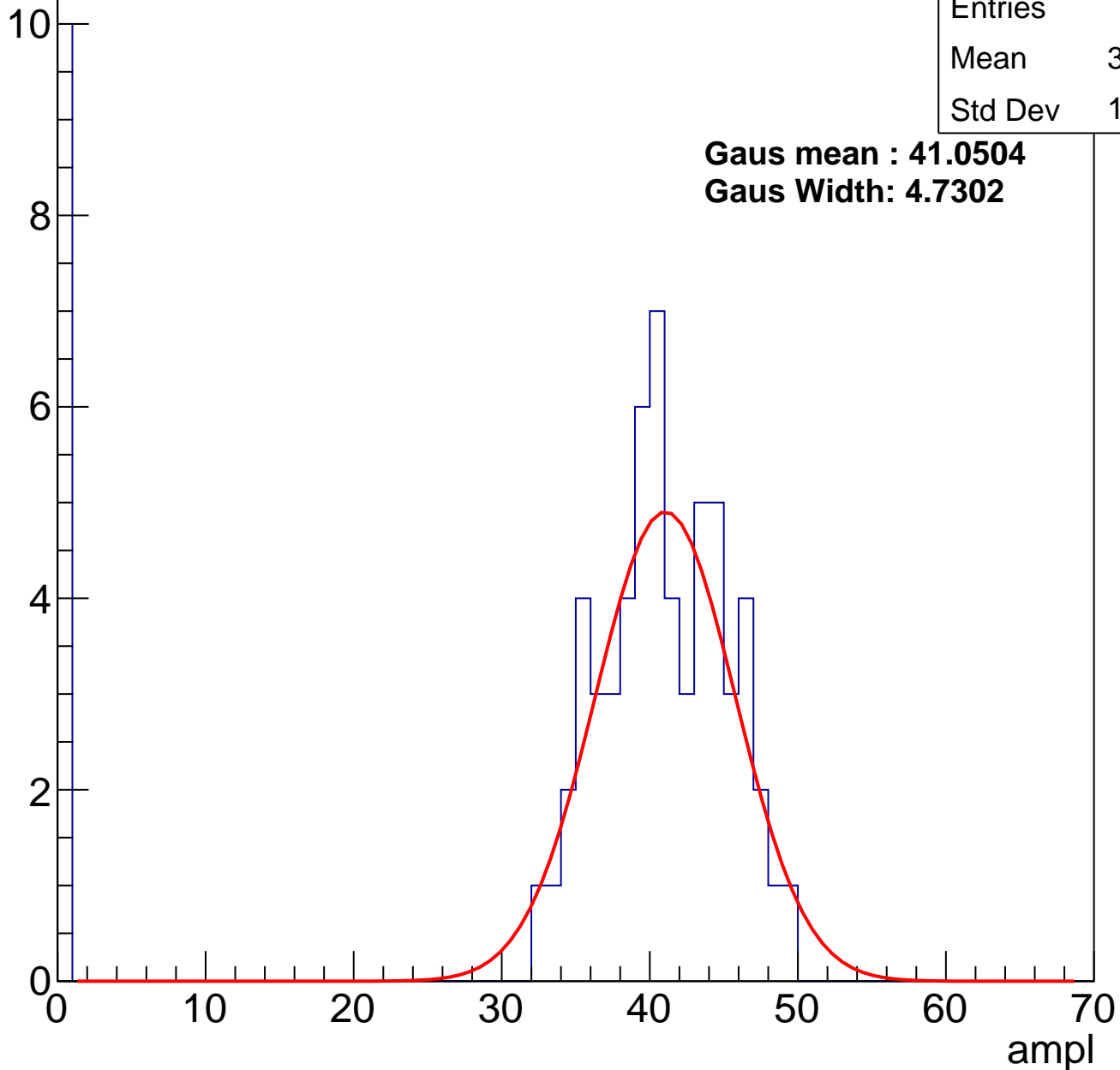
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	69
Mean	34.68
Std Dev	14.76

**Gaus mean : 41.0504**

**Gaus Width: 4.7302**

Entry

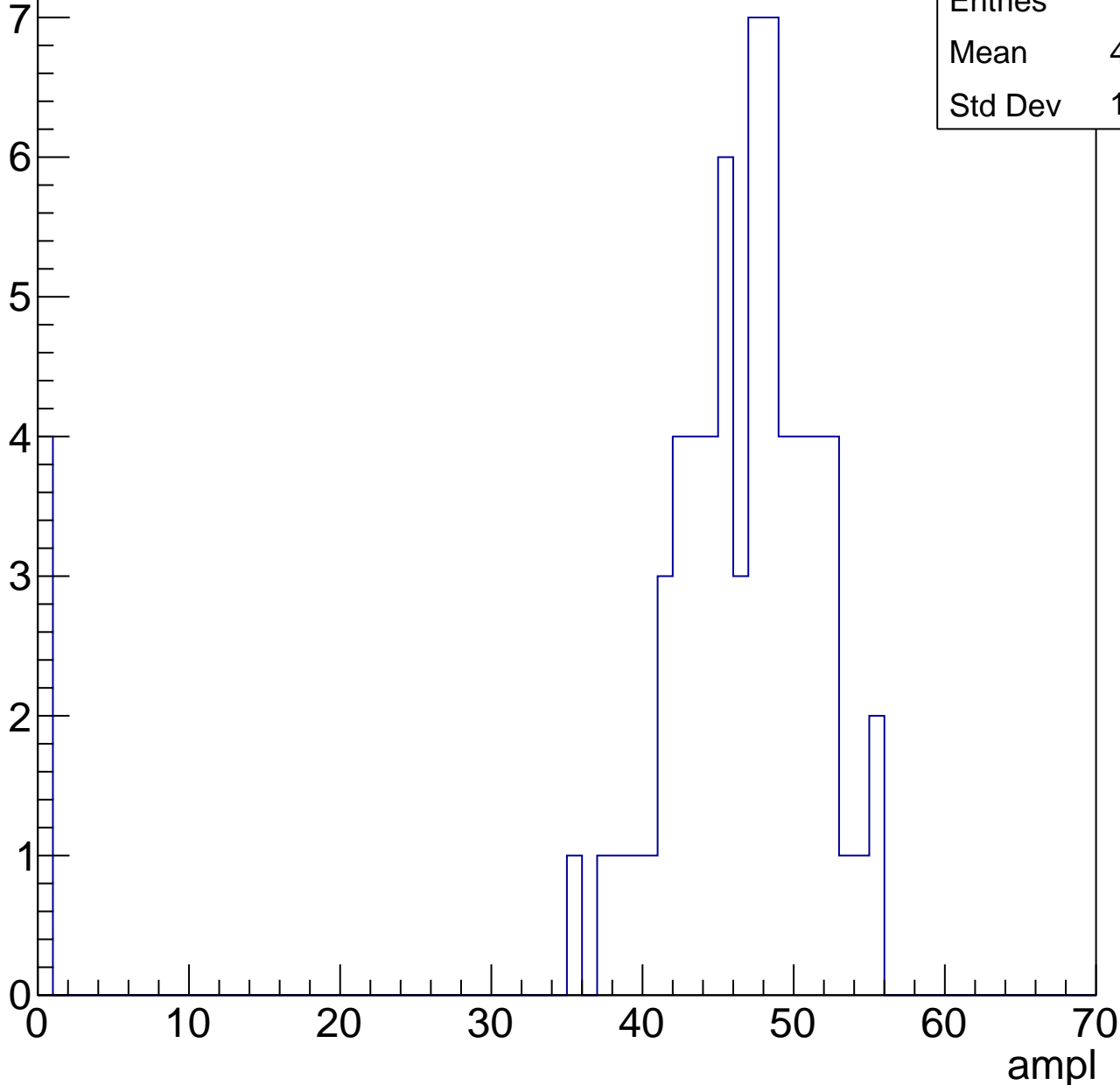


# B1L103S, U22-ch104, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	43.67
Std Dev	11.78

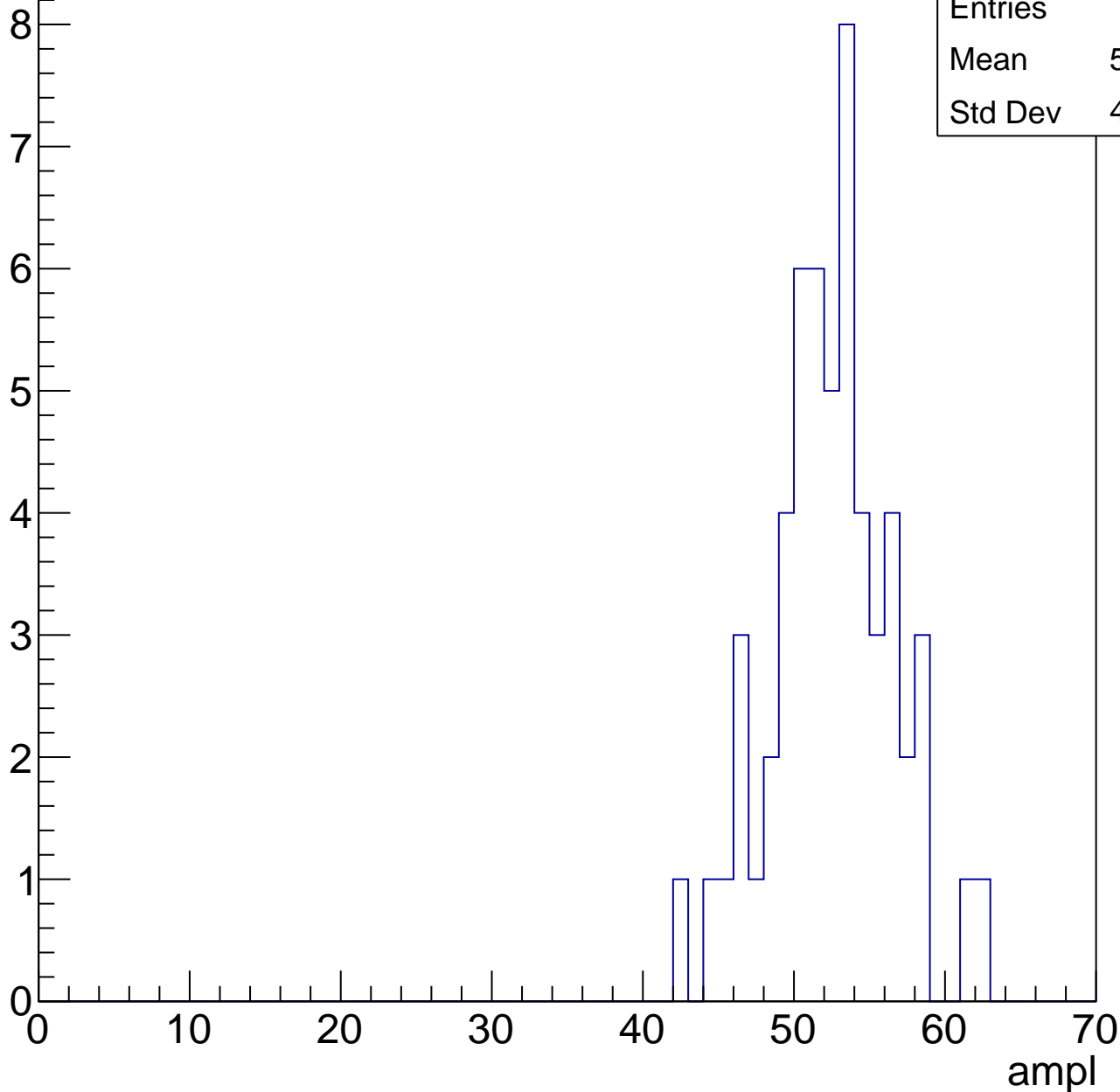


# B1L103S, U22-ch104, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	52.04
Std Dev	4.004

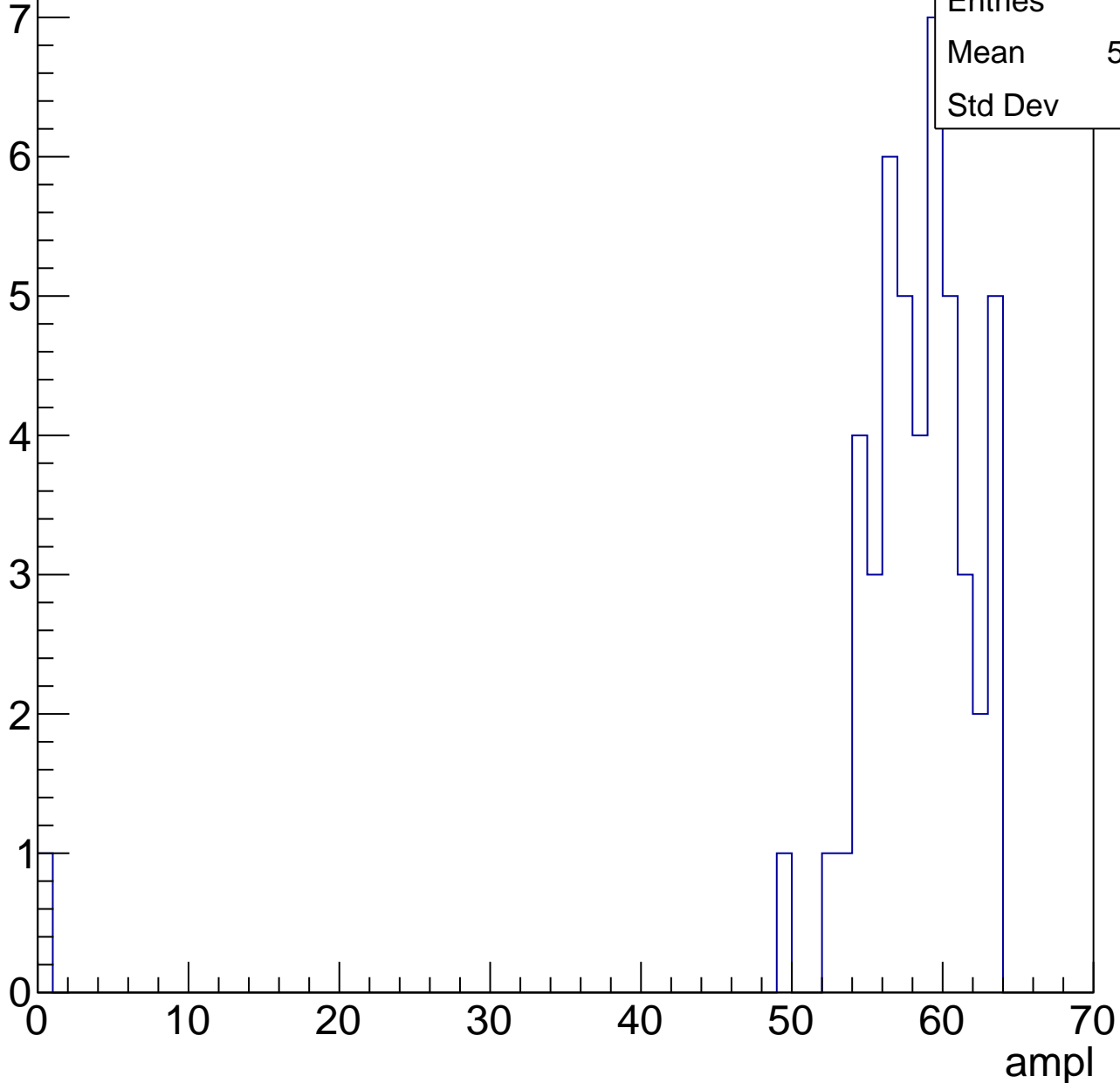


# B1L103S, U22-ch104, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	56.73
Std Dev	8.85

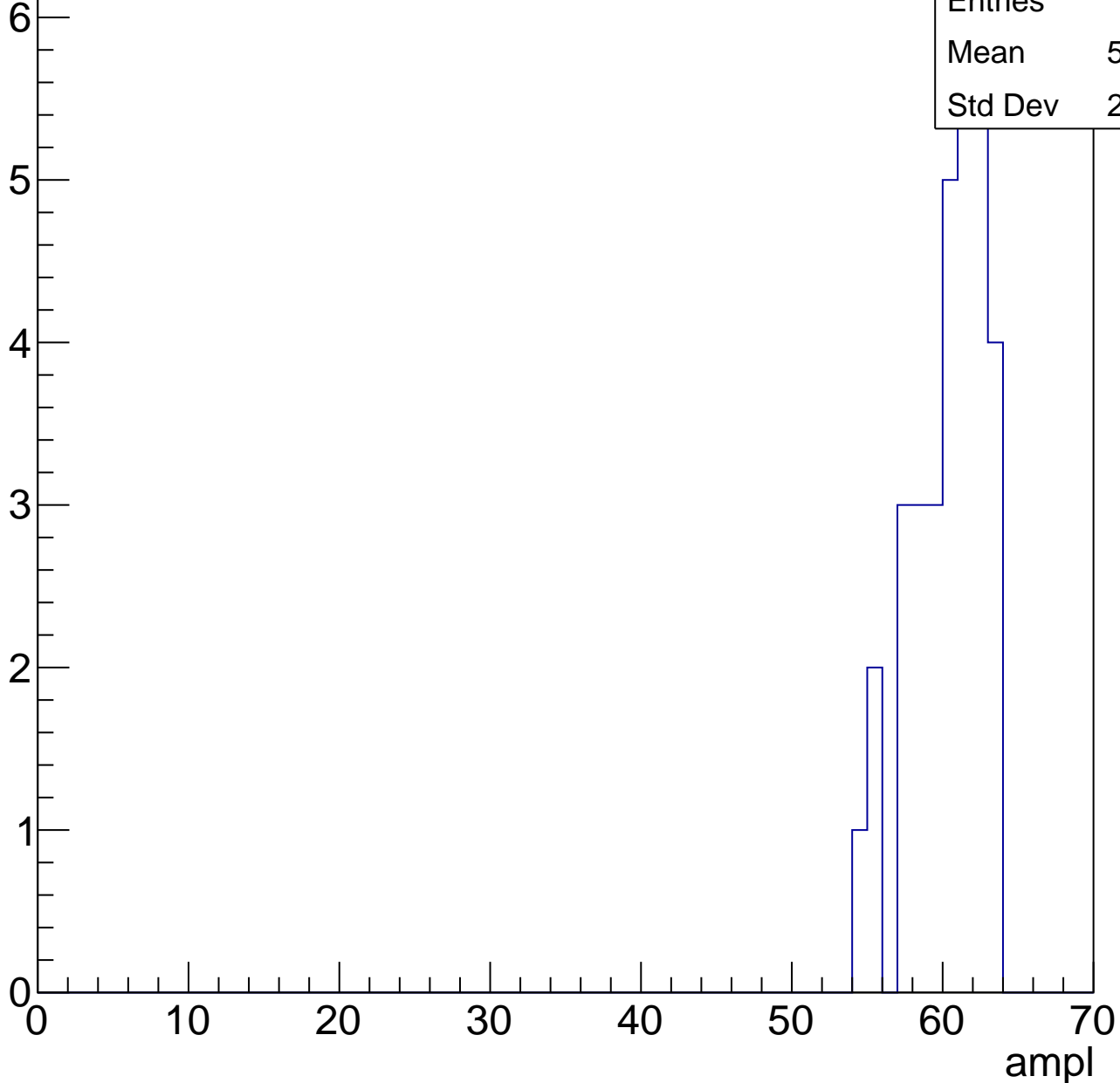


# B1L103S, U22-ch104, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	33
Mean	59.88
Std Dev	2.422

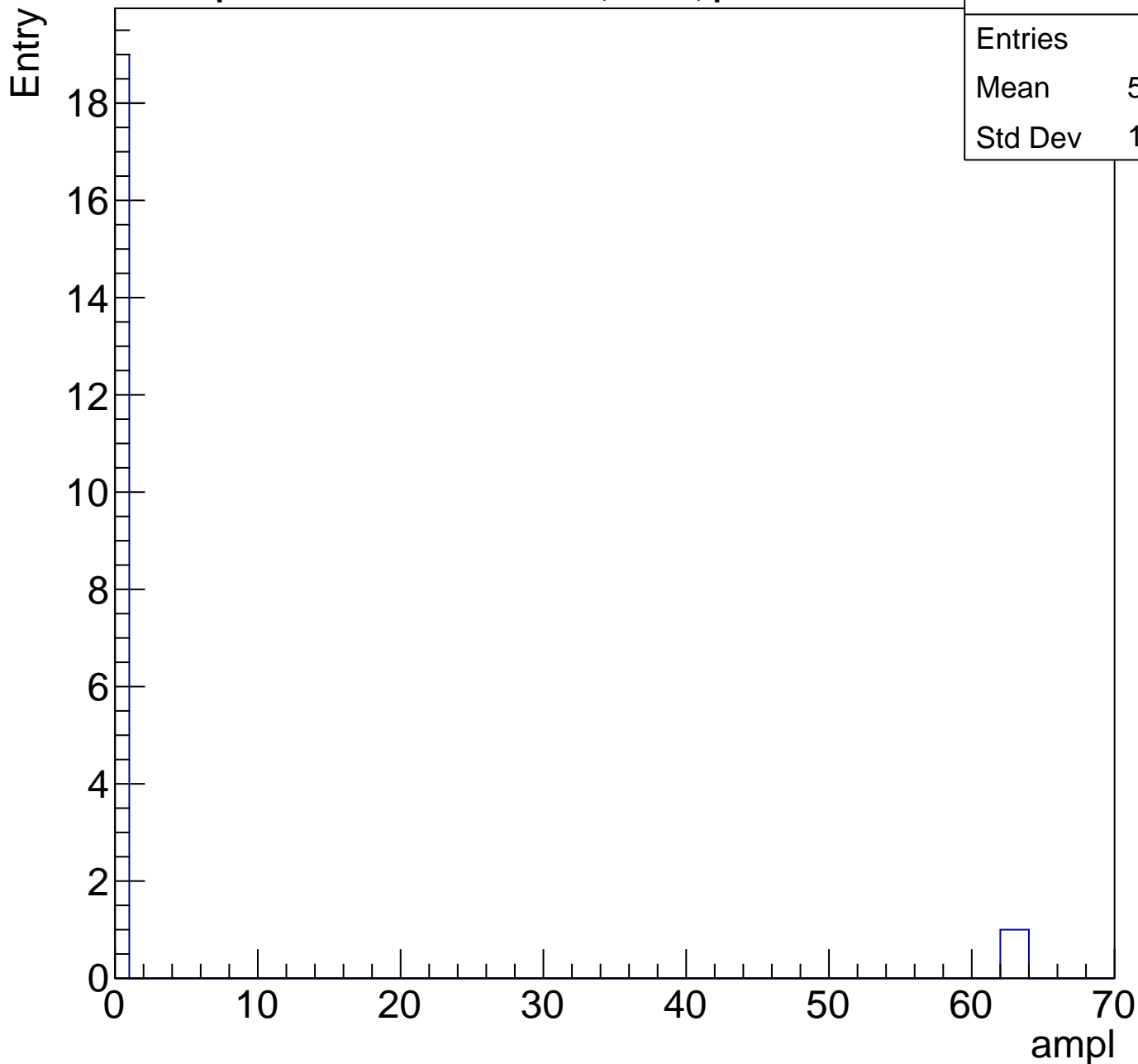




# B1L103S, U22-ch104, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



# B1L103S, U22-ch105, adc0

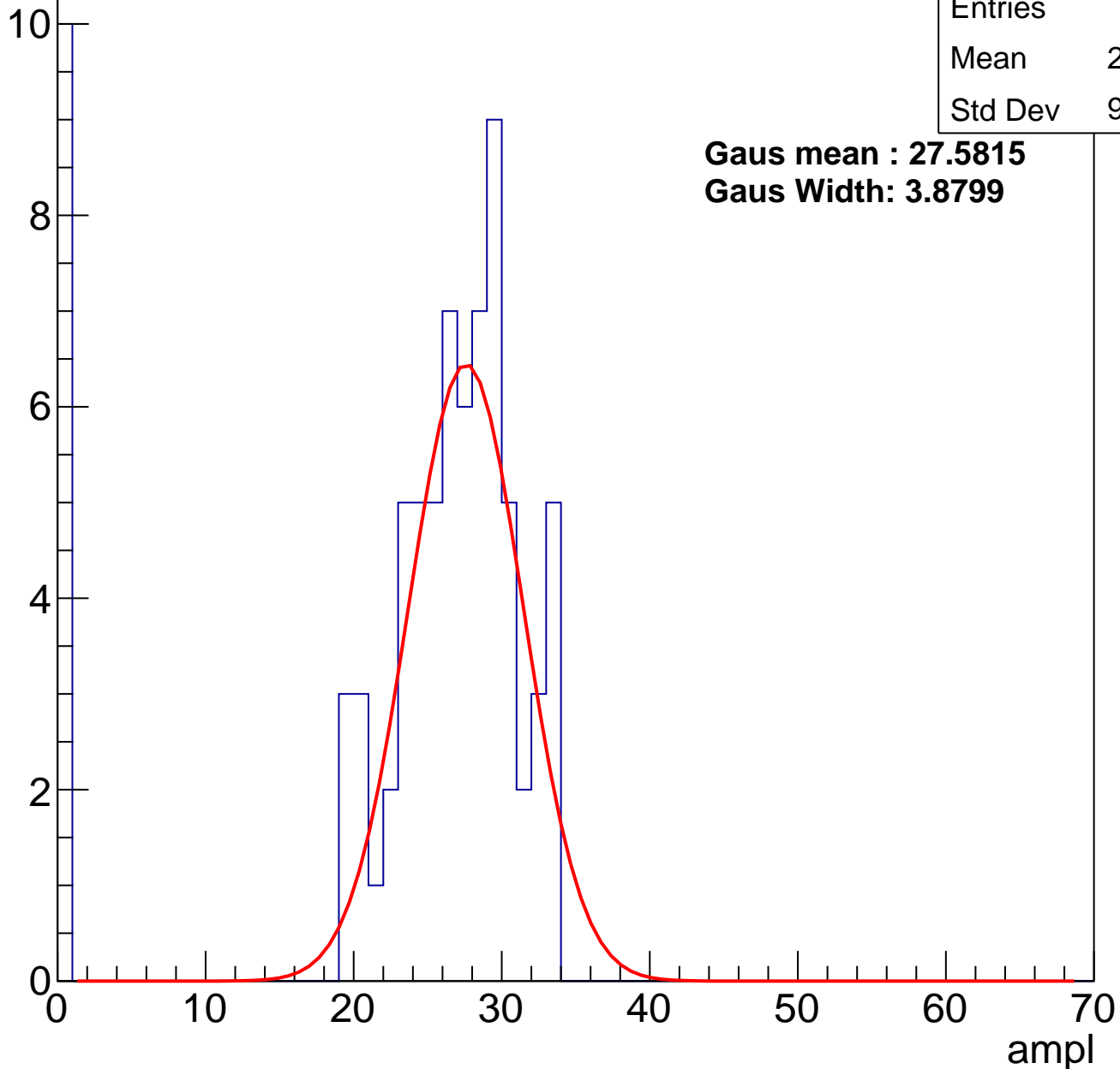
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	23.28
Std Dev	9.584

**Gaus mean : 27.5815**

**Gaus Width: 3.8799**

Entry



# B1L103S, U22-ch105, adc1

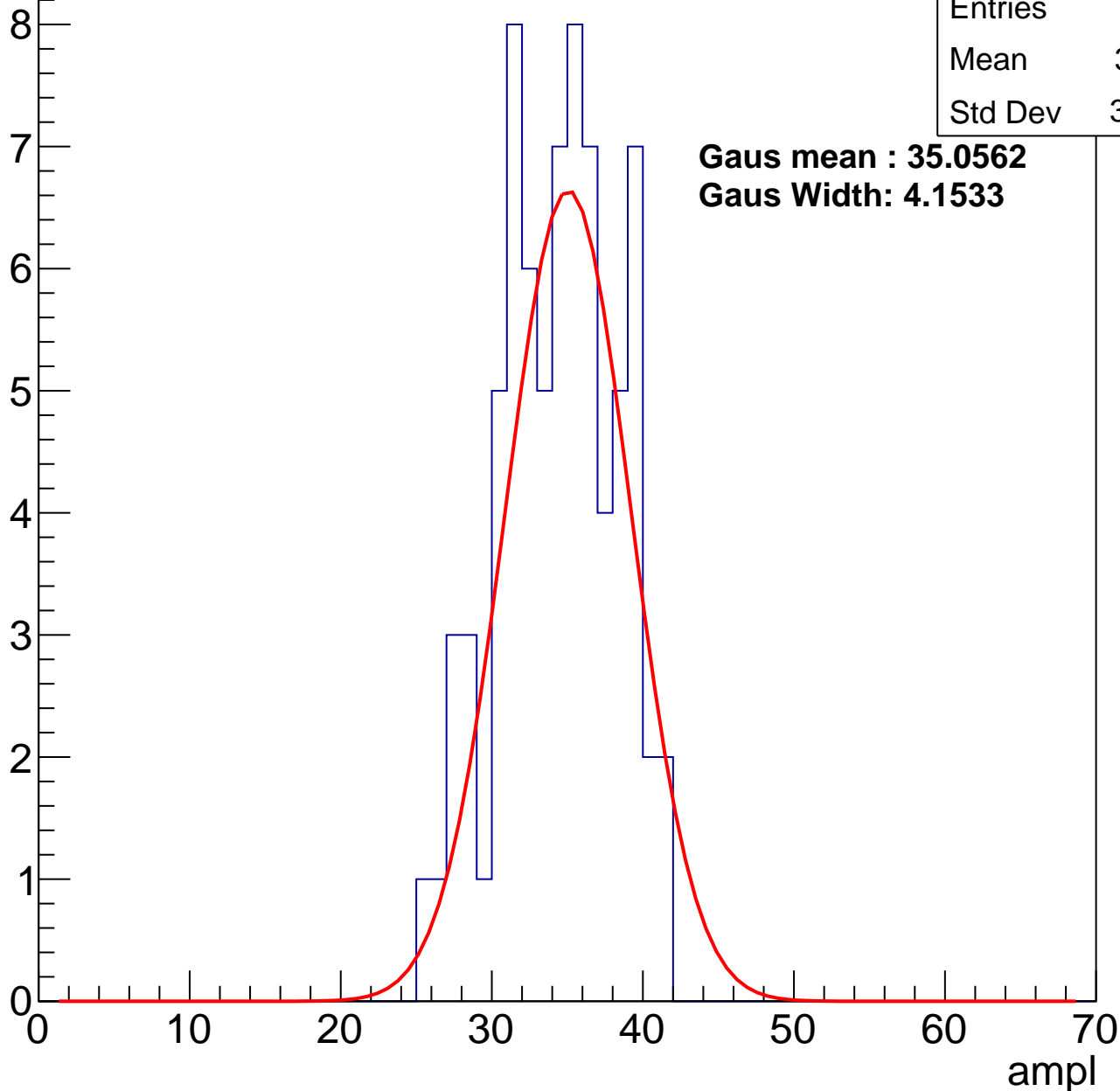
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	33.91
Std Dev	3.834

**Gaus mean : 35.0562**

**Gaus Width: 4.1533**



# B1L103S, U22-ch105, adc2

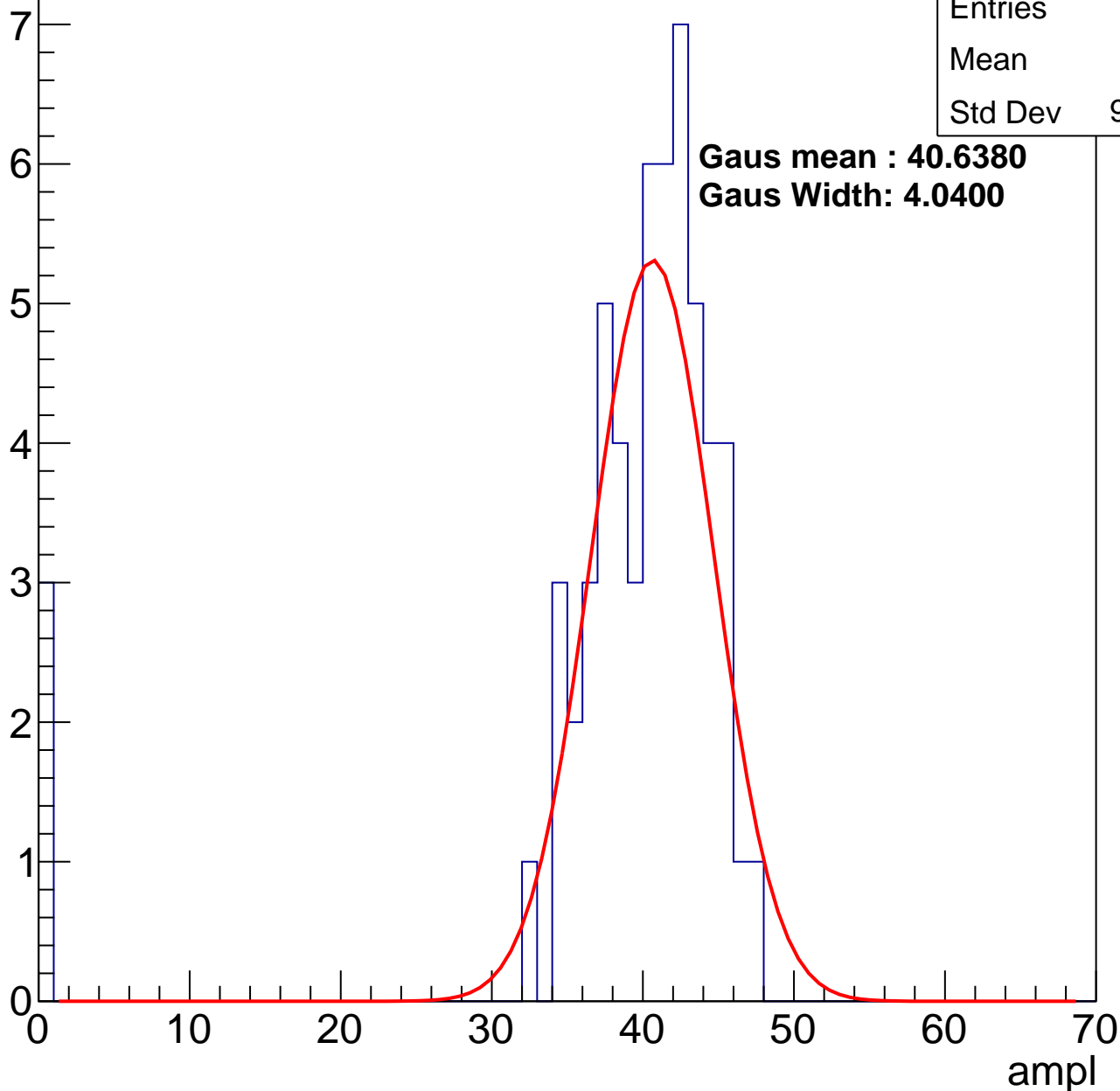
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	38.1
Std Dev	9.517

**Gaus mean : 40.6380**

**Gaus Width: 4.0400**



# B1L103S, U22-ch105, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	40.64
Std Dev	16.16

Entry

10

8

6

4

2

0

0

10

20

30

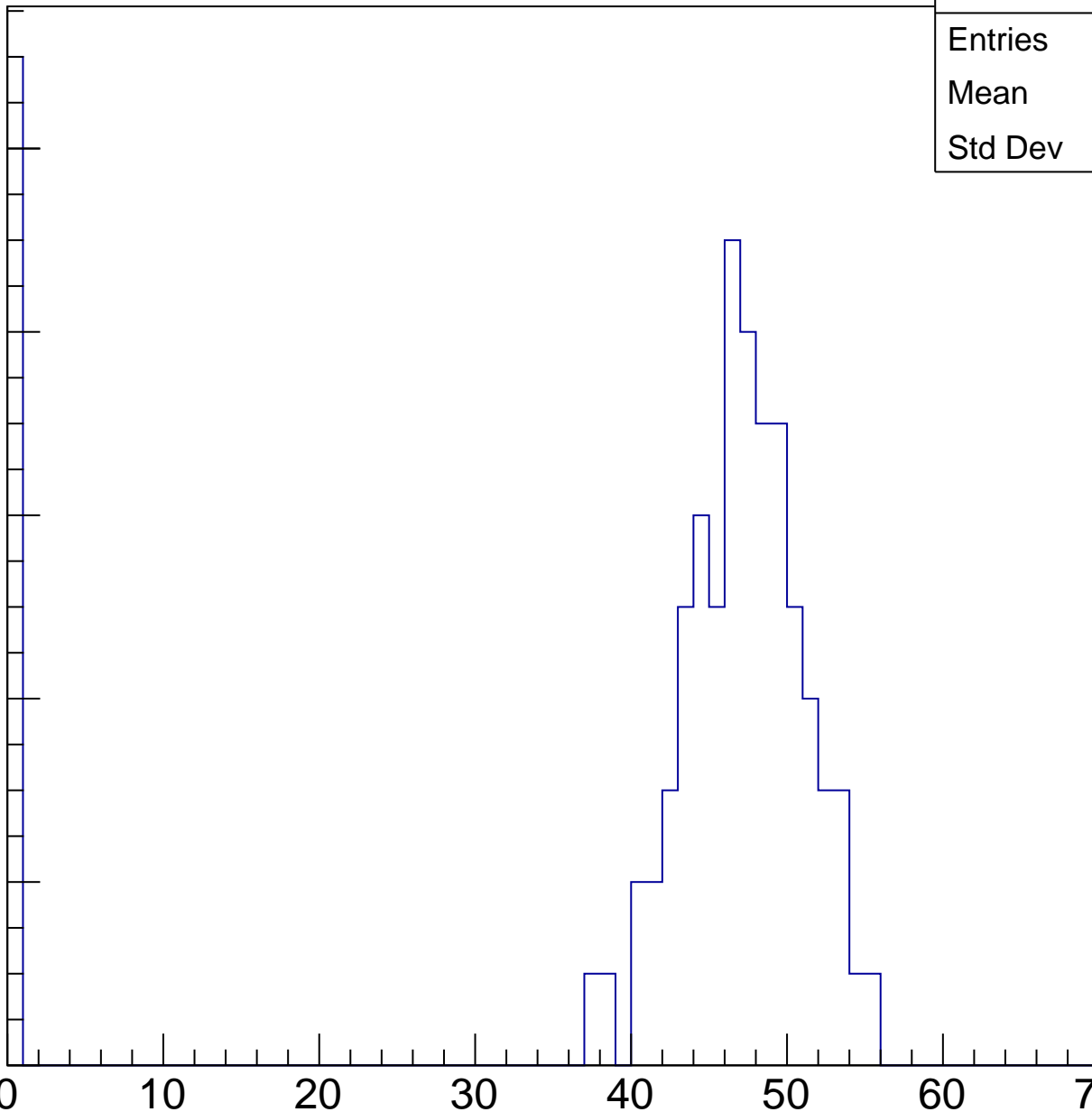
40

50

60

70

ampl

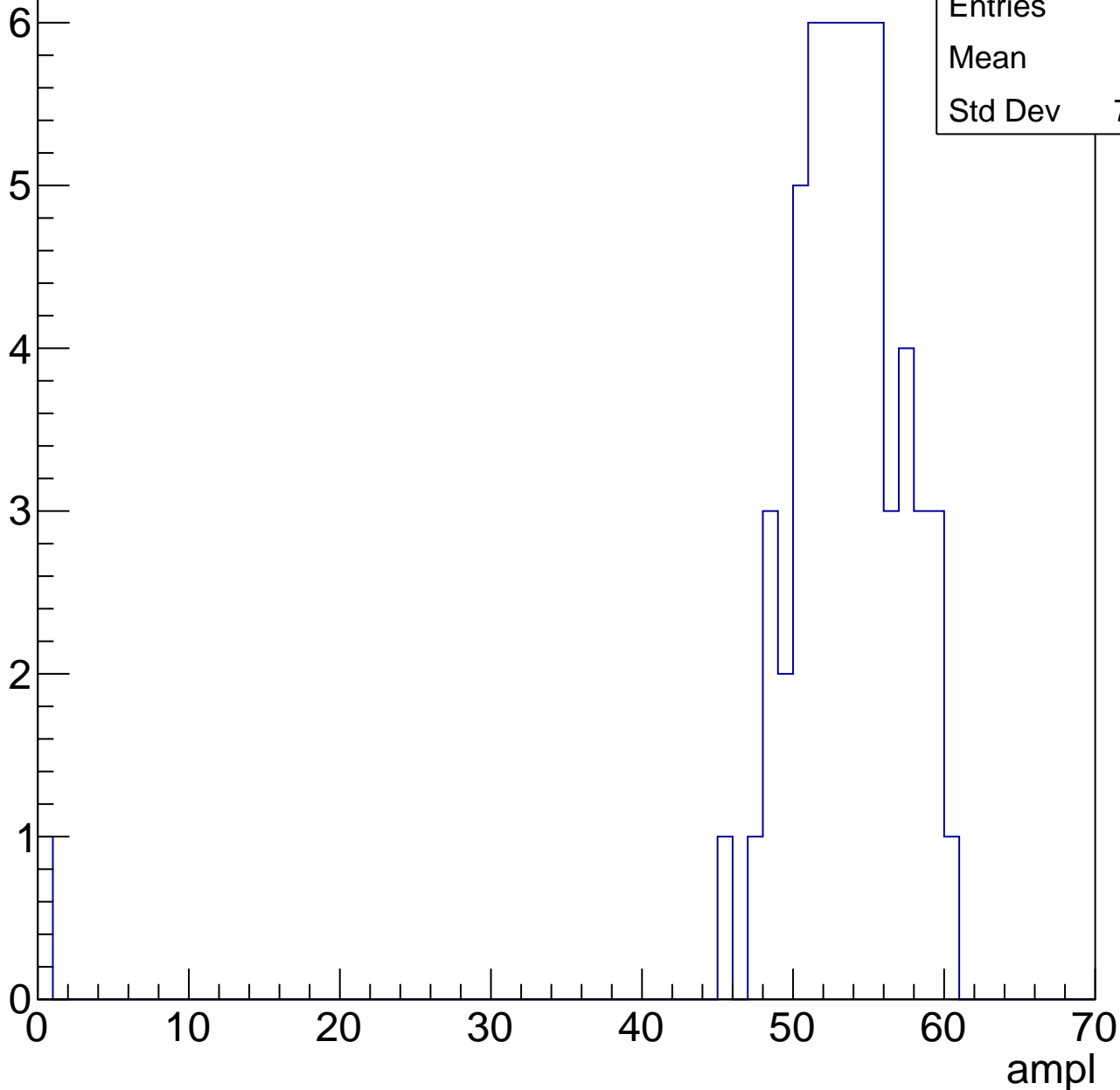


# B1L103S, U22-ch105, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	52.3
Std Dev	7.741

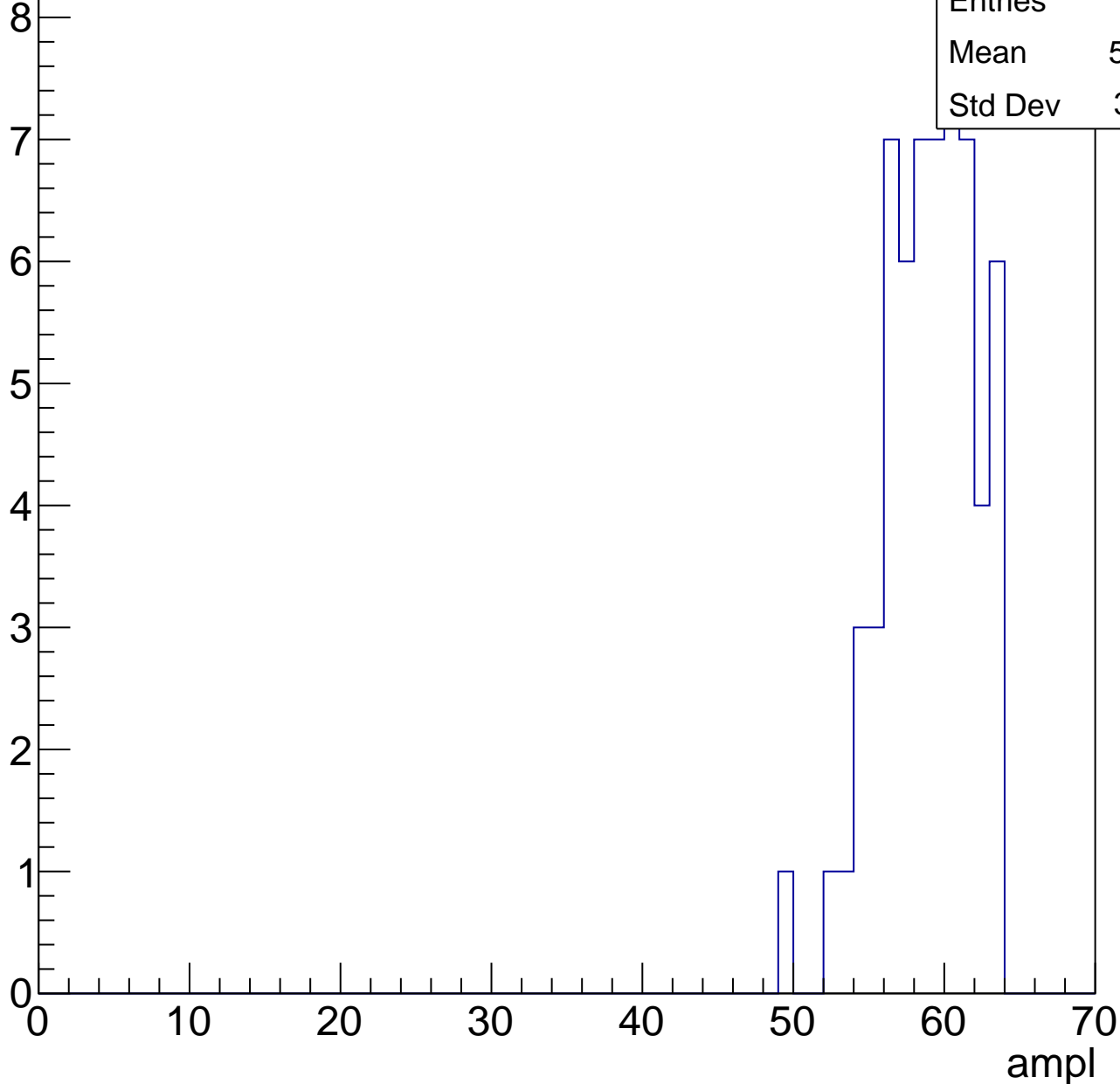


# B1L103S, U22-ch105, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	58.48
Std Dev	3.011

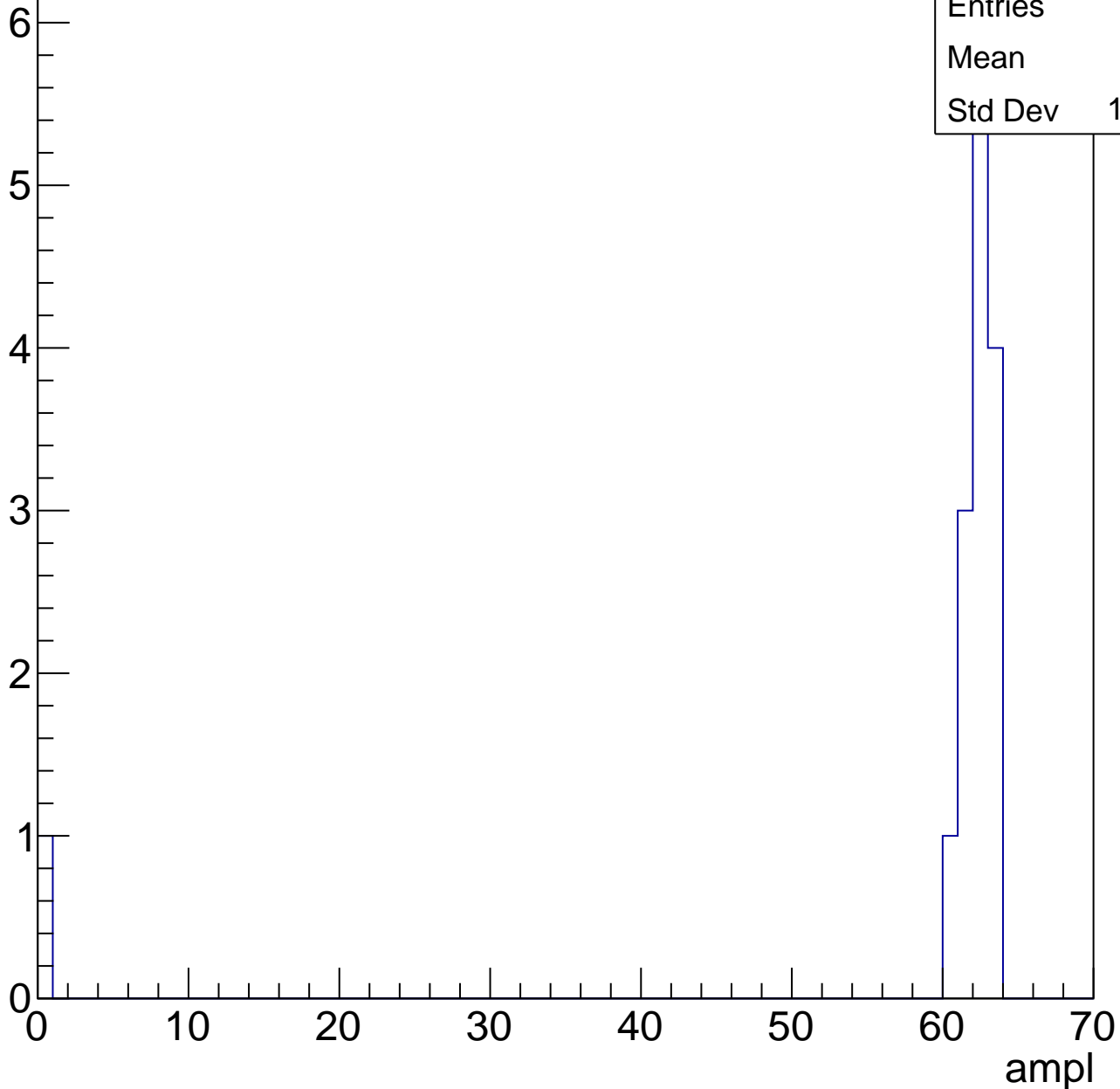


# B1L103S, U22-ch105, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.8
Std Dev	15.47





# B1L103S, U22-ch105, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	18
Mean	1
Std Dev	4.123

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

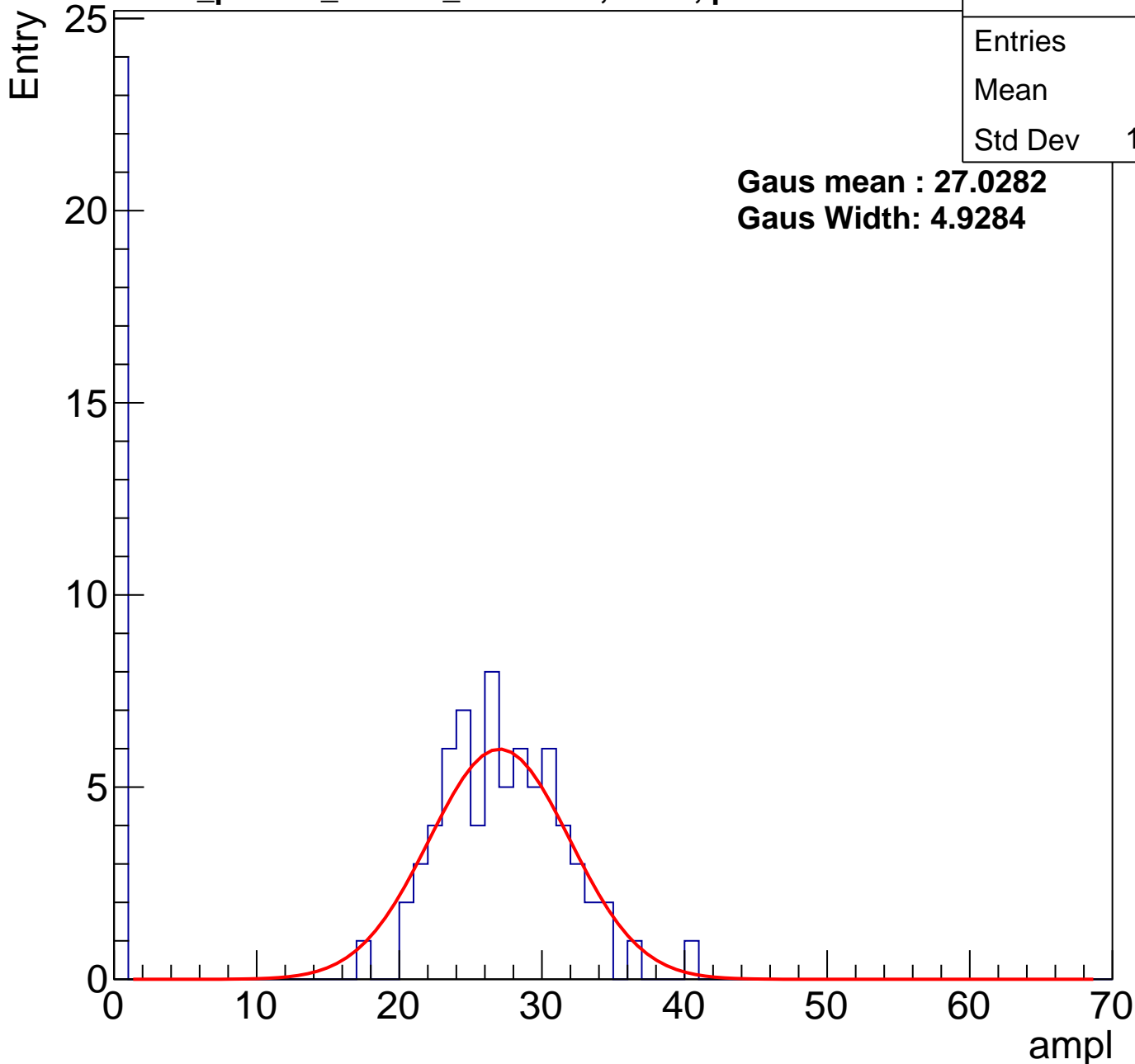
# B1L103S, U22-ch106, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	94
Mean	20
Std Dev	12.26

**Gaus mean : 27.0282**

**Gaus Width: 4.9284**



# B1L103S, U22-ch106, adc1

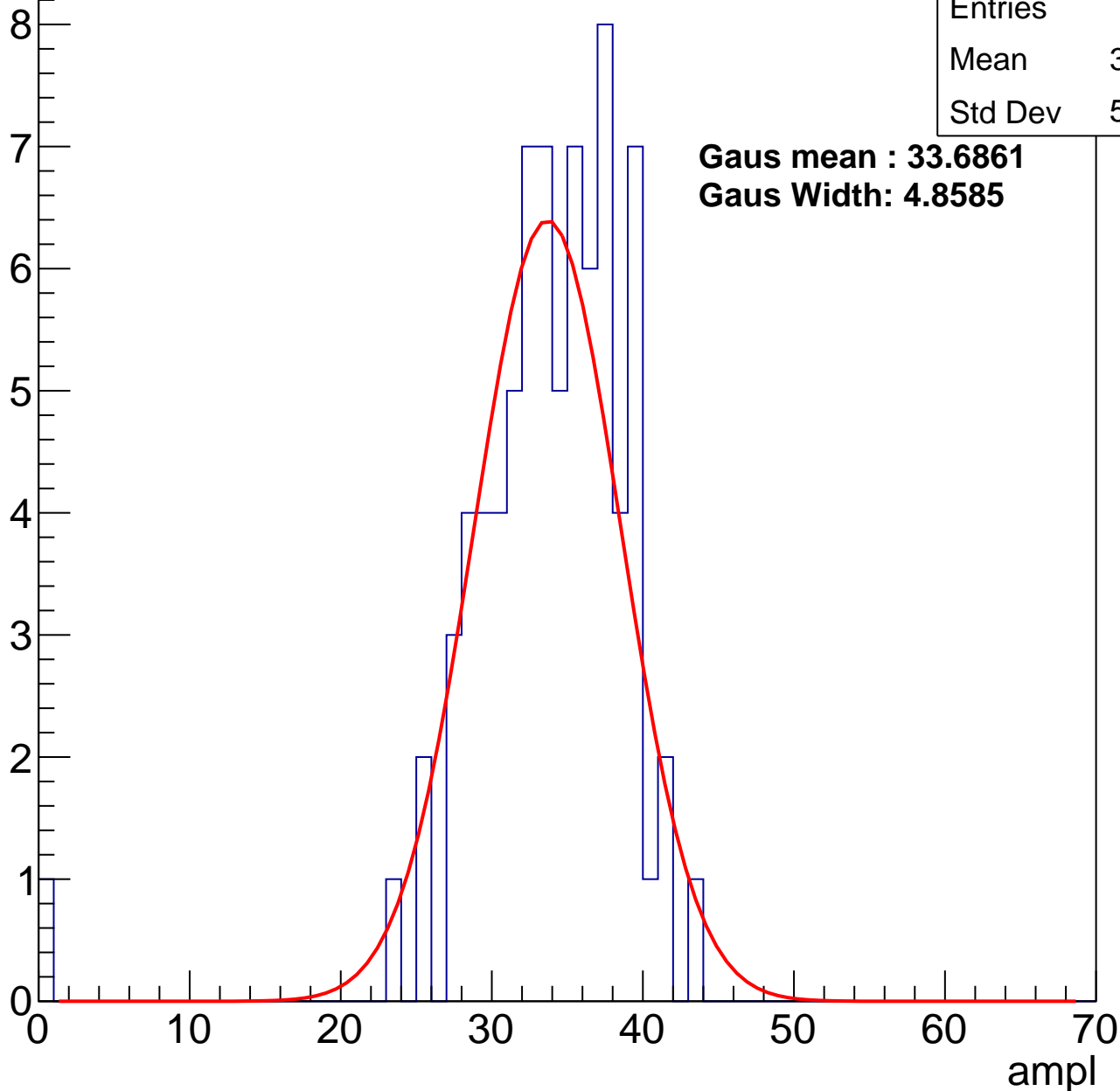
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	33.28
Std Dev	5.614

**Gaus mean : 33.6861**

**Gaus Width: 4.8585**



# B1L103S, U22-ch106, adc2

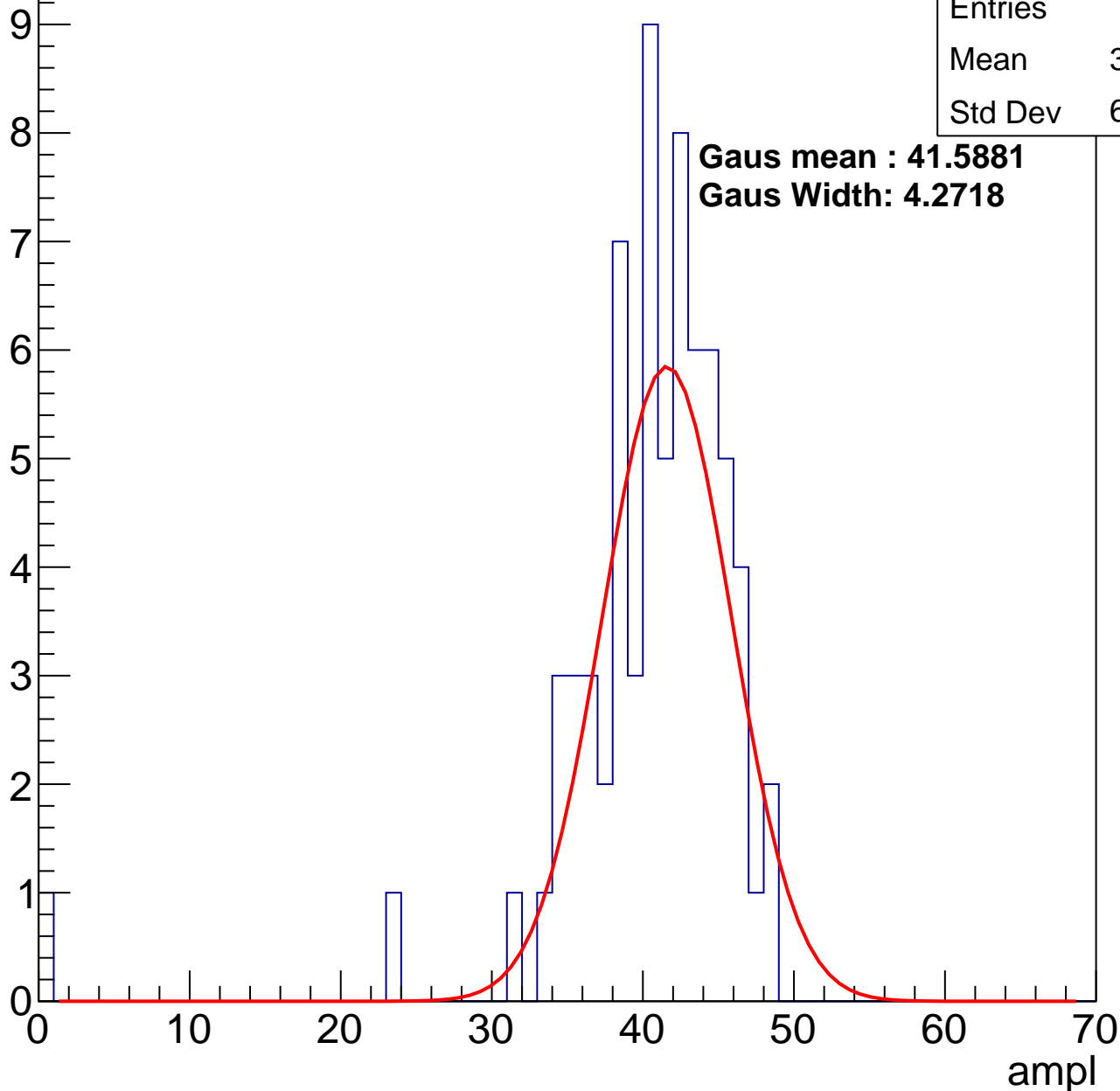
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	71
Mean	39.92
Std Dev	6.426

**Gaus mean : 41.5881**

**Gaus Width: 4.2718**

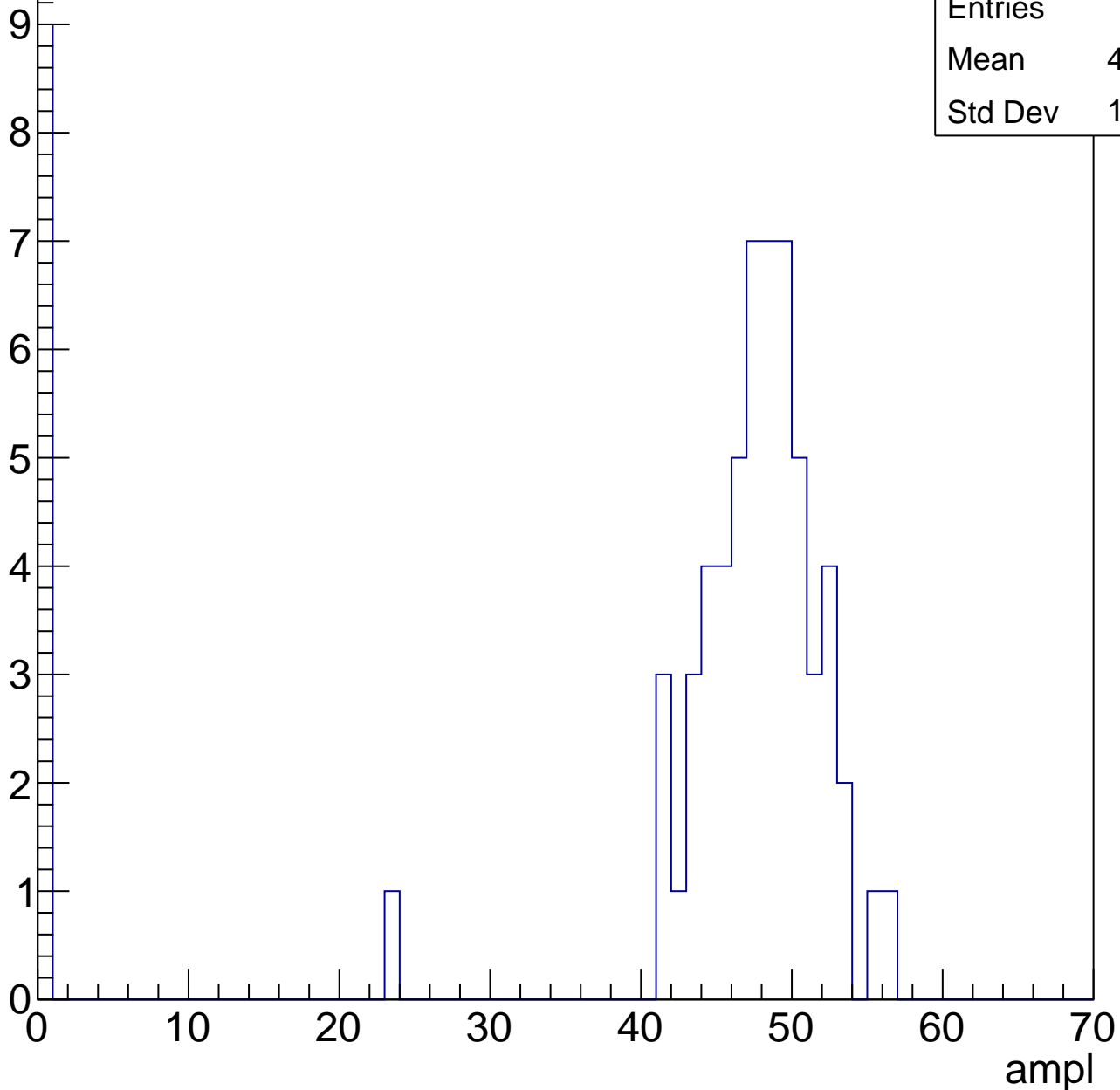


# B1L103S, U22-ch106, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	40.88
Std Dev	16.68

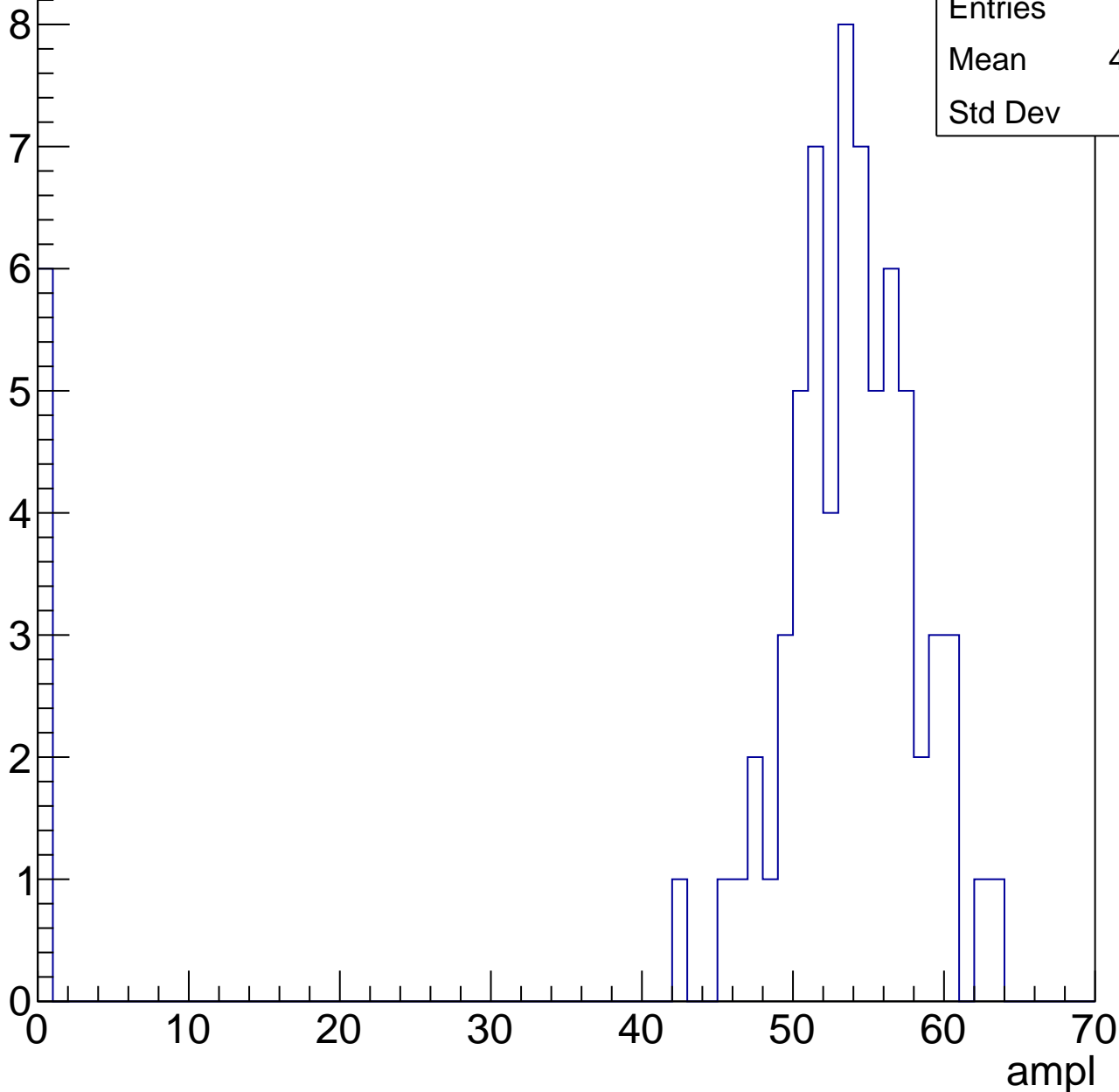


# B1L103S, U22-ch106, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

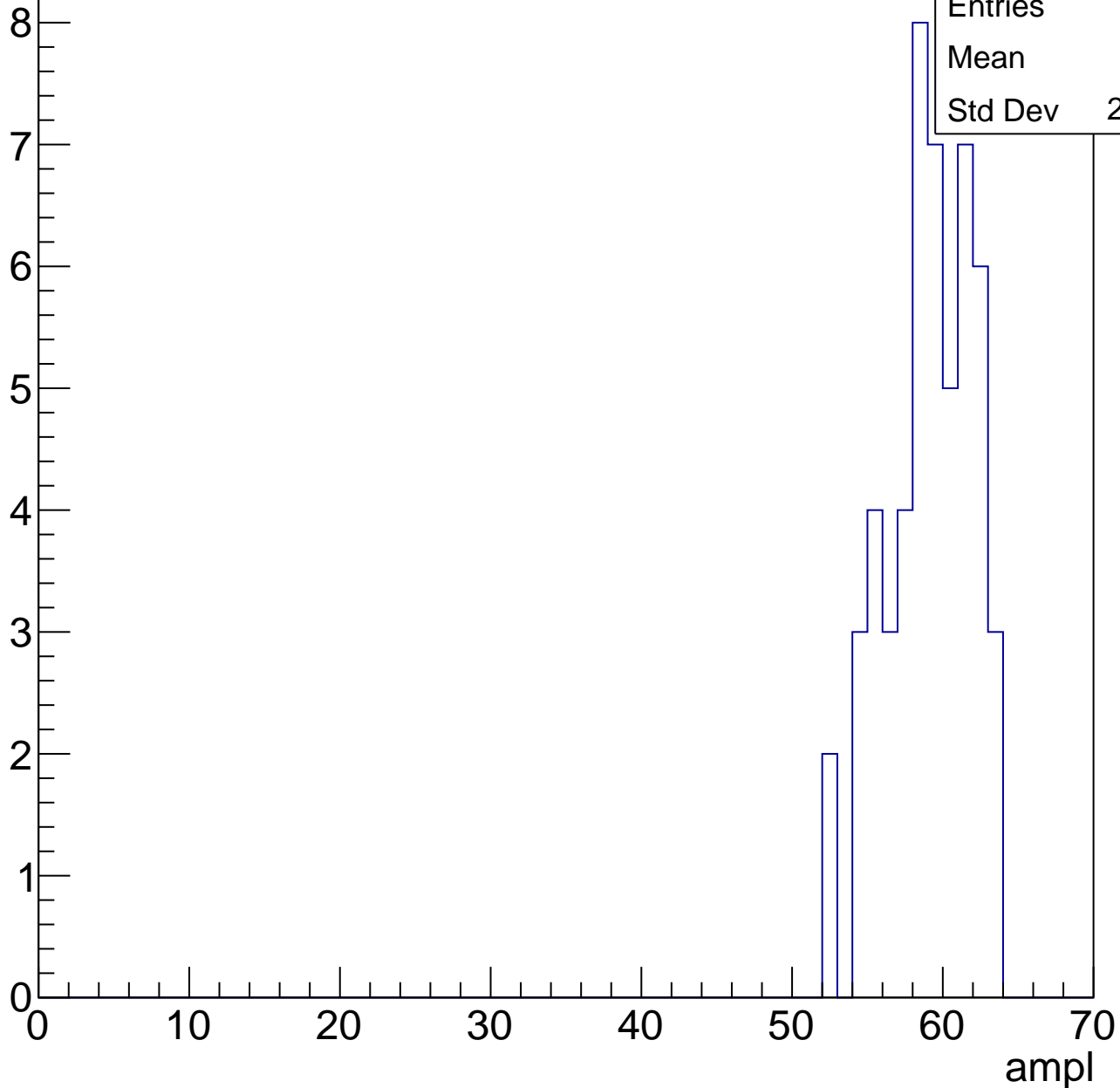
Entries	72
Mean	49.07
Std Dev	15.3



# B1L103S, U22-ch106, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

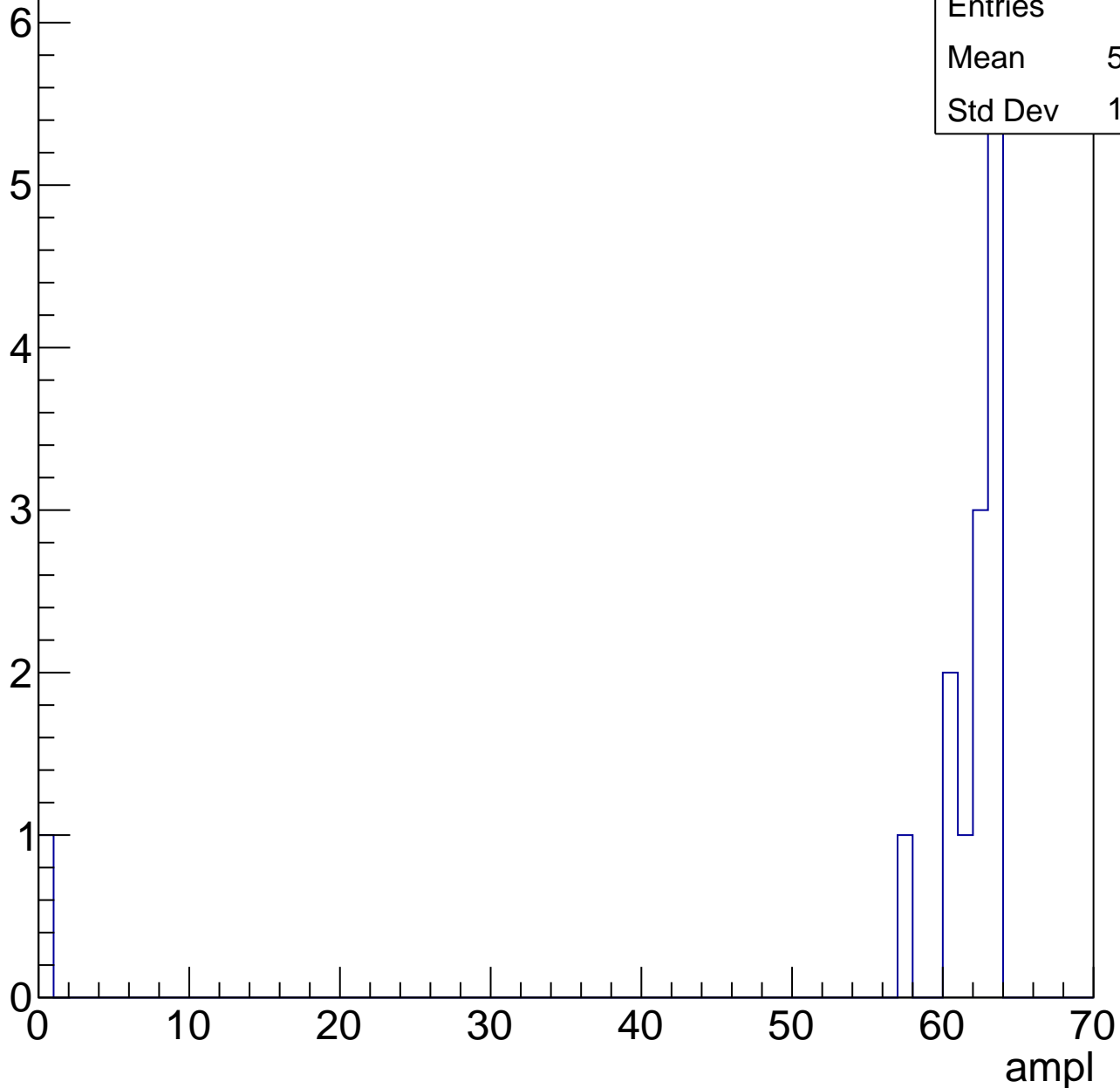


# B1L103S, U22-ch106, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	14
Mean	57.29
Std Dev	15.98

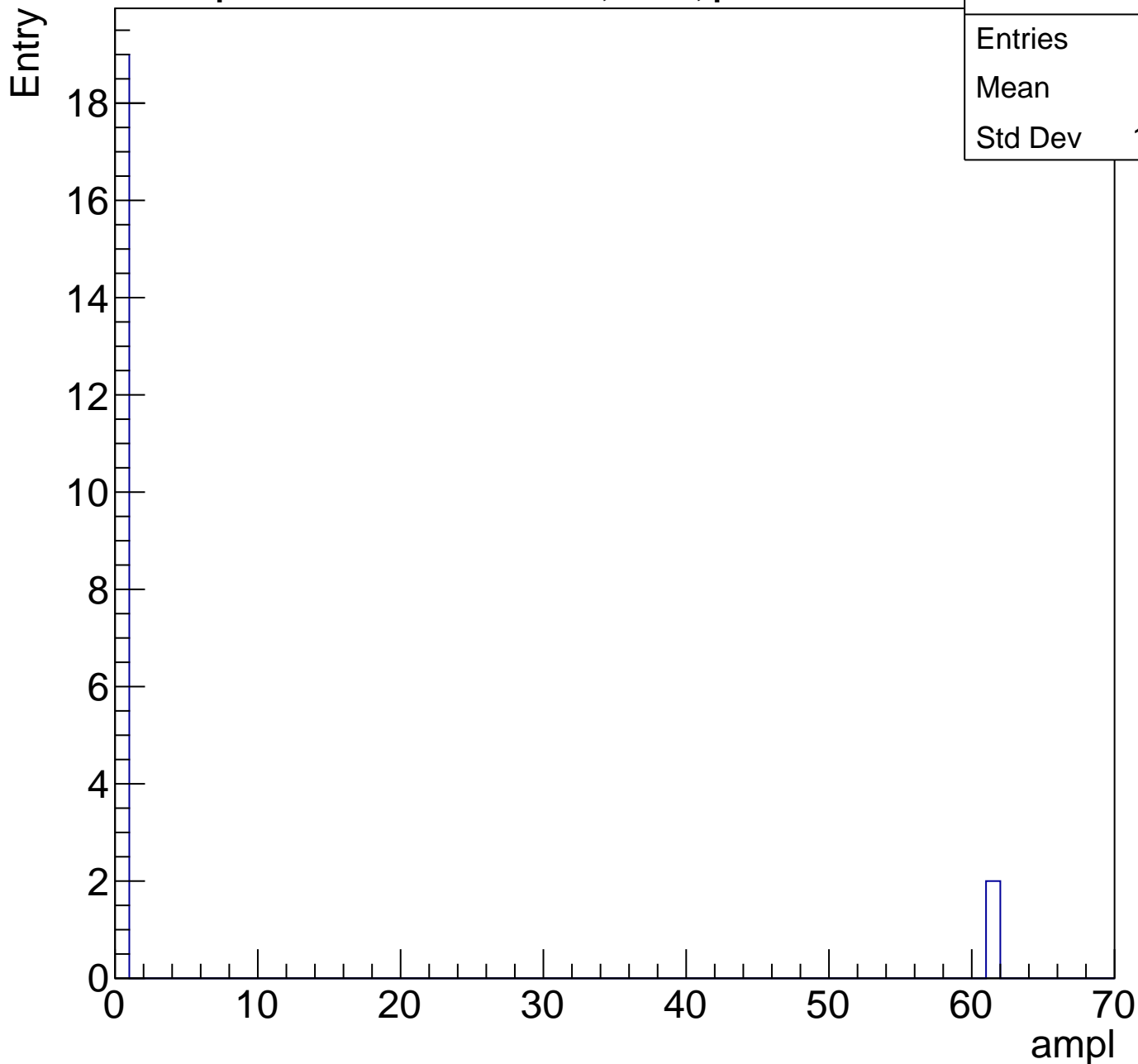




# B1L103S, U22-ch106, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91



# B1L103S, U22-ch107, adc0

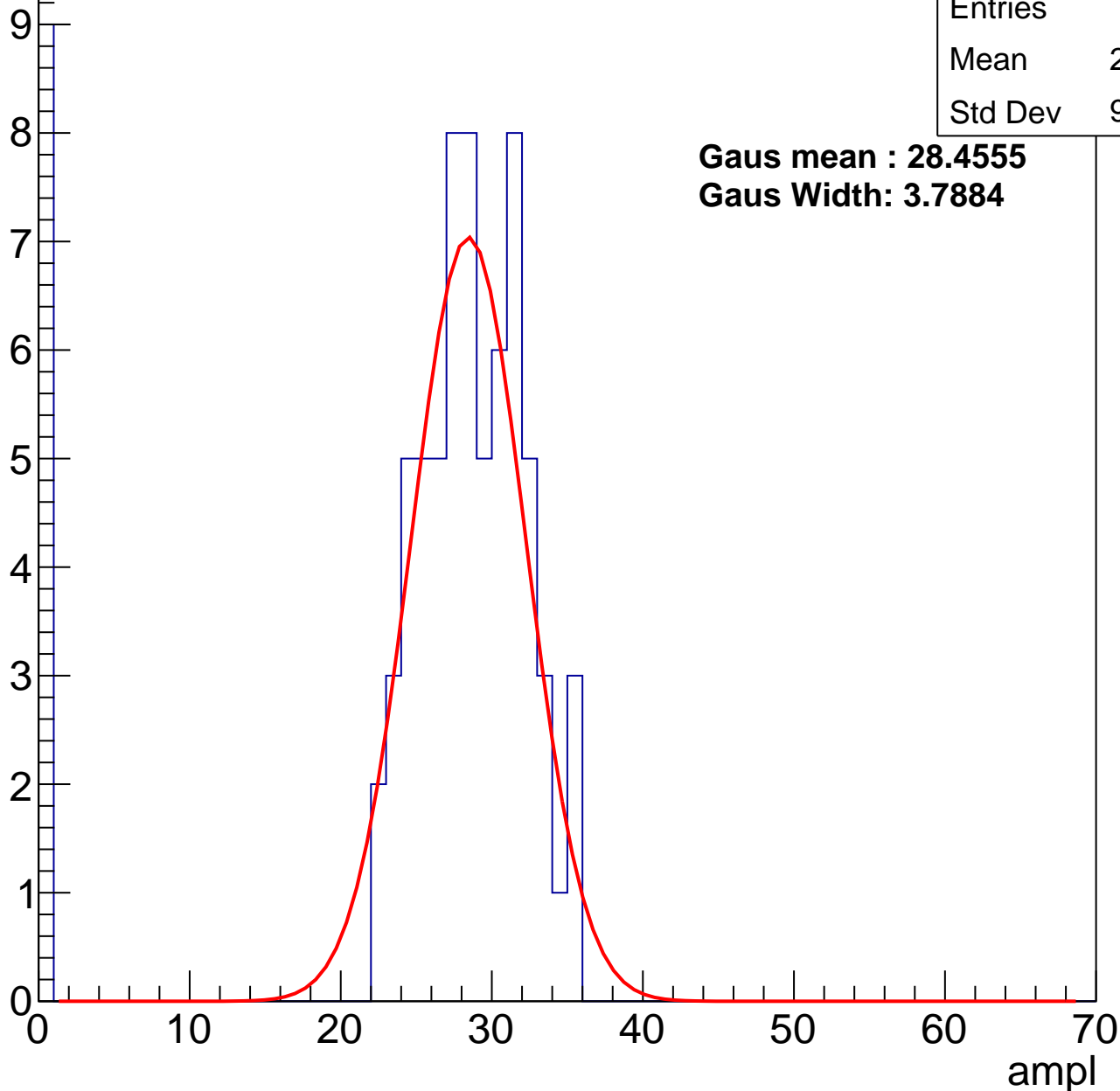
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	76
Mean	24.99
Std Dev	9.669

**Gaus mean : 28.4555**

**Gaus Width: 3.7884**



# B1L103S, U22-ch107, adc1

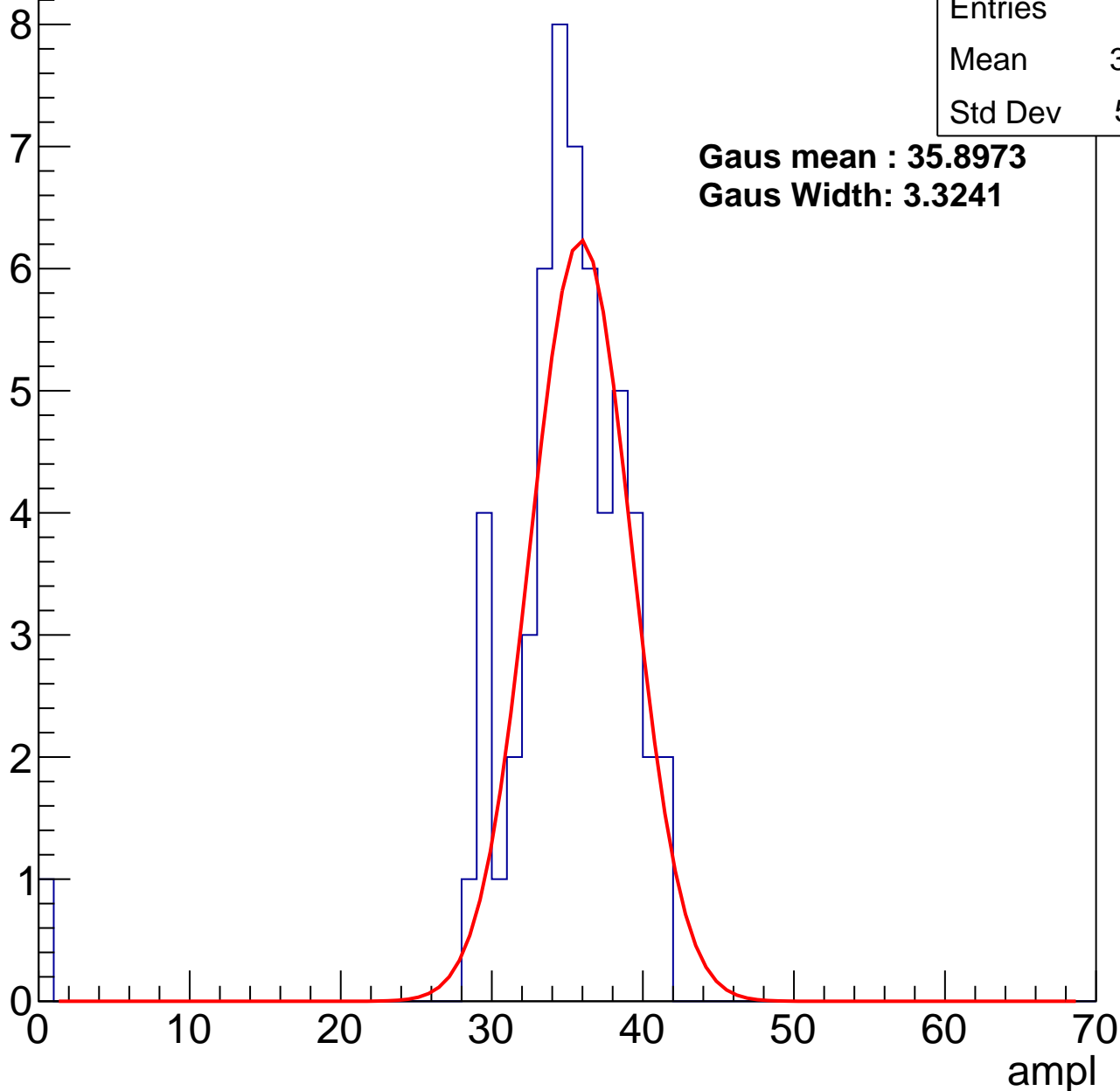
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	34.27
Std Dev	5.601

**Gaus mean : 35.8973**

**Gaus Width: 3.3241**



# B1L103S, U22-ch107, adc2

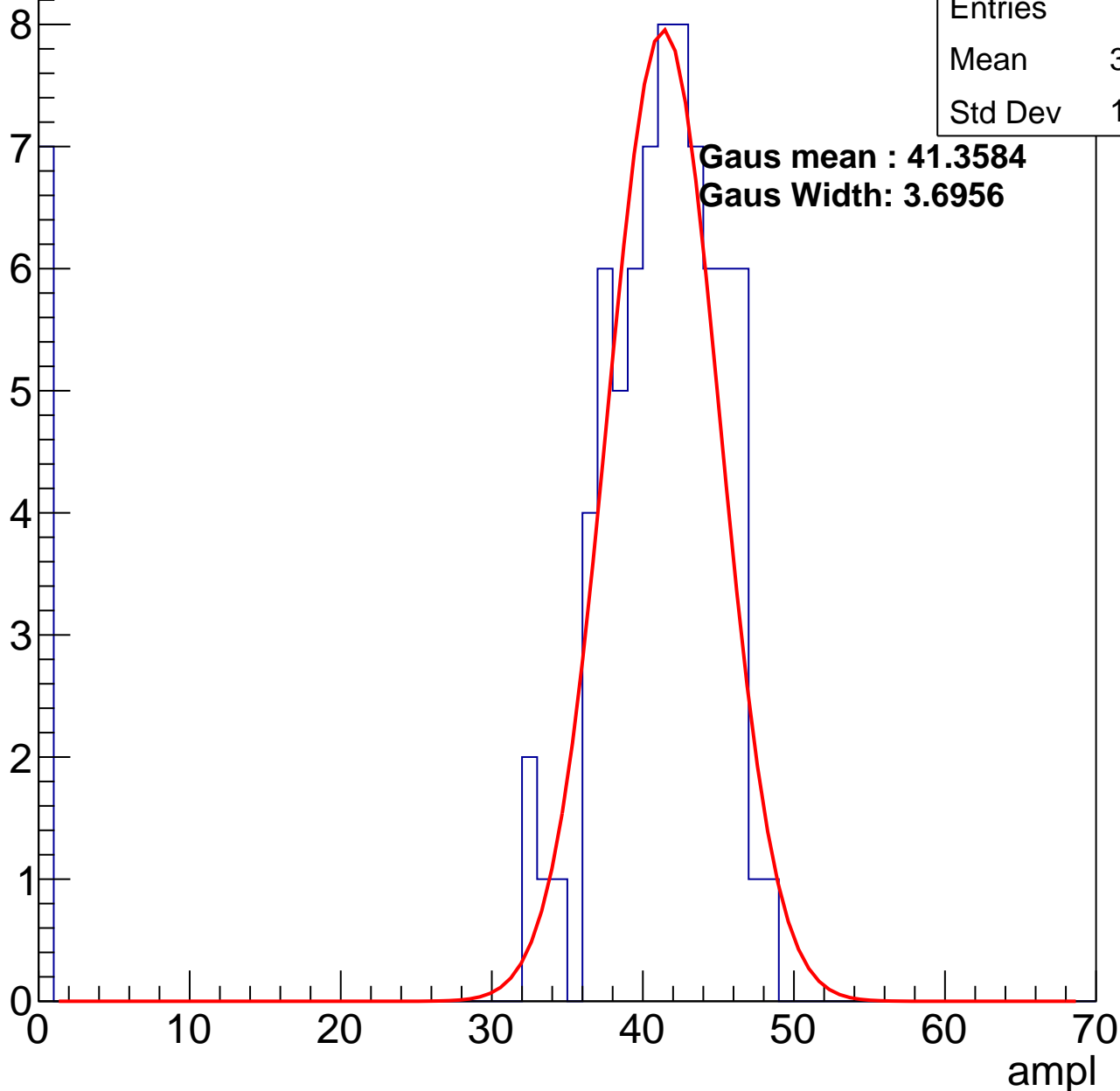
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	82
Mean	37.45
Std Dev	11.95

**Gaus mean : 41.3584**

**Gaus Width: 3.6956**

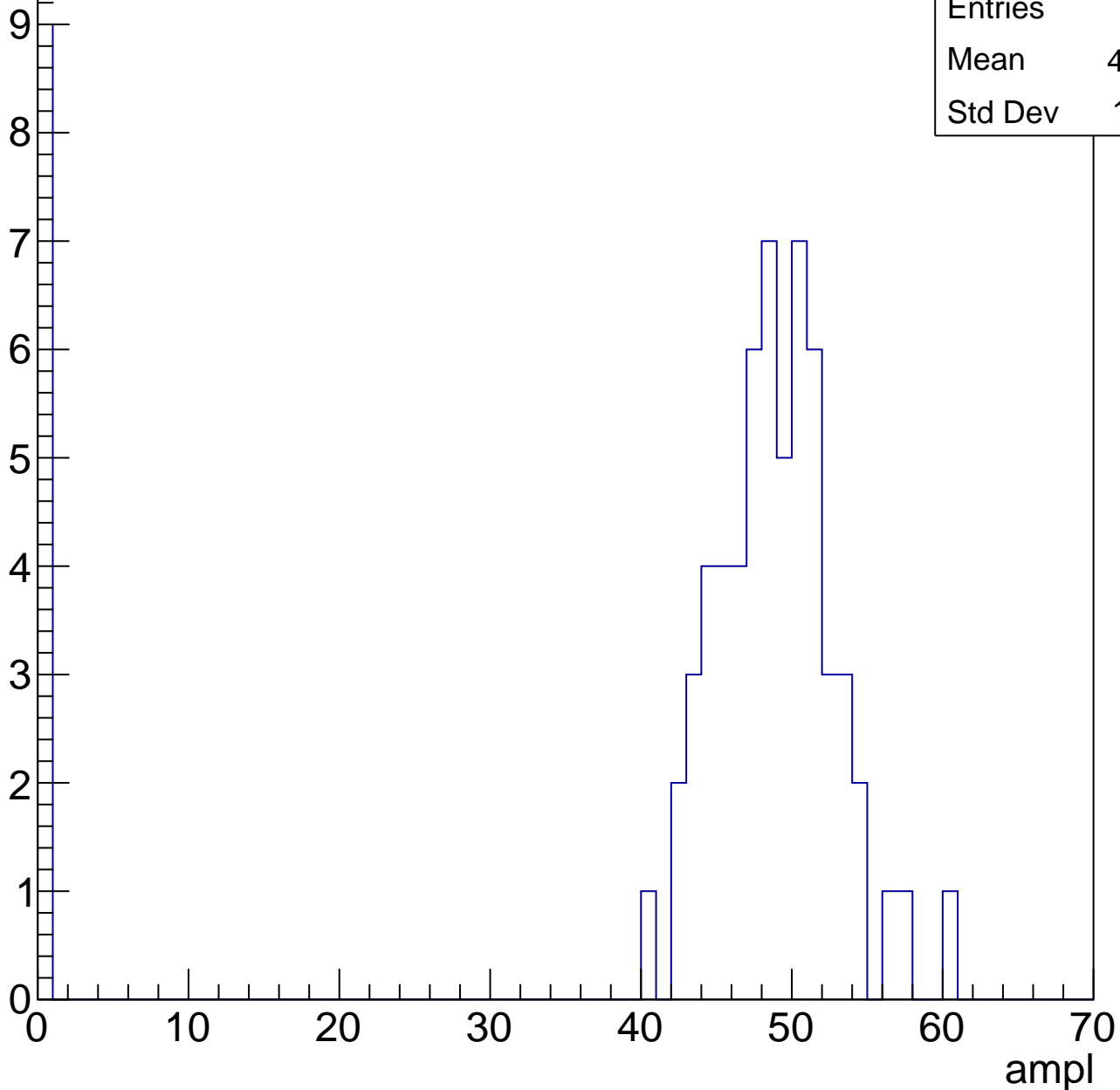


# B1L103S, U22-ch107, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	42.14
Std Dev	16.71

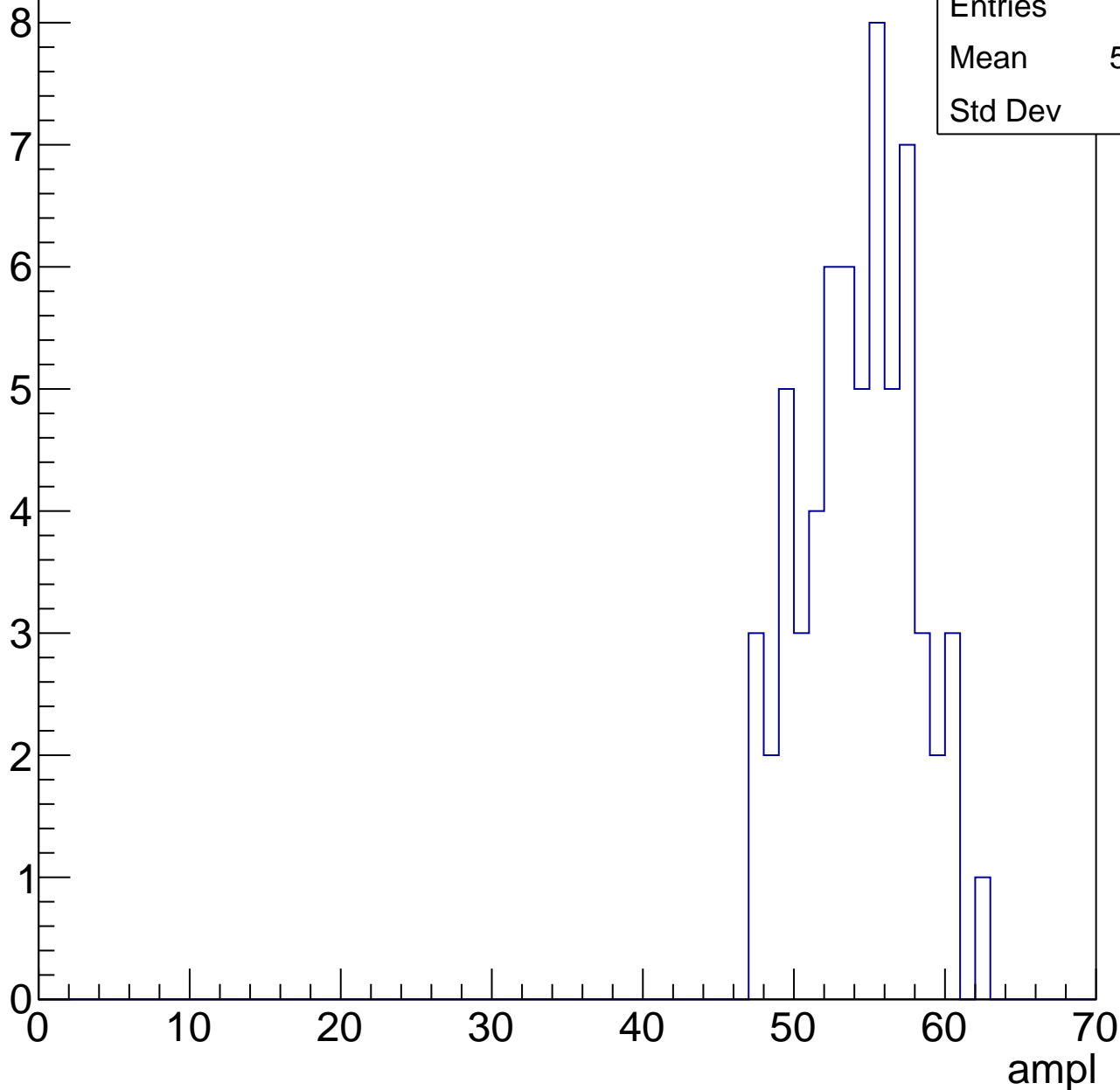


# B1L103S, U22-ch107, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	53.79
Std Dev	3.6

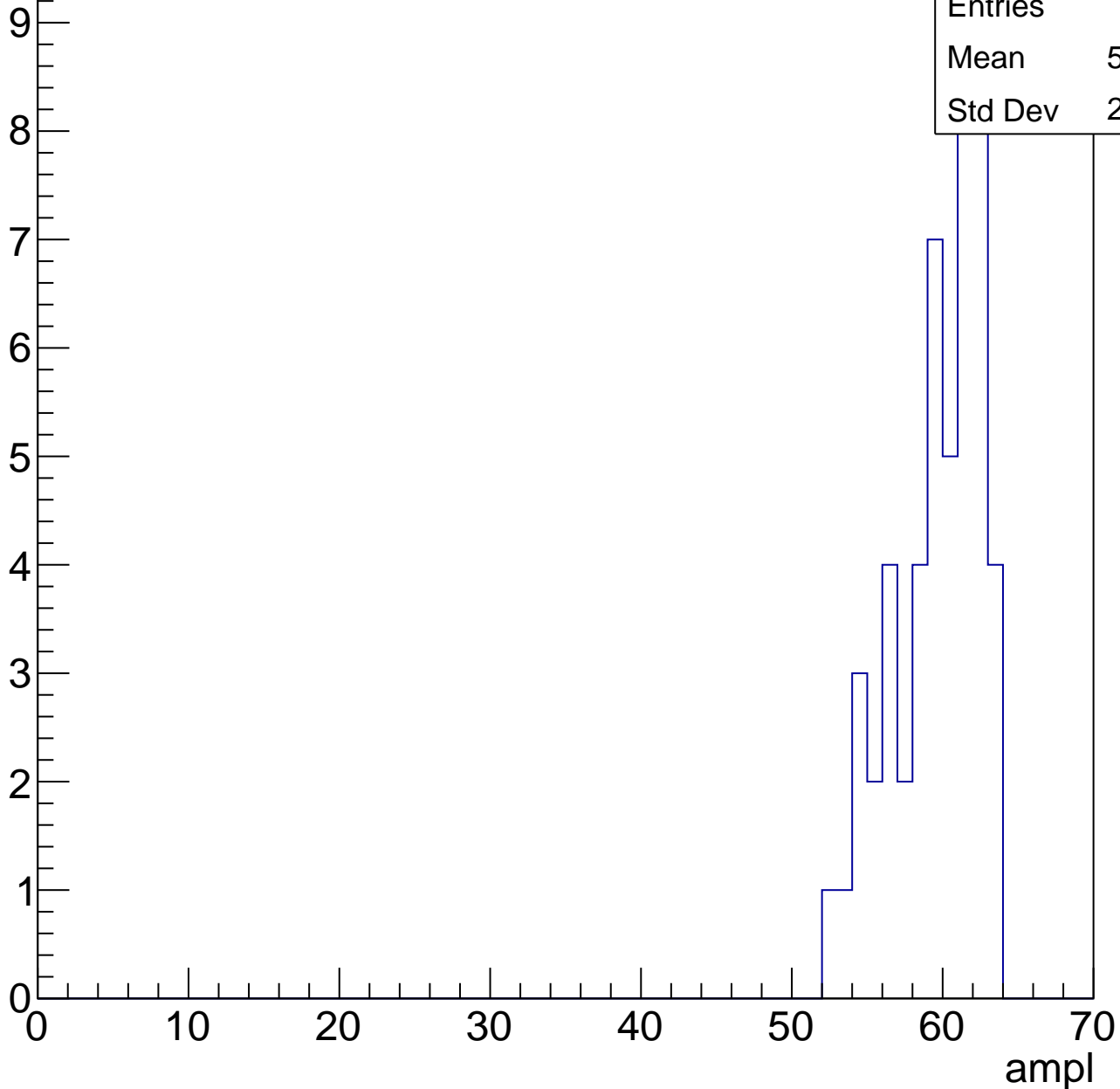


# B1L103S, U22-ch107, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	59.14
Std Dev	2.885

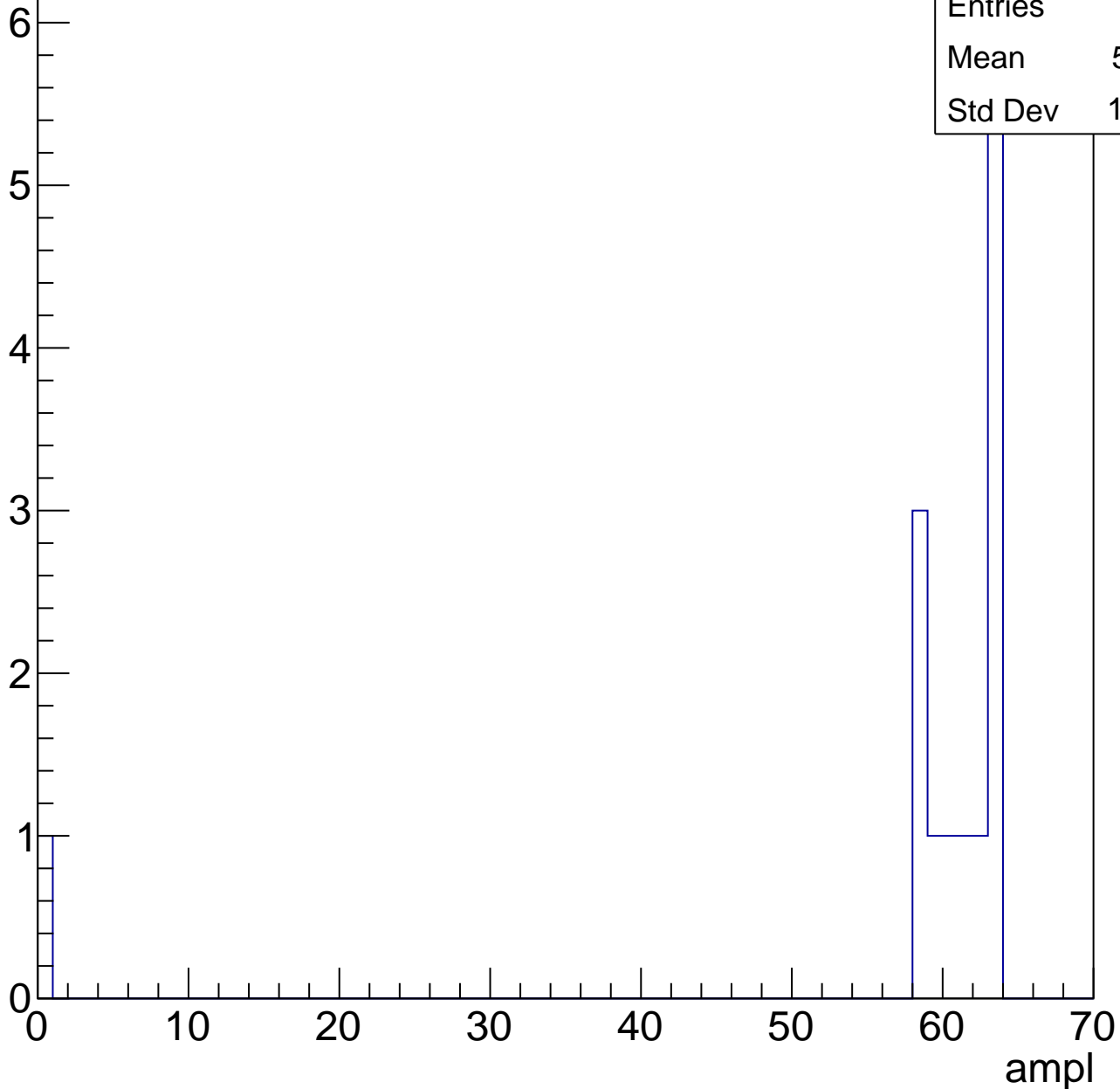


# B1L103S, U22-ch107, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	14
Mean	56.71
Std Dev	15.86





# B1L103S, U22-ch107, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	18
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch108, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	110
Mean	17.84
Std Dev	11.39

**Gaus mean : 25.4298**

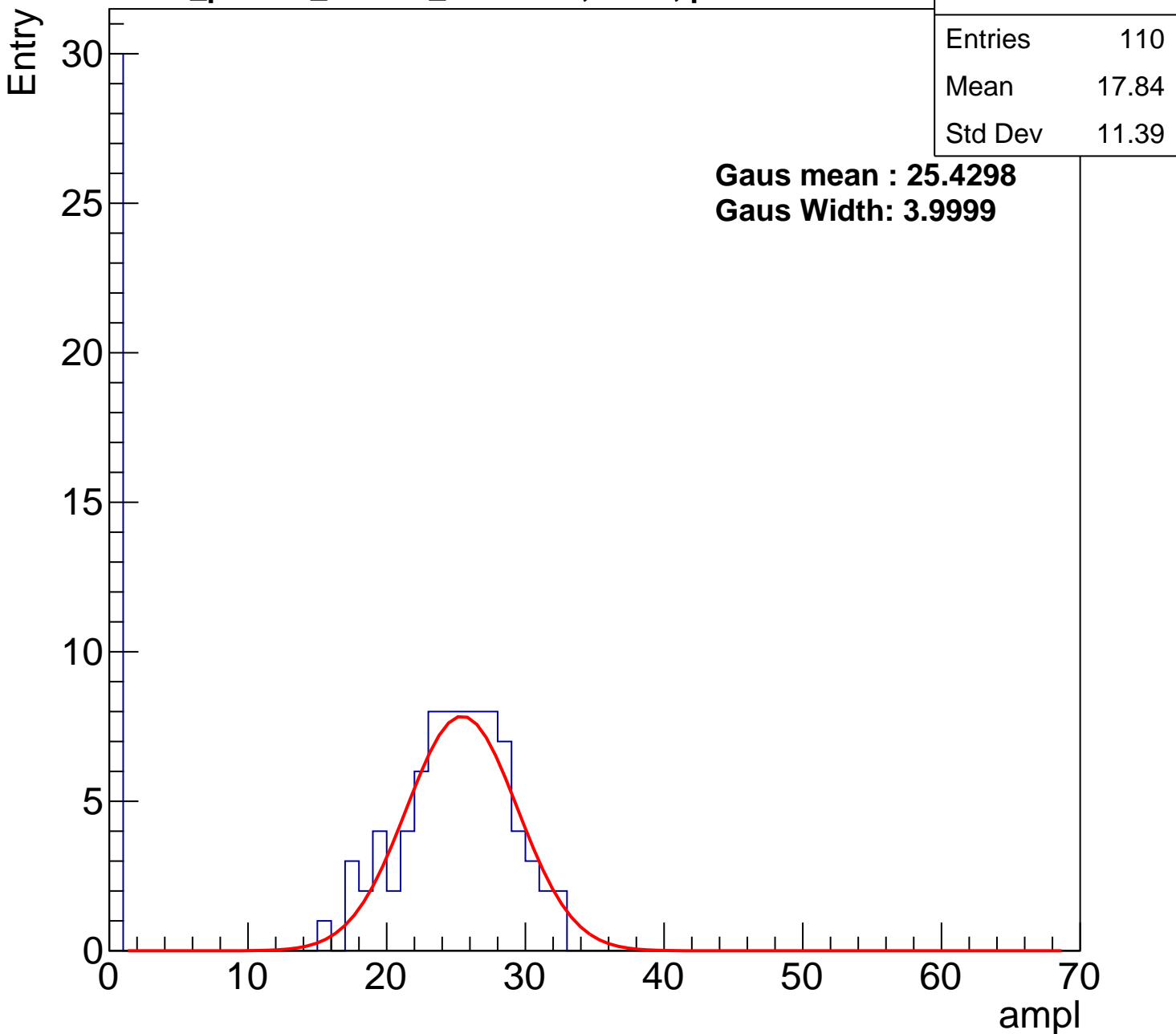
**Gaus Width: 3.9999**

Entry

30  
25  
20  
15  
10  
5  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch108, adc1

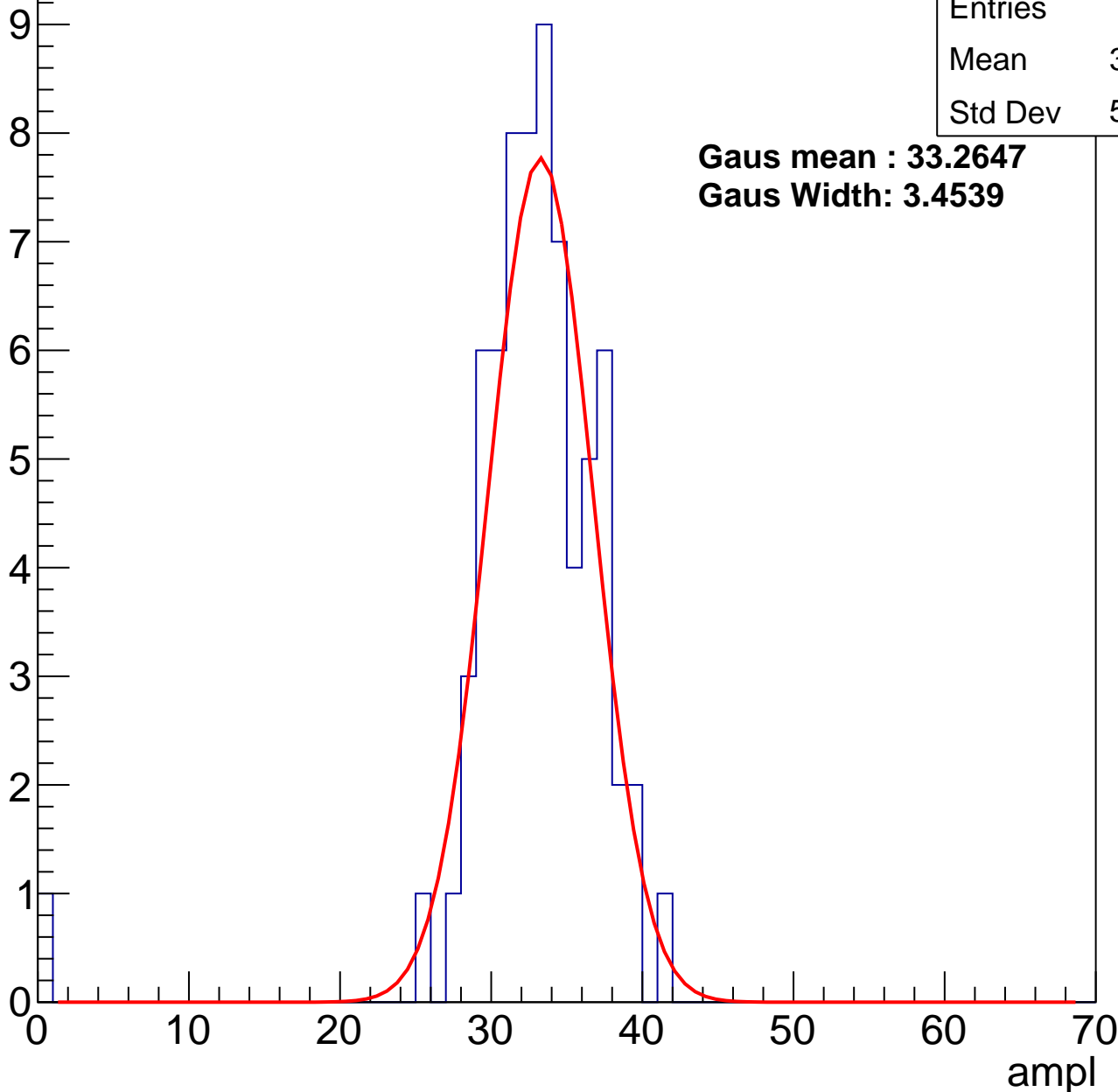
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	32.37
Std Dev	5.037

**Gaus mean : 33.2647**

**Gaus Width: 3.4539**



# B1L103S, U22-ch108, adc2

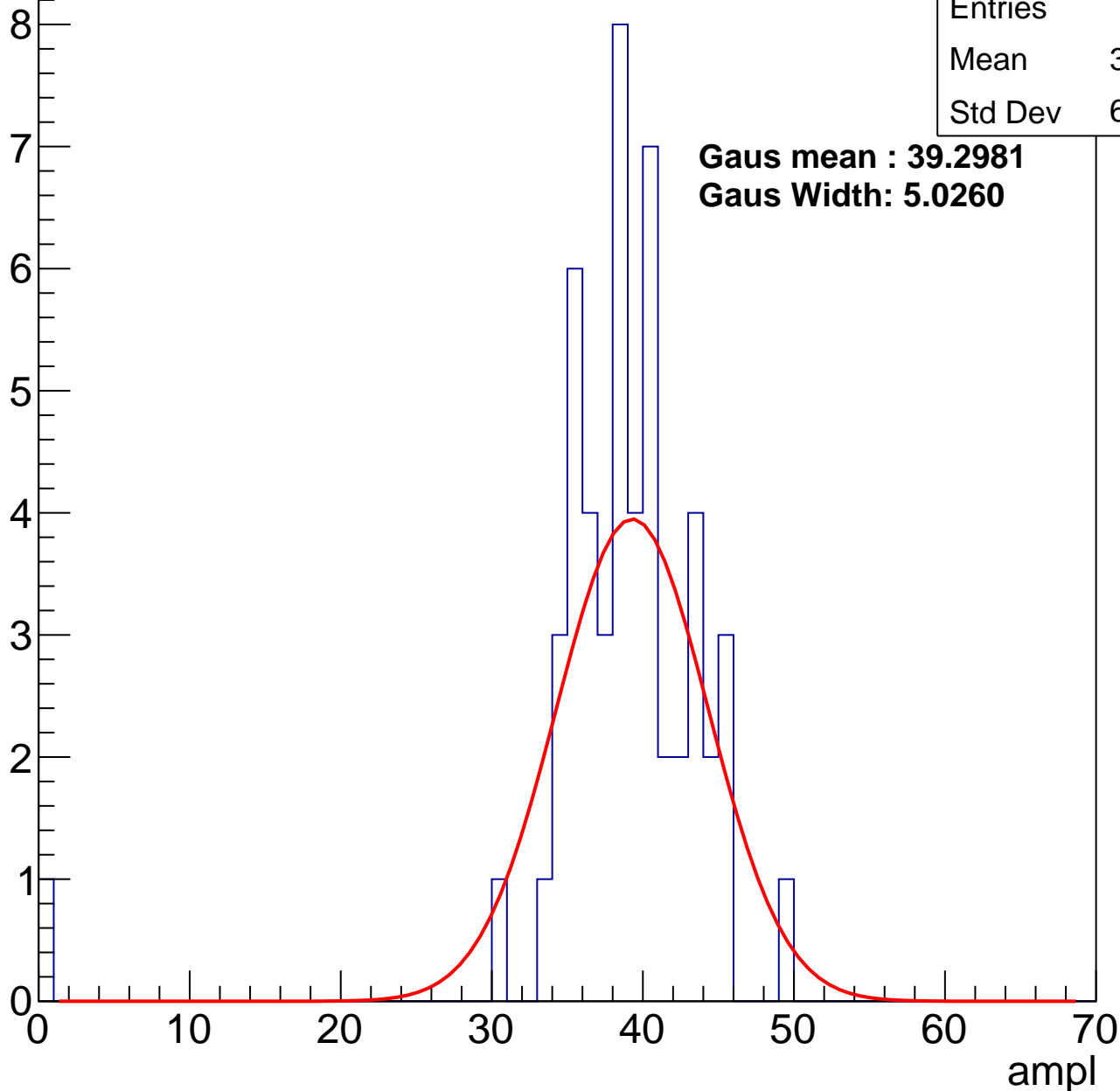
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	38.08
Std Dev	6.468

**Gaus mean : 39.2981**

**Gaus Width: 5.0260**



# B1L103S, U22-ch108, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	88
Mean	39.4
Std Dev	15.36

Entry

10

8

6

4

2

0

0

10

20

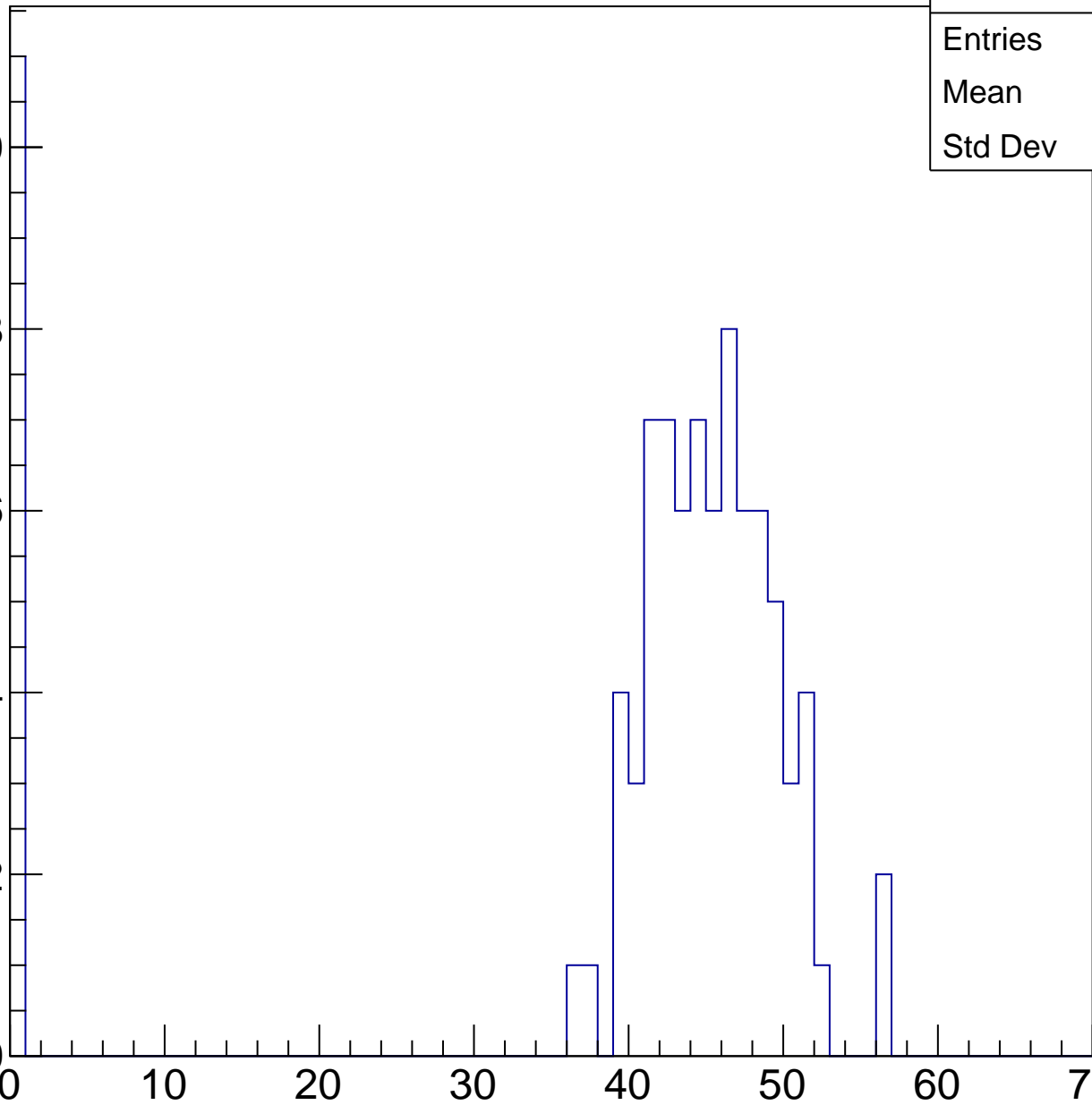
30

40

50

60

ampl

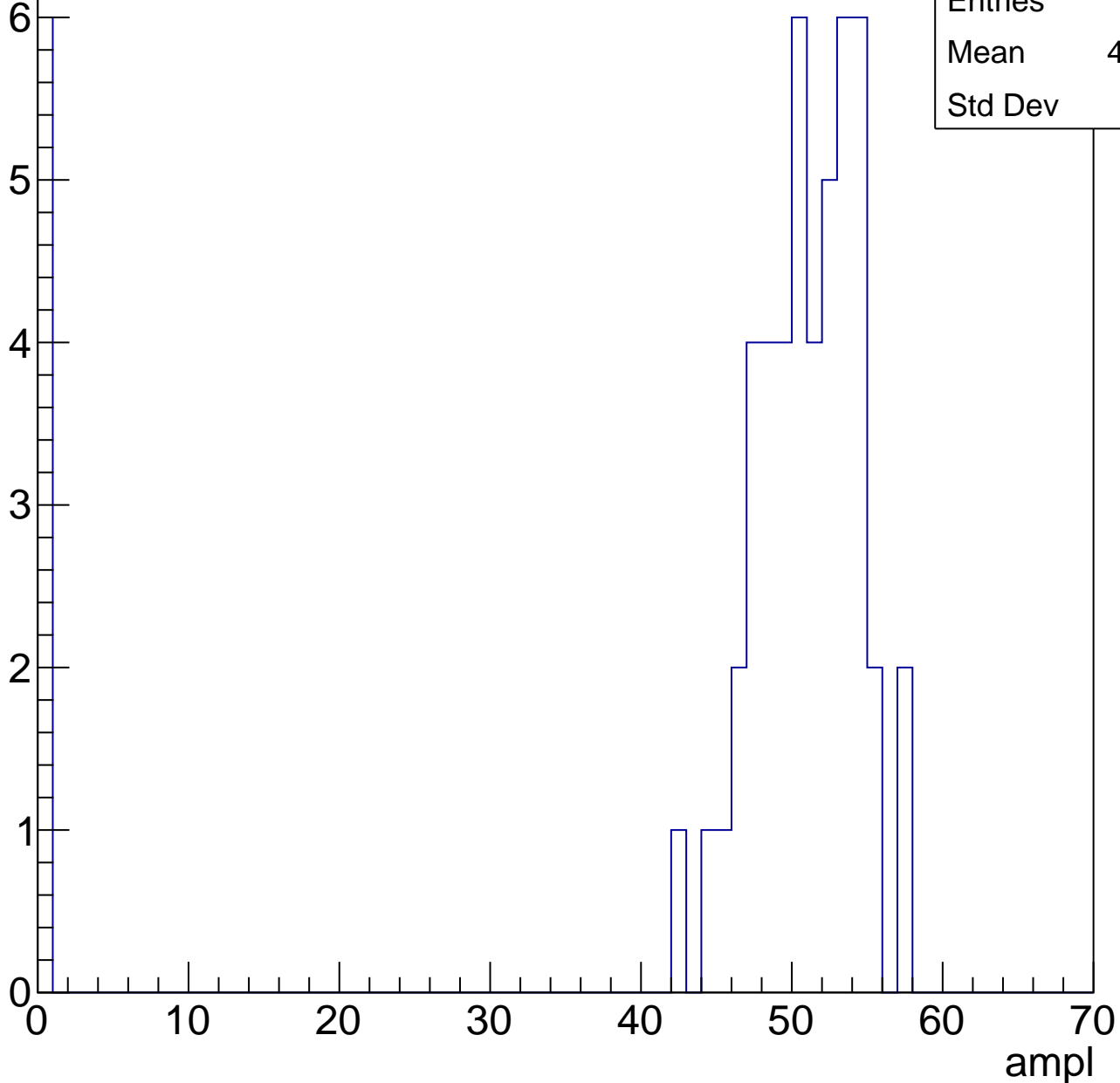


# B1L103S, U22-ch108, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	44.98
Std Dev	16.2

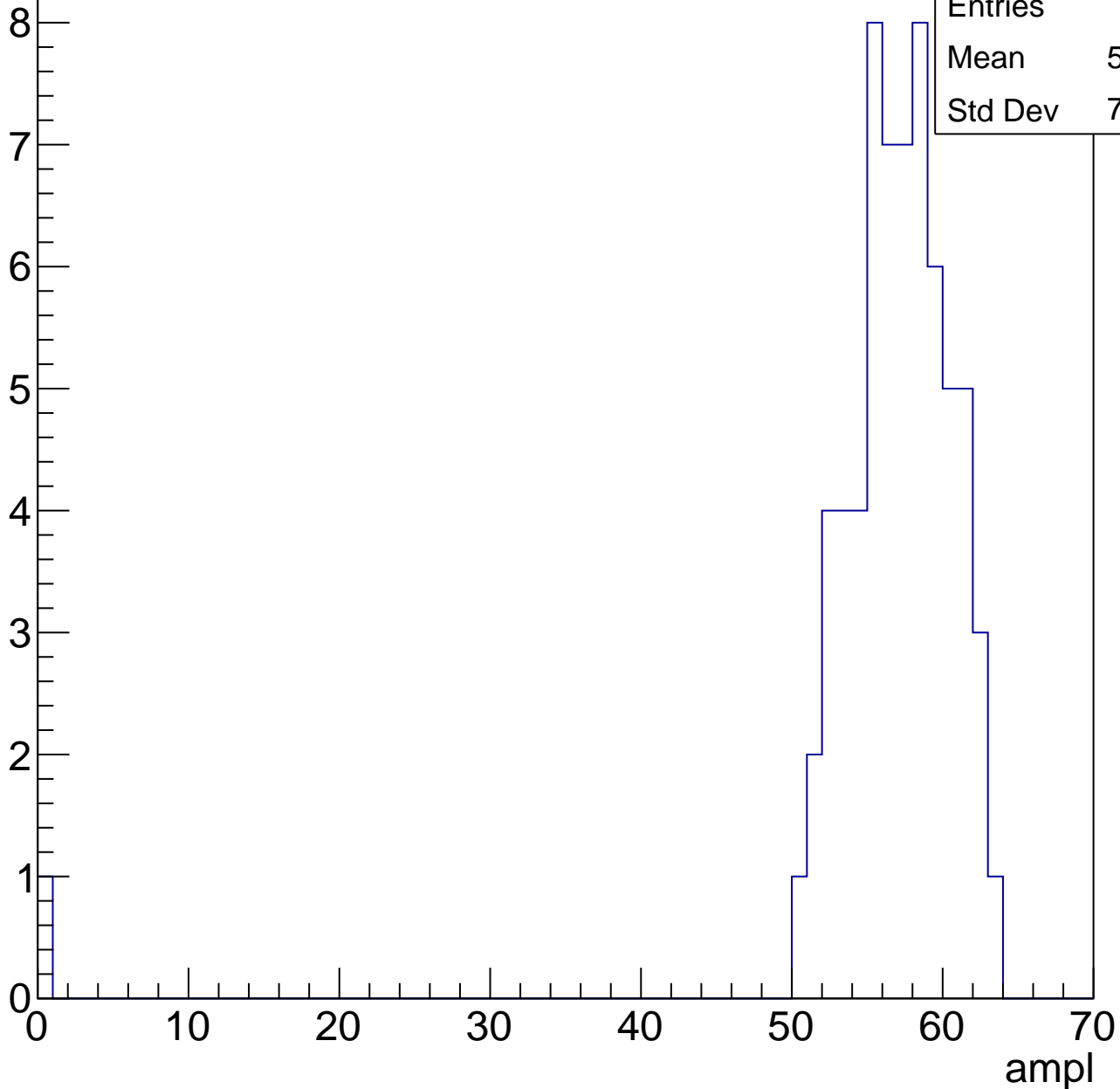


# B1L103S, U22-ch108, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	55.92
Std Dev	7.588

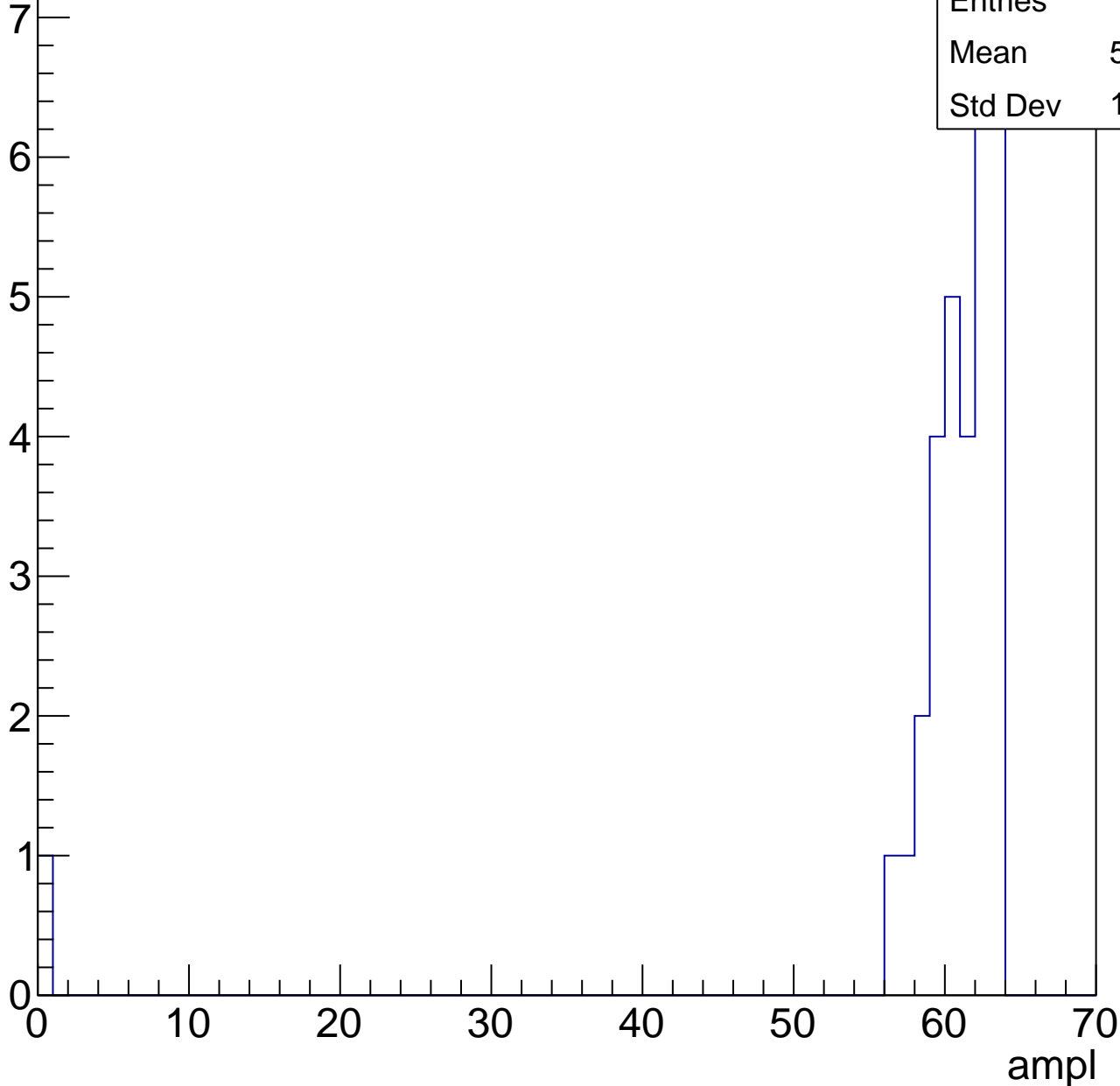


# B1L103S, U22-ch108, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	32
Mean	58.88
Std Dev	10.74

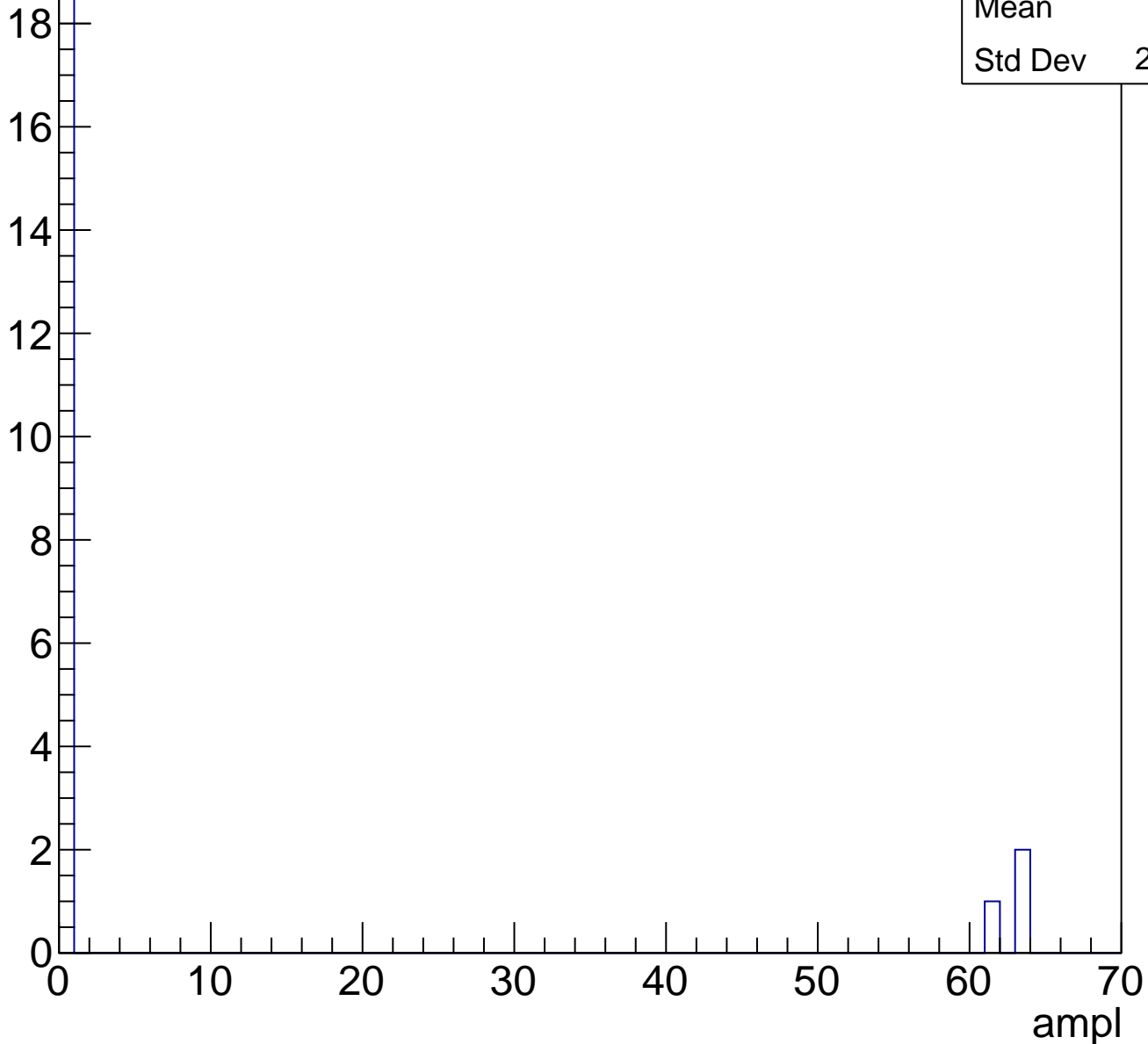




# B1L103S, U22-ch108, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



Entries	22
Mean	8.5
Std Dev	21.39

# B1L103S, U22-ch109, adc0

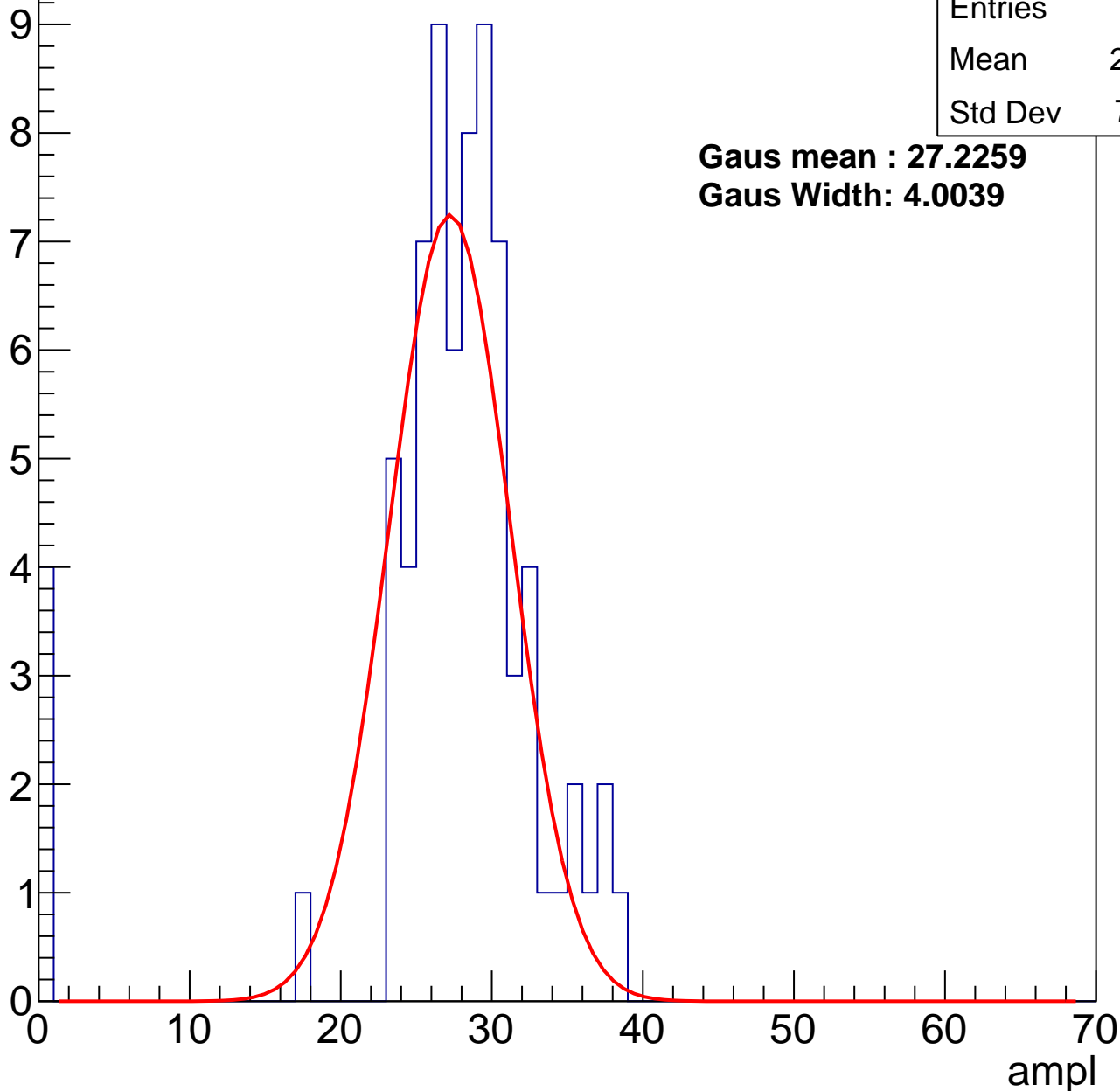
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	26.67
Std Dev	7.331

**Gaus mean : 27.2259**

**Gaus Width: 4.0039**



# B1L103S, U22-ch109, adc1

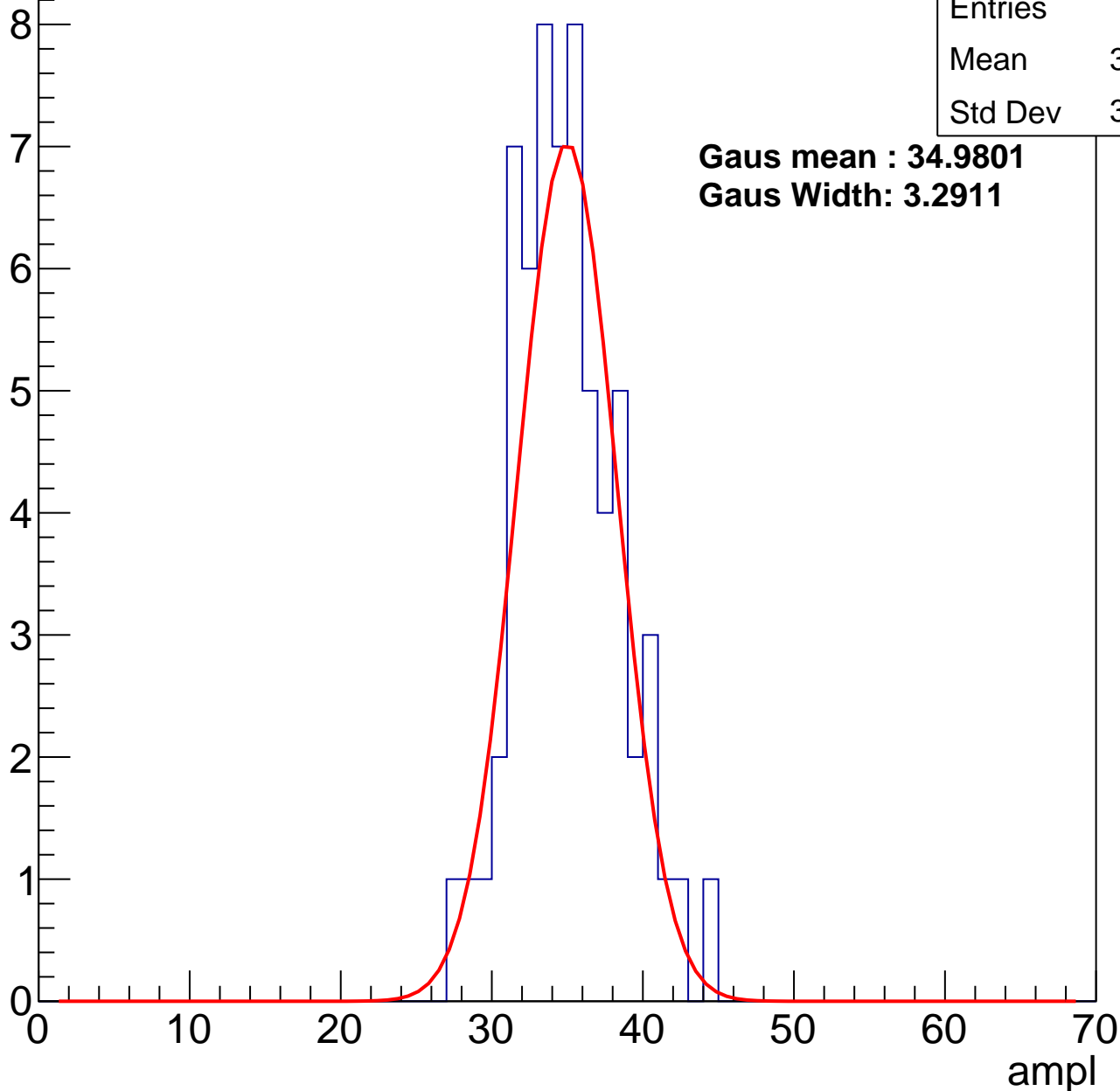
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	34.57
Std Dev	3.407

**Gaus mean : 34.9801**

**Gaus Width: 3.2911**



# B1L103S, U22-ch109, adc2

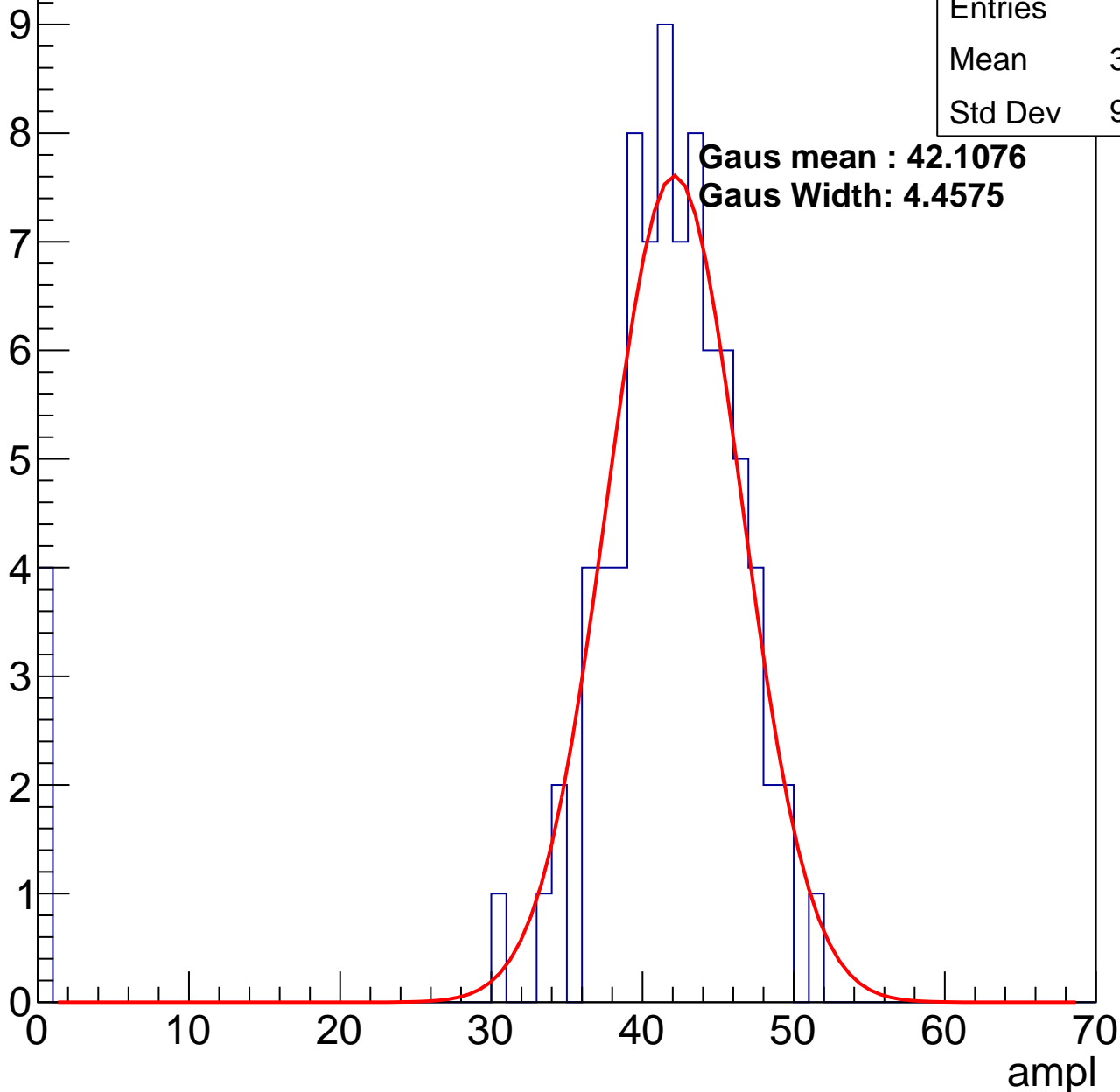
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	85
Mean	39.66
Std Dev	9.627

**Gaus mean : 42.1076**

**Gaus Width: 4.4575**

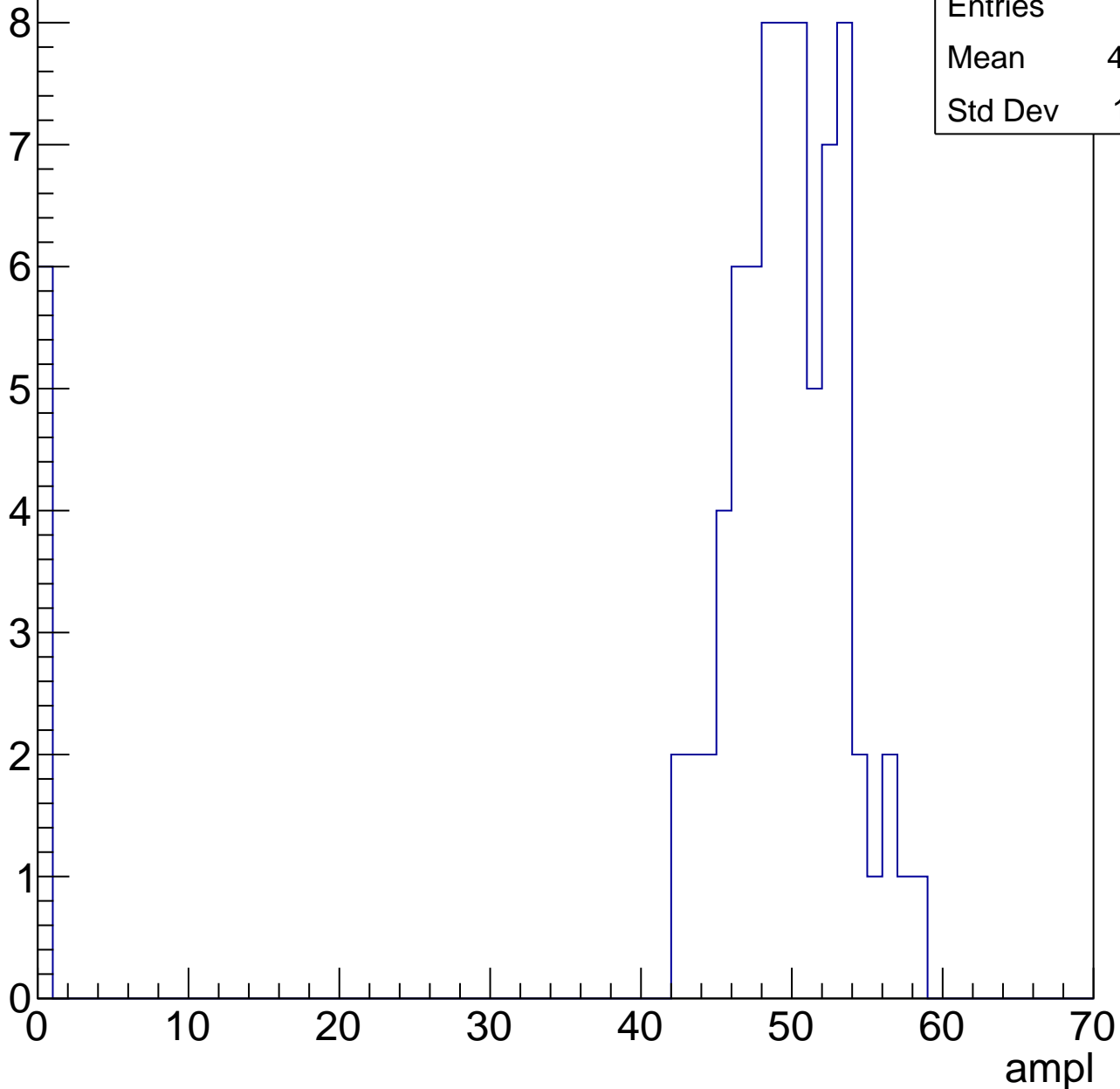


# B1L103S, U22-ch109, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	79
Mean	45.63
Std Dev	13.51

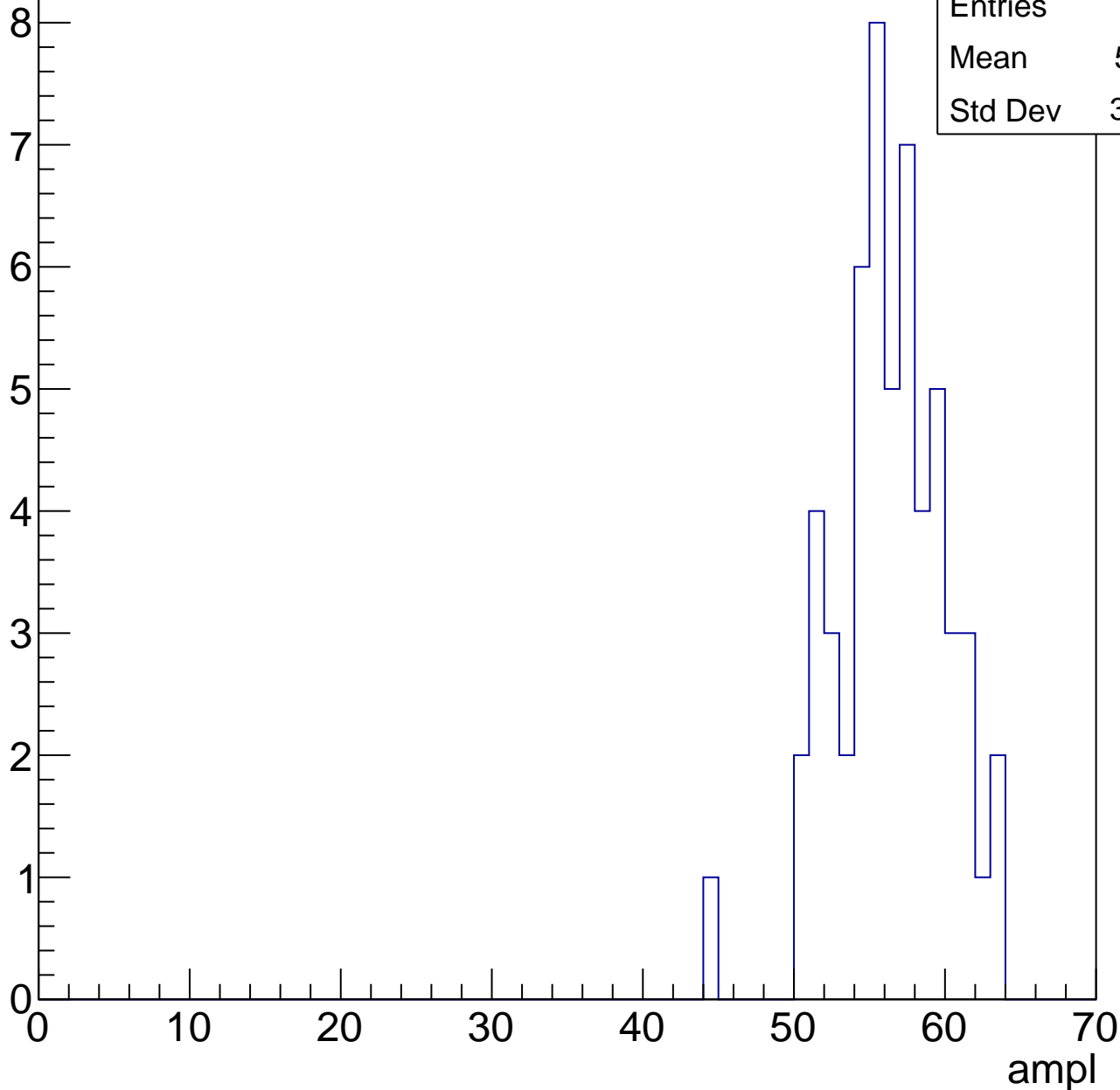


# B1L103S, U22-ch109, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	55.91
Std Dev	3.632

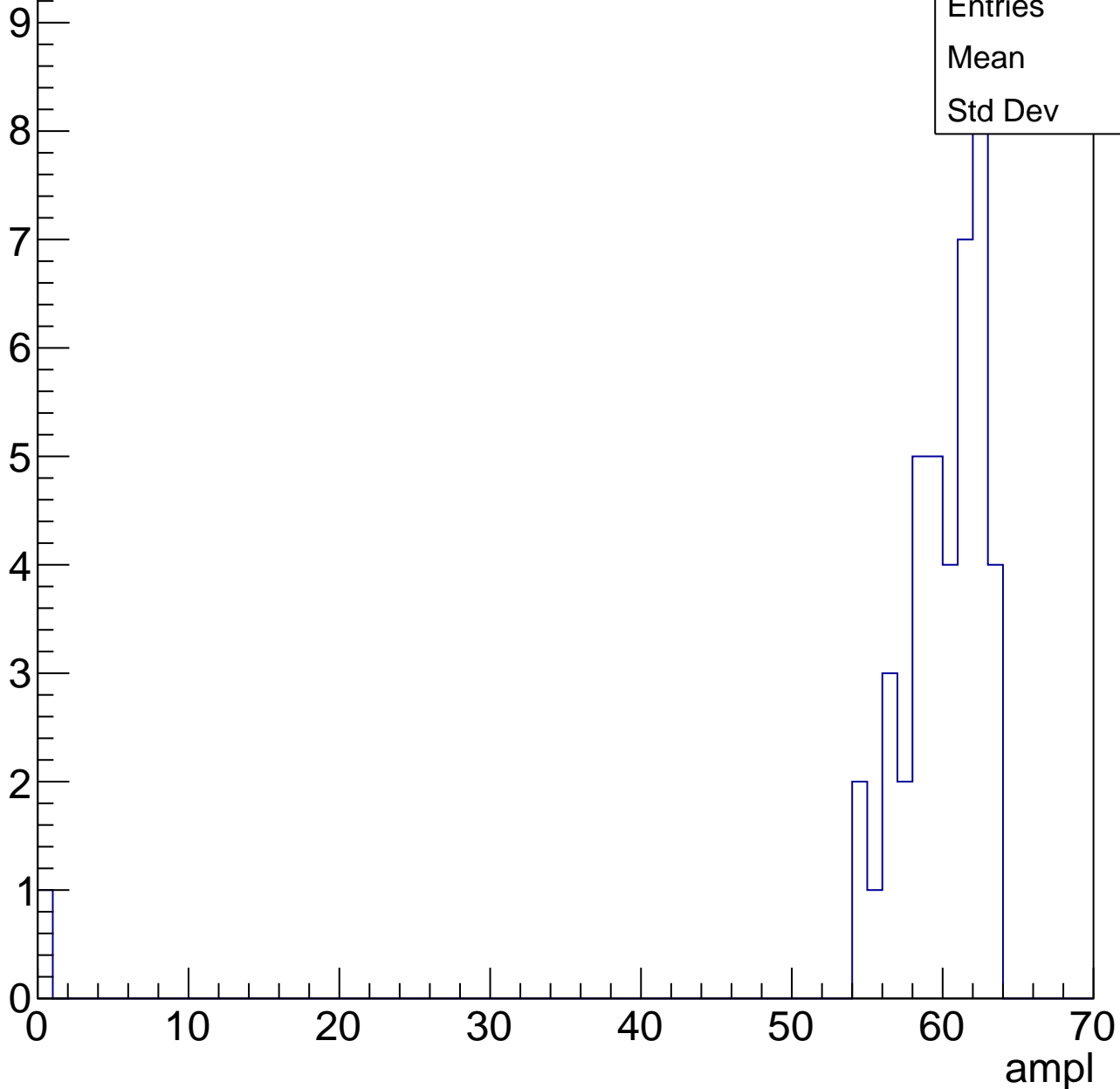


# B1L103S, U22-ch109, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	43
Mean	58.3
Std Dev	9.33



# B1L103S, U22-ch109, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

6

Mean

61.5

Std Dev

1.5



# B1L103S, U22-ch109, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73

Entry



# B1L103S, U22-ch110, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	24.49
Std Dev	9.797

**Gaus mean : 28.6071**

**Gaus Width: 3.4965**

Entry

10

8

6

4

2

0

0

10

20

30

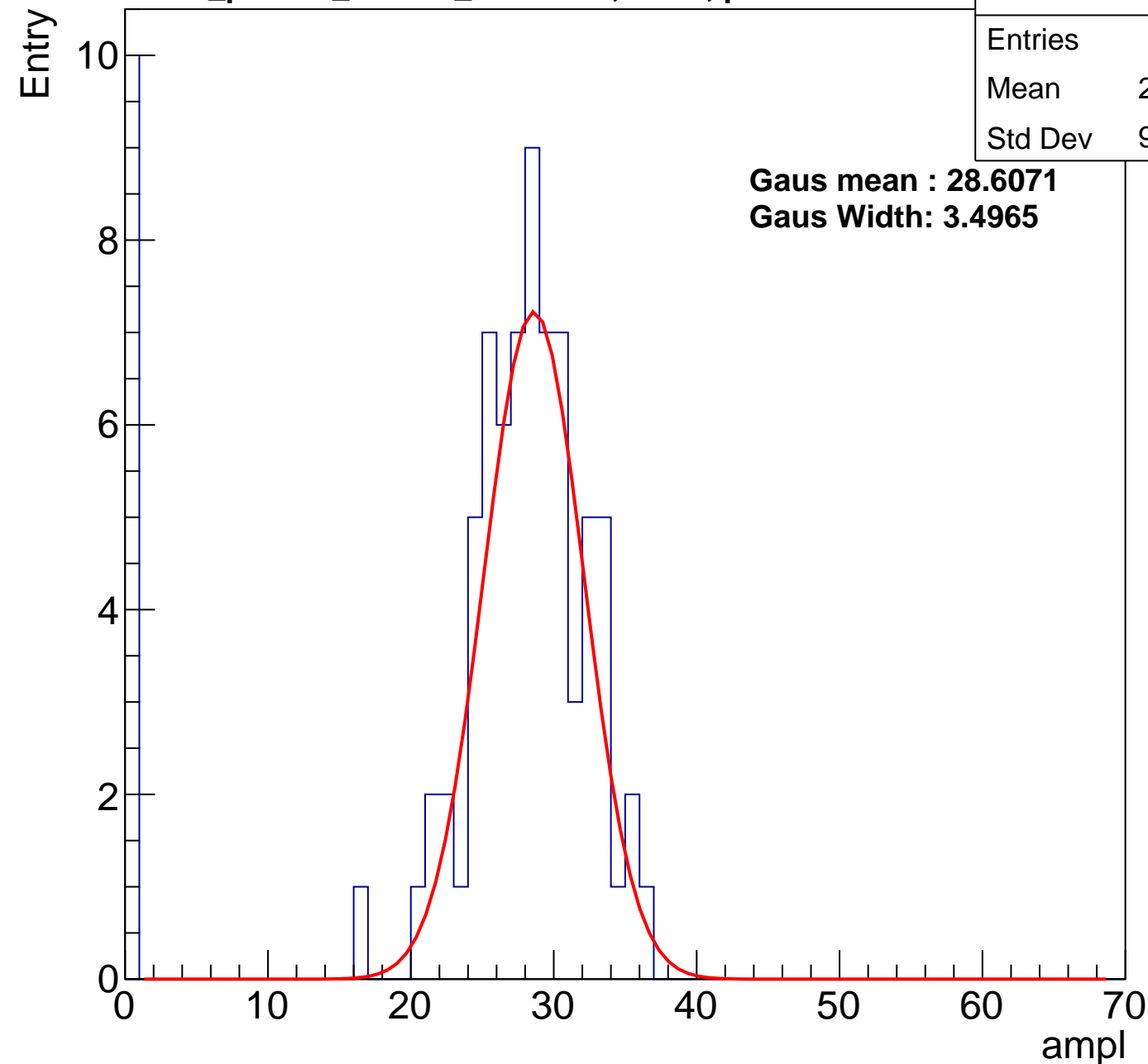
40

50

60

70

ampl



# B1L103S, U22-ch110, adc1

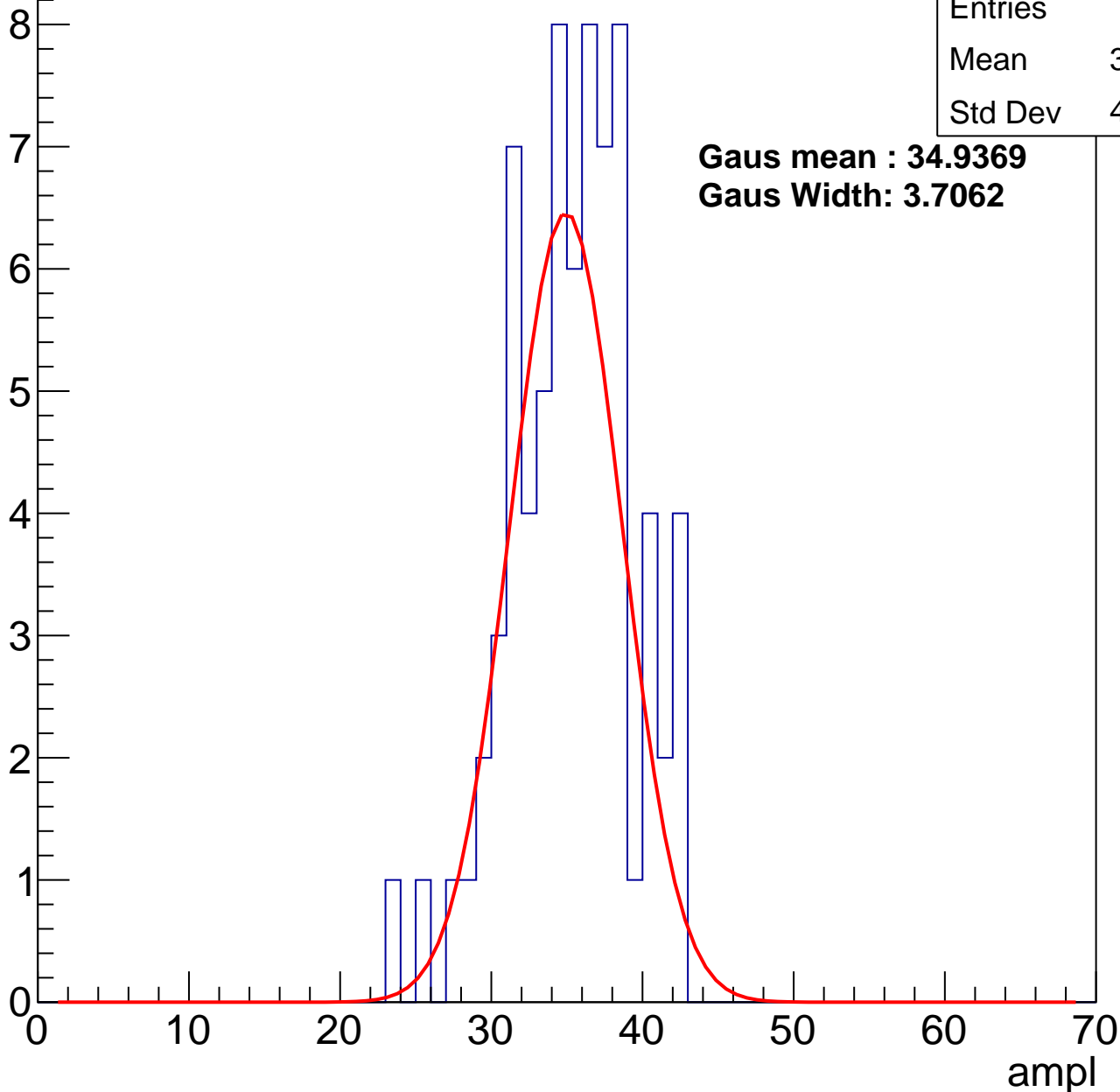
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	34.84
Std Dev	4.003

**Gaus mean : 34.9369**

**Gaus Width: 3.7062**



# B1L103S, U22-ch110, adc2

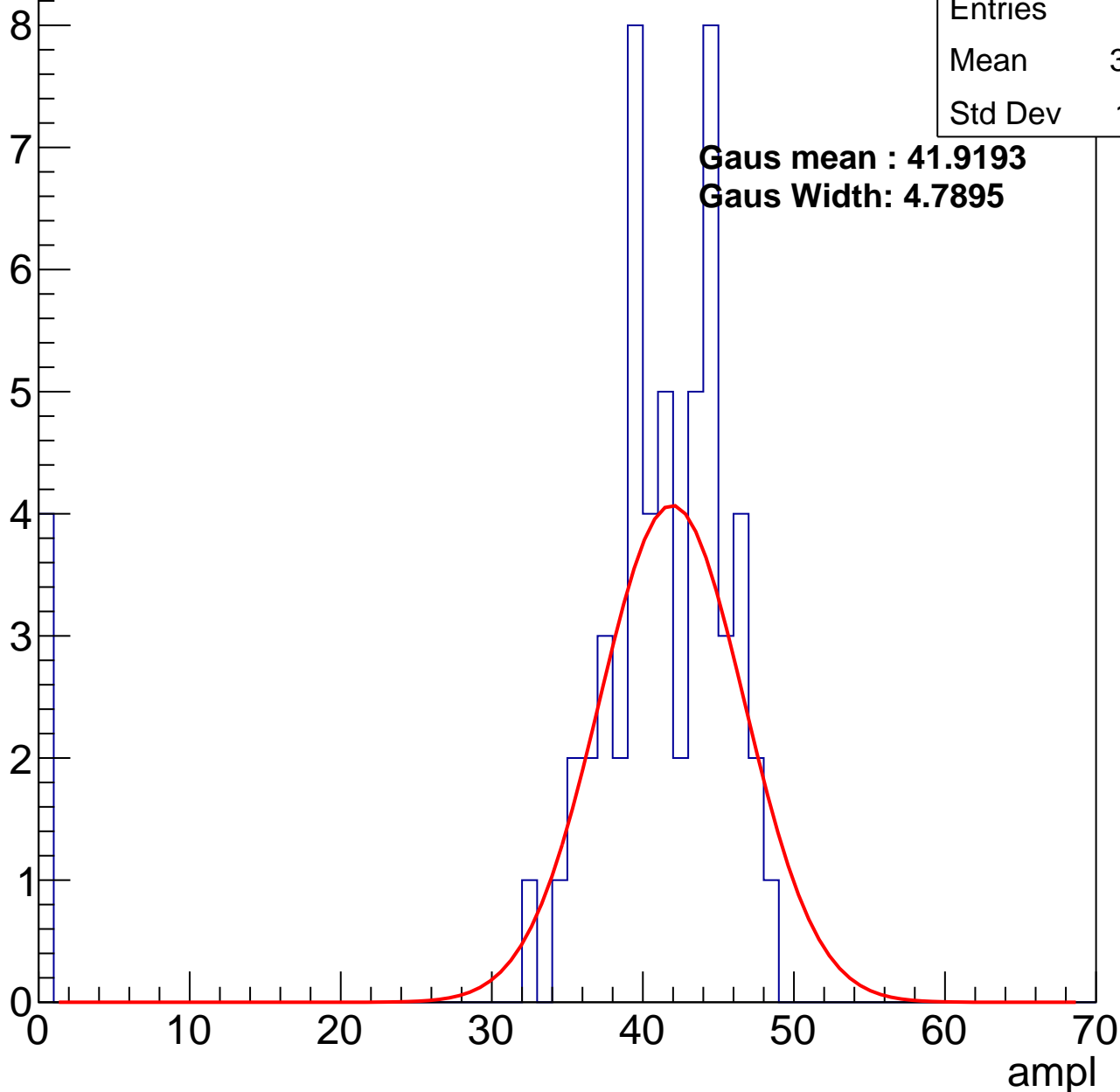
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	38.32
Std Dev	11.11

**Gaus mean : 41.9193**

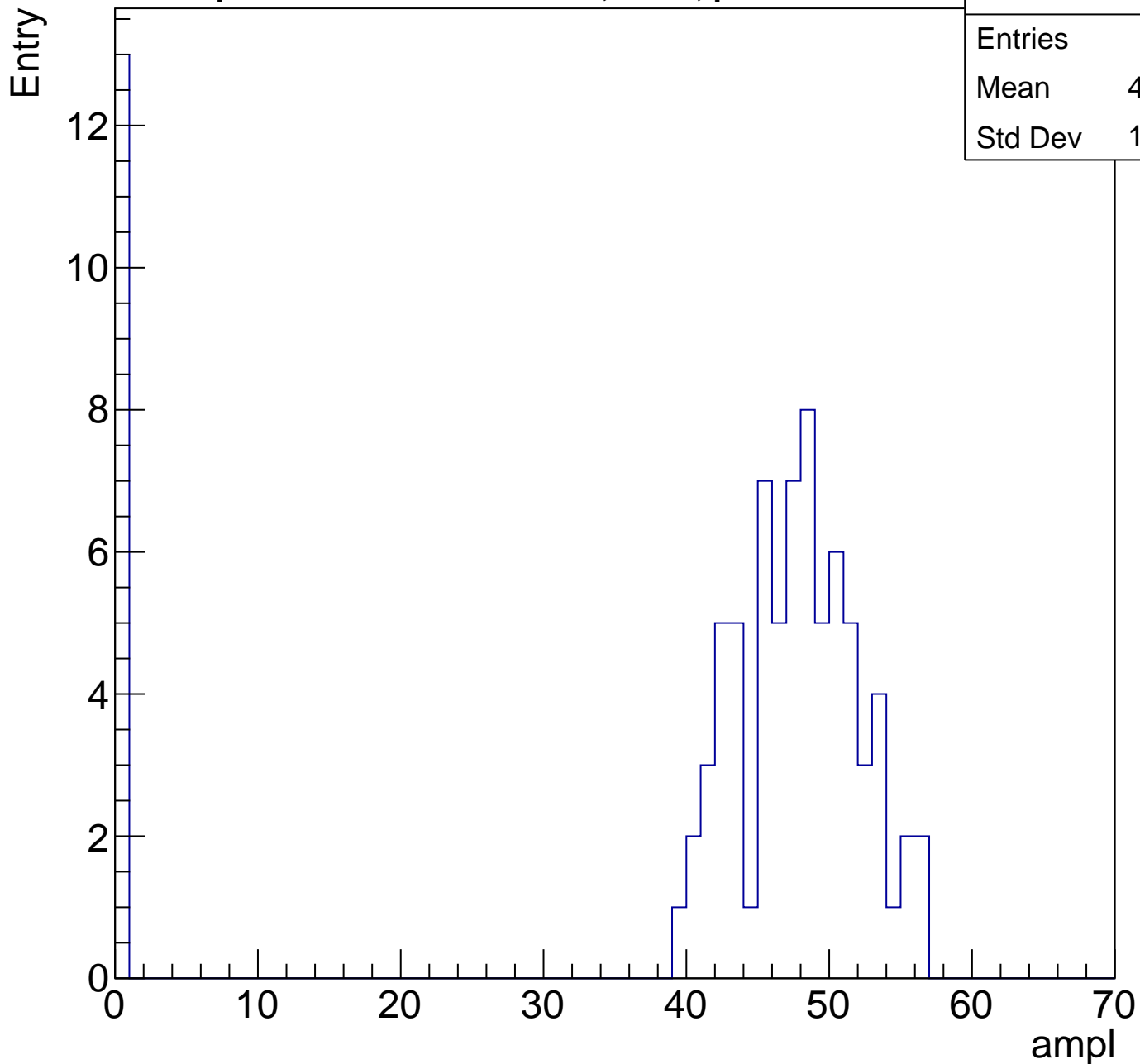
**Gaus Width: 4.7895**



# B1L103S, U22-ch110, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	40.15
Std Dev	17.48

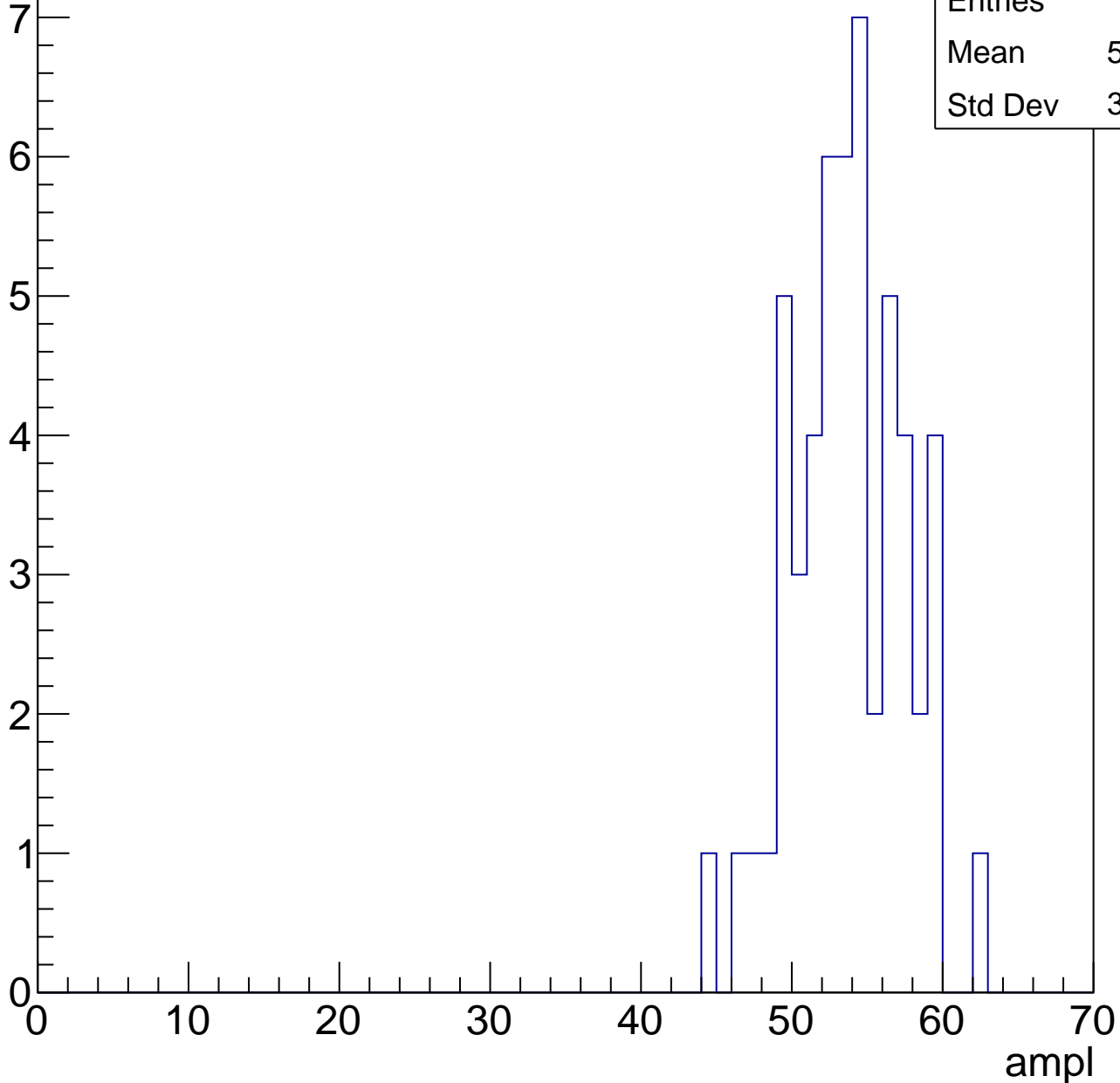


# B1L103S, U22-ch110, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	53.28
Std Dev	3.672

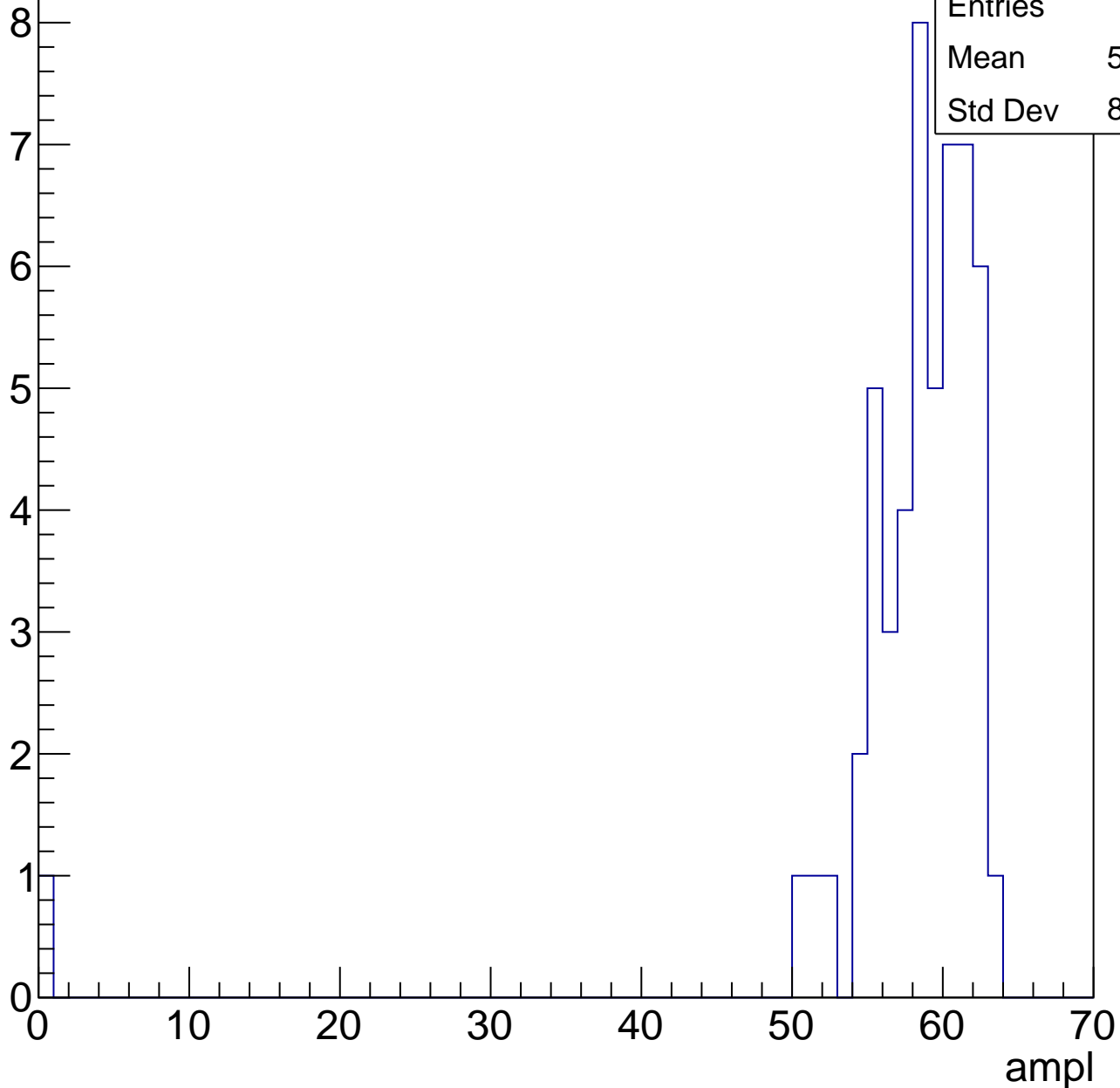


# B1L103S, U22-ch110, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	52
Mean	57.17
Std Dev	8.534

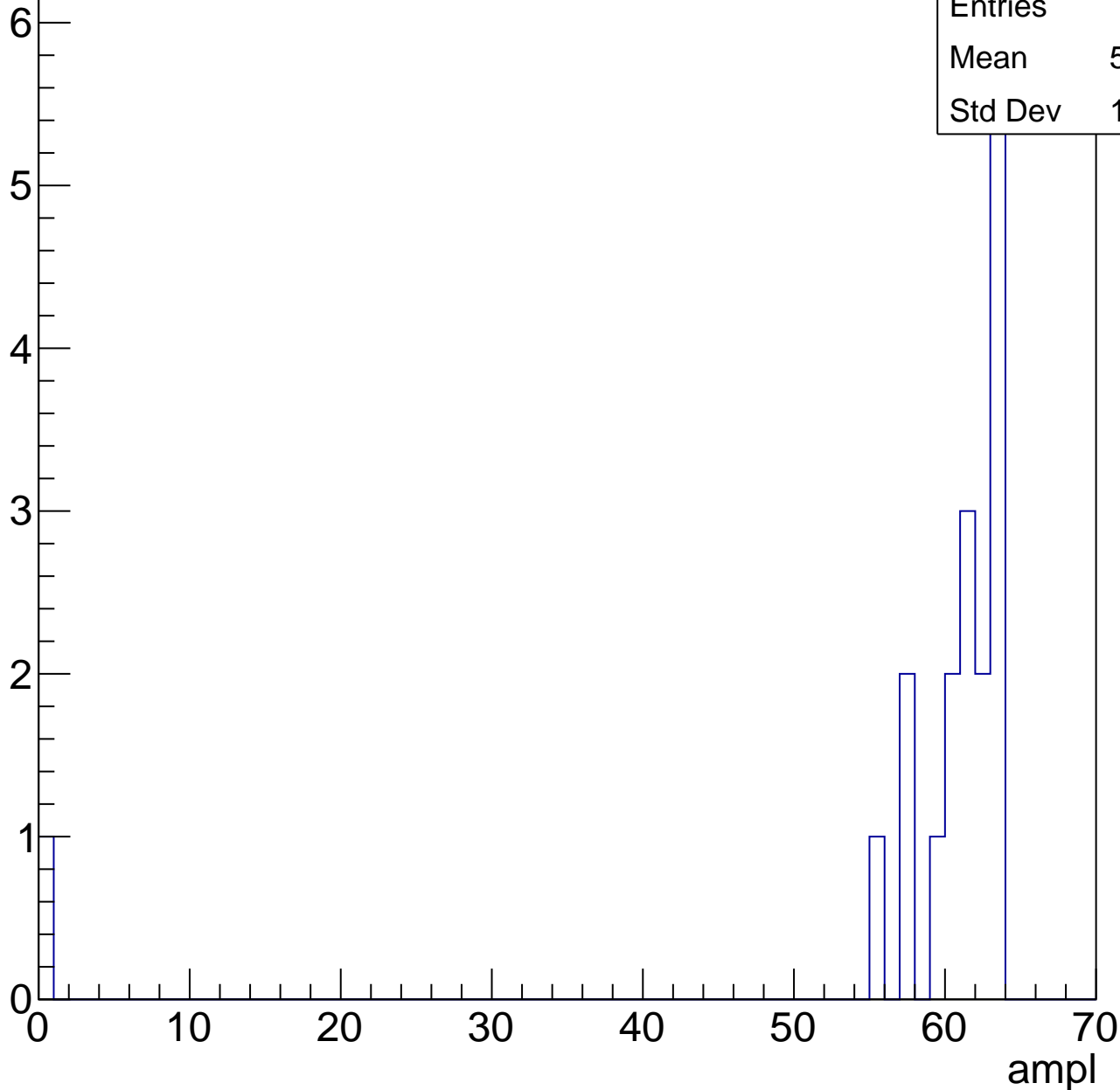


# B1L103S, U22-ch110, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	18
Mean	57.39
Std Dev	14.12



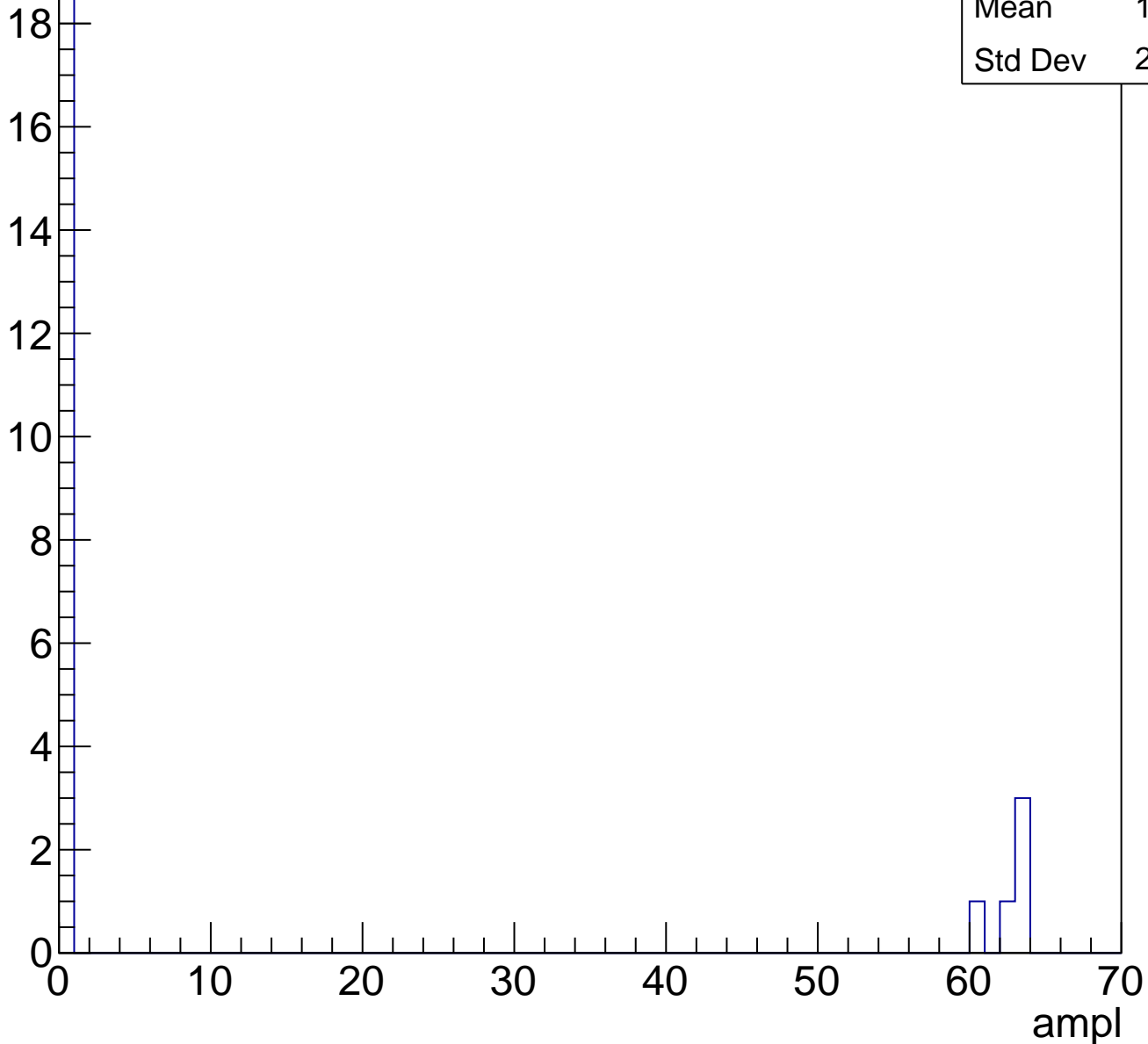


# B1L103S, U22-ch110, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	24
Mean	12.96
Std Dev	25.27

Entry



# B1L103S, U22-ch111, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	74
Mean	21.04
Std Dev	11.05

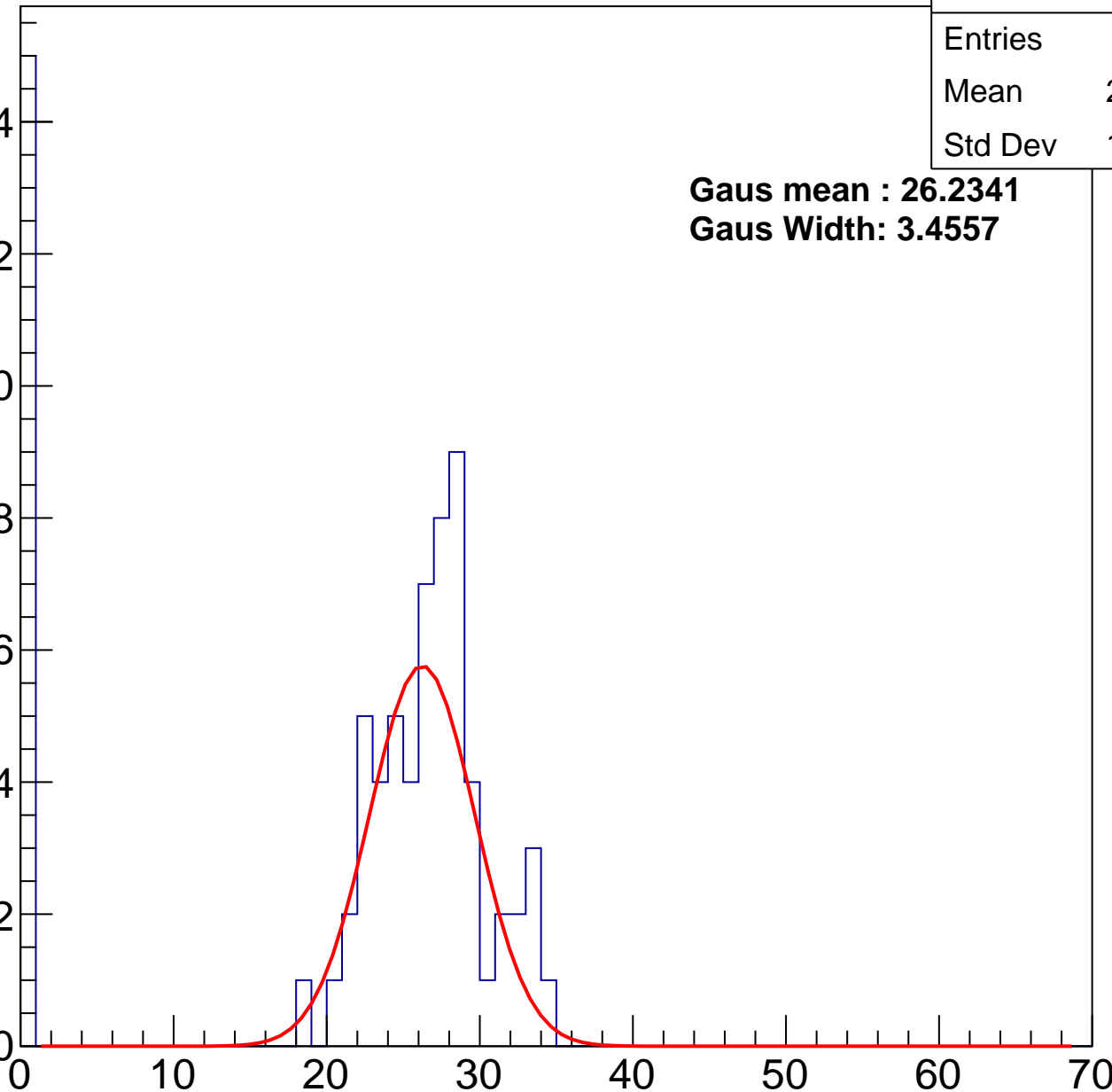
**Gaus mean : 26.2341**

**Gaus Width: 3.4557**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch111, adc1

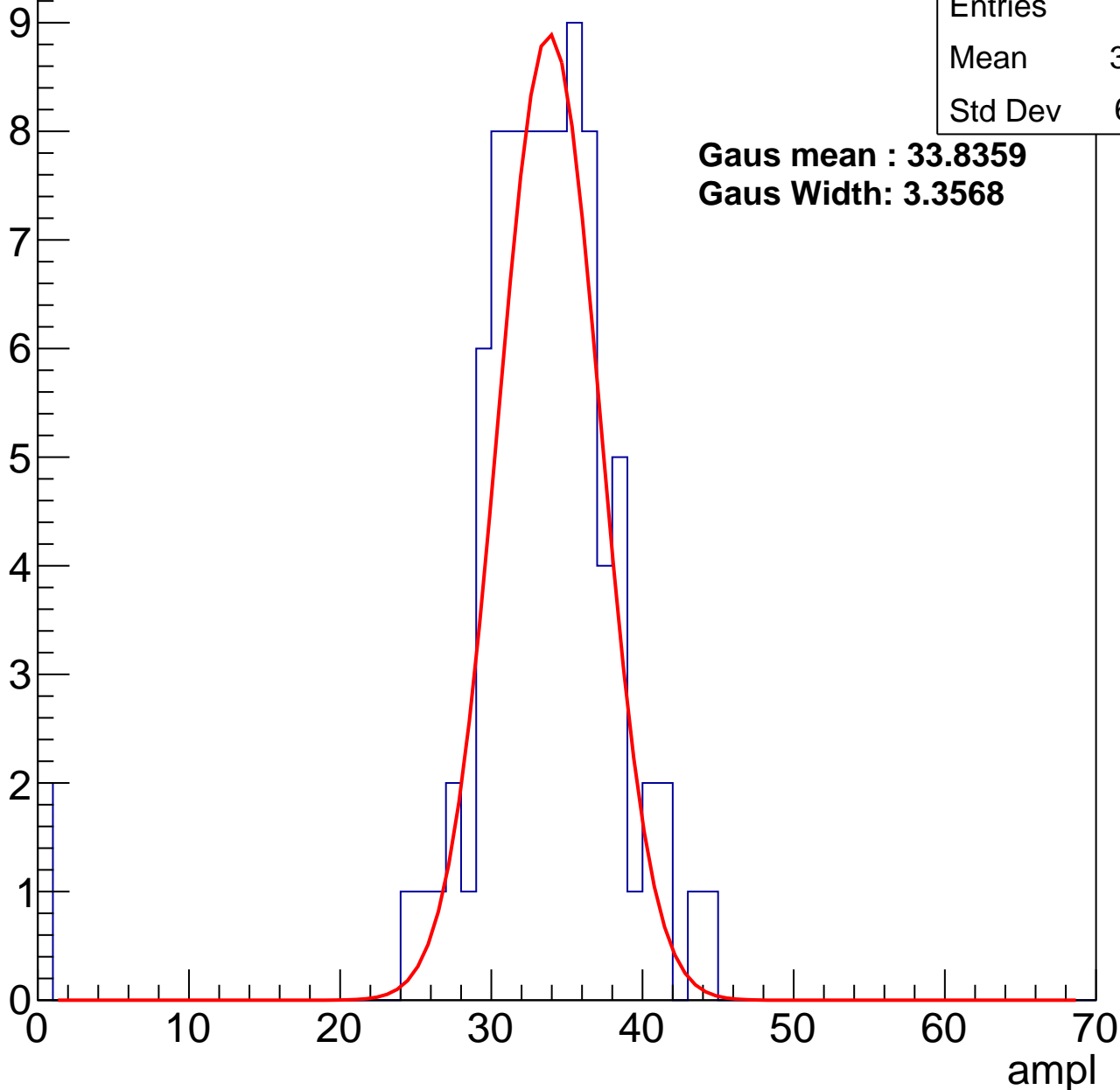
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	87
Mean	32.64
Std Dev	6.301

**Gaus mean : 33.8359**

**Gaus Width: 3.3568**



# B1L103S, U22-ch111, adc2

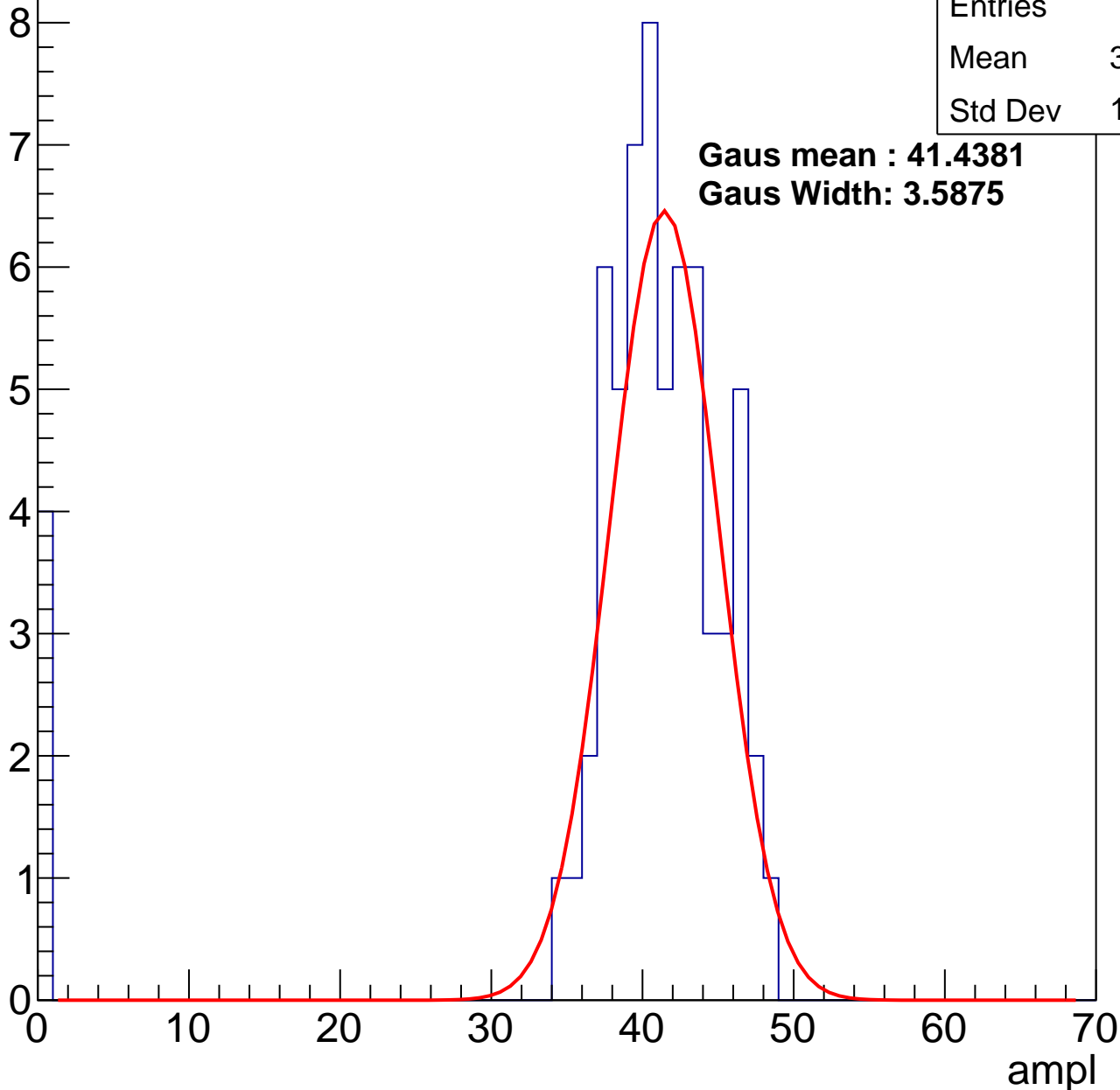
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.46
Std Dev	10.35

**Gaus mean : 41.4381**

**Gaus Width: 3.5875**



# B1L103S, U22-ch111, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	73
Mean	39.79
Std Dev	17.11

Entry

10

8

6

4

2

0

0

10

20

30

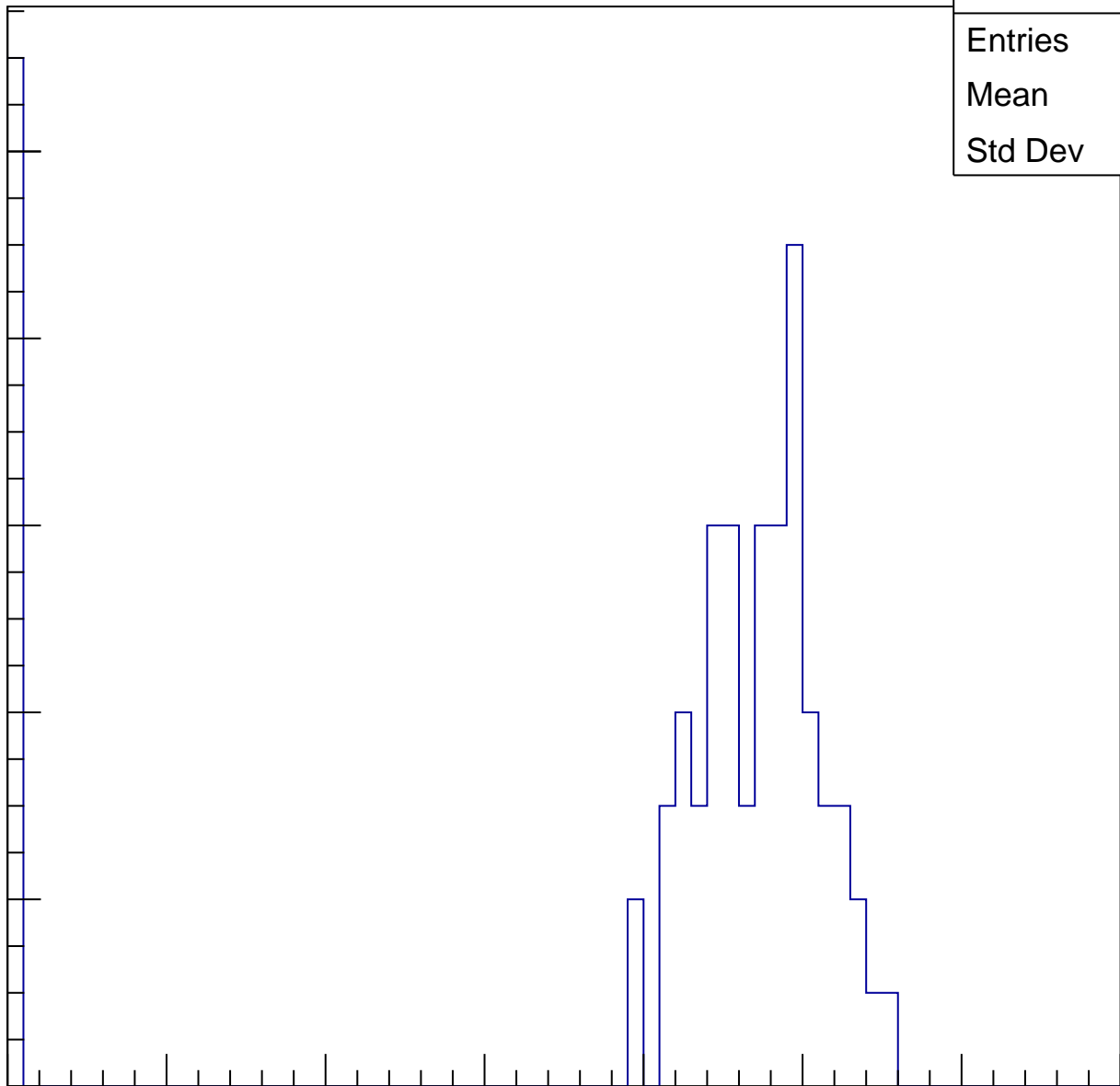
40

50

60

70

ampl

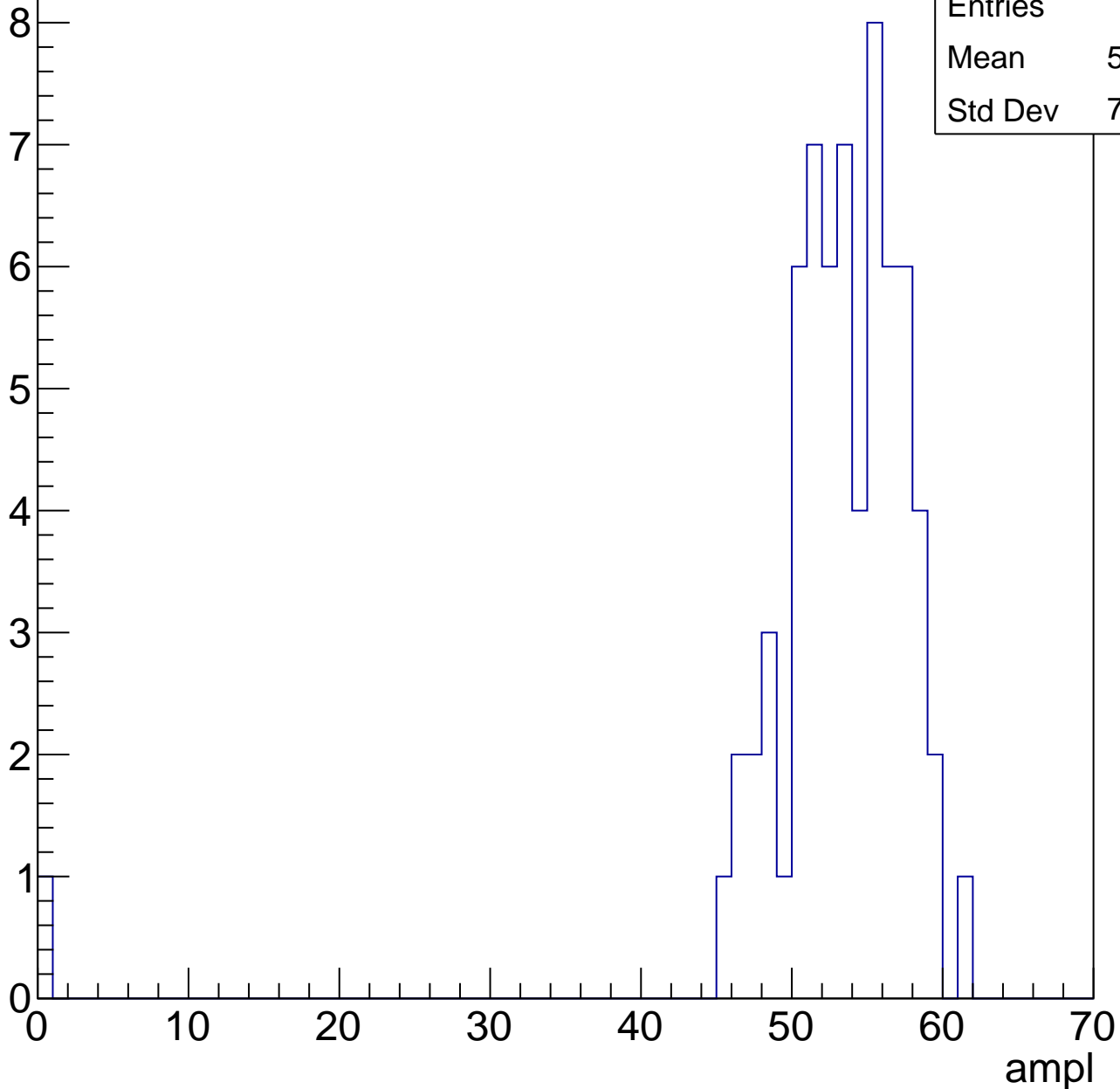


# B1L103S, U22-ch111, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	52.37
Std Dev	7.348

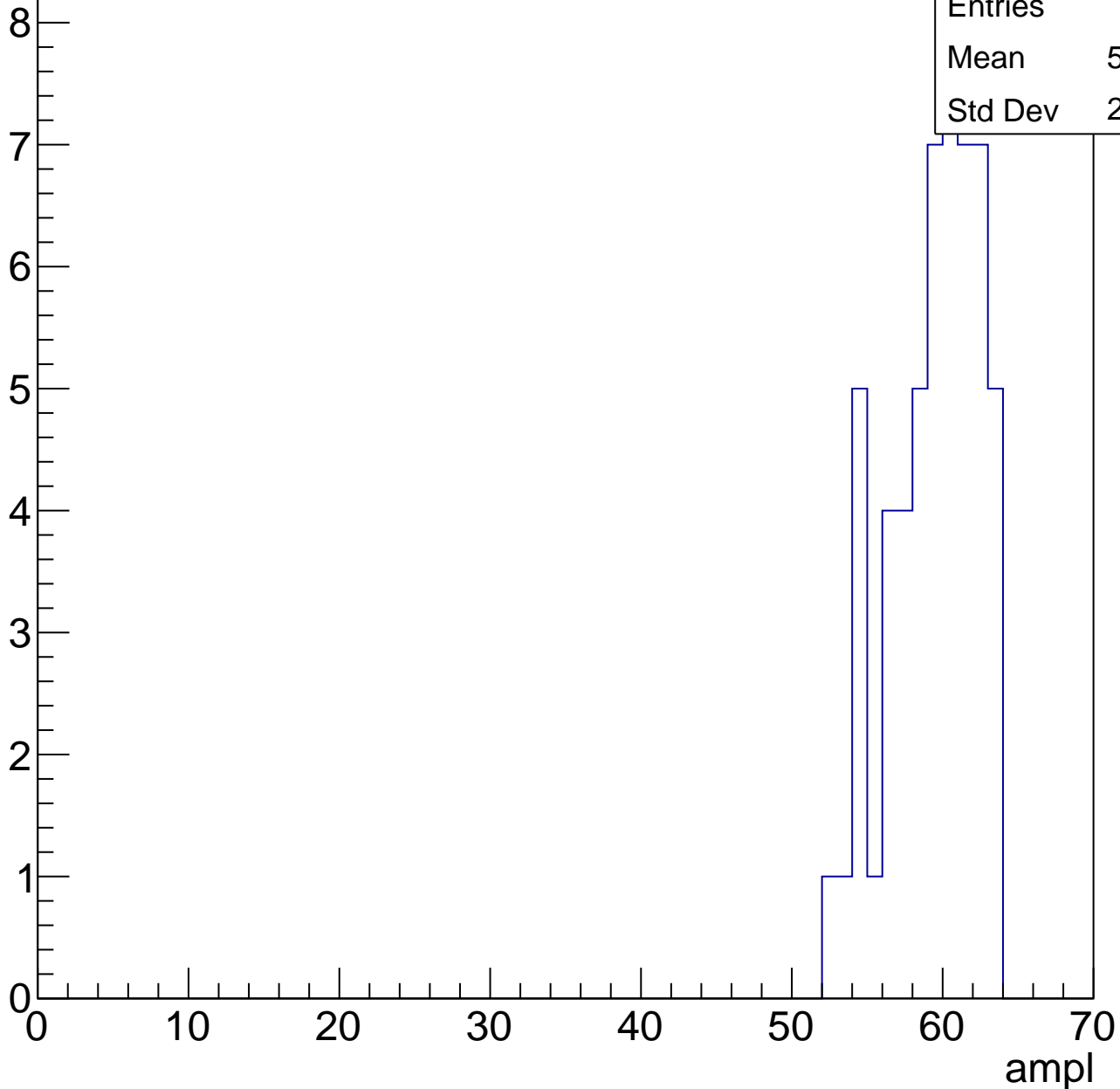


# B1L103S, U22-ch111, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	58.93
Std Dev	2.897

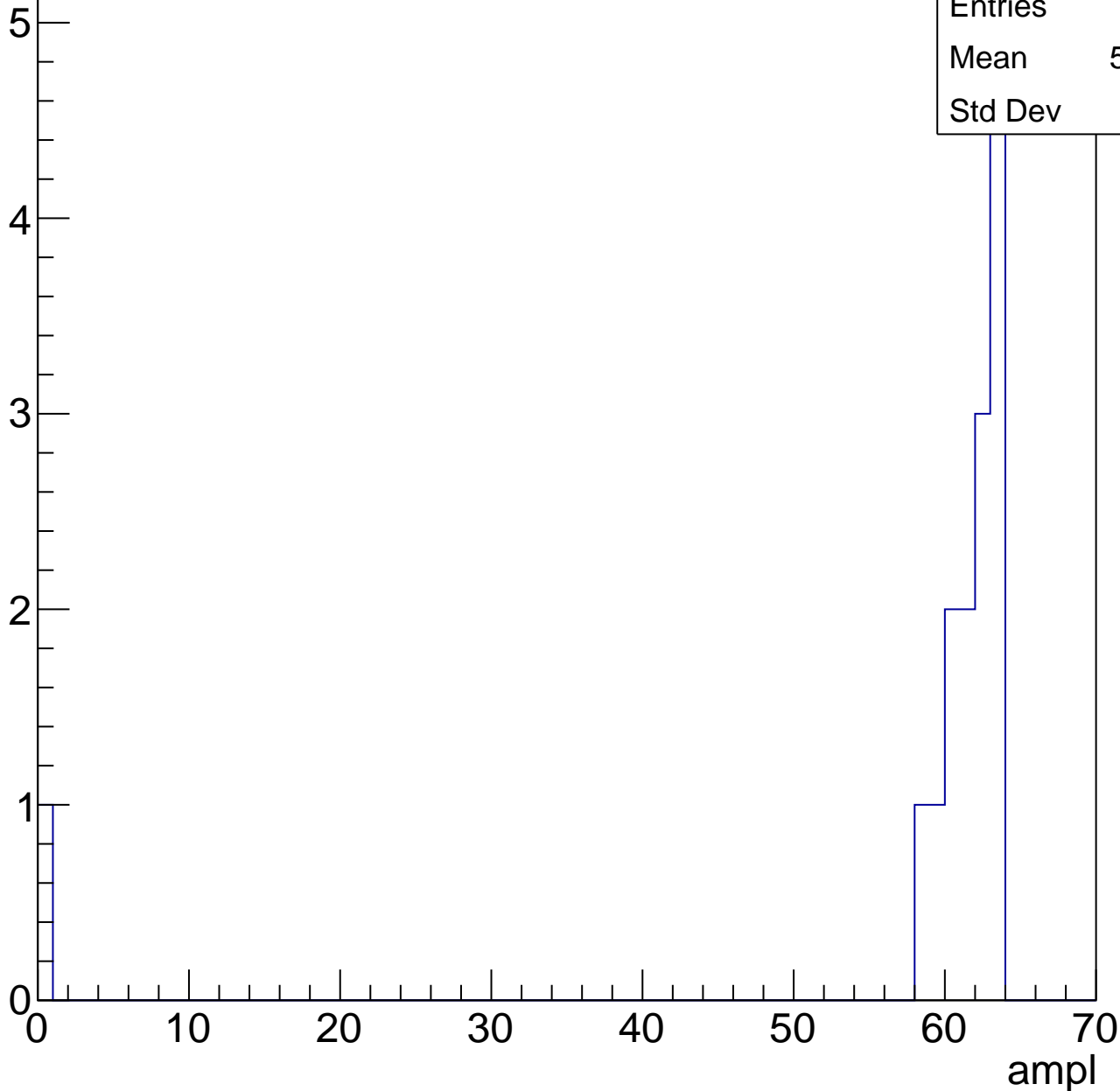


# B1L103S, U22-ch111, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	15
Mean	57.33
Std Dev	15.4





# B1L103S, U22-ch111, adc7

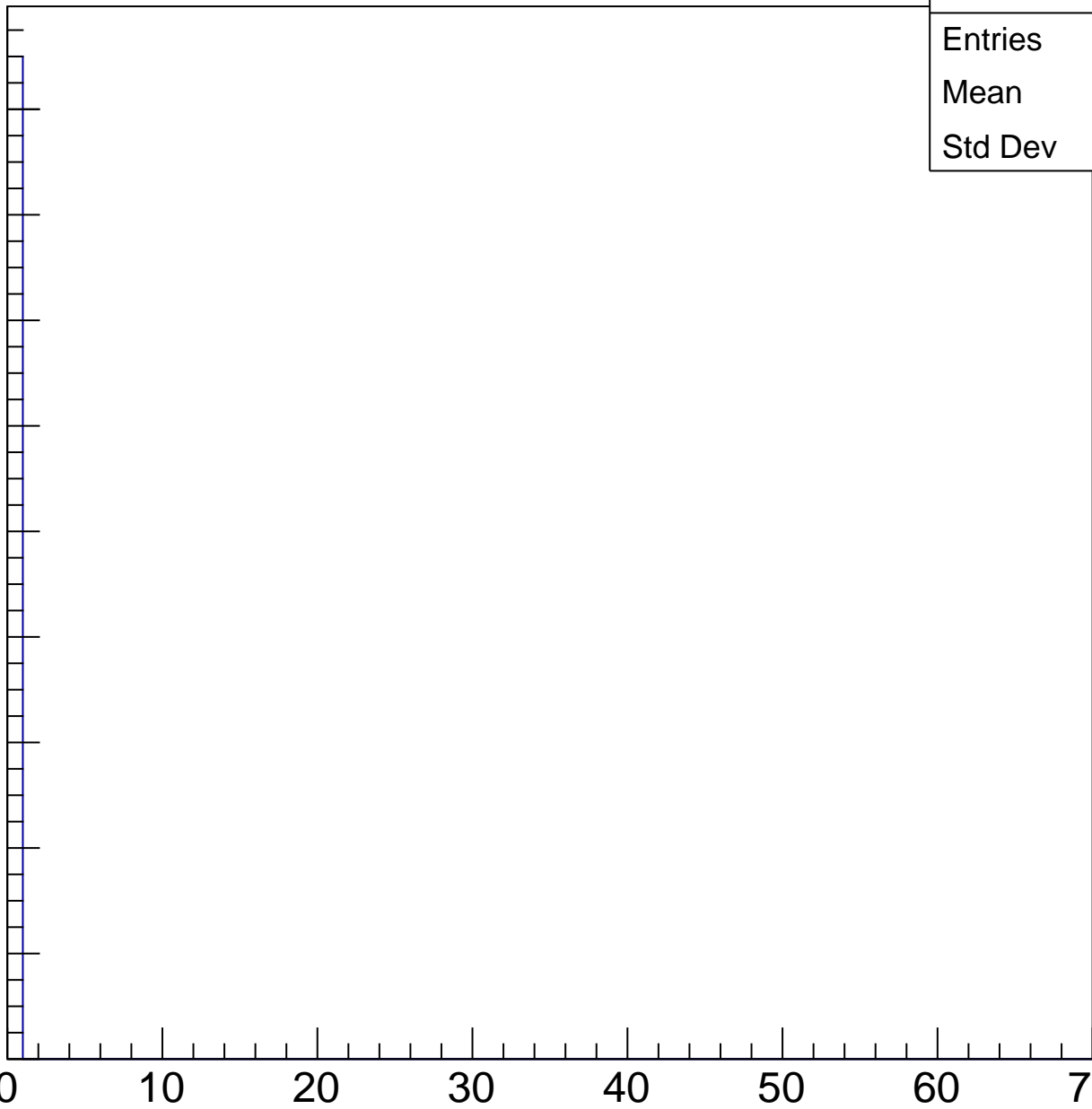
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	19
Mean	0
Std Dev	0

ampl



# B1L103S, U22-ch112, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	101
Mean	25.27
Std Dev	11.34

**Gaus mean : 29.0834**

**Gaus Width: 4.5652**

Entry

12

10

8

6

4

2

0

0

10

20

30

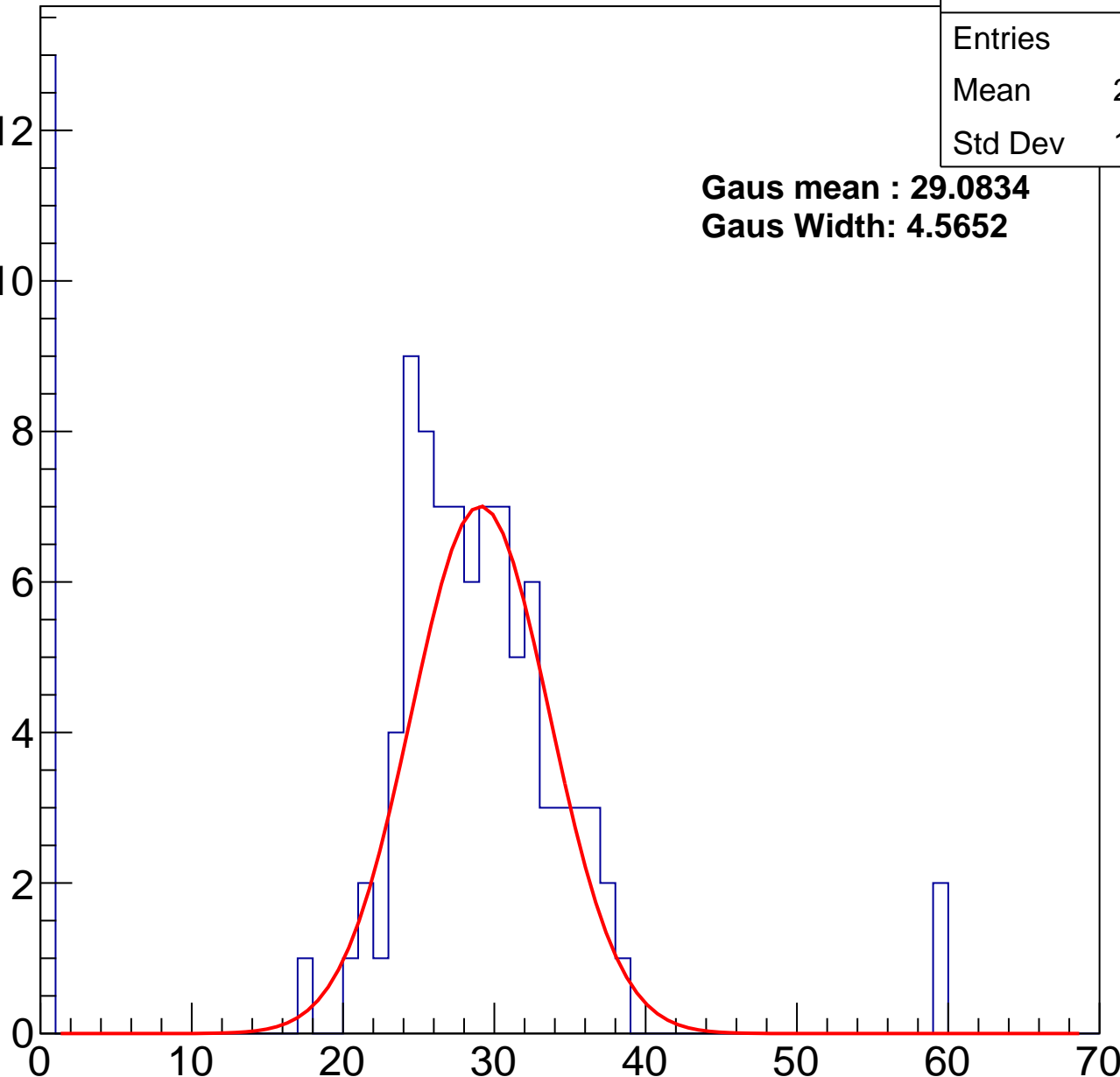
40

50

60

70

ampl



# B1L103S, U22-ch112, adc1

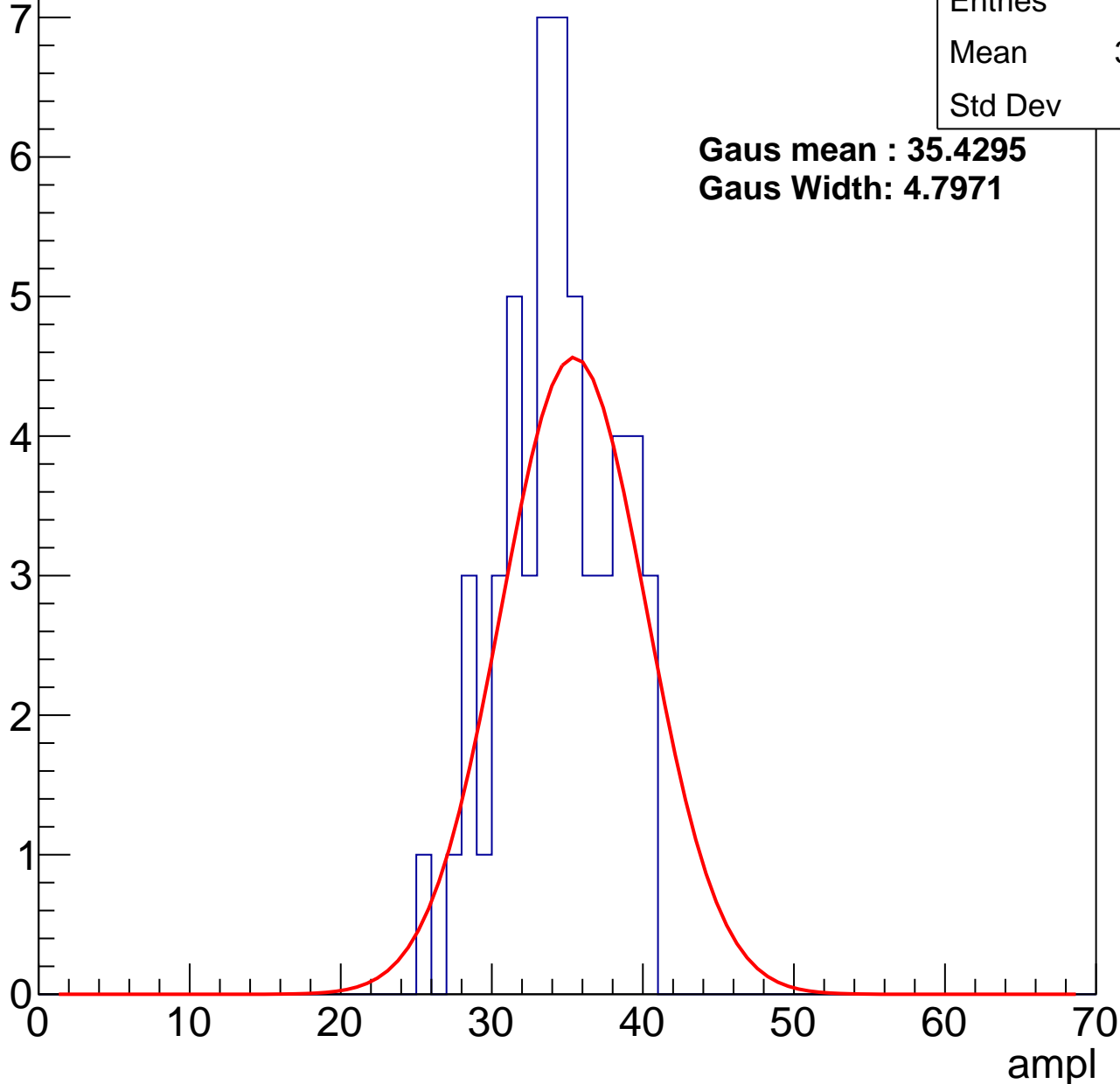
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	33.91
Std Dev	3.61

**Gaus mean : 35.4295**

**Gaus Width: 4.7971**



# B1L103S, U22-ch112, adc2

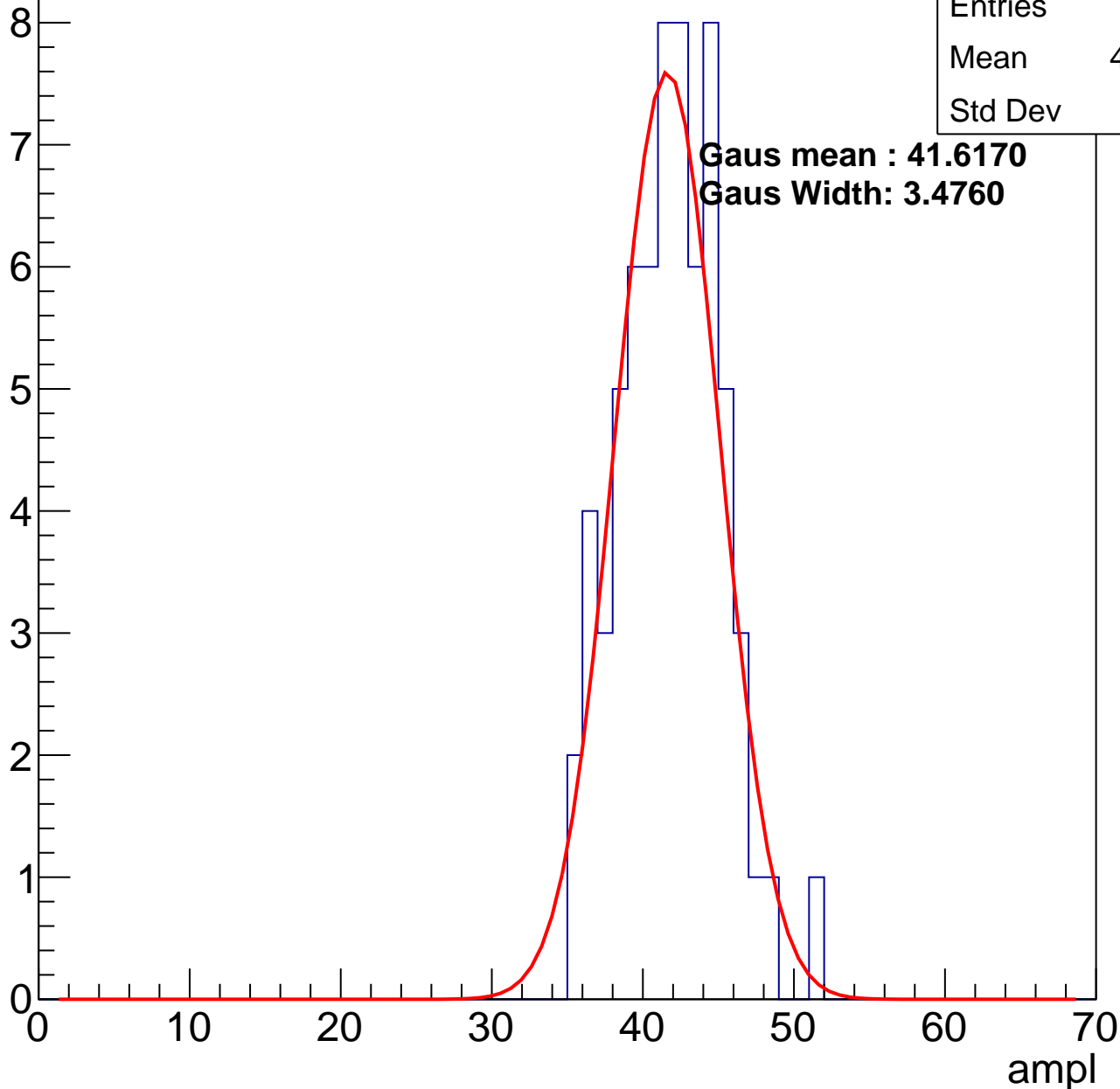
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	41.37
Std Dev	3.3

**Gaus mean : 41.6170**

**Gaus Width: 3.4760**

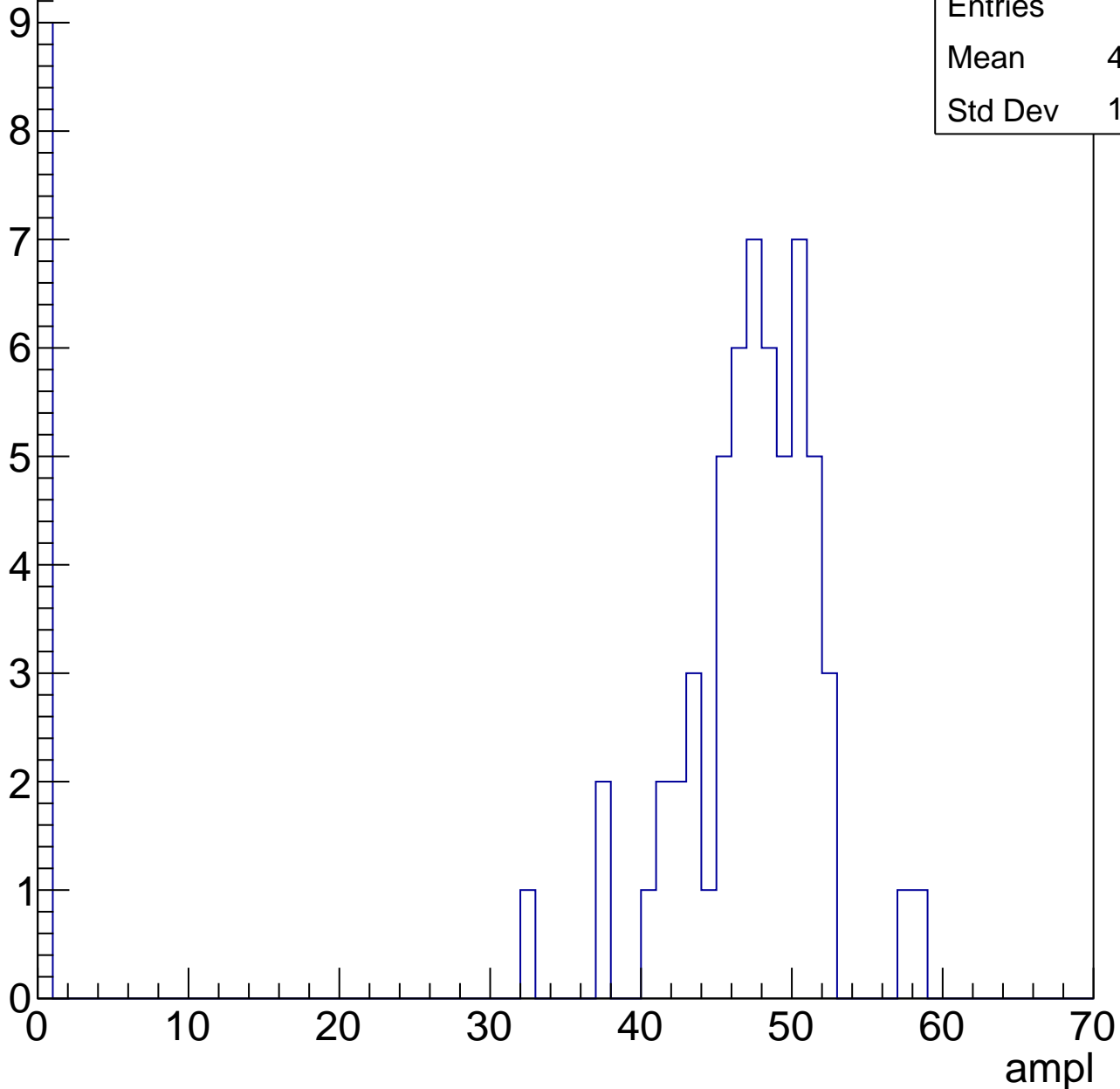


# B1L103S, U22-ch112, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	40.66
Std Dev	16.54

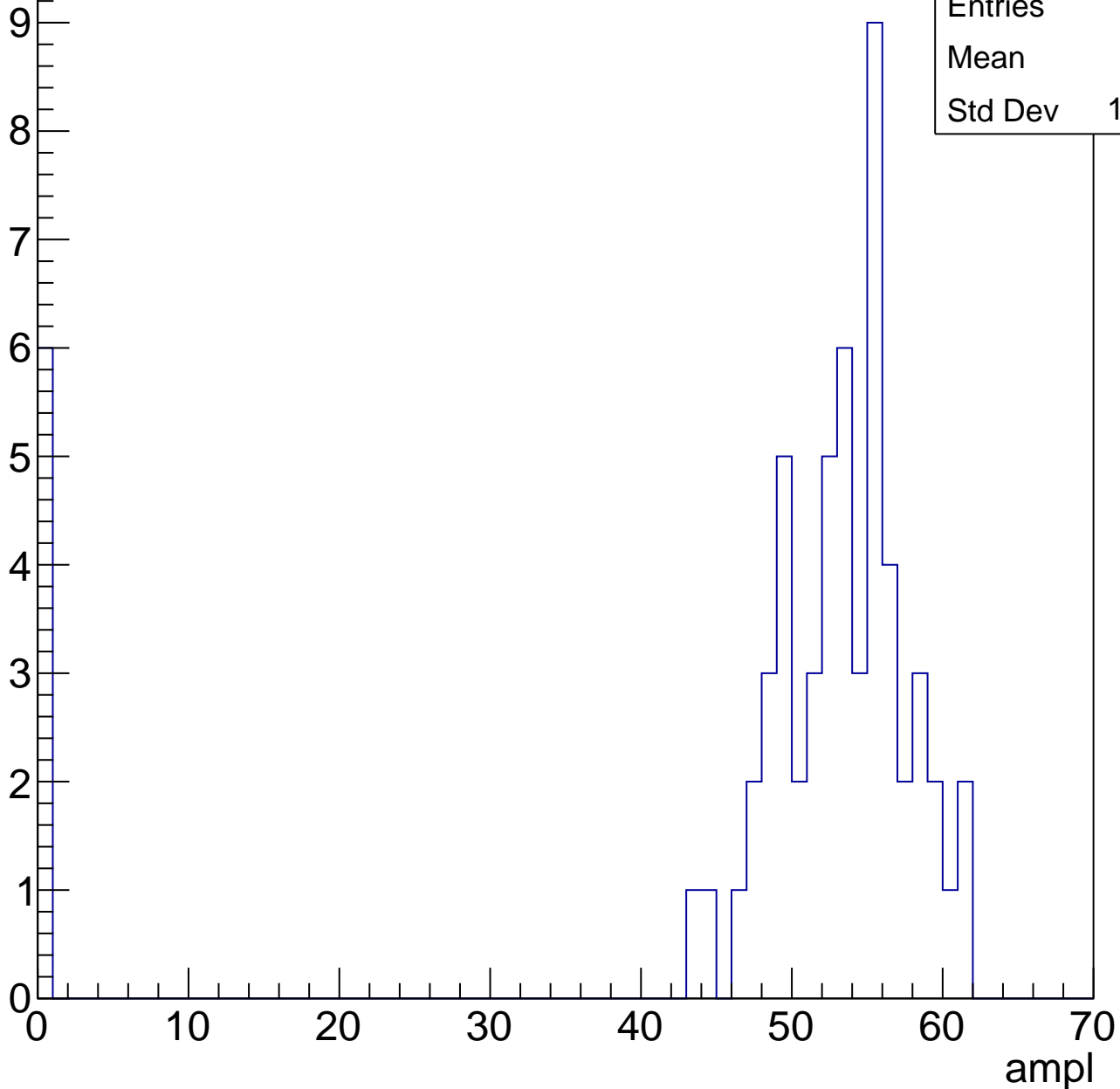


# B1L103S, U22-ch112, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	47.8
Std Dev	16.26

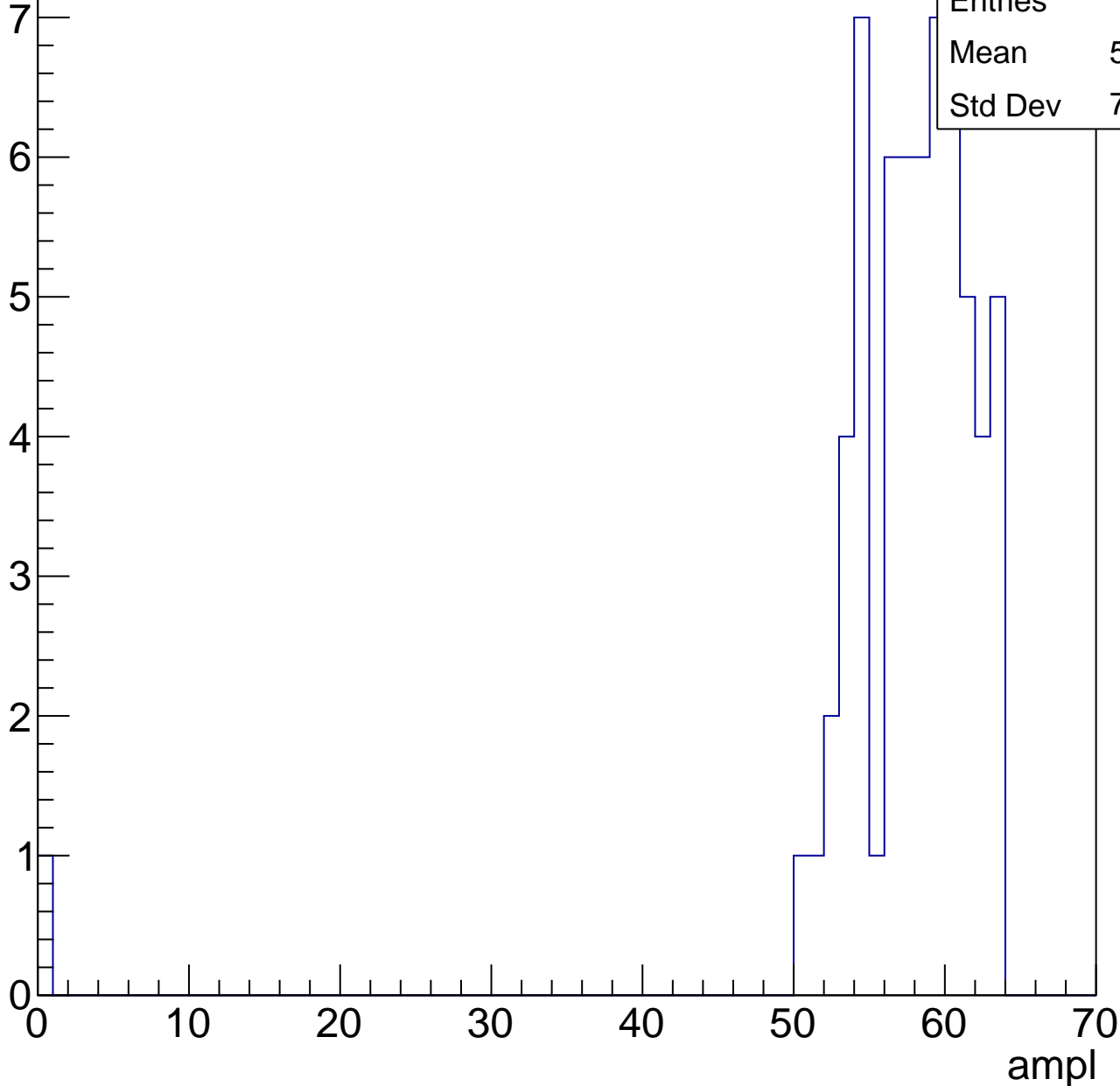


# B1L103S, U22-ch112, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	56.78
Std Dev	7.943

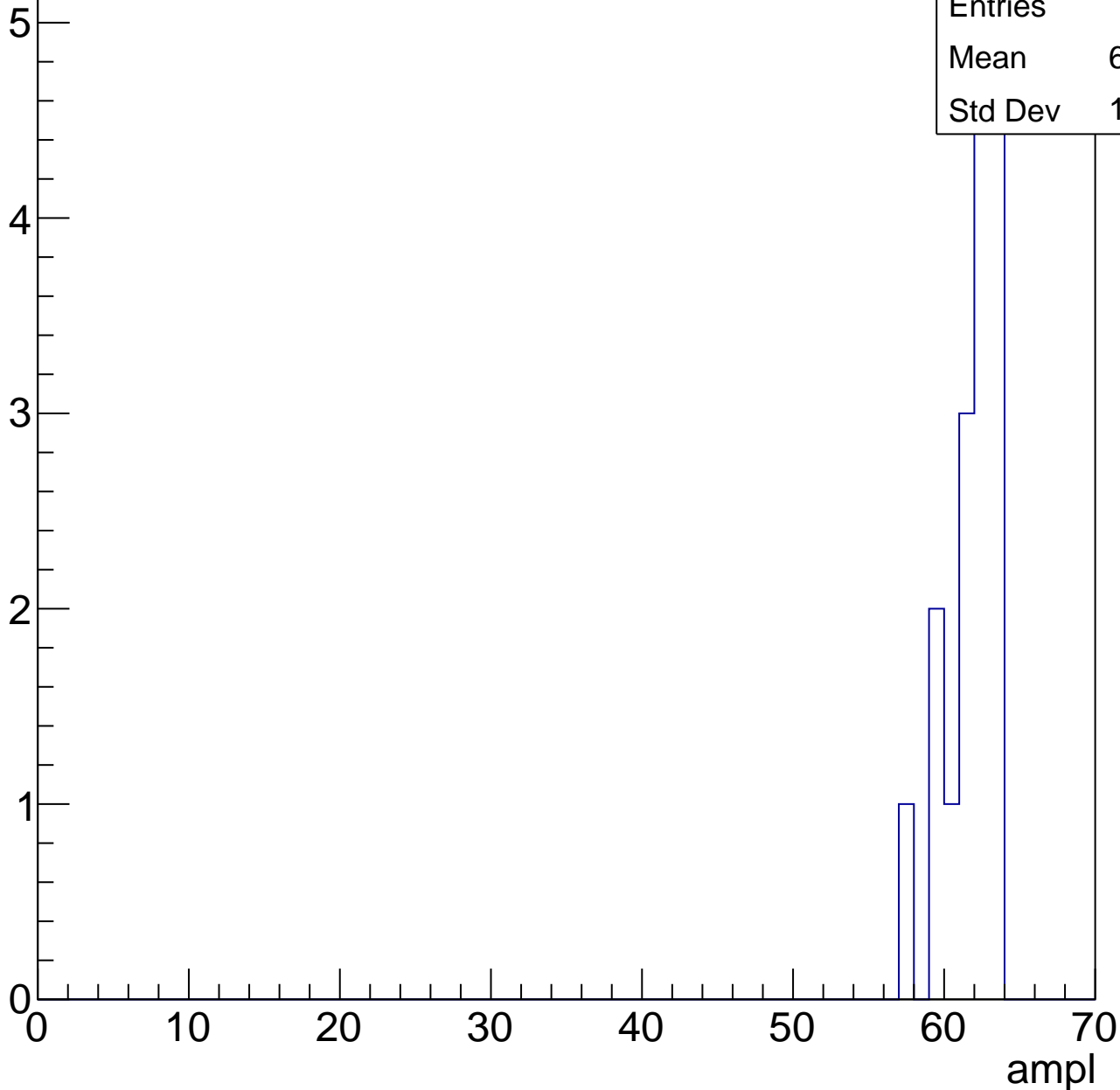


# B1L103S, U22-ch112, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.35
Std Dev	1.678



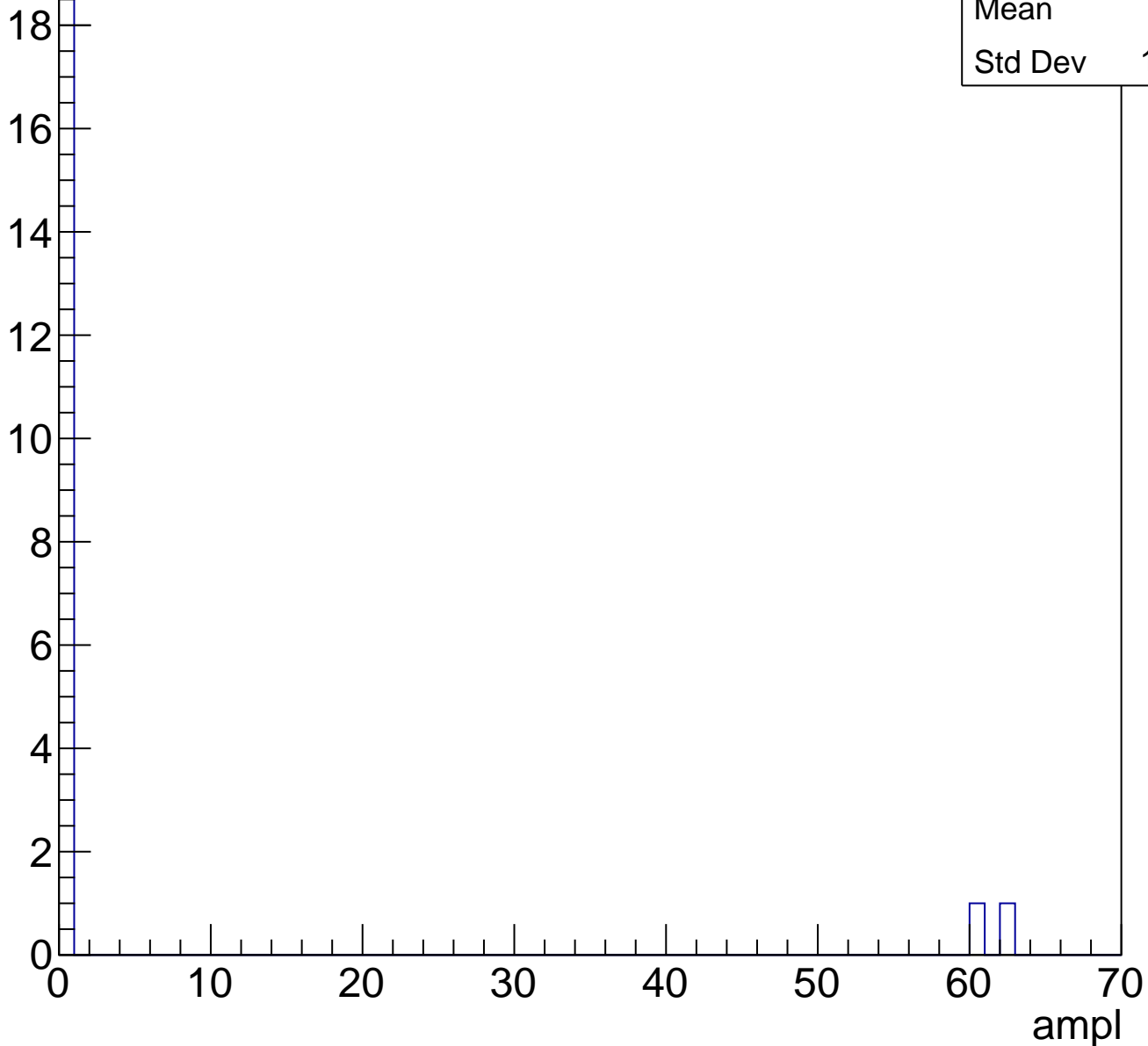


# B1L103S, U22-ch112, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.81
Std Dev	17.91

Entry



# B1L103S, U22-ch113, adc0

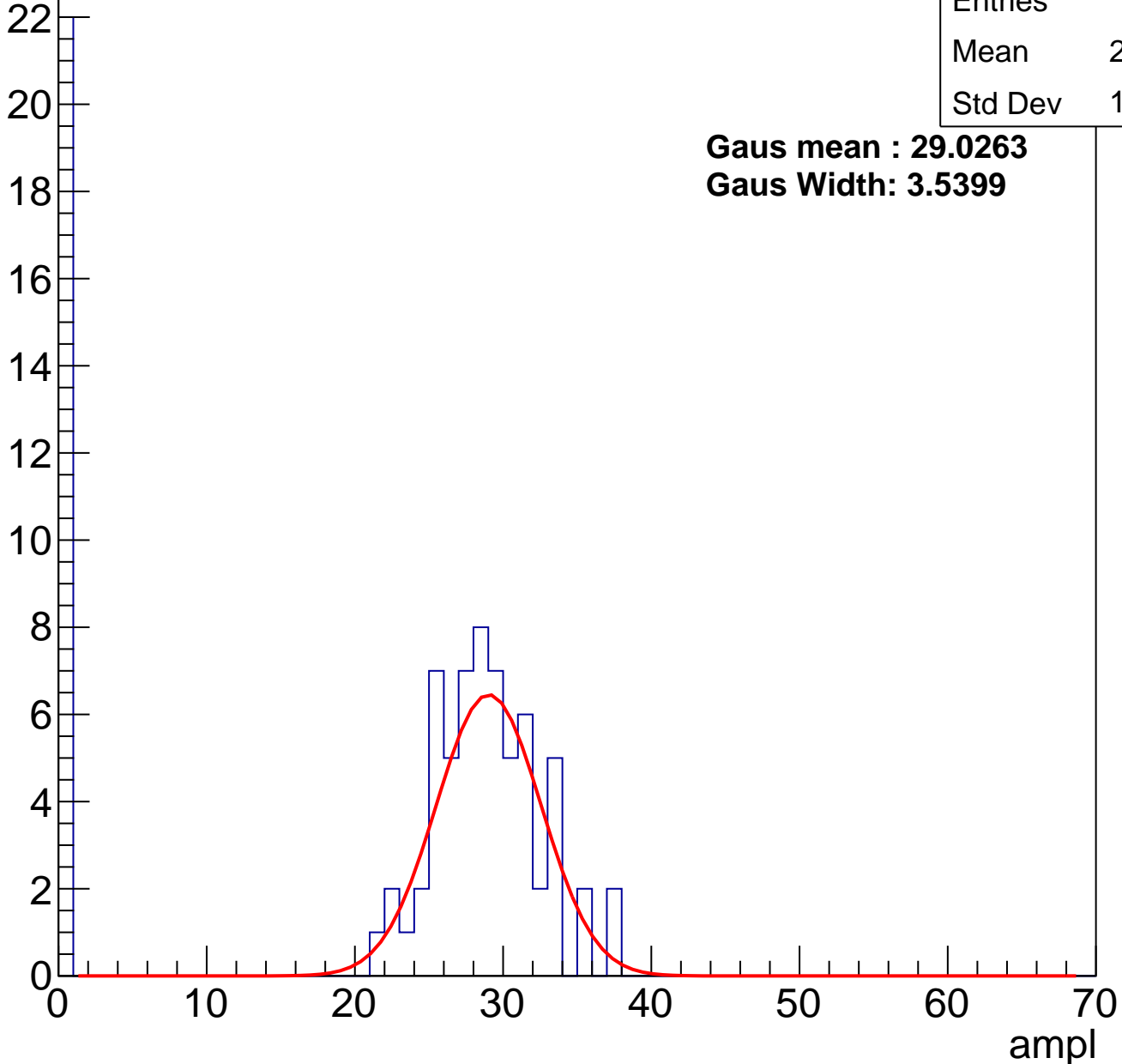
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	84
Mean	21.02
Std Dev	12.88

**Gaus mean : 29.0263**

**Gaus Width: 3.5399**

Entry



# B1L103S, U22-ch113, adc1

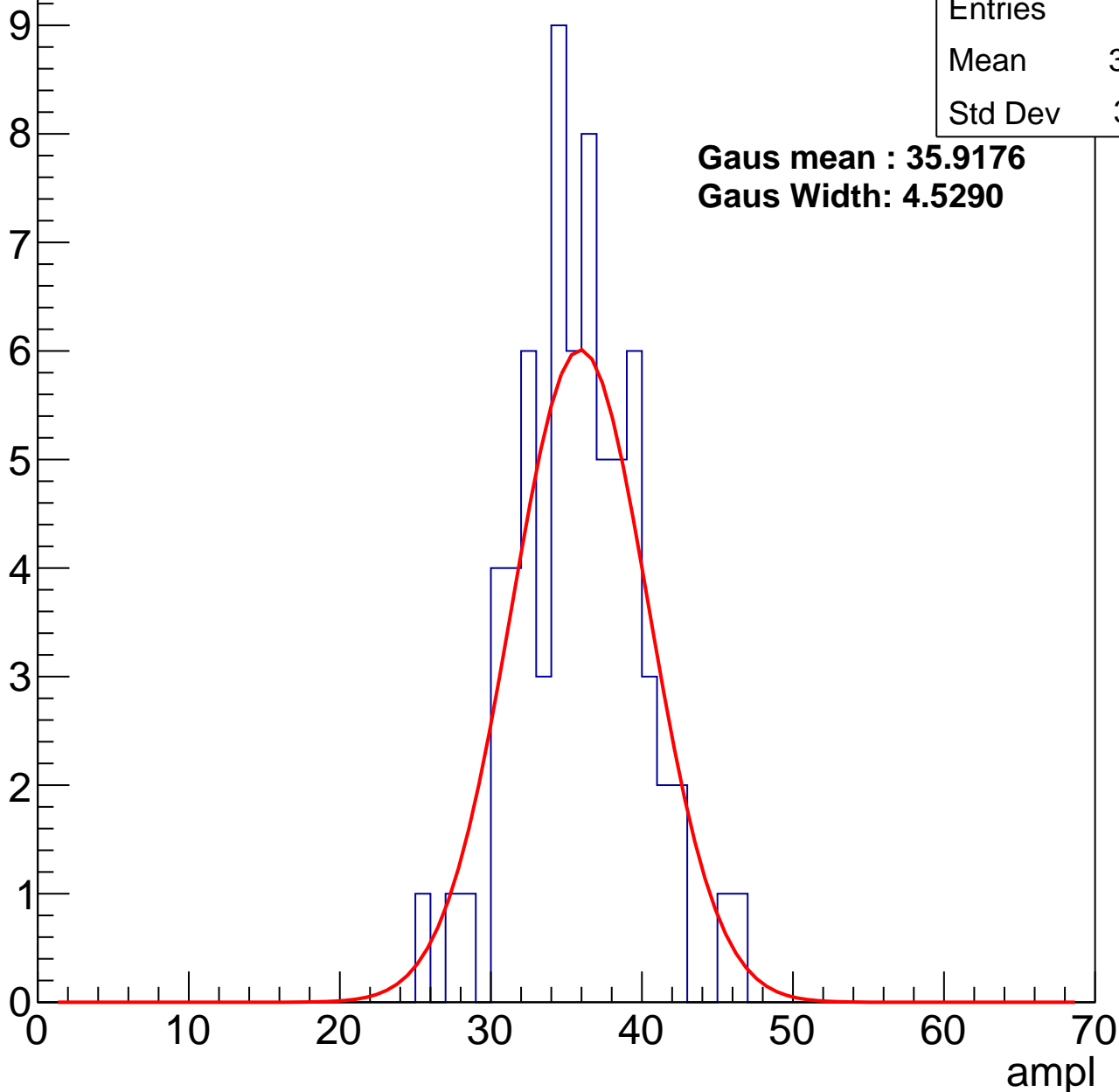
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	35.37
Std Dev	3.981

**Gaus mean : 35.9176**

**Gaus Width: 4.5290**



# B1L103S, U22-ch113, adc2

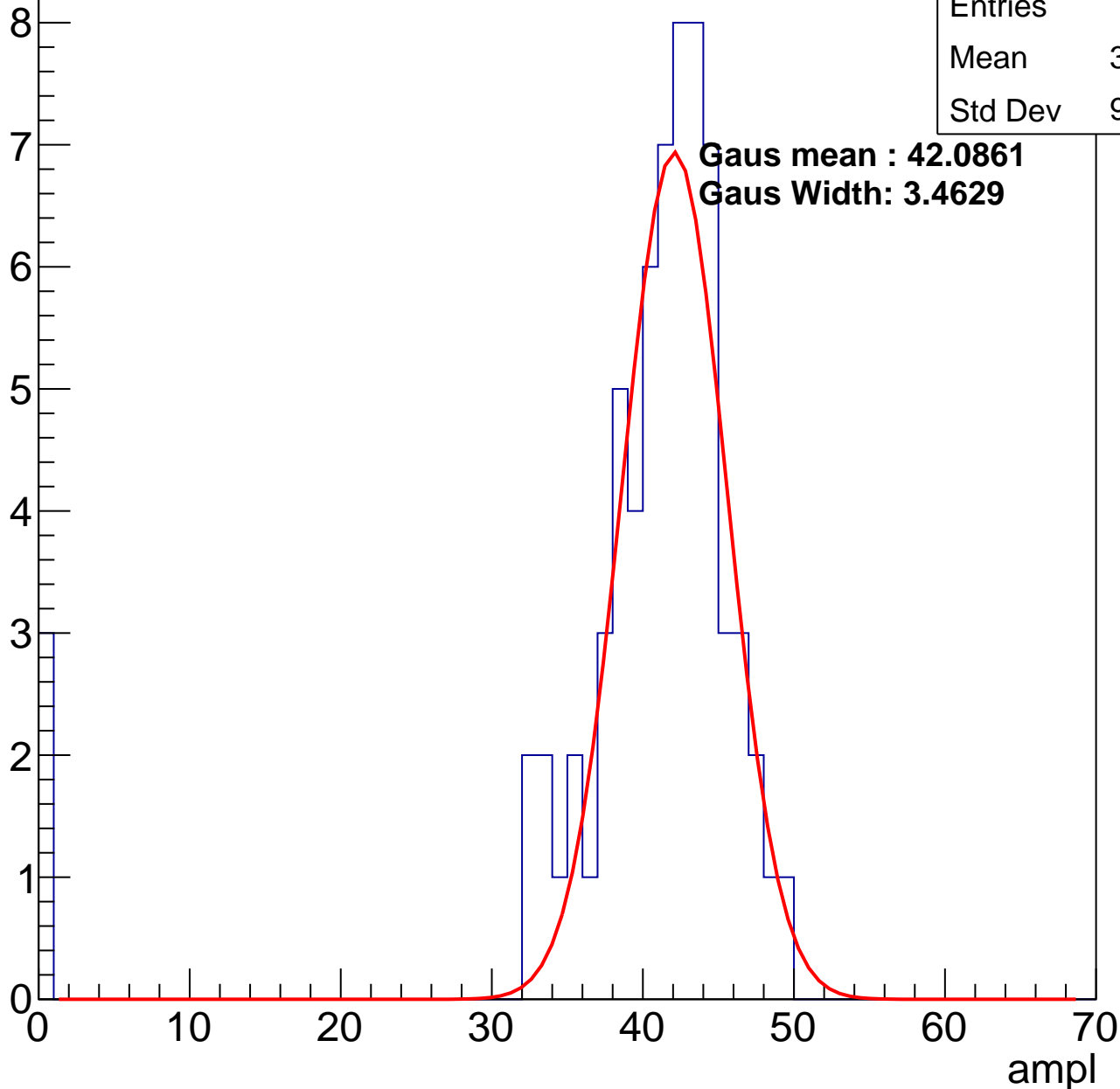
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	39.22
Std Dev	9.157

**Gaus mean : 42.0861**

**Gaus Width: 3.4629**



# B1L103S, U22-ch113, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	80
Mean	40.76
Std Dev	18.25

Entry

12

10

8

6

4

2

0

0

10

20

30

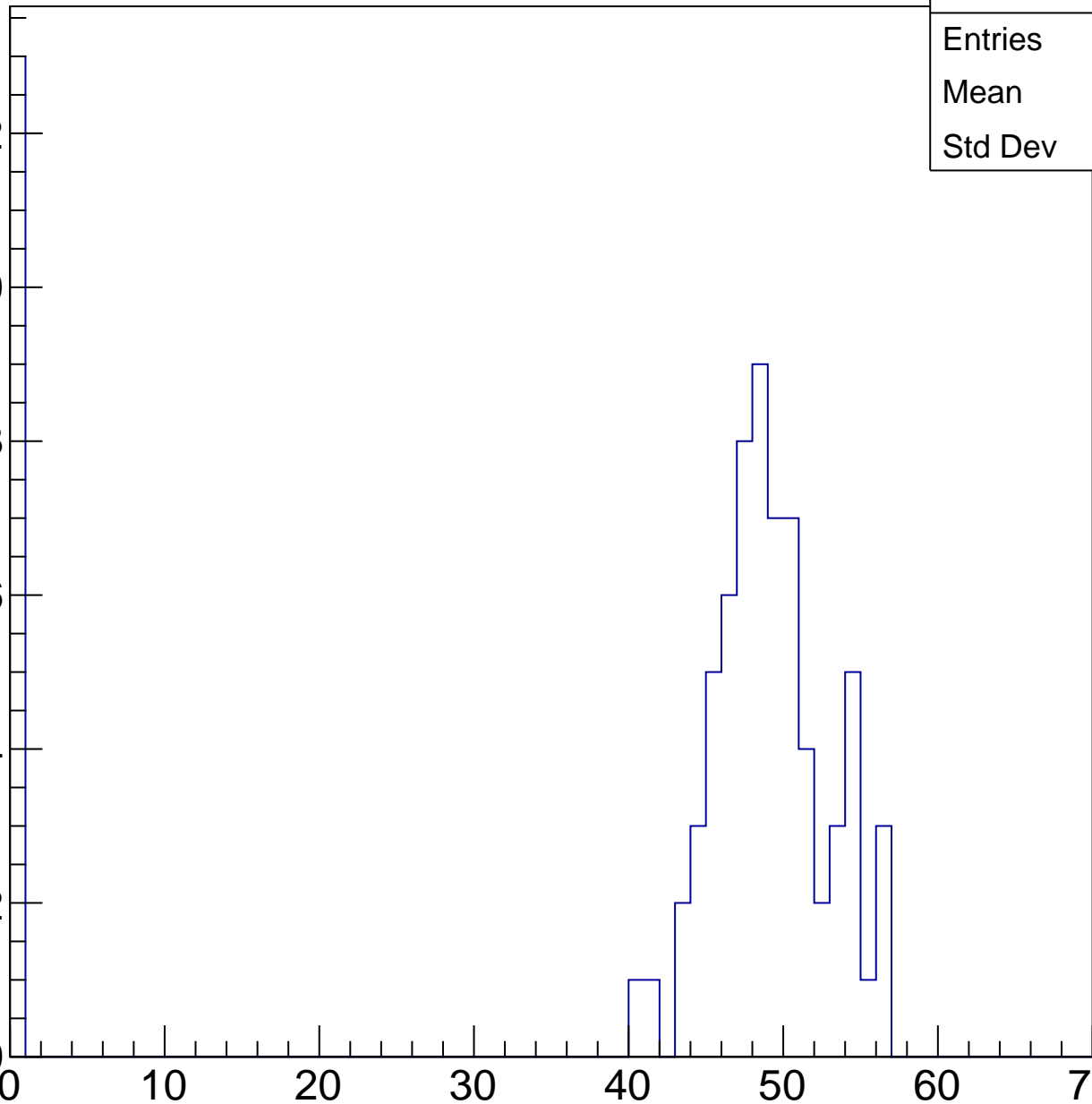
40

50

60

70

ampl

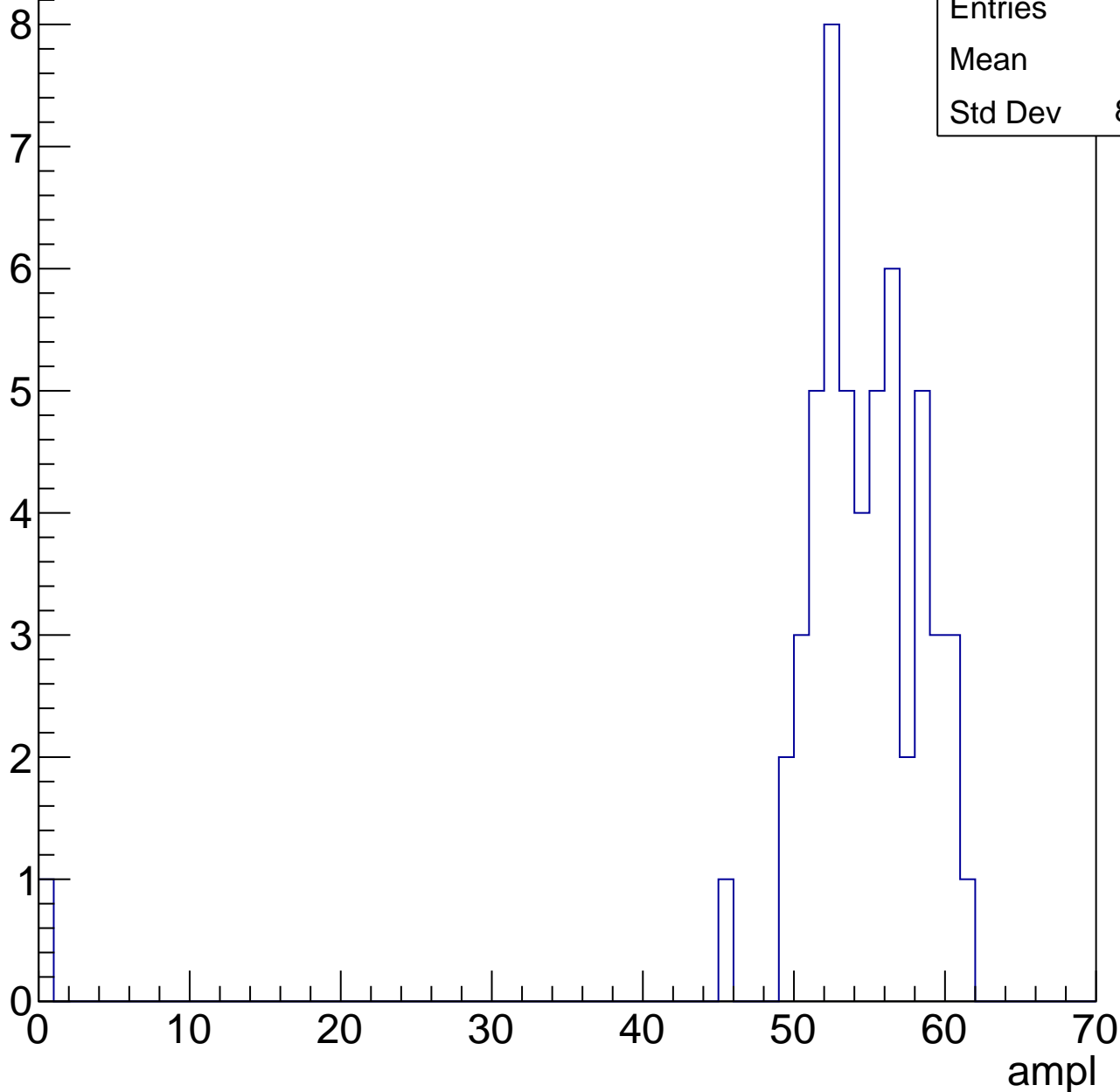


# B1L103S, U22-ch113, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.3
Std Dev	8.061

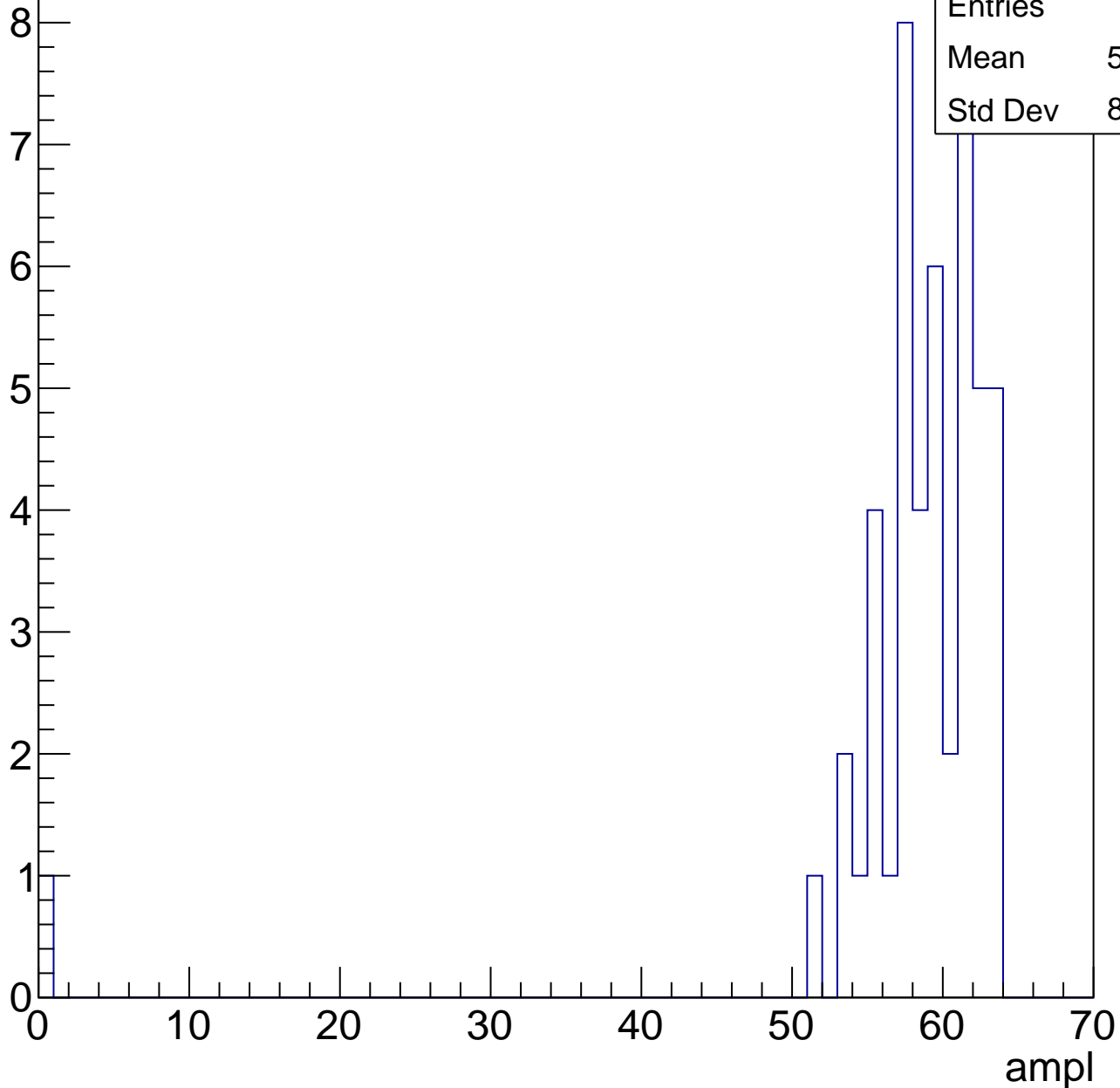


# B1L103S, U22-ch113, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	48
Mean	57.54
Std Dev	8.907

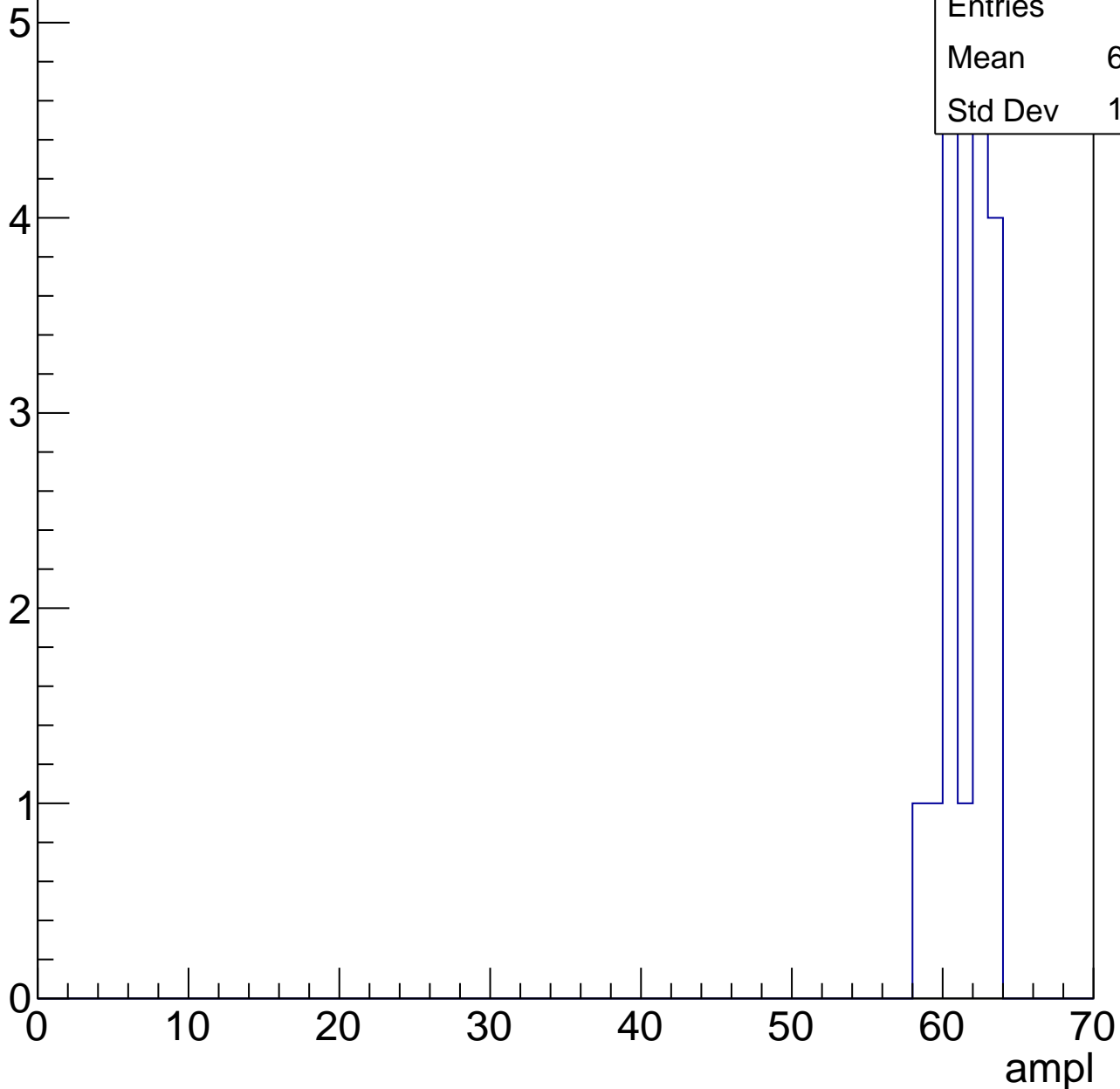


# B1L103S, U22-ch113, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	17
Mean	61.18
Std Dev	1.504





# B1L103S, U22-ch113, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



# B1L103S, U22-ch114, adc0

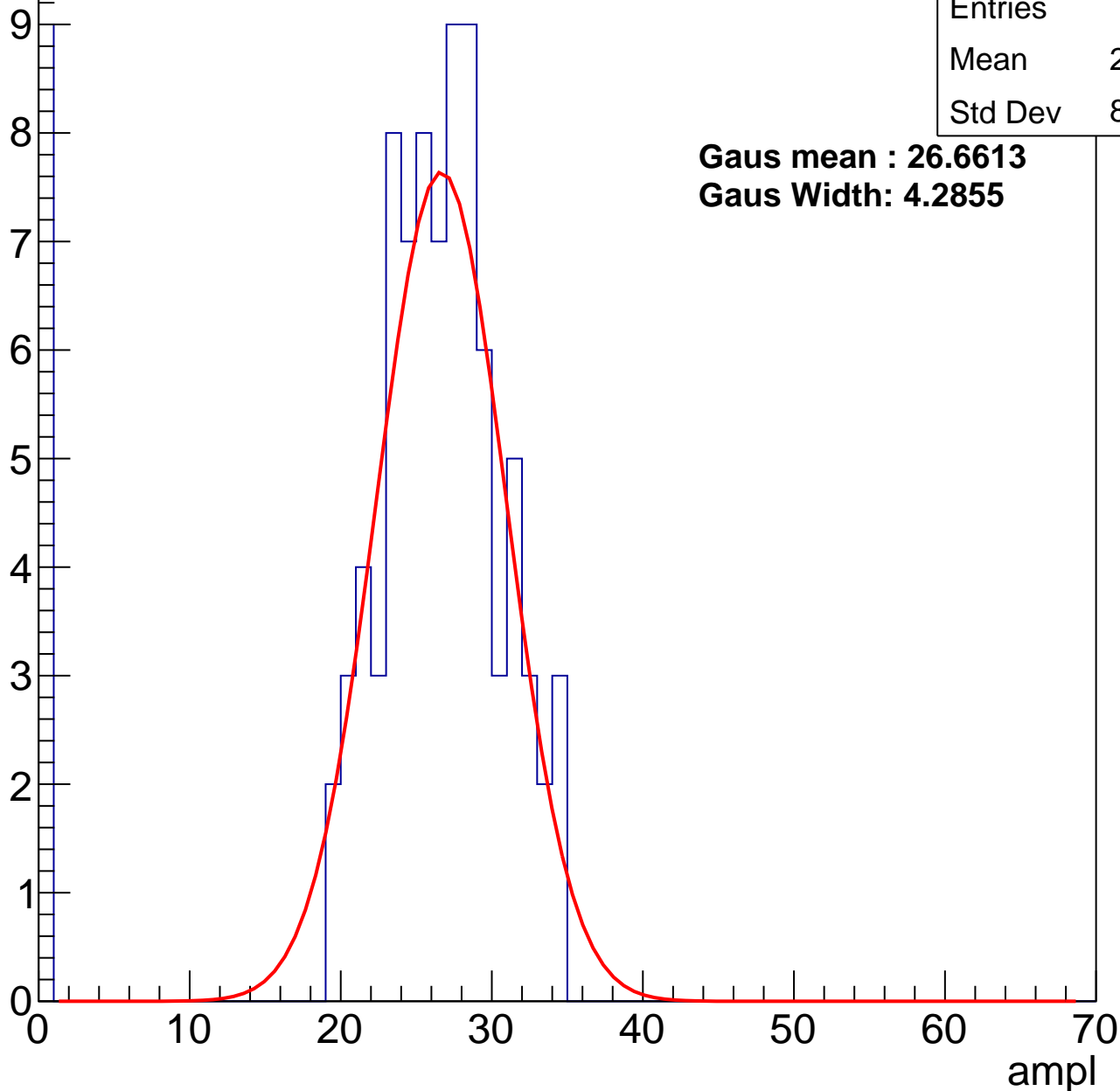
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	91
Mean	23.74
Std Dev	8.612

**Gaus mean : 26.6613**

**Gaus Width: 4.2855**



# B1L103S, U22-ch114, adc1

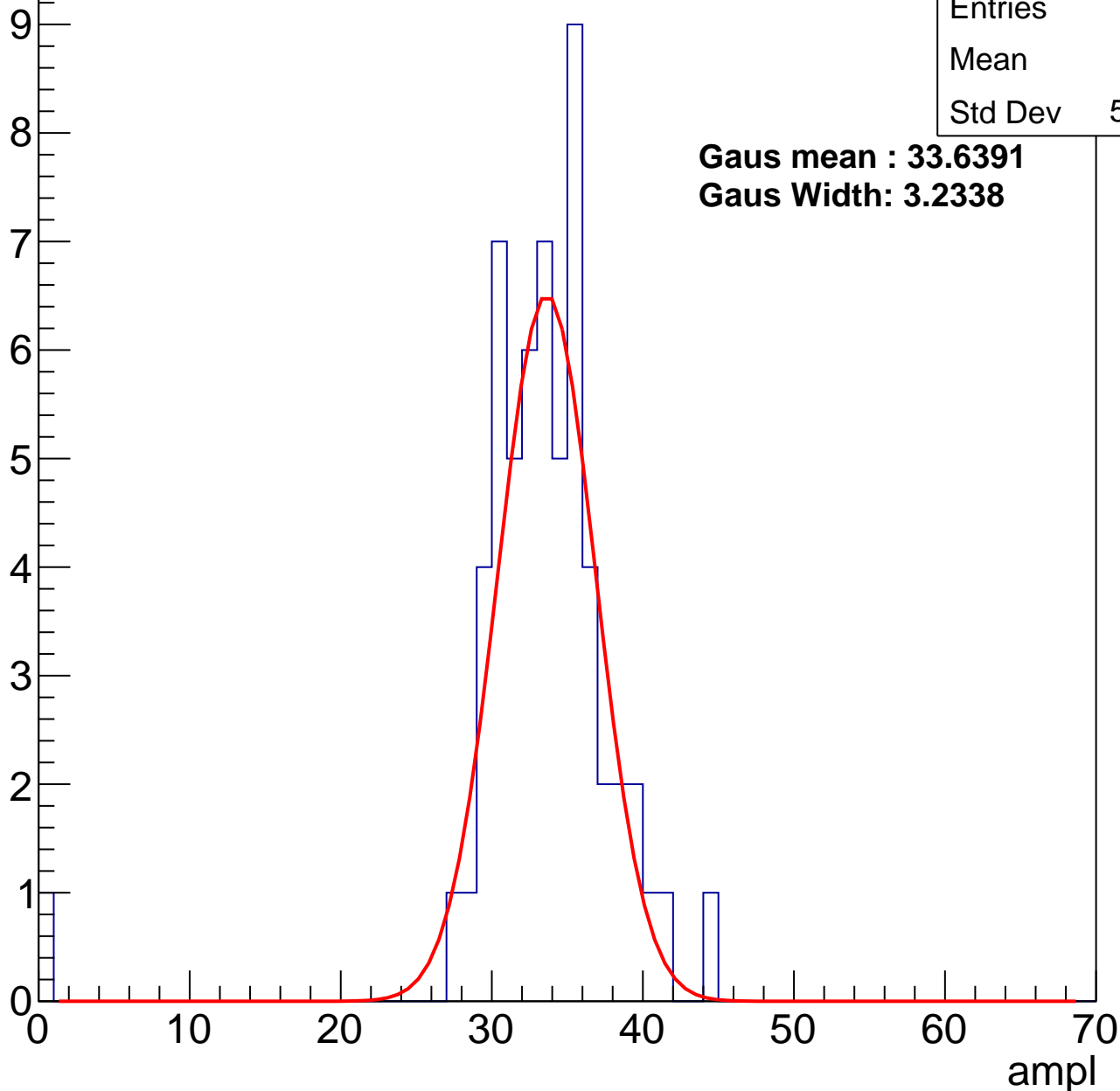
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	32.9
Std Dev	5.473

**Gaus mean : 33.6391**

**Gaus Width: 3.2338**



# B1L103S, U22-ch114, adc2

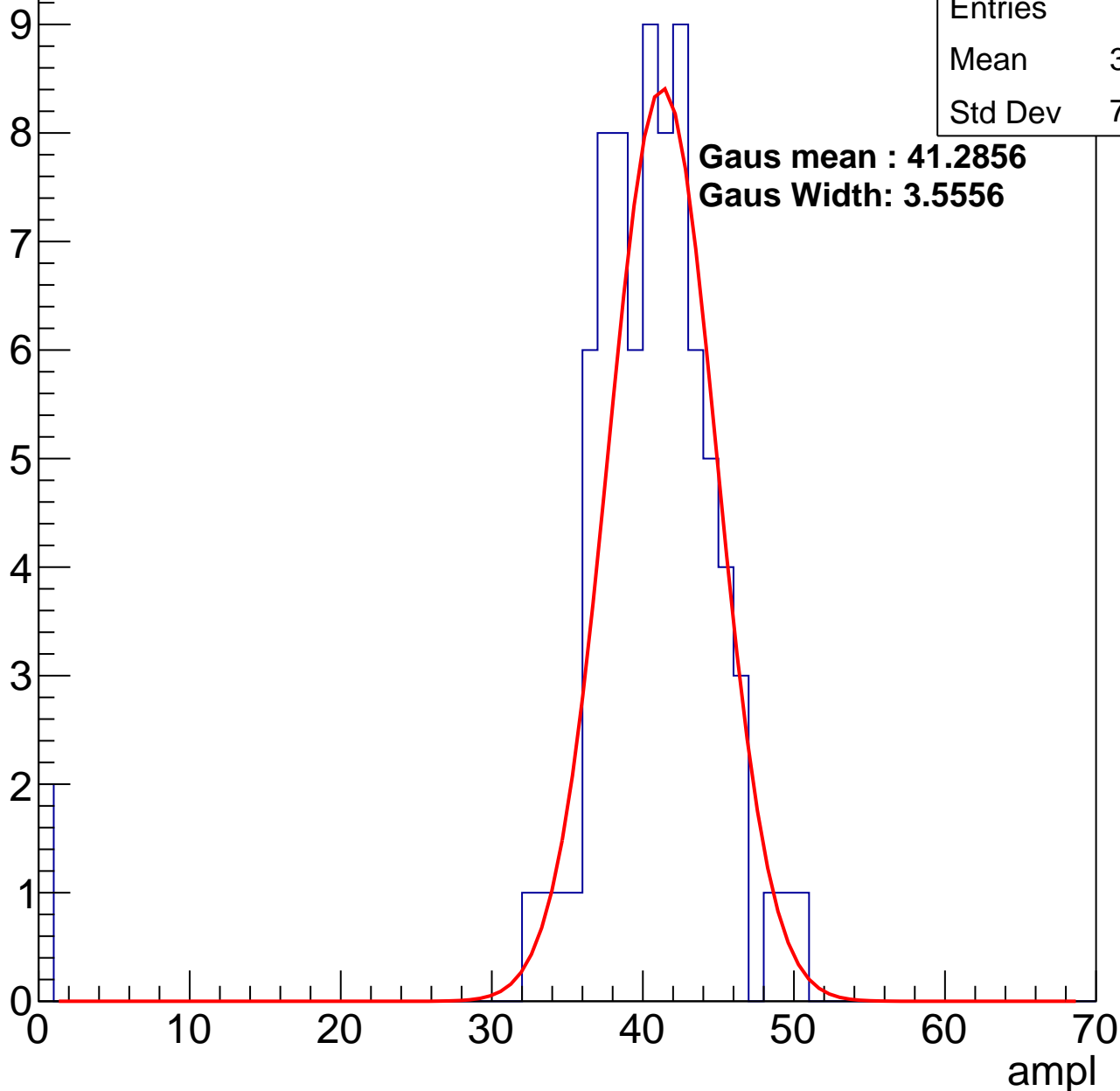
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	81
Mean	39.42
Std Dev	7.187

**Gaus mean : 41.2856**

**Gaus Width: 3.5556**



# B1L103S, U22-ch114, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	66
Mean	39.06
Std Dev	17.82

Entry

10

8

6

4

2

0

0

10

20

30

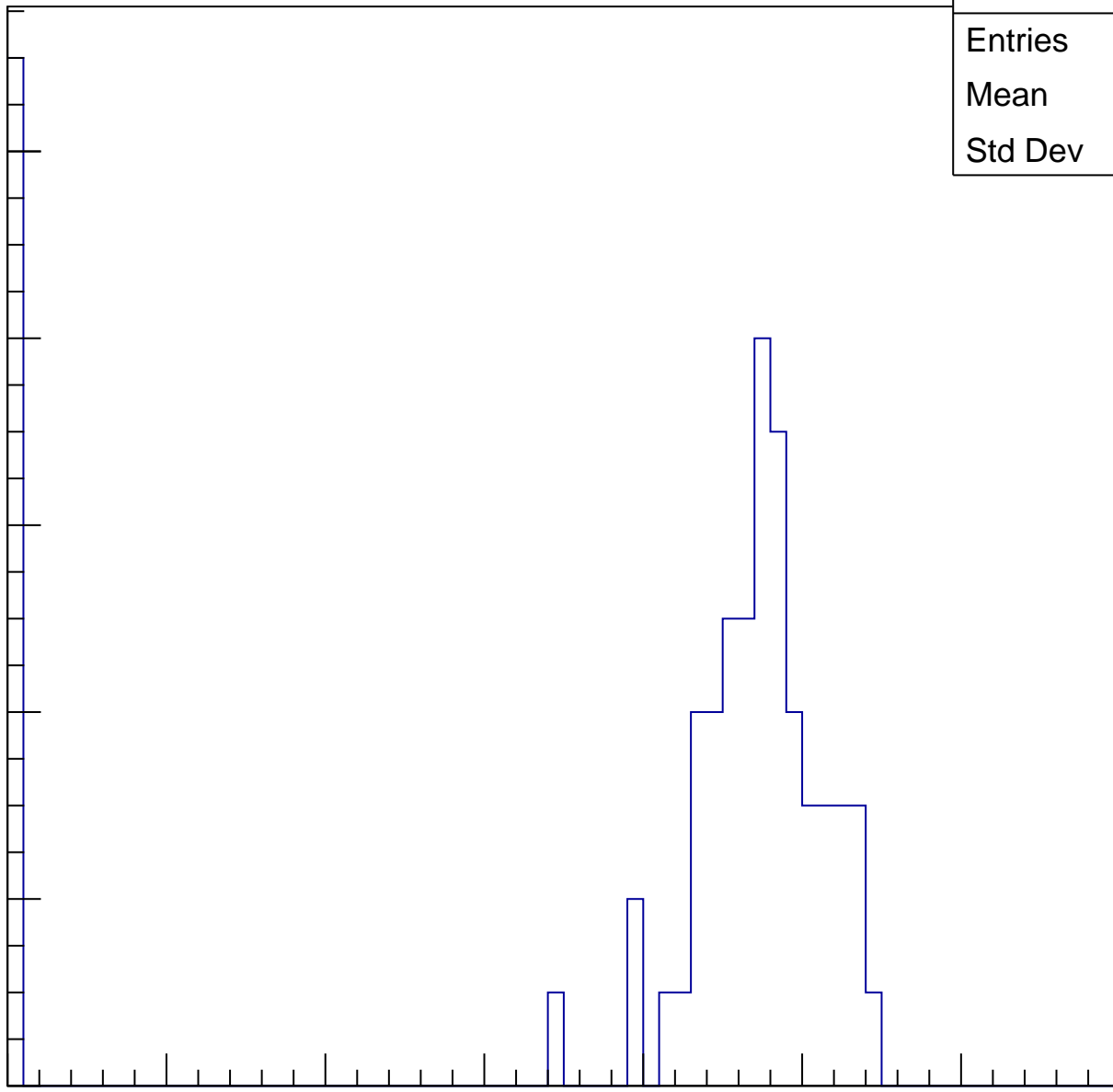
40

50

60

70

ampl

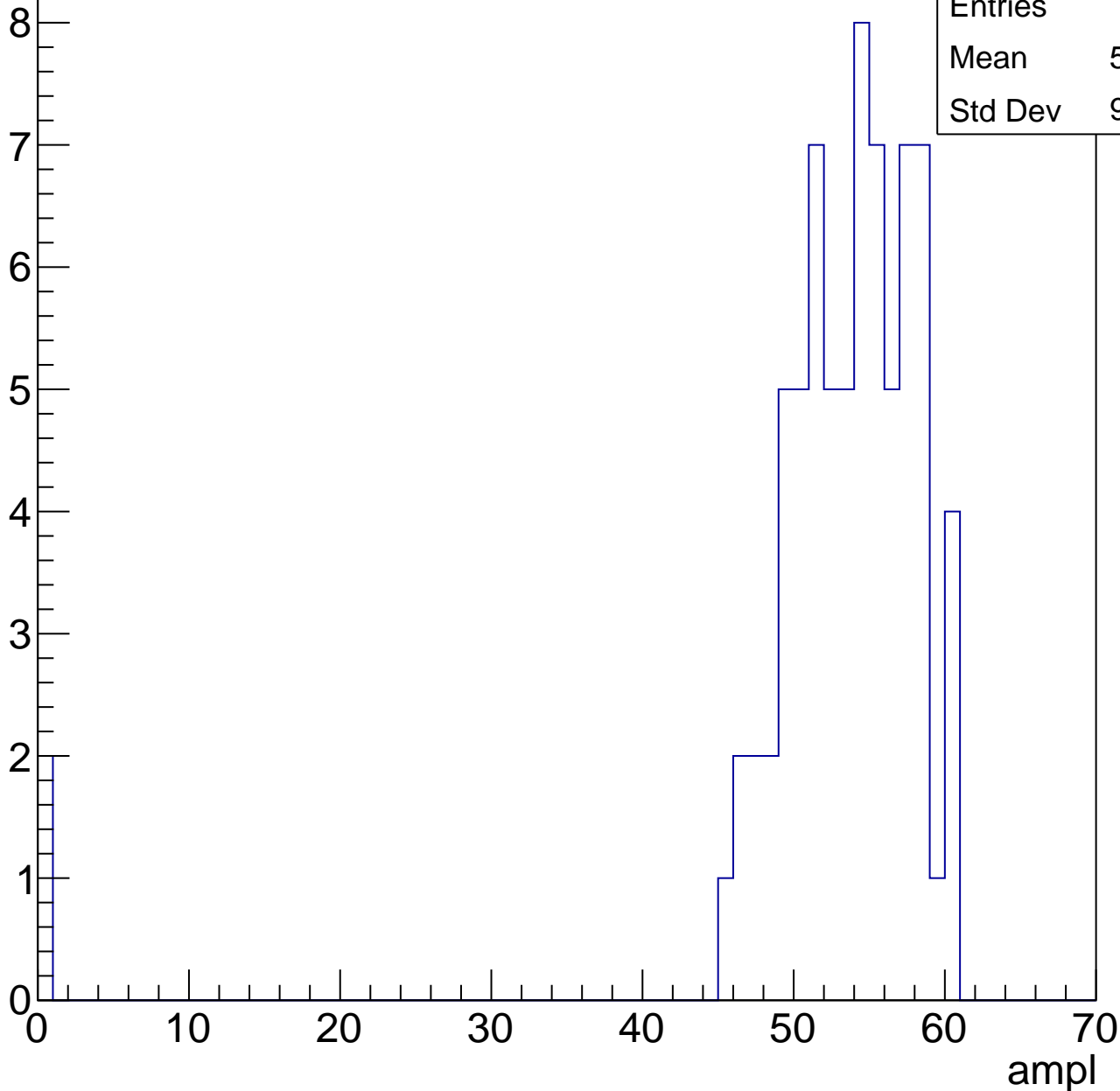


# B1L103S, U22-ch114, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	52.07
Std Dev	9.377

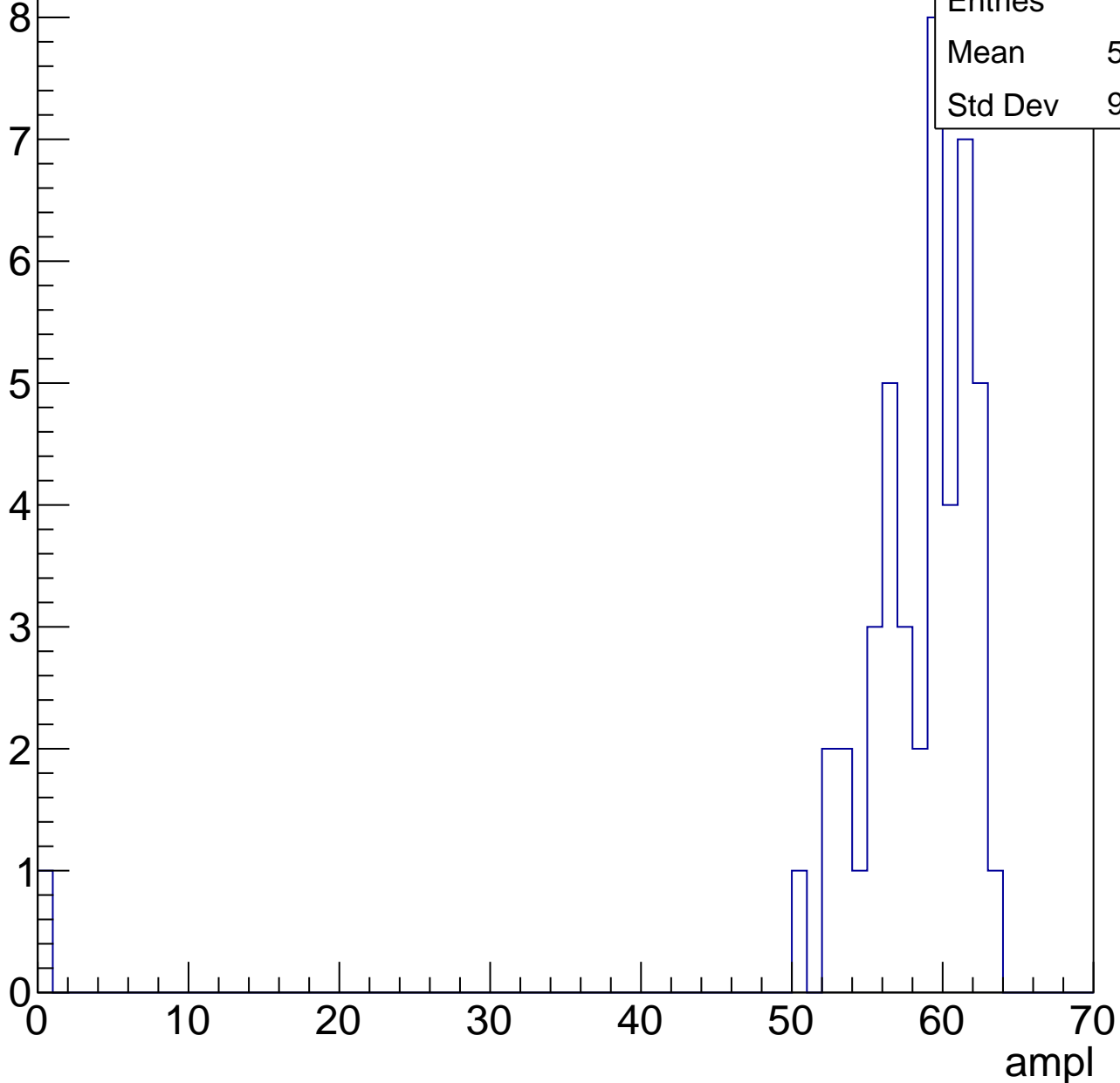


# B1L103S, U22-ch114, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	45
Mean	56.84
Std Dev	9.126

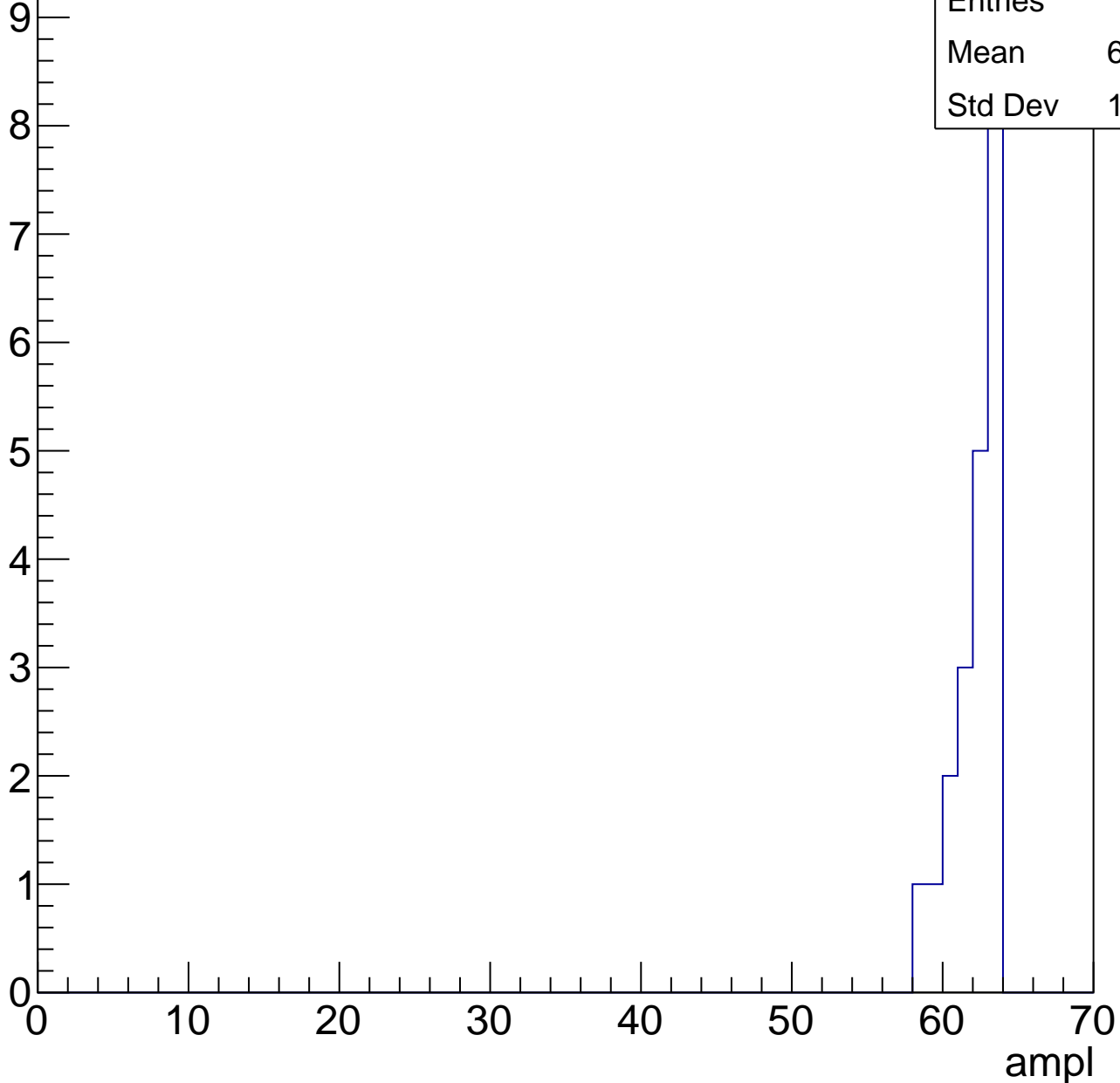


# B1L103S, U22-ch114, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	21
Mean	61.76
Std Dev	1.444





# B1L103S, U22-ch114, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	18
Mean	2.778
Std Dev	11.45

Entry

16  
14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

# B1L103S, U22-ch115, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	87
Mean	24.36
Std Dev	9.961

**Gaus mean : 28.0653**

**Gaus Width: 4.0371**

Entry

10

8

6

4

2

0

0

10

20

30

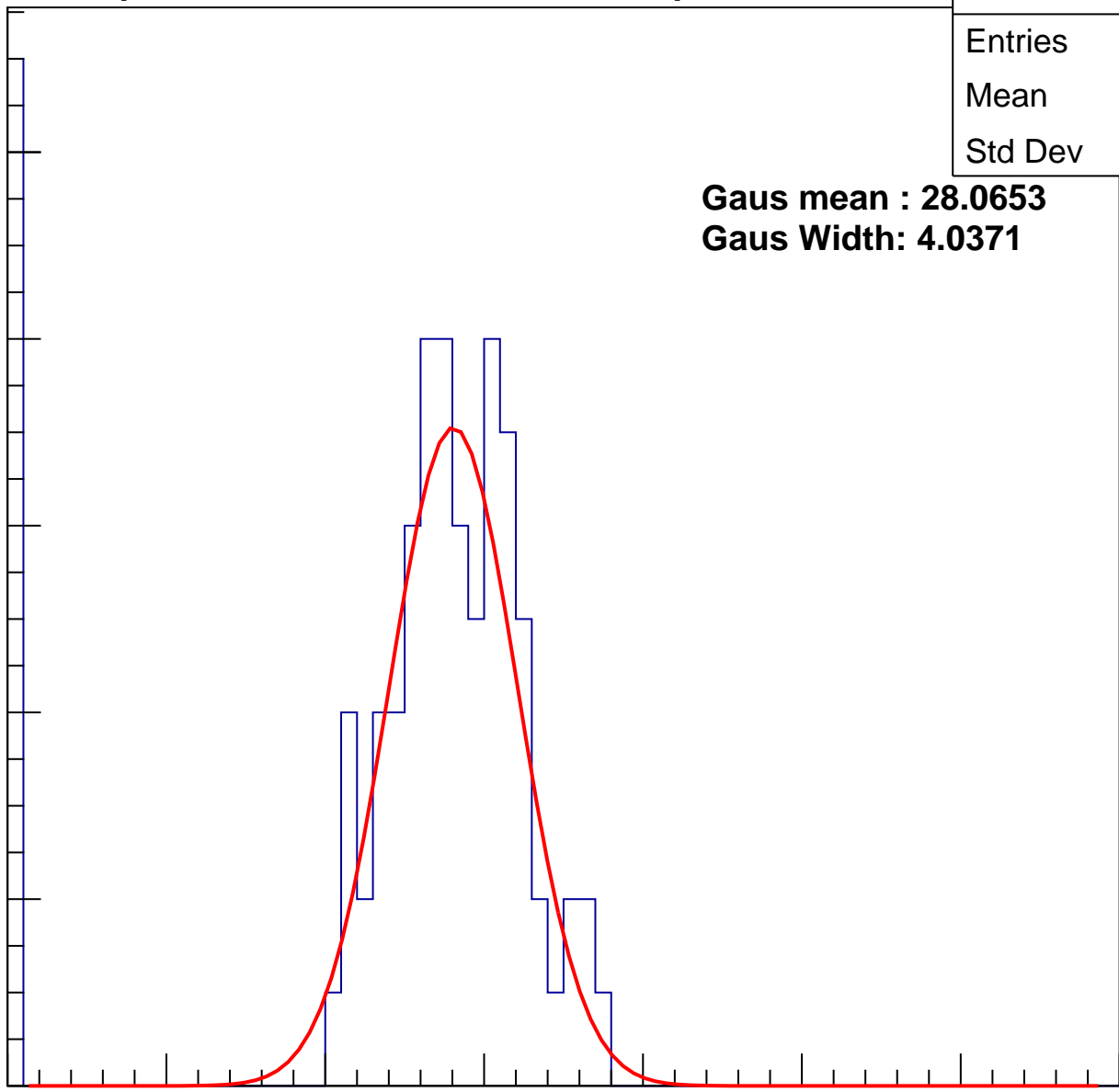
40

50

60

70

ampl



# B1L103S, U22-ch115, adc1

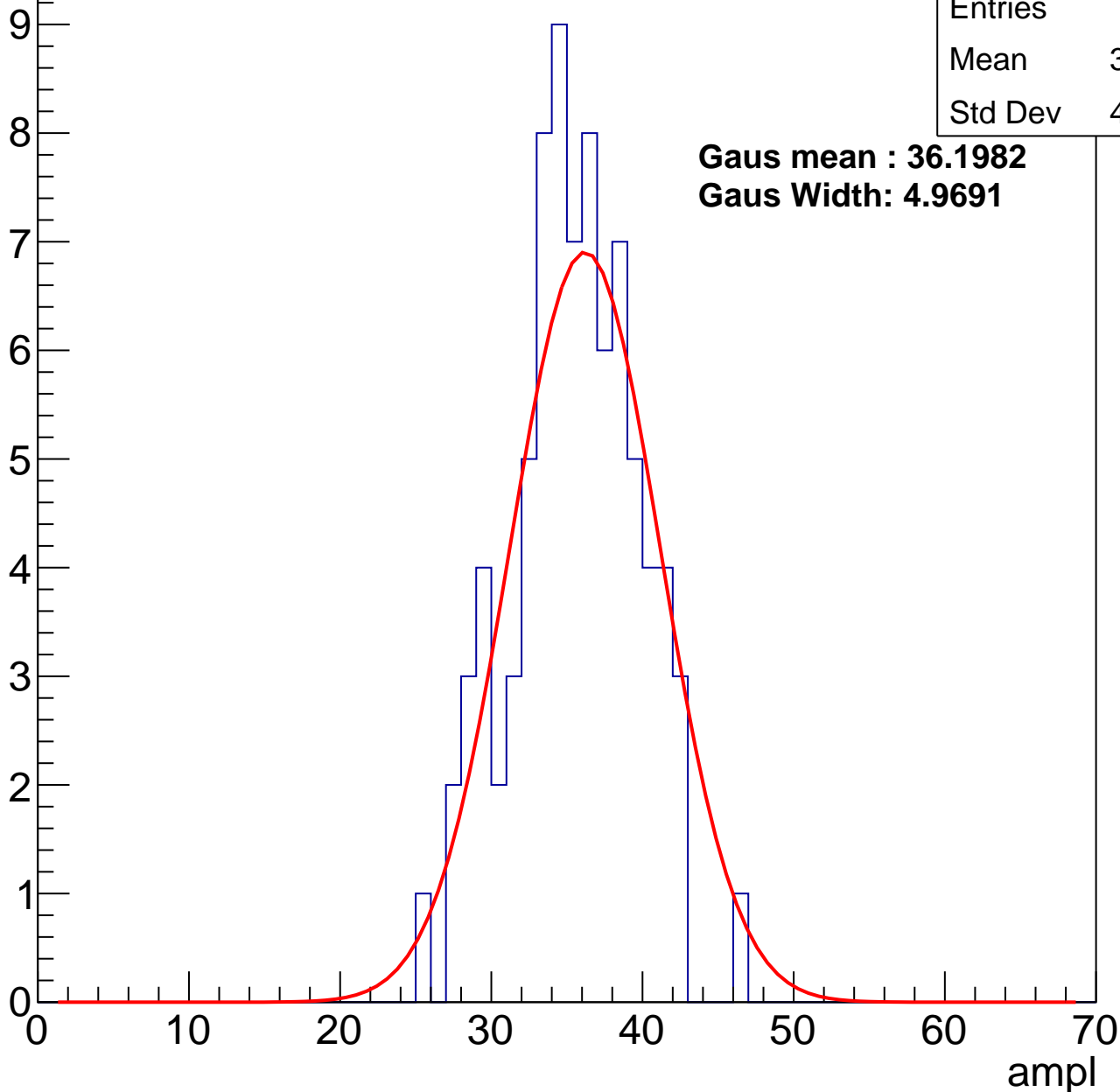
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	82
Mean	35.05
Std Dev	4.117

**Gaus mean : 36.1982**

**Gaus Width: 4.9691**



# B1L103S, U22-ch115, adc2

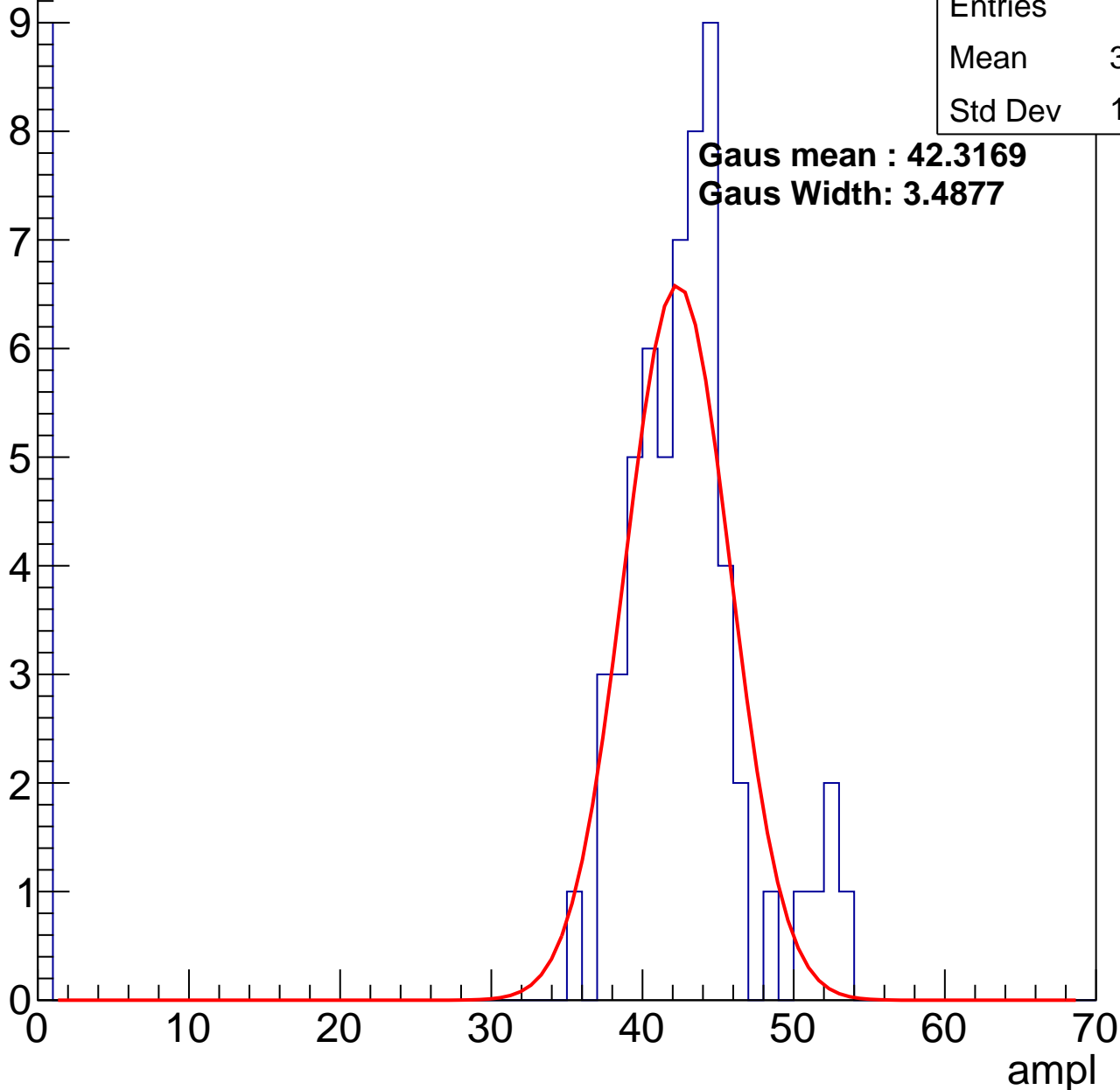
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	68
Mean	36.94
Std Dev	14.85

**Gaus mean : 42.3169**

**Gaus Width: 3.4877**



# B1L103S, U22-ch115, adc3

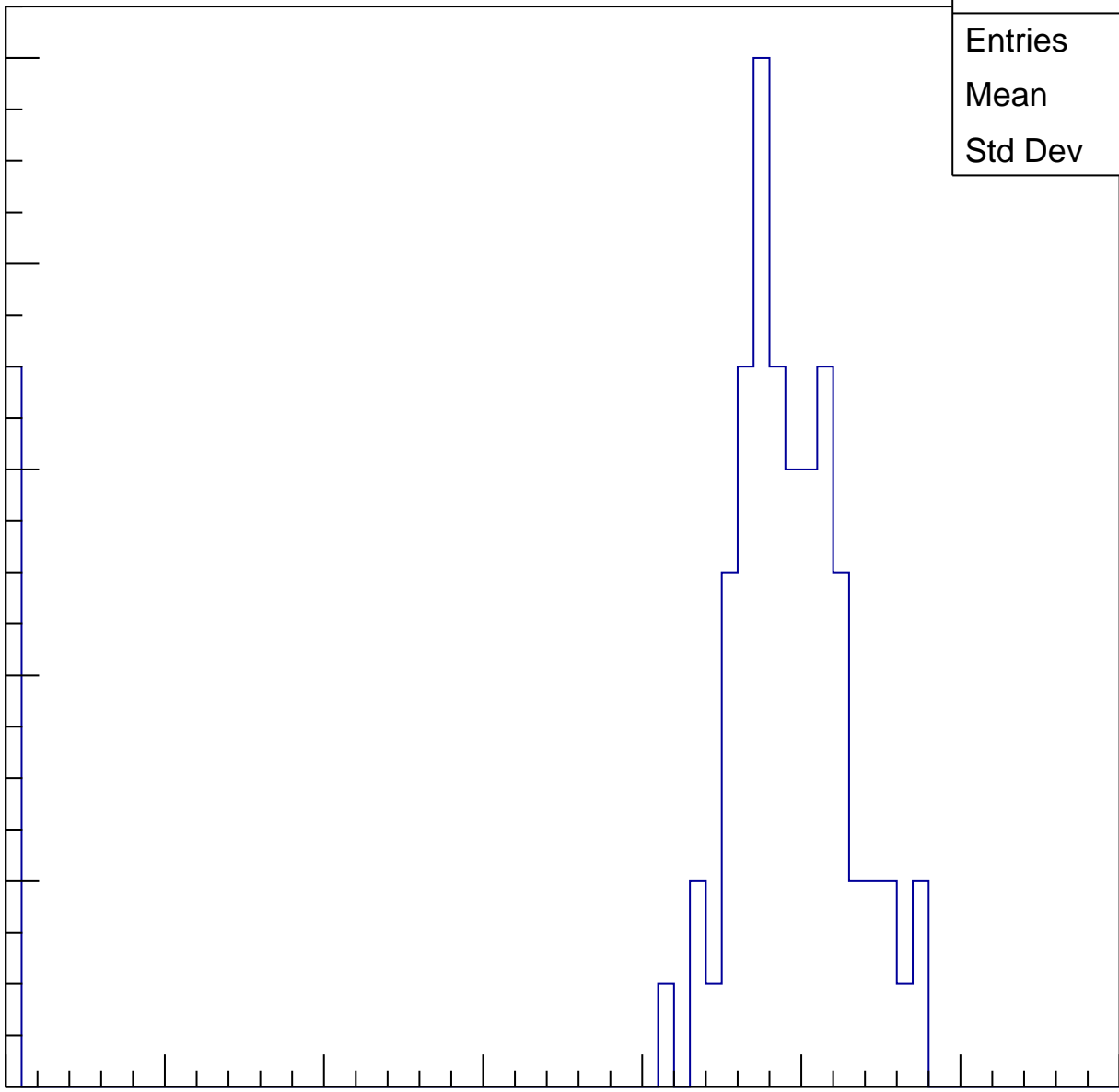
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	73
Mean	44.23
Std Dev	14.76

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70  
ampl

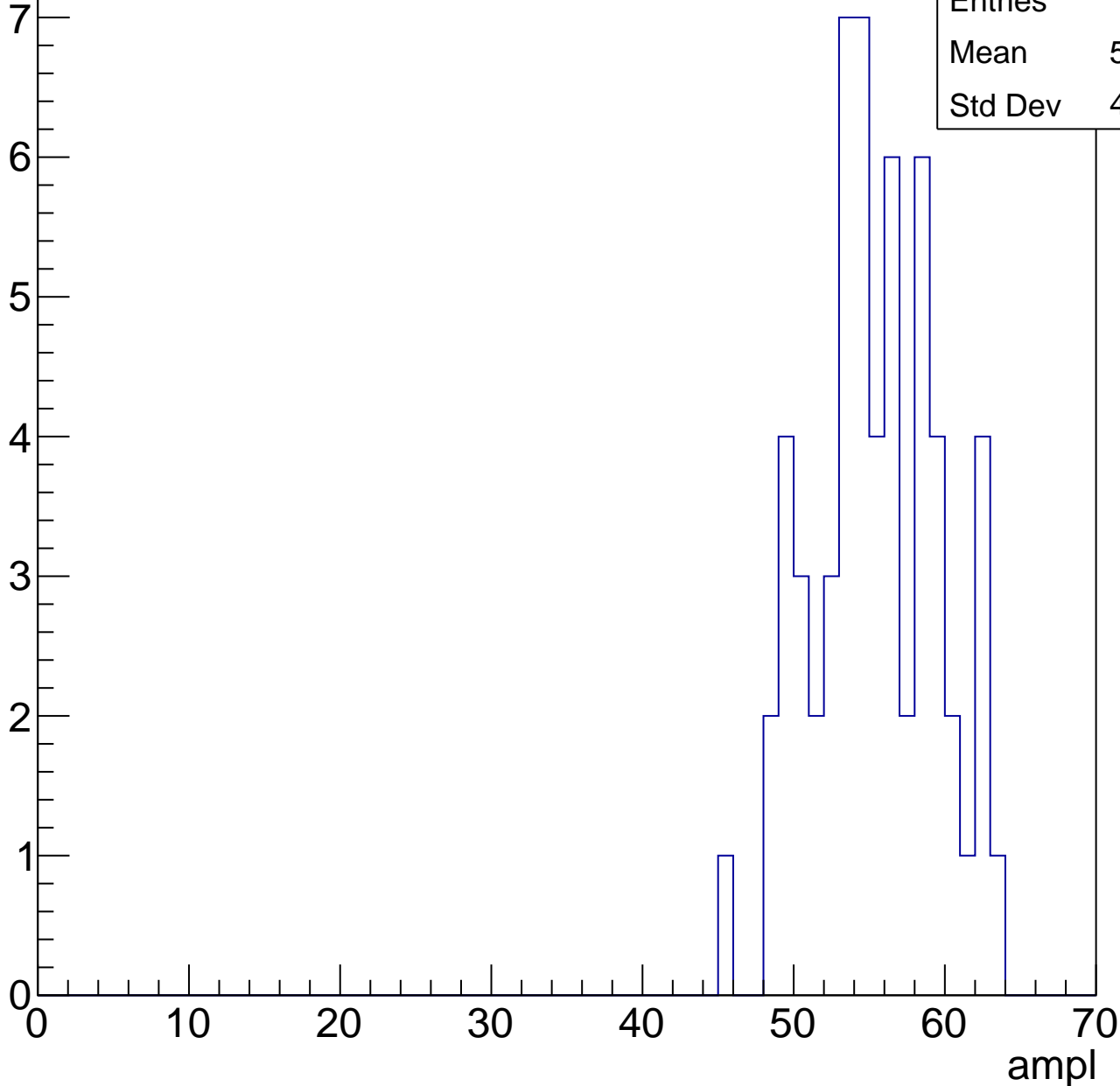


# B1L103S, U22-ch115, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	59
Mean	54.92
Std Dev	4.093

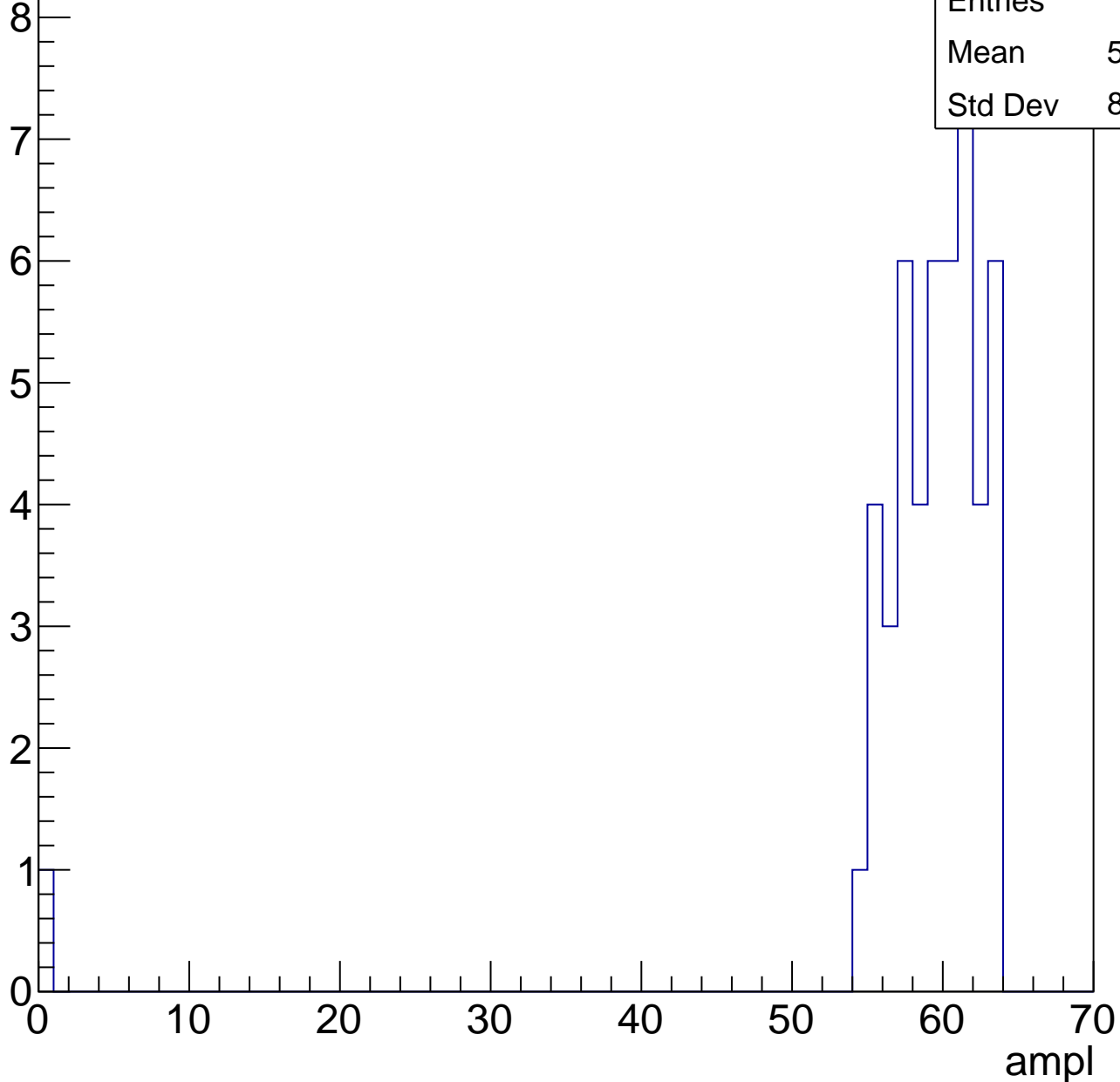


# B1L103S, U22-ch115, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

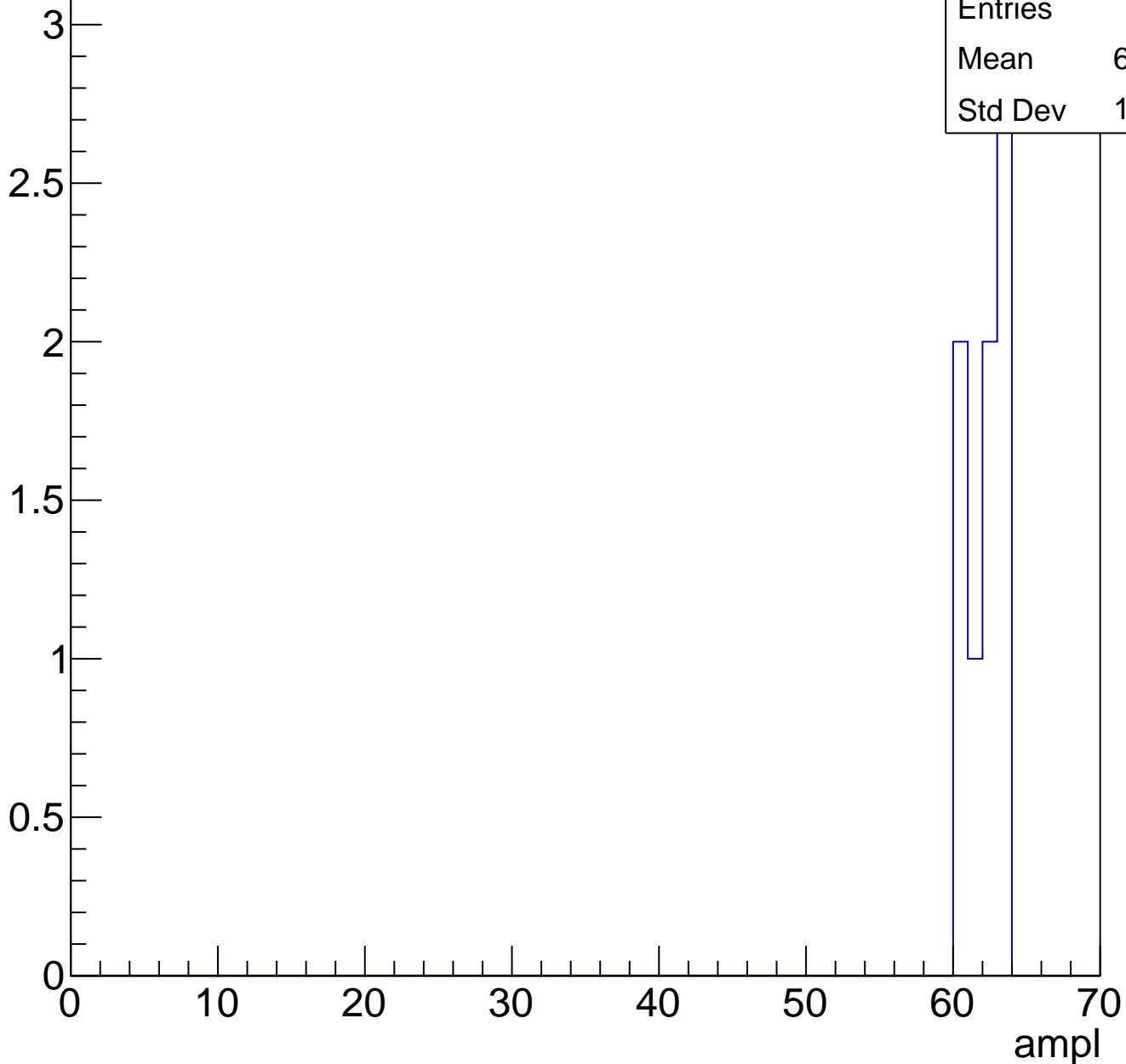
Entries	49
Mean	58.04
Std Dev	8.748



# B1L103S, U22-ch115, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



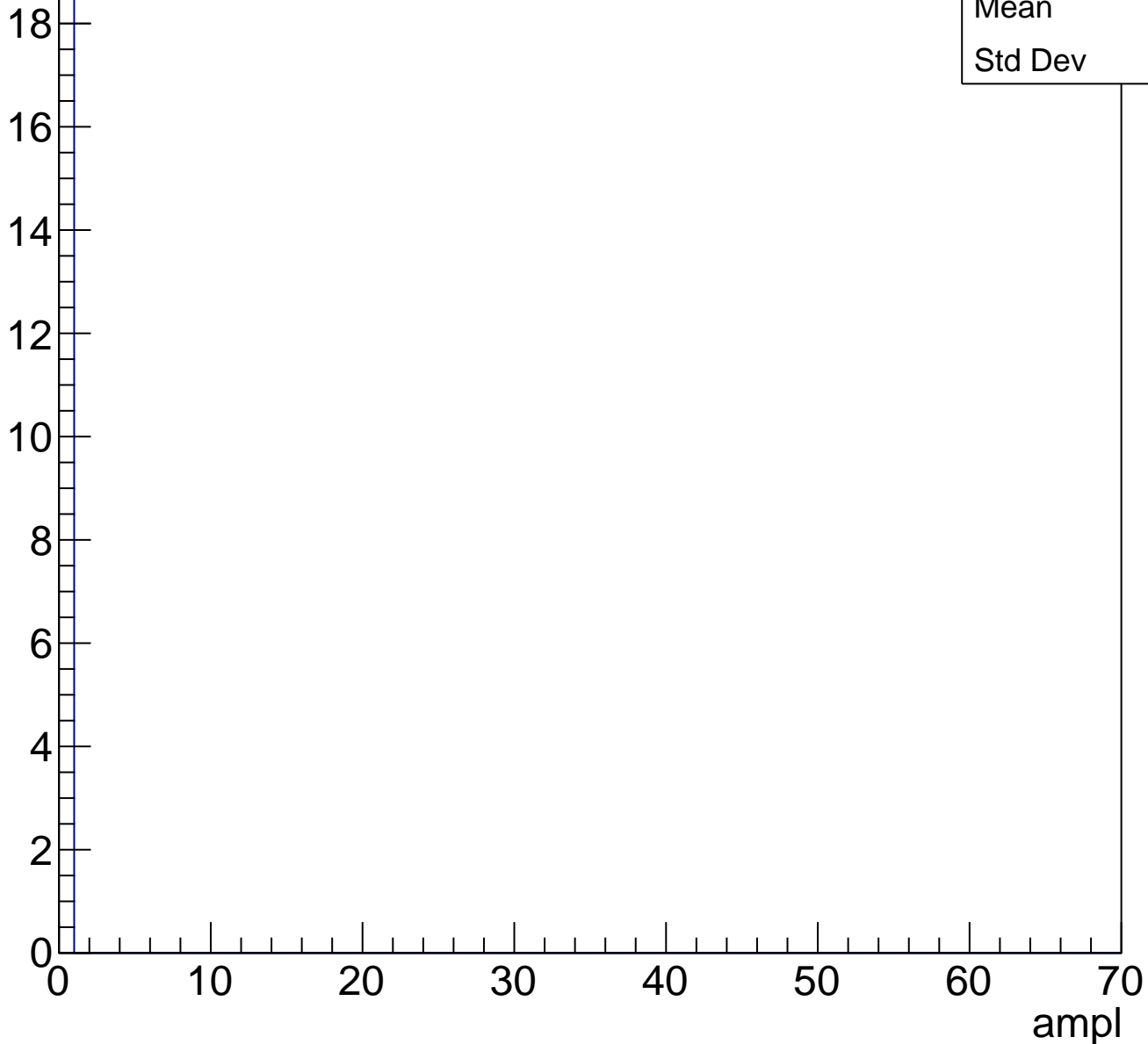


# B1L103S, U22-ch115, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	19
Mean	0
Std Dev	0

Entry



# B1L103S, U22-ch116, adc0

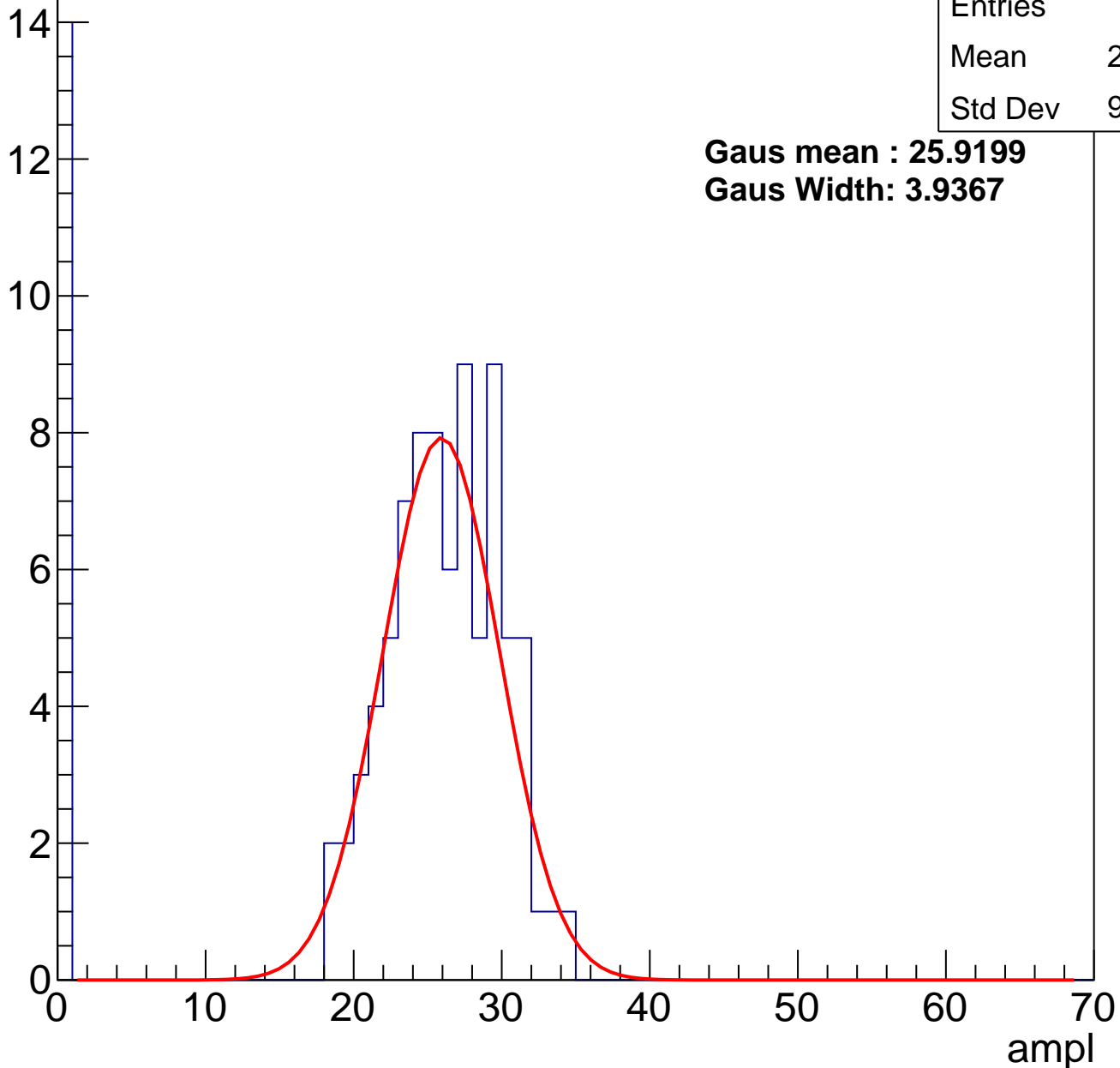
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	95
Mean	21.95
Std Dev	9.725

**Gaus mean : 25.9199**

**Gaus Width: 3.9367**

Entry



# B1L103S, U22-ch116, adc1

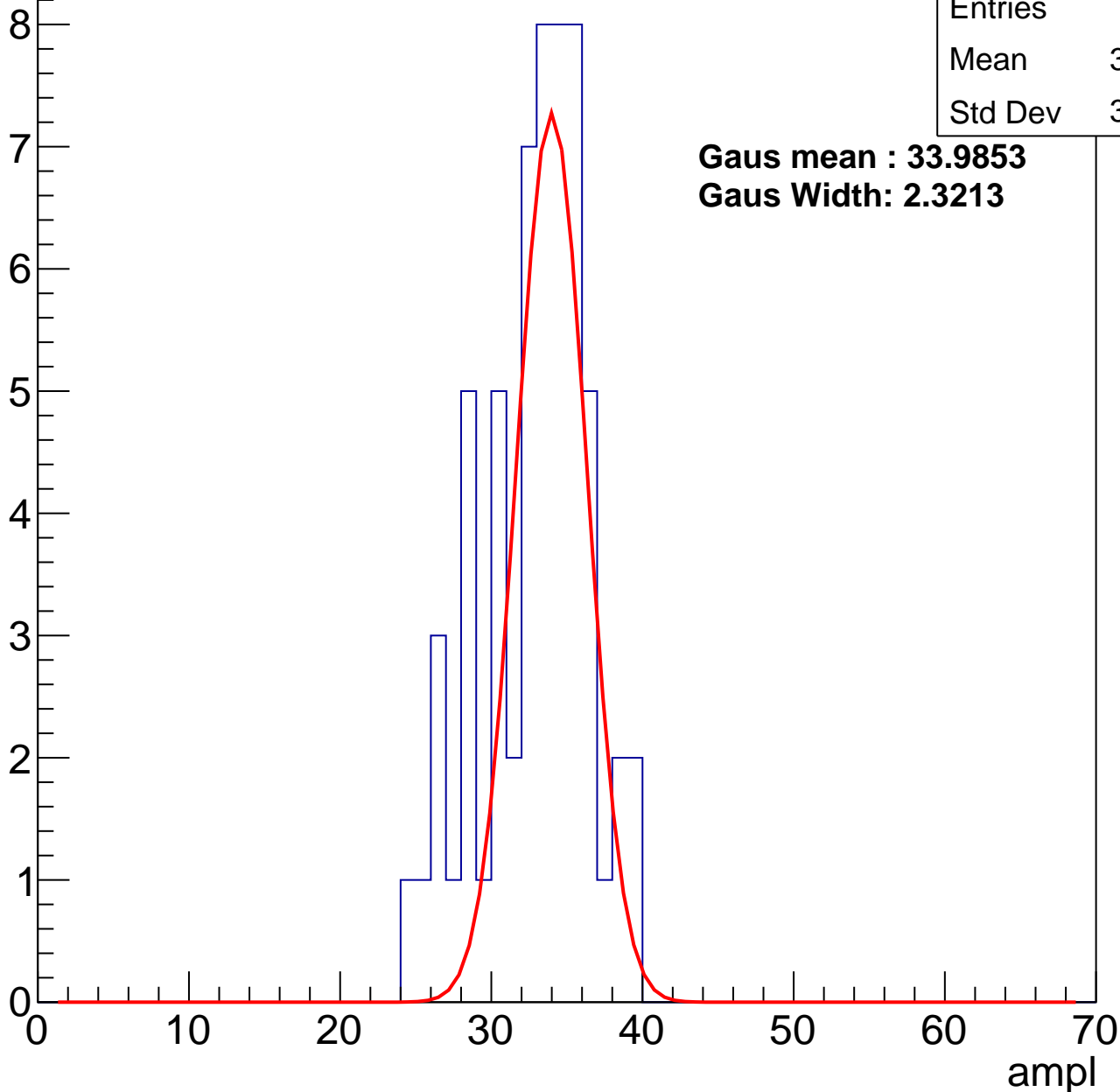
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	32.43
Std Dev	3.495

**Gaus mean : 33.9853**

**Gaus Width: 2.3213**



# B1L103S, U22-ch116, adc2

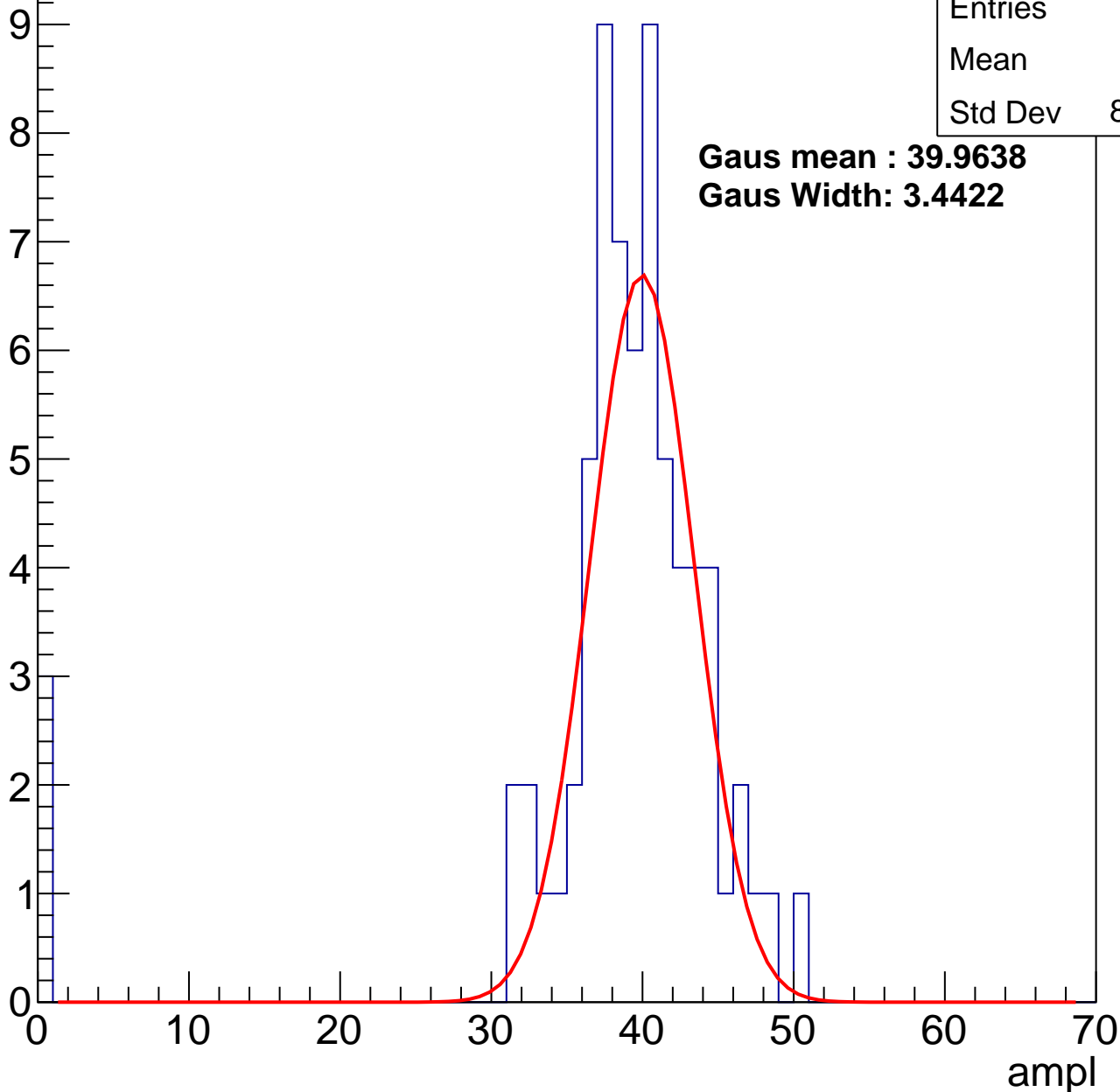
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	37.7
Std Dev	8.848

**Gaus mean : 39.9638**

**Gaus Width: 3.4422**

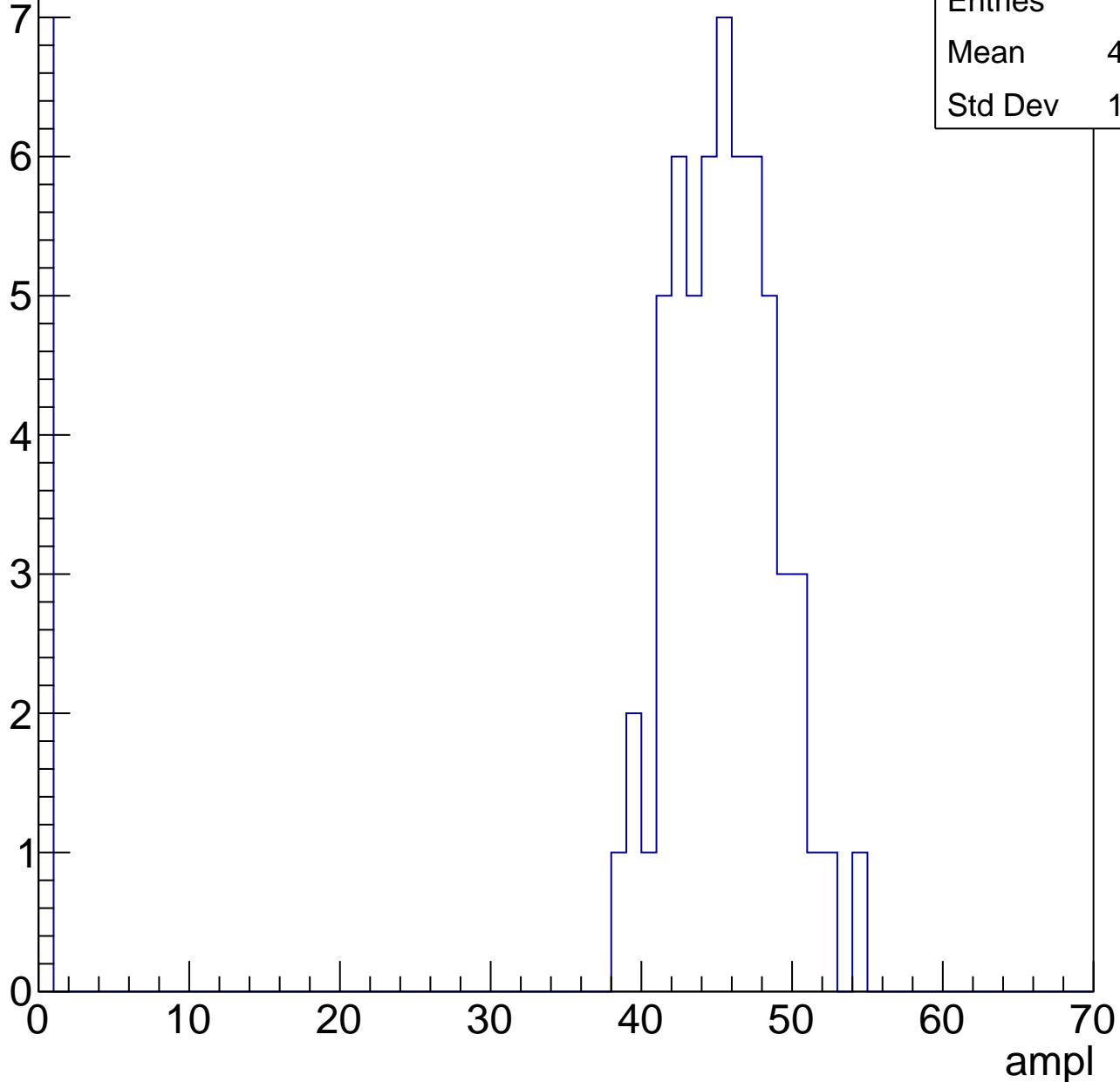


# B1L103S, U22-ch116, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	66
Mean	40.29
Std Dev	14.24

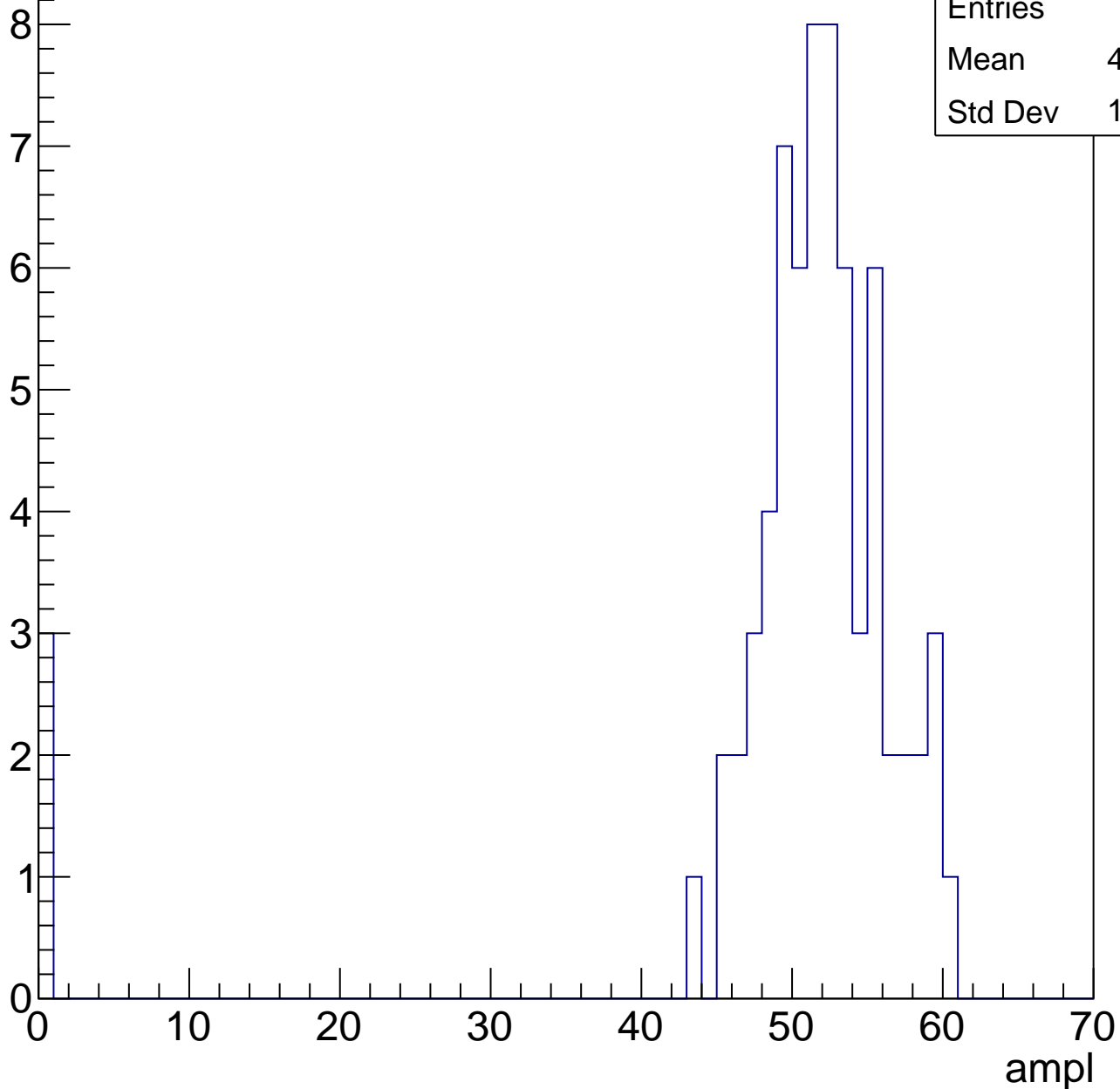


# B1L103S, U22-ch116, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	69
Mean	49.48
Std Dev	11.16

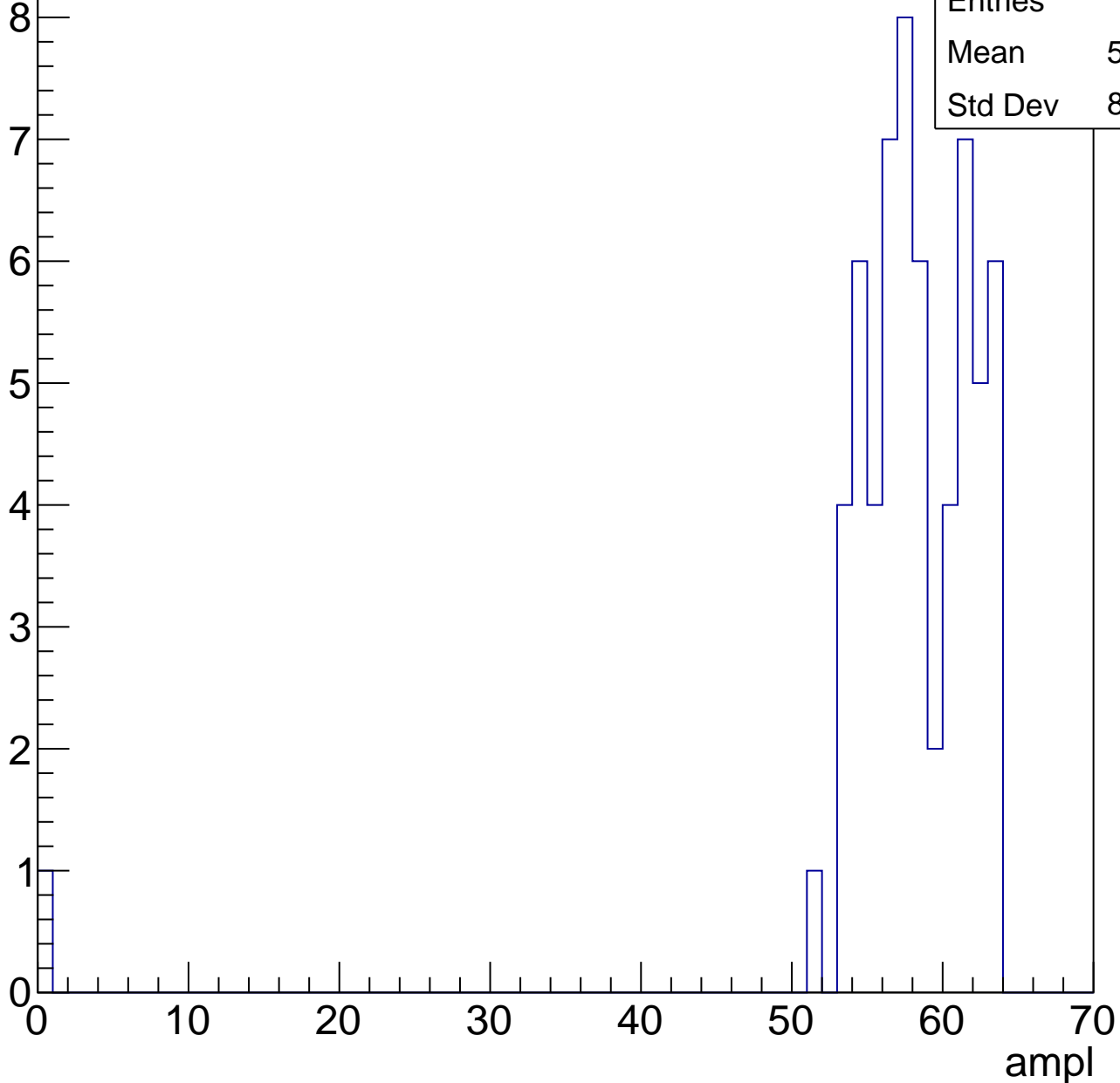


# B1L103S, U22-ch116, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	56.98
Std Dev	8.026

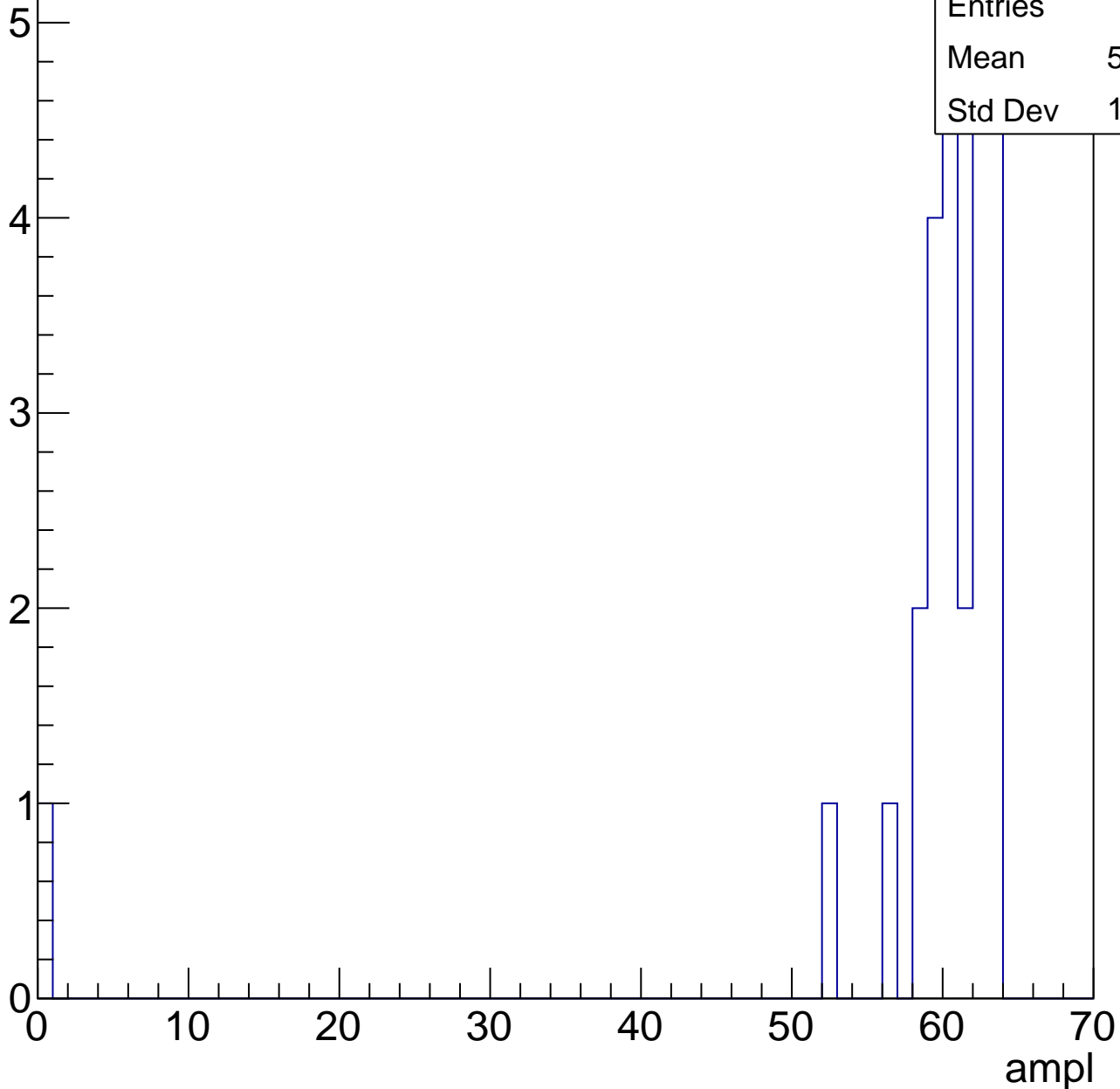


# B1L103S, U22-ch116, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	26
Mean	57.96
Std Dev	11.85

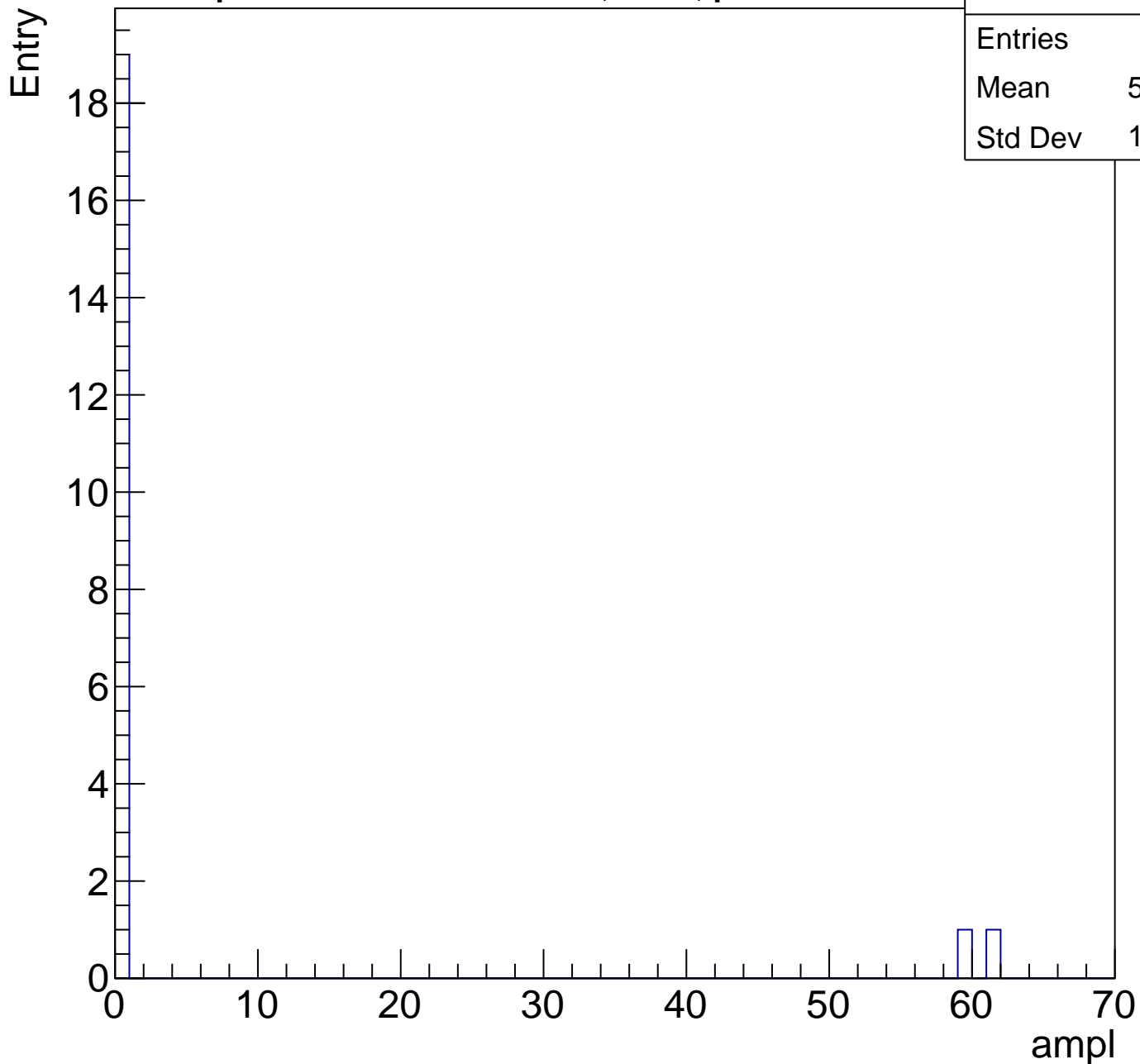




# B1L103S, U22-ch116, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.714
Std Dev	17.62



# B1L103S, U22-ch117, adc0

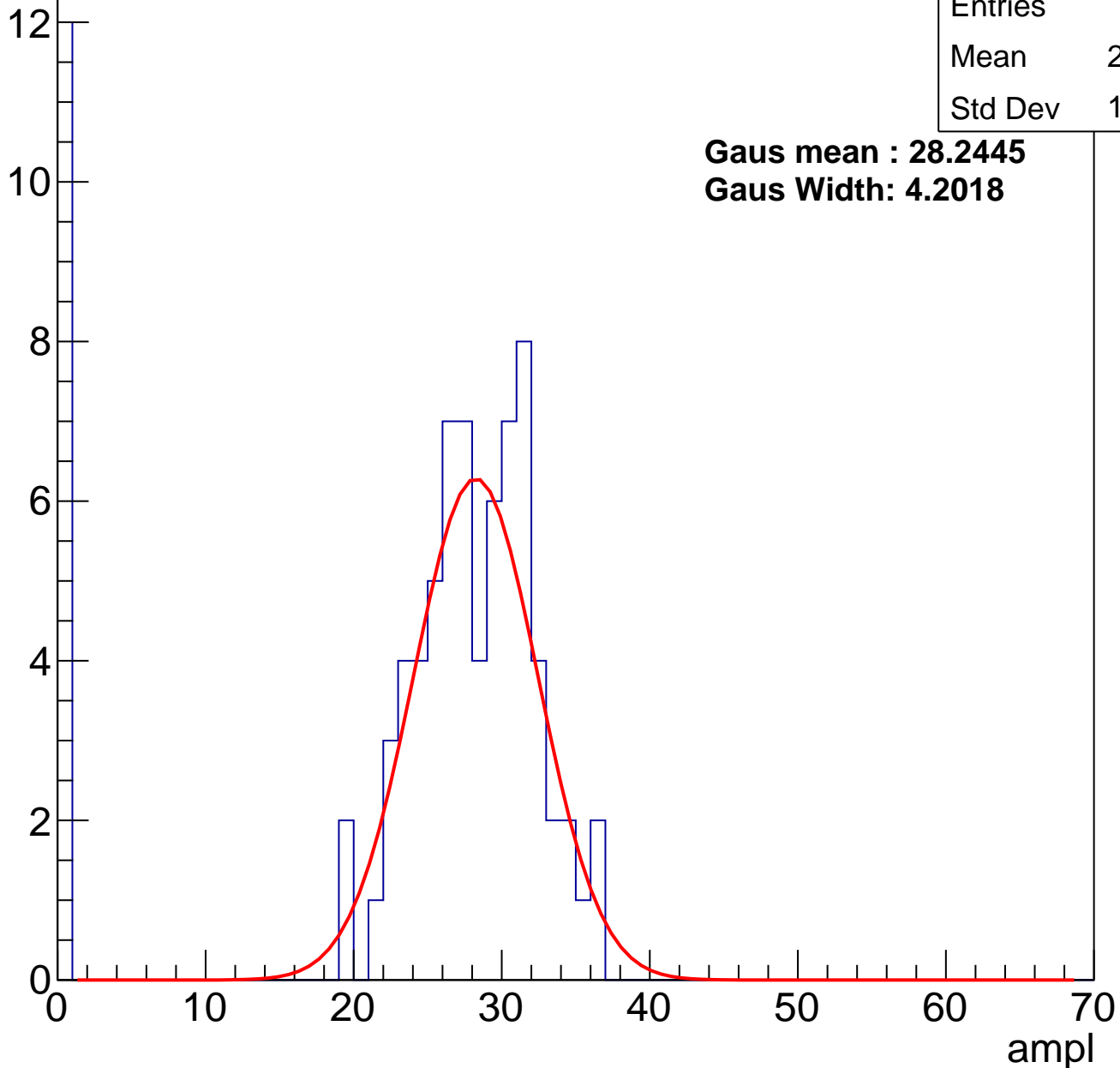
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	23.73
Std Dev	10.52

**Gaus mean : 28.2445**

**Gaus Width: 4.2018**

Entry



# B1L103S, U22-ch117, adc1

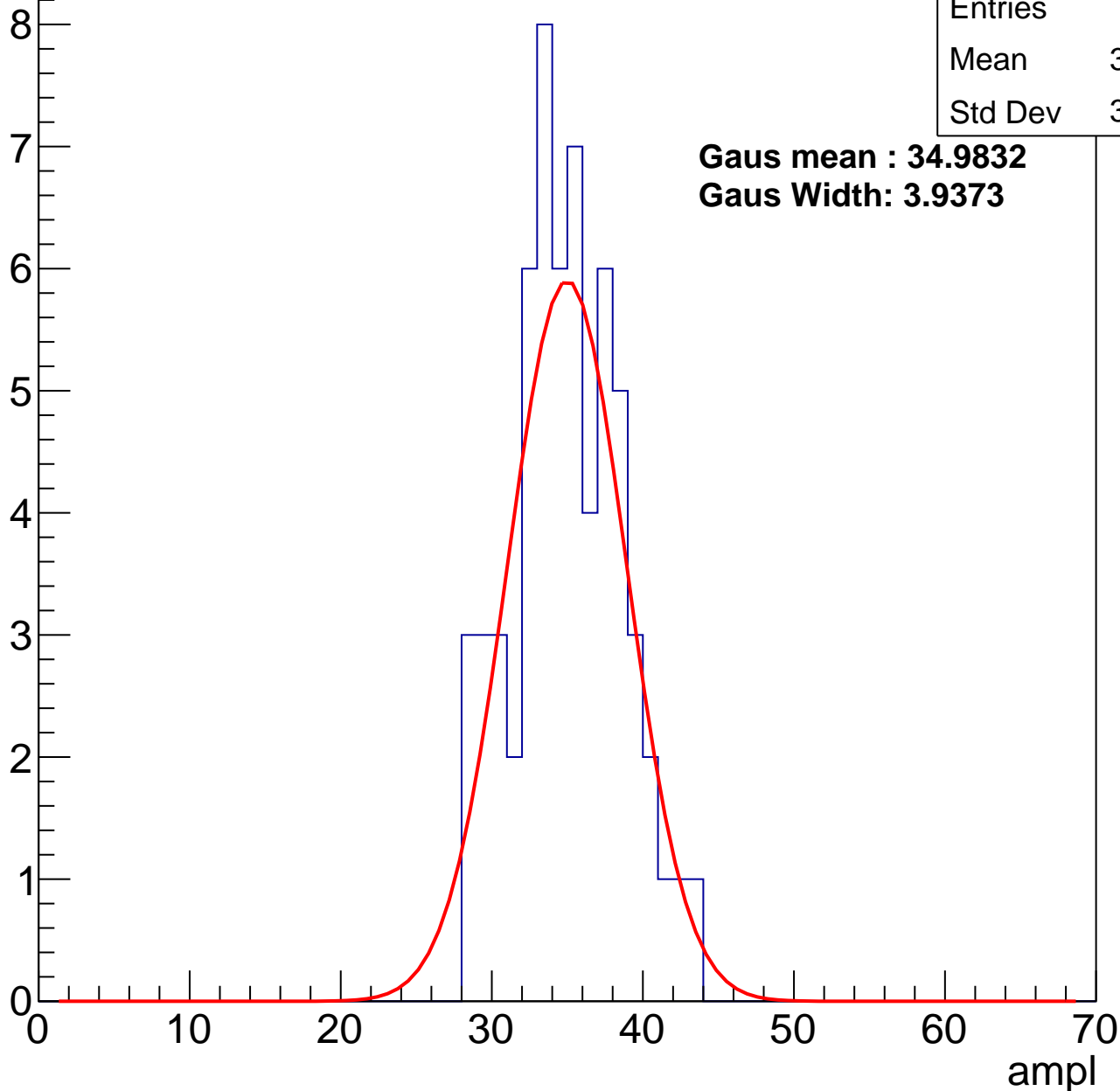
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	34.54
Std Dev	3.532

**Gaus mean : 34.9832**

**Gaus Width: 3.9373**



# B1L103S, U22-ch117, adc2

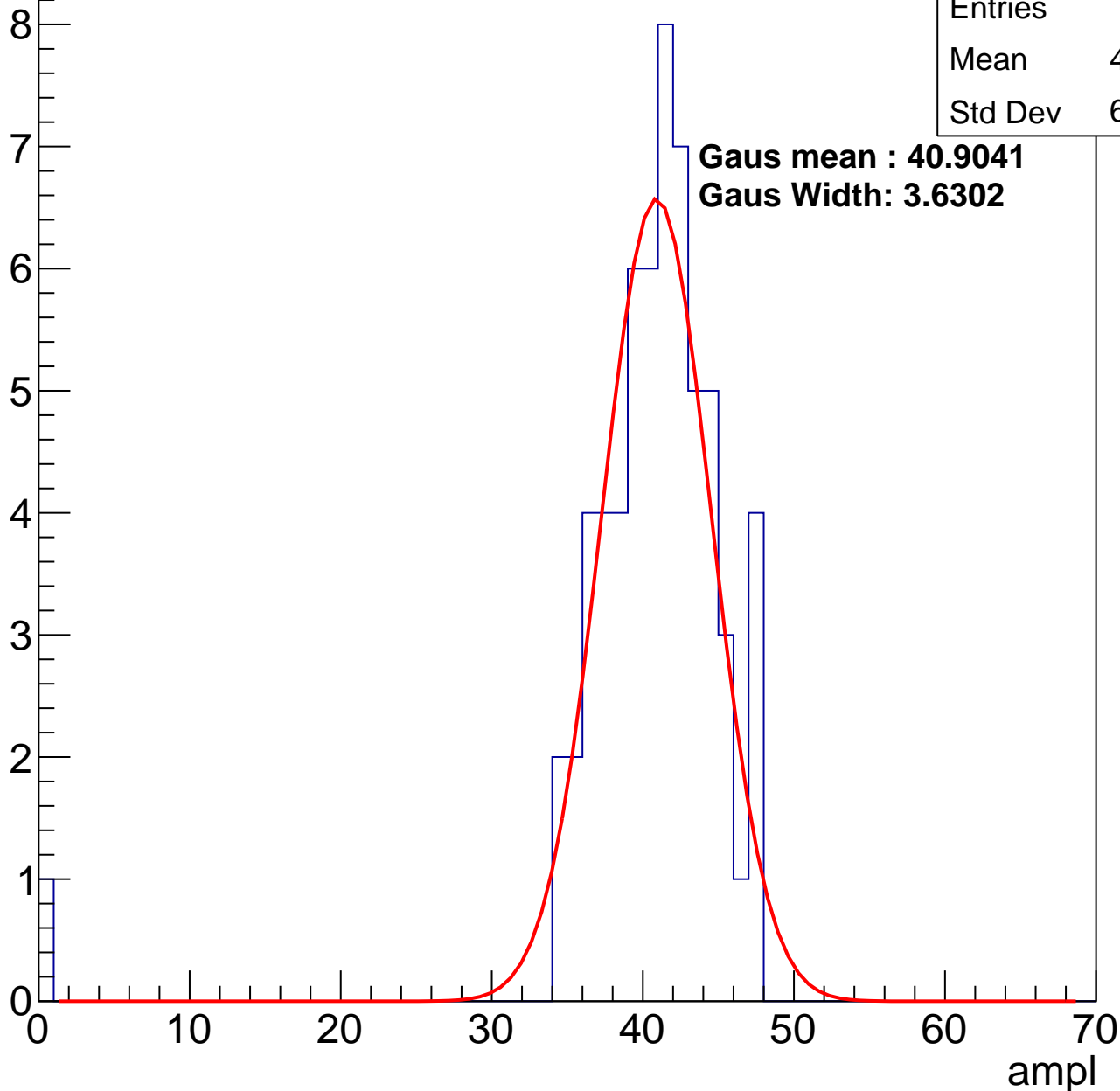
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	40.03
Std Dev	6.106

**Gaus mean : 40.9041**

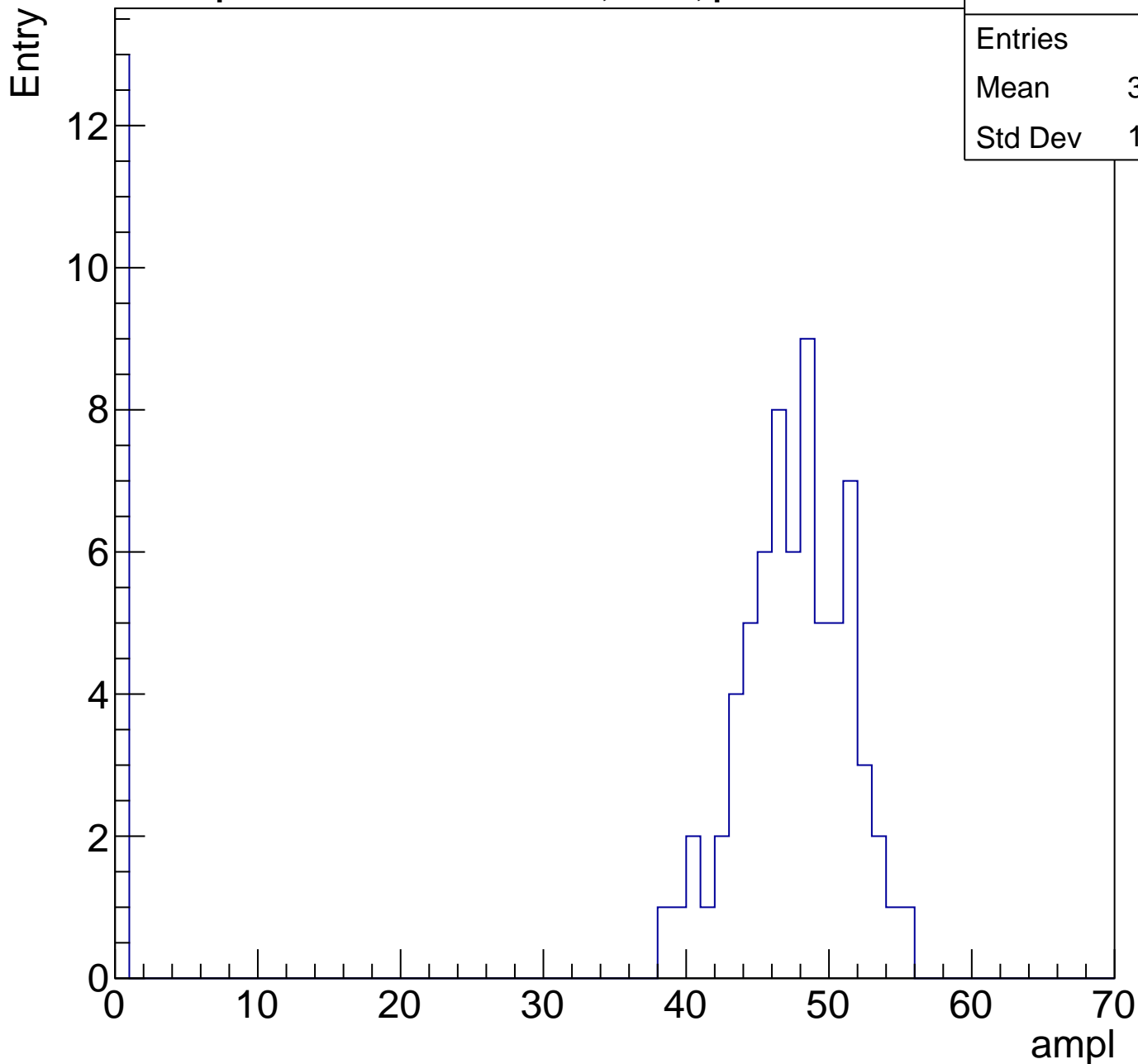
**Gaus Width: 3.6302**



# B1L103S, U22-ch117, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	82
Mean	39.62
Std Dev	17.52

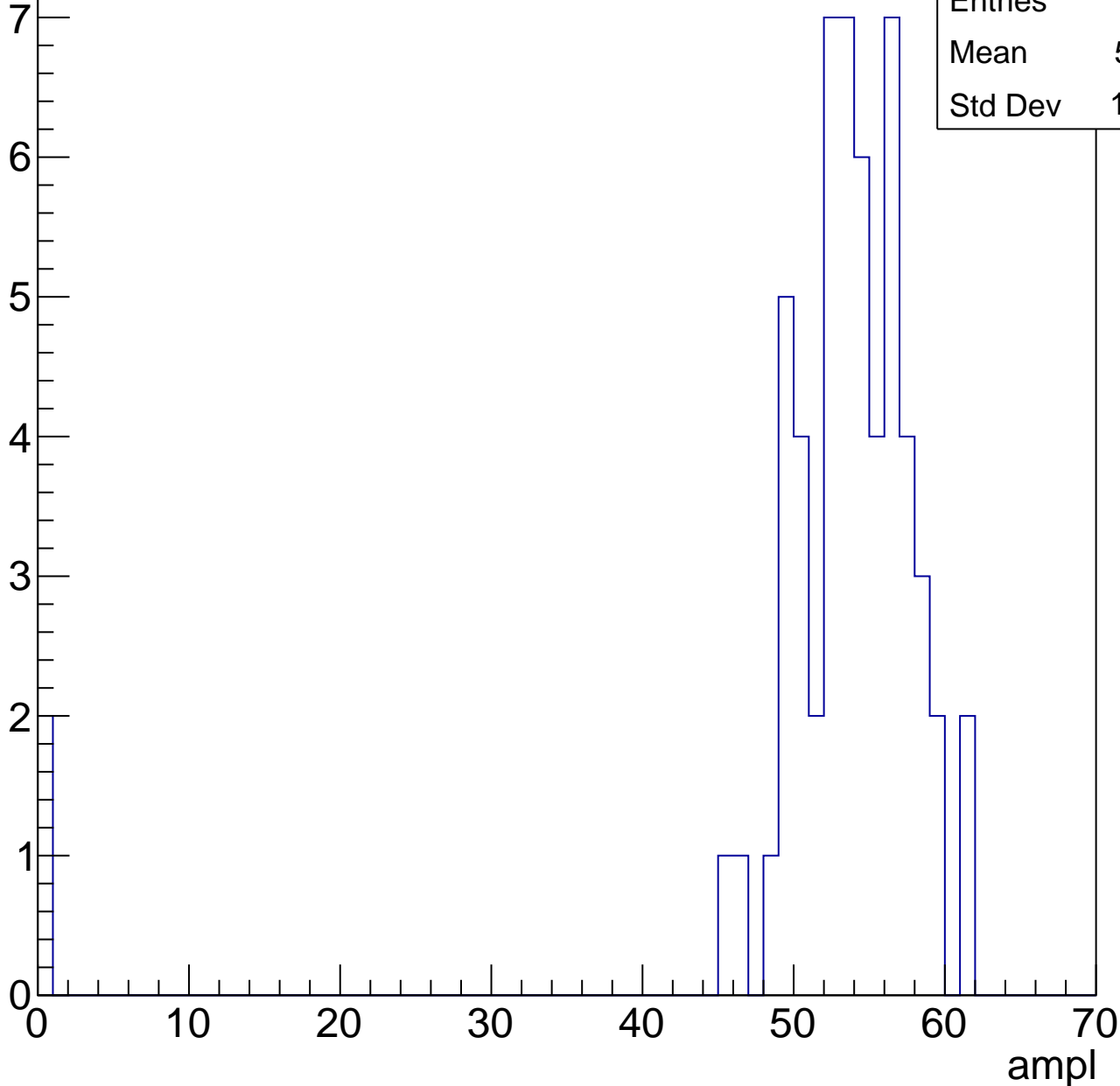


# B1L103S, U22-ch117, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	51.71
Std Dev	10.35

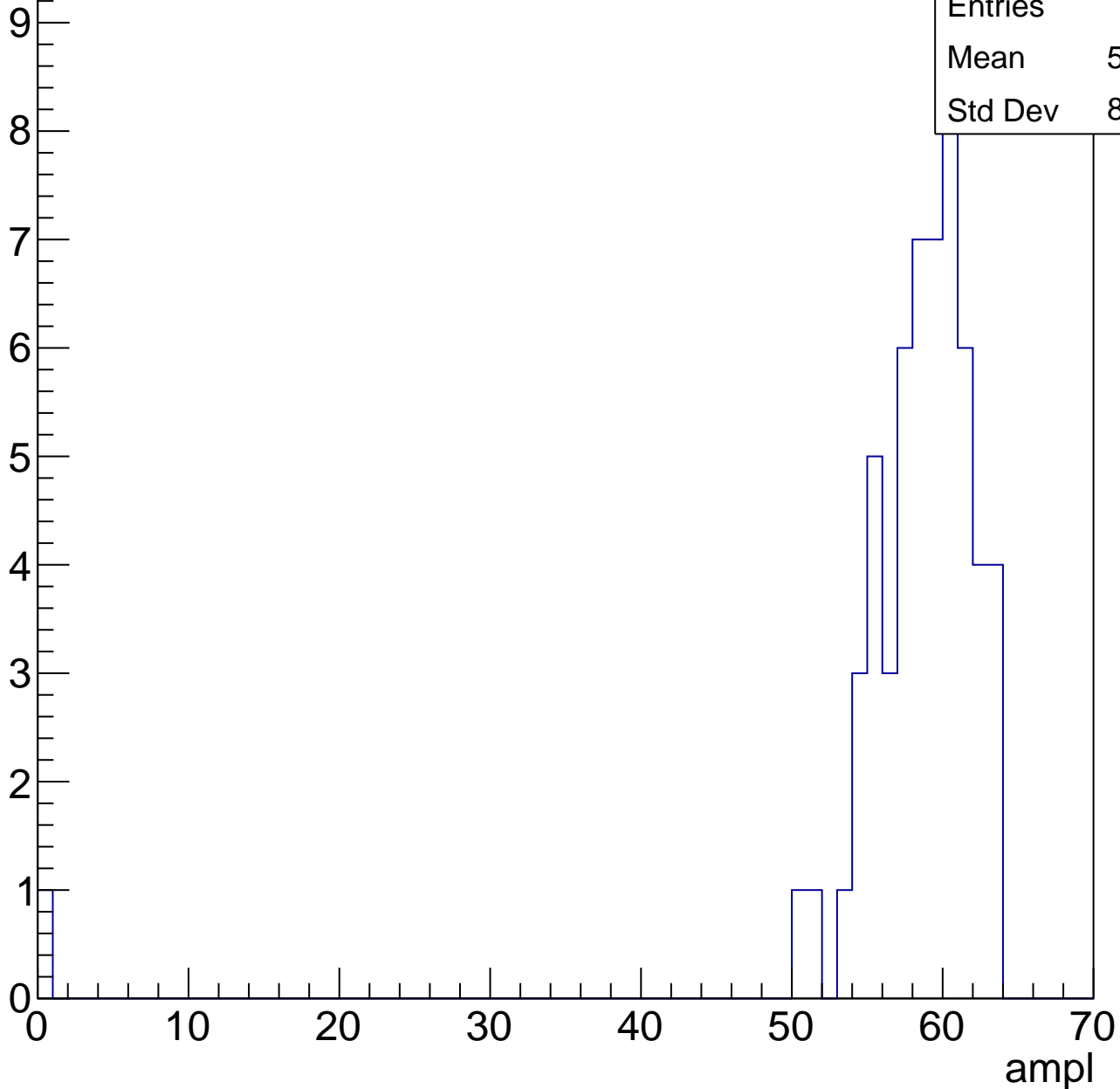


# B1L103S, U22-ch117, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.34
Std Dev	8.146

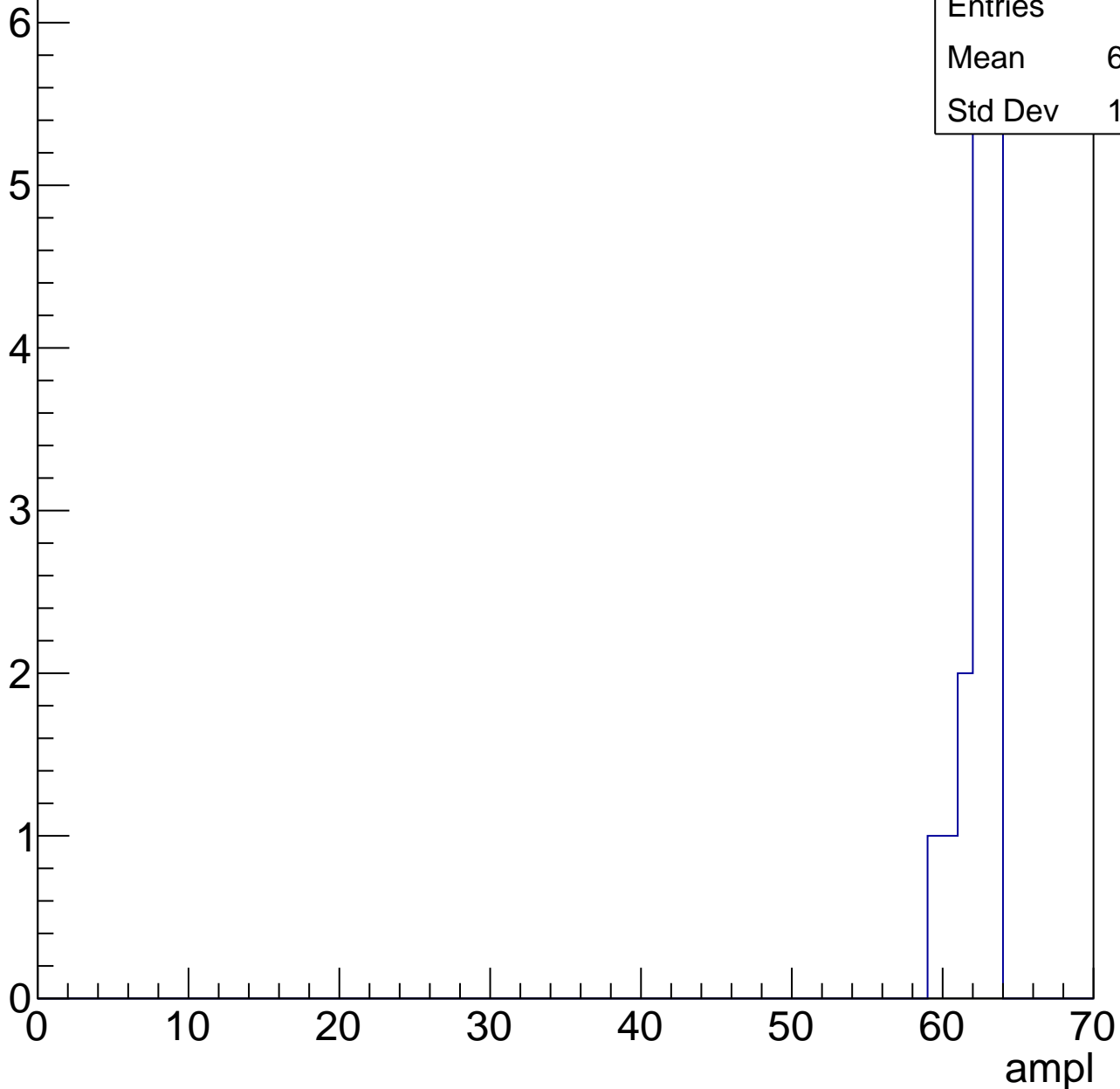


# B1L103S, U22-ch117, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	16
Mean	61.94
Std Dev	1.144





# B1L103S, U22-ch117, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry



# B1L103S, U22-ch118, adc0

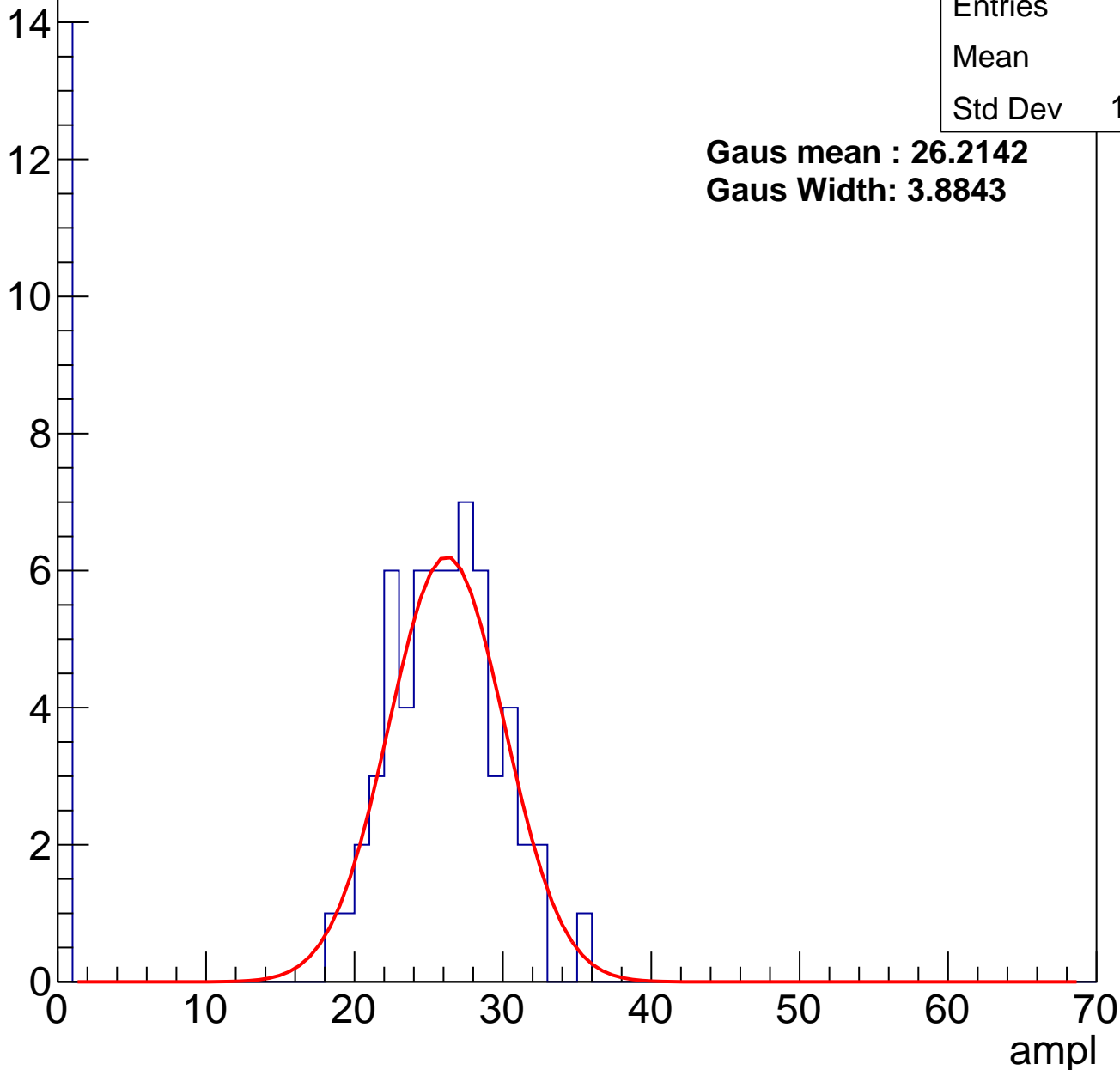
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	74
Mean	20.8
Std Dev	10.53

**Gaus mean : 26.2142**

**Gaus Width: 3.8843**

Entry



# B1L103S, U22-ch118, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	79
Mean	32.76
Std Dev	3.972

**Gaus mean : 33.4410**

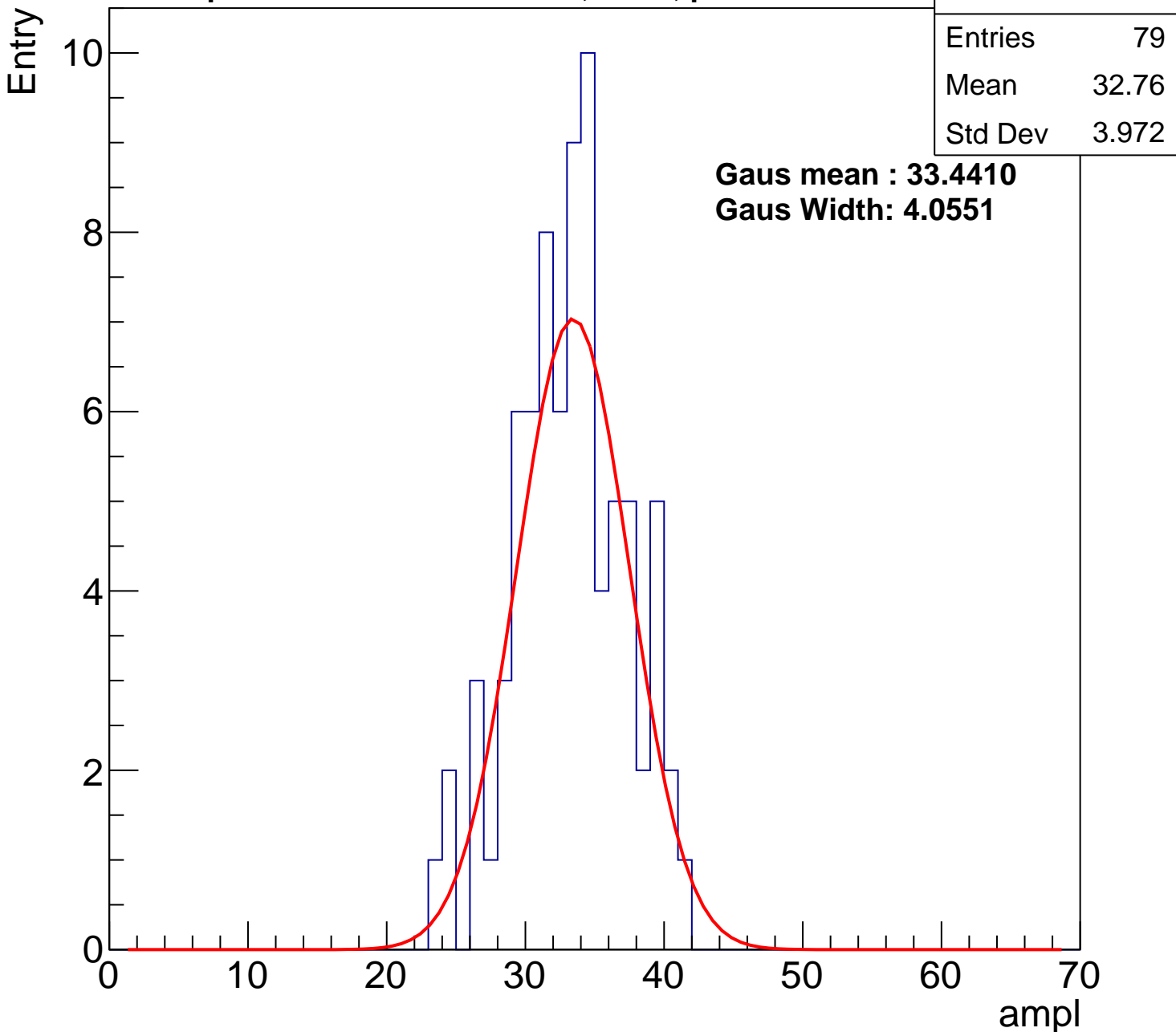
**Gaus Width: 4.0551**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch118, adc2

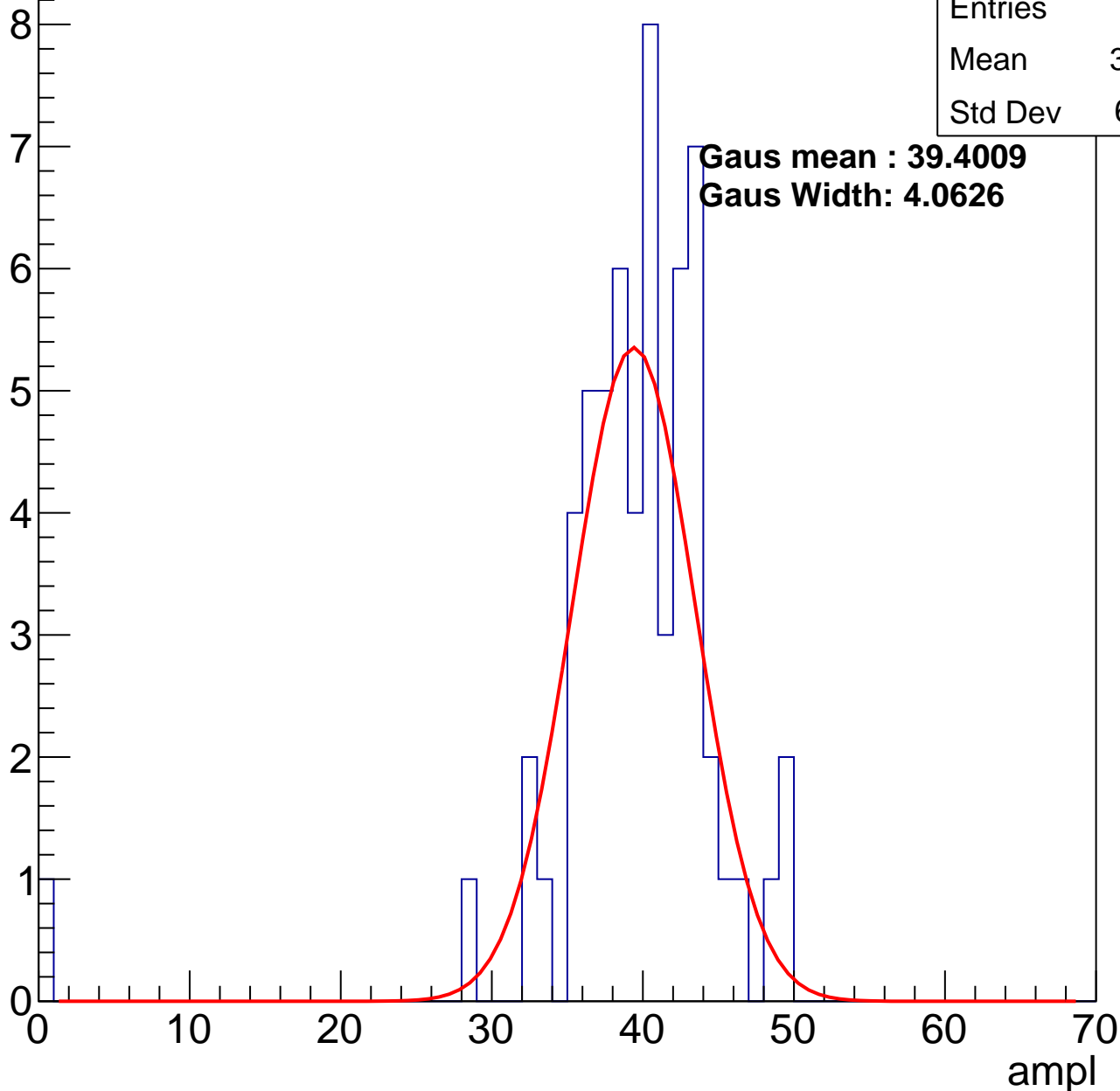
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	38.92
Std Dev	6.471

**Gaus mean : 39.4009**

**Gaus Width: 4.0626**



# B1L103S, U22-ch118, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	40.11
Std Dev	15.92

Entry

10

8

6

4

2

0

0

10

20

30

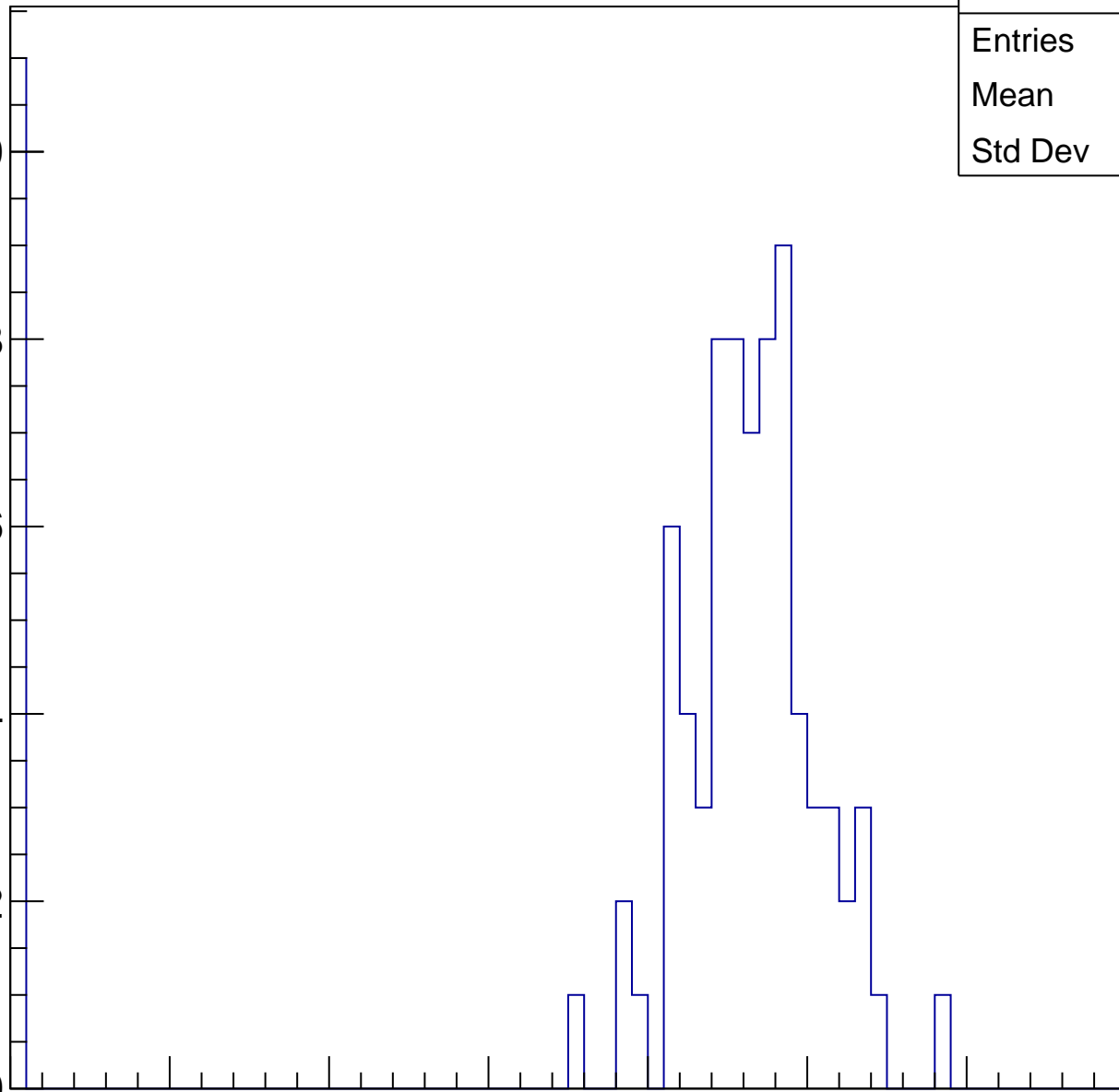
40

50

60

ampl

70

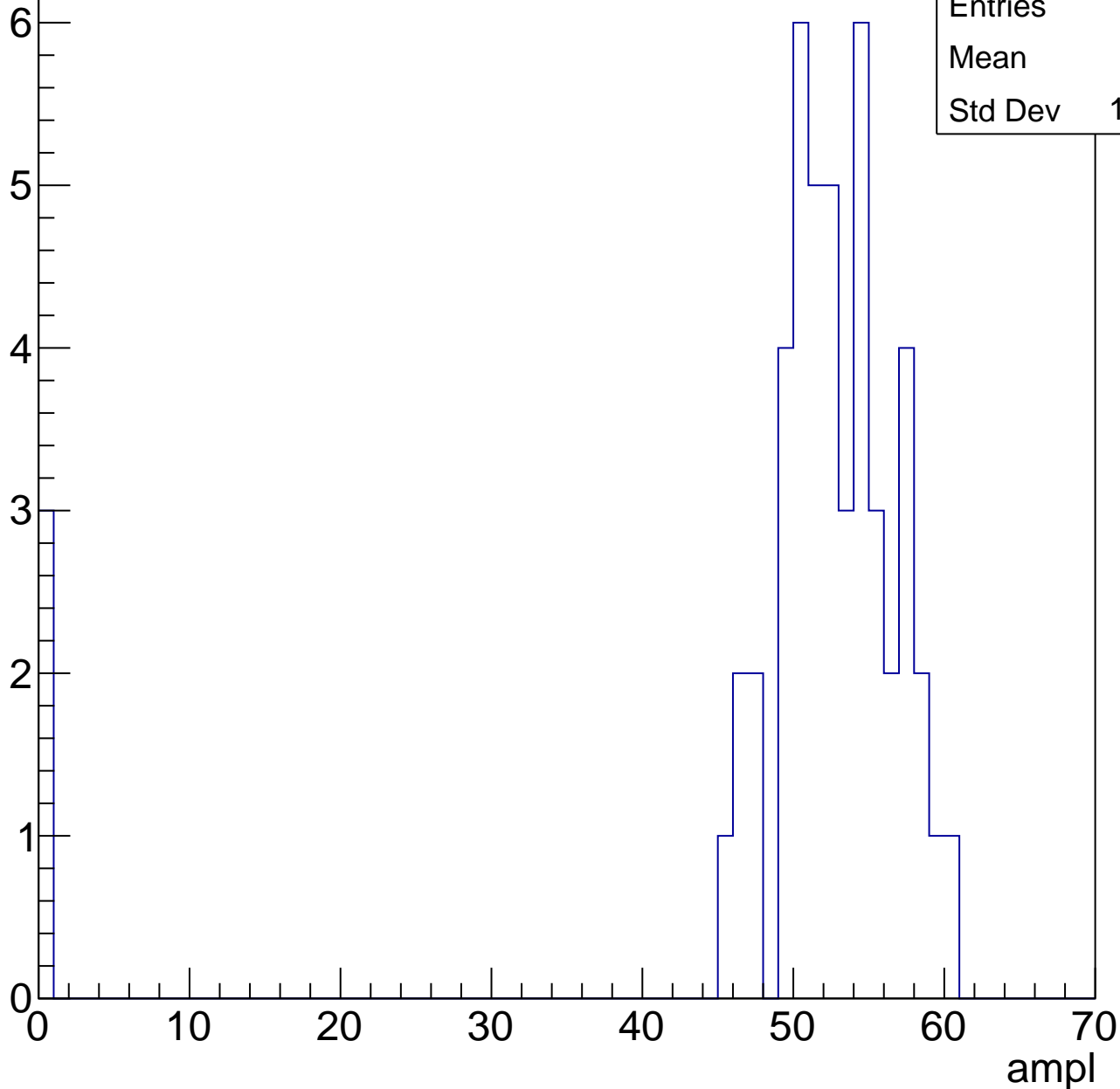


# B1L103S, U22-ch118, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	50
Mean	49.3
Std Dev	12.93

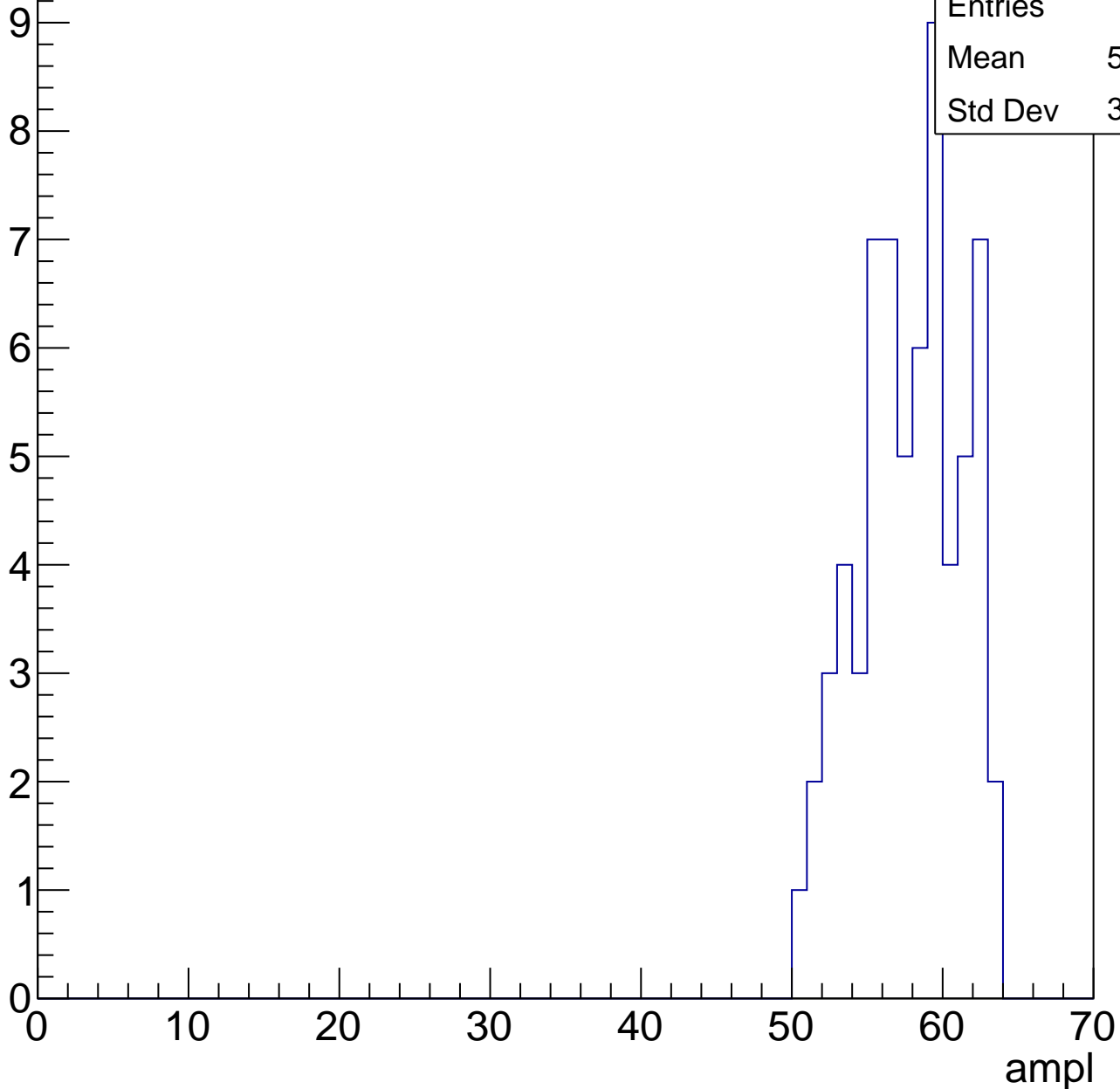


# B1L103S, U22-ch118, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	57.35
Std Dev	3.326

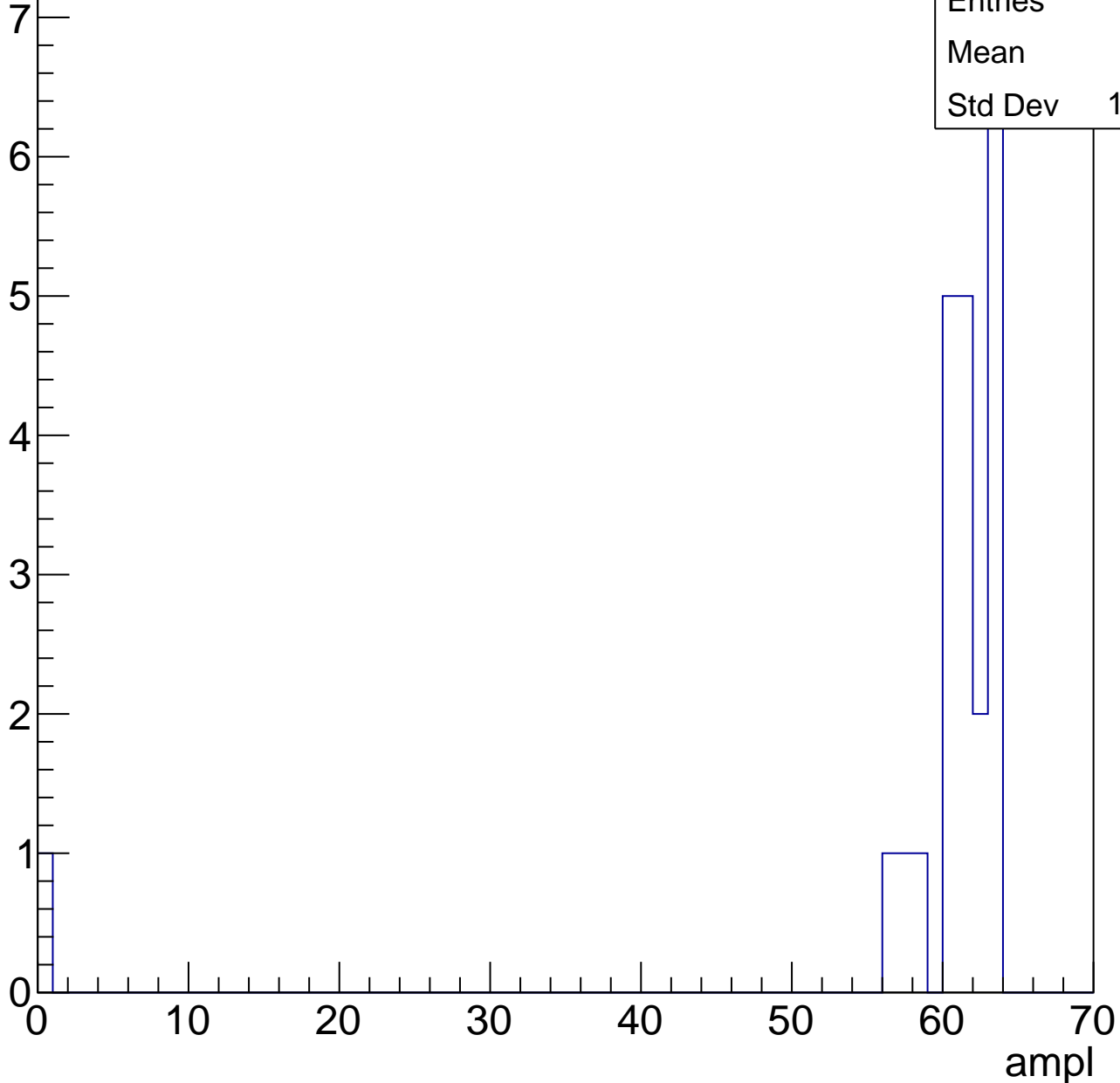


# B1L103S, U22-ch118, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	23
Mean	58.3
Std Dev	12.58

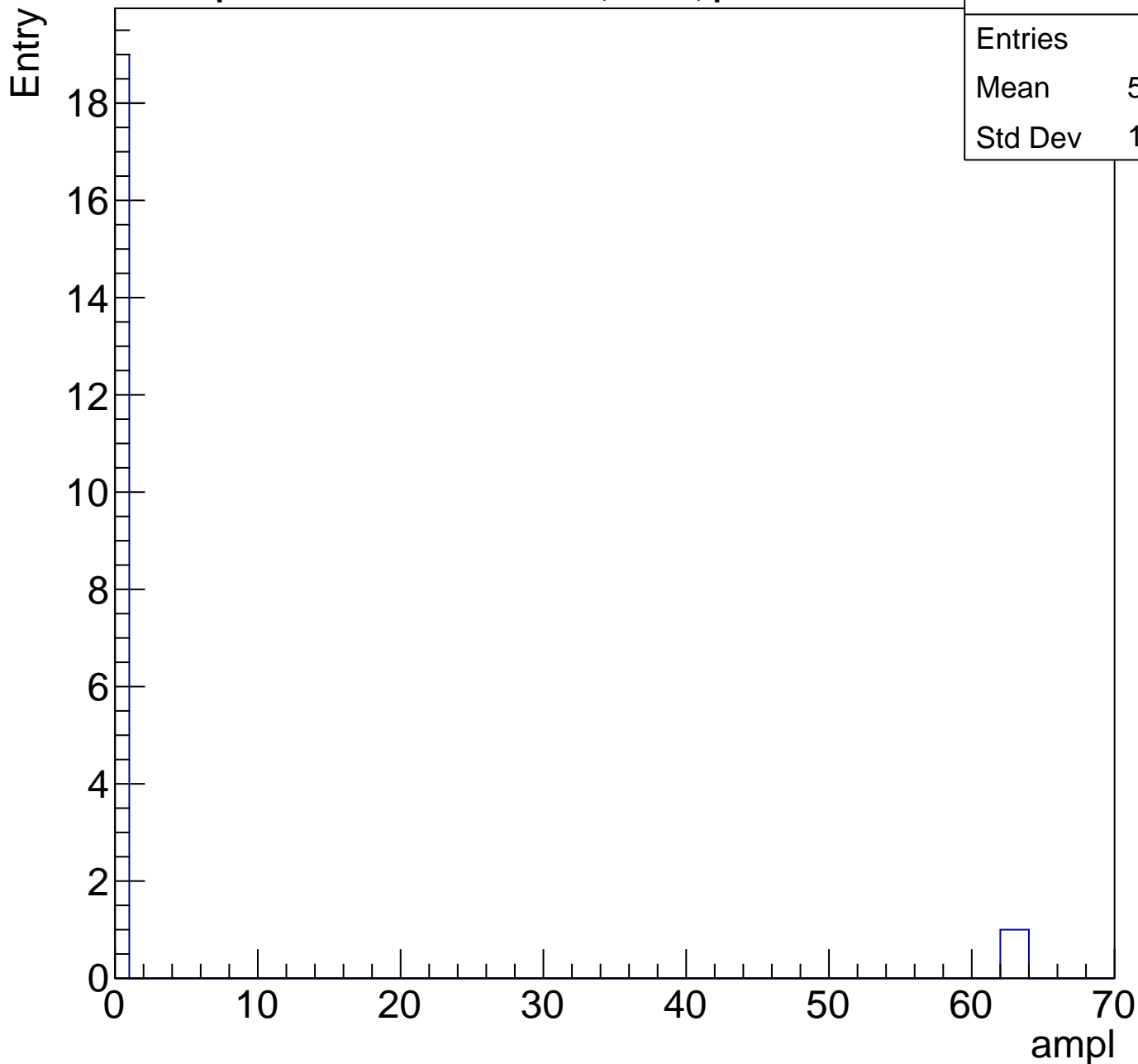




# B1L103S, U22-ch118, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.952
Std Dev	18.35



# B1L103S, U22-ch119, adc0

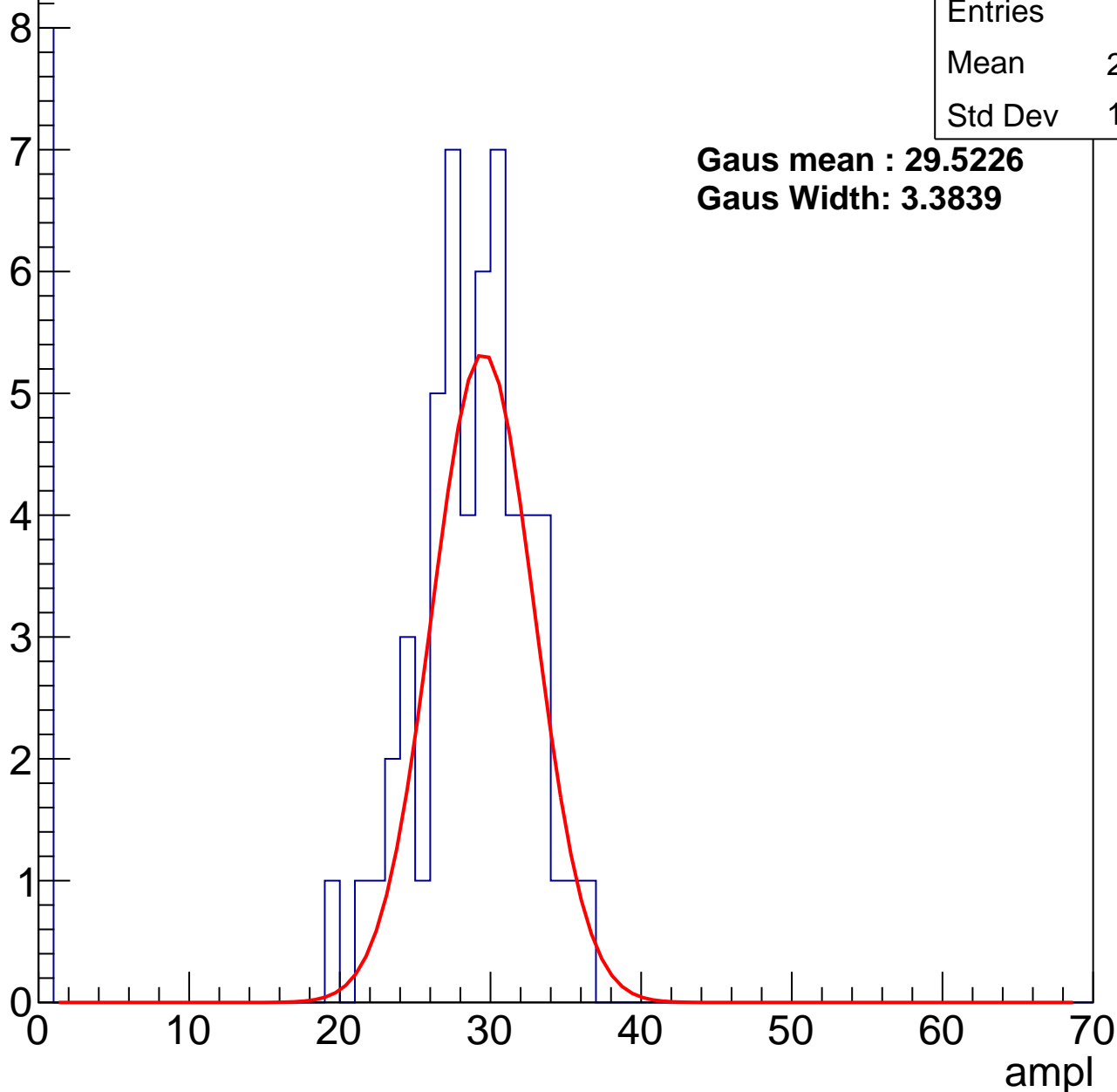
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	61
Mean	24.74
Std Dev	10.17

**Gaus mean : 29.5226**

**Gaus Width: 3.3839**



# B1L103S, U22-ch119, adc1

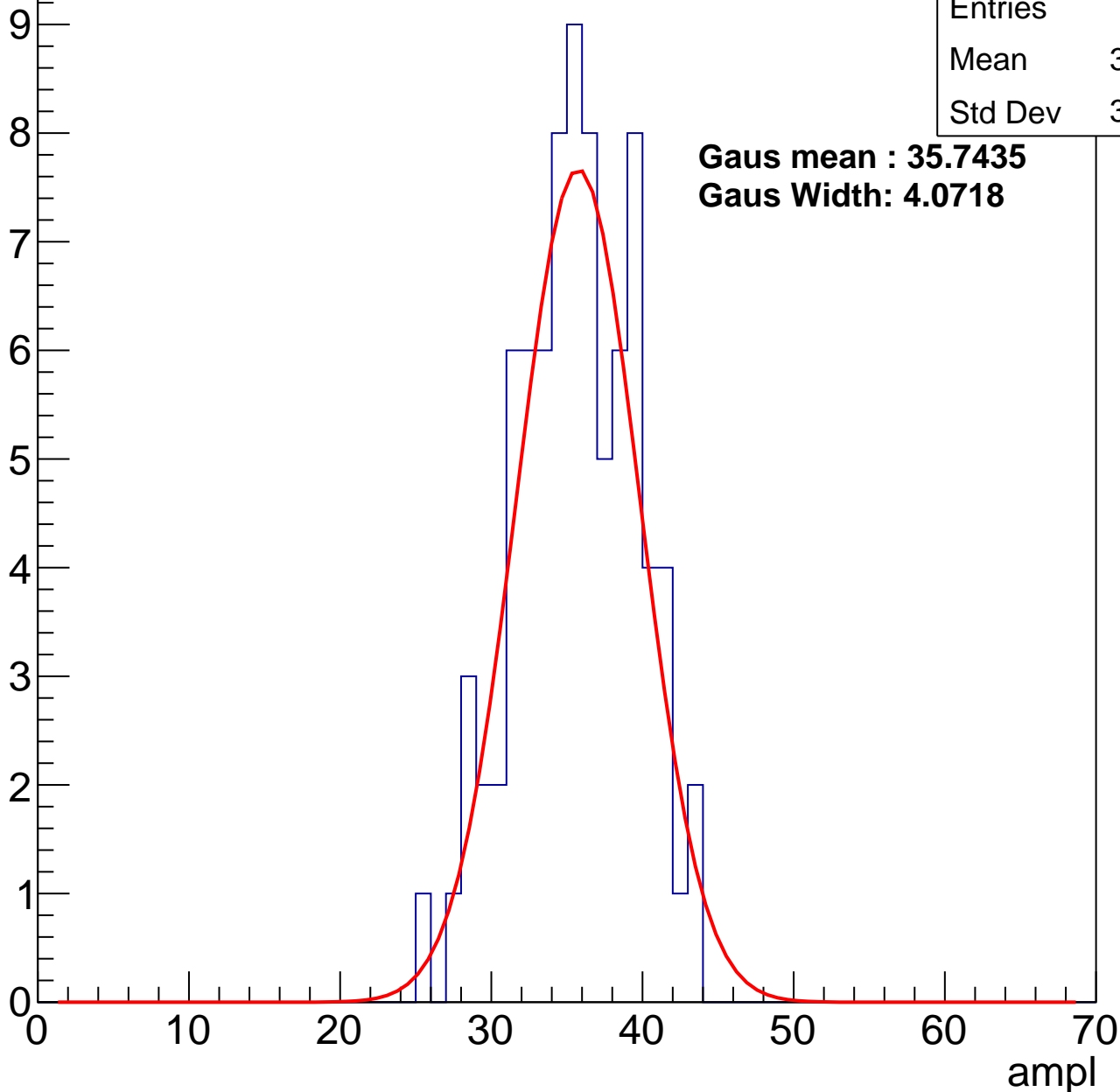
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	82
Mean	35.15
Std Dev	3.895

**Gaus mean : 35.7435**

**Gaus Width: 4.0718**



# B1L103S, U22-ch119, adc2

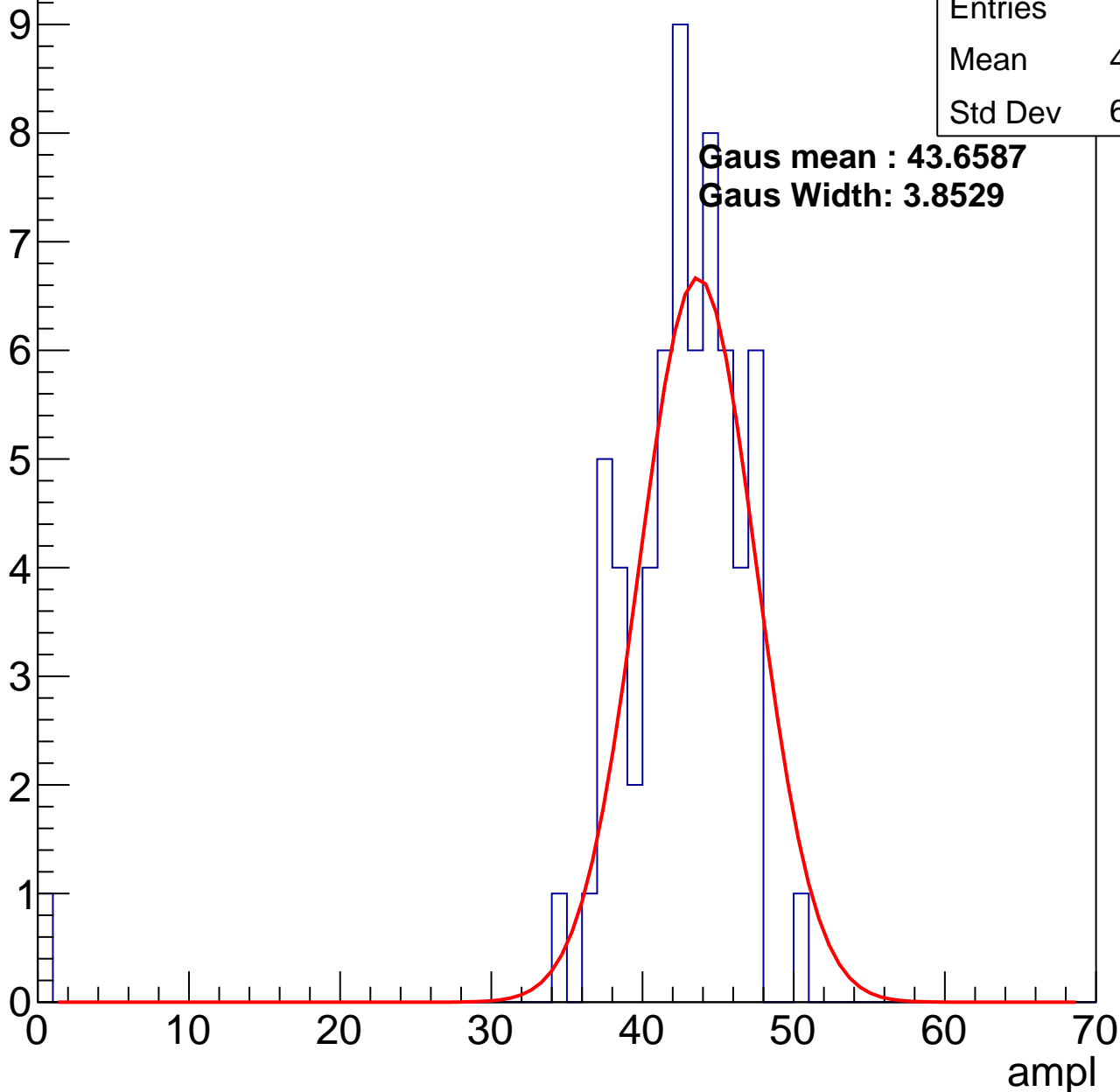
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	41.64
Std Dev	6.193

**Gaus mean : 43.6587**

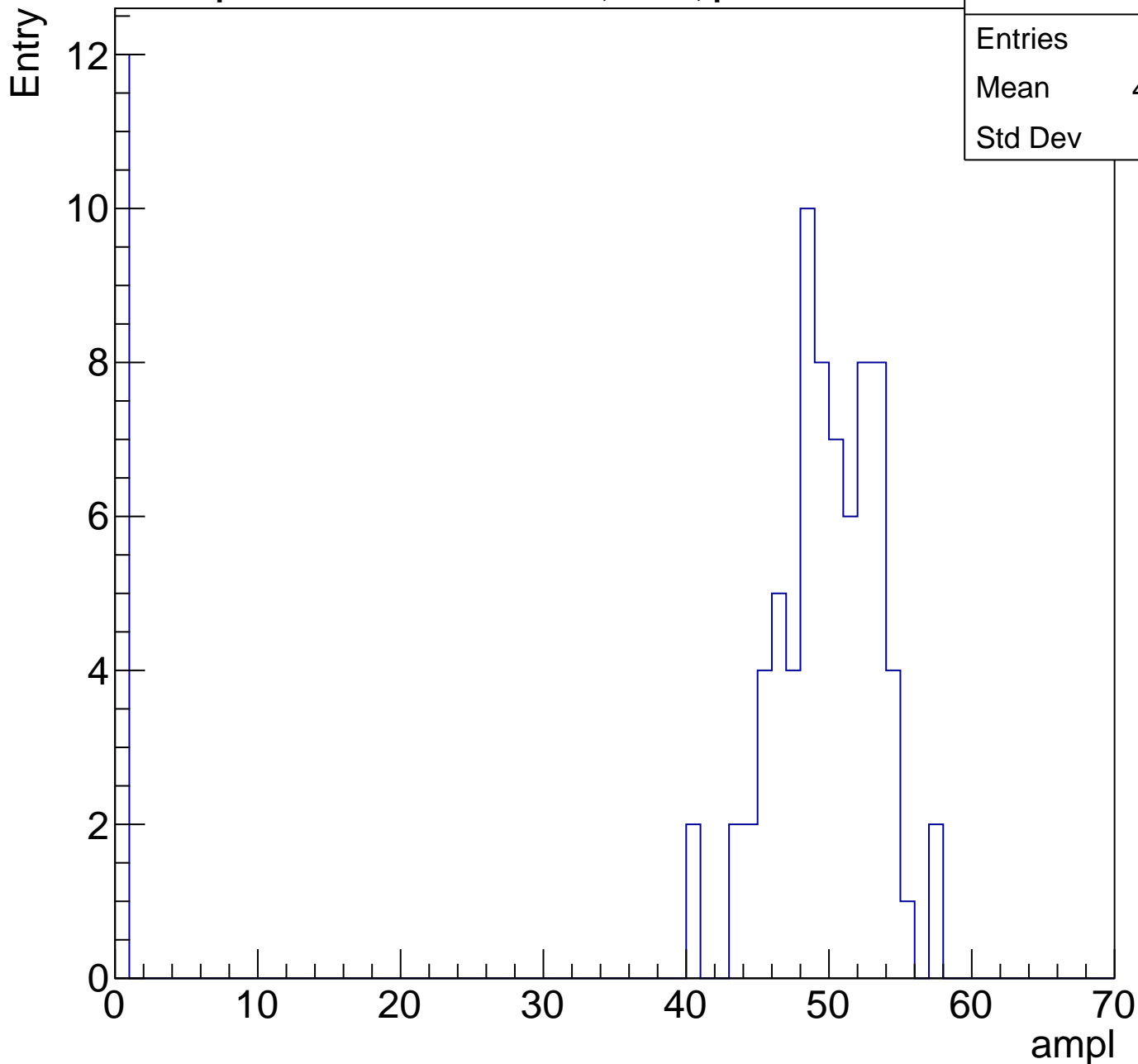
**Gaus Width: 3.8529**



# B1L103S, U22-ch119, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	42.41
Std Dev	17.5

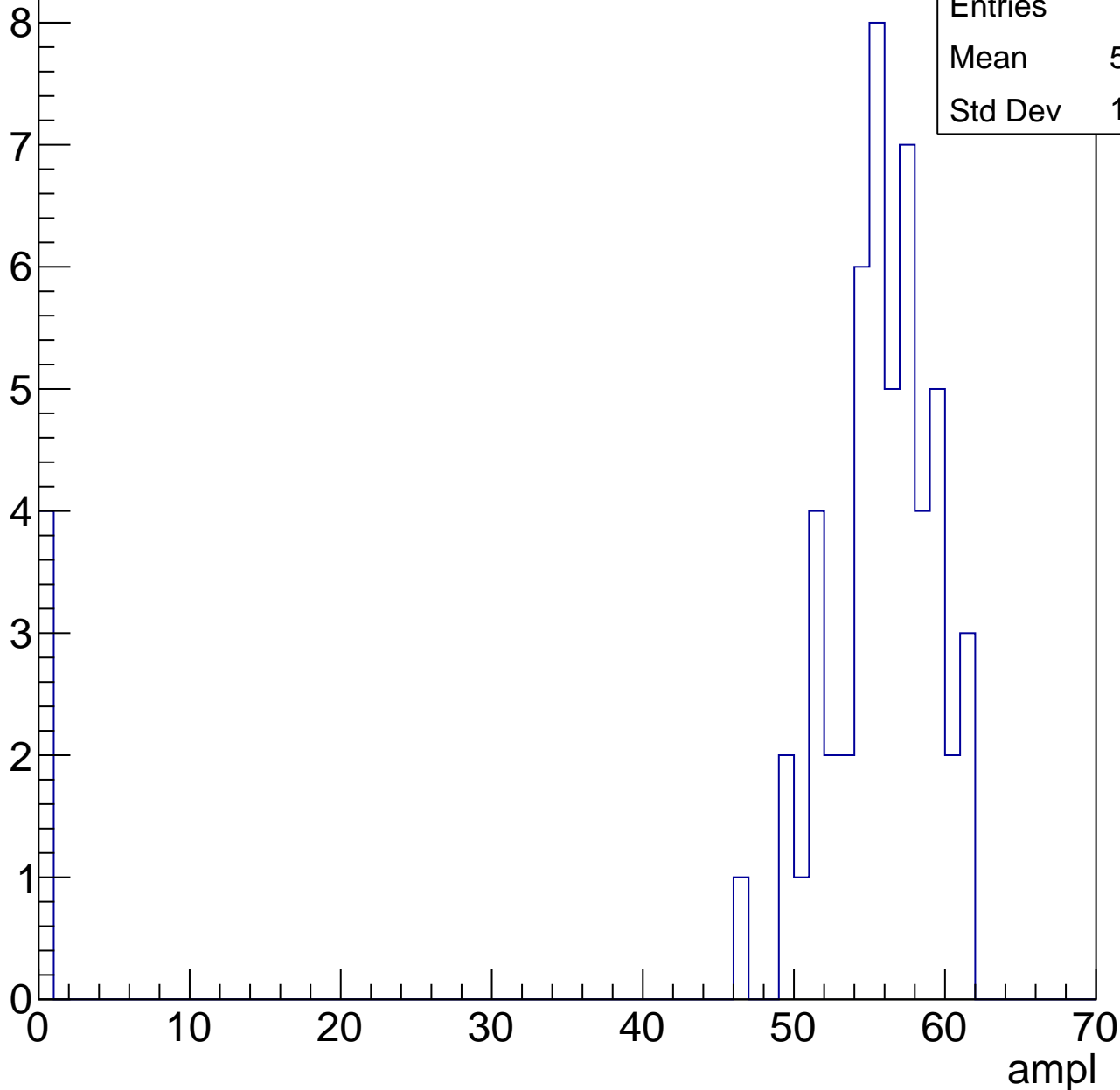


# B1L103S, U22-ch119, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	51.45
Std Dev	14.62

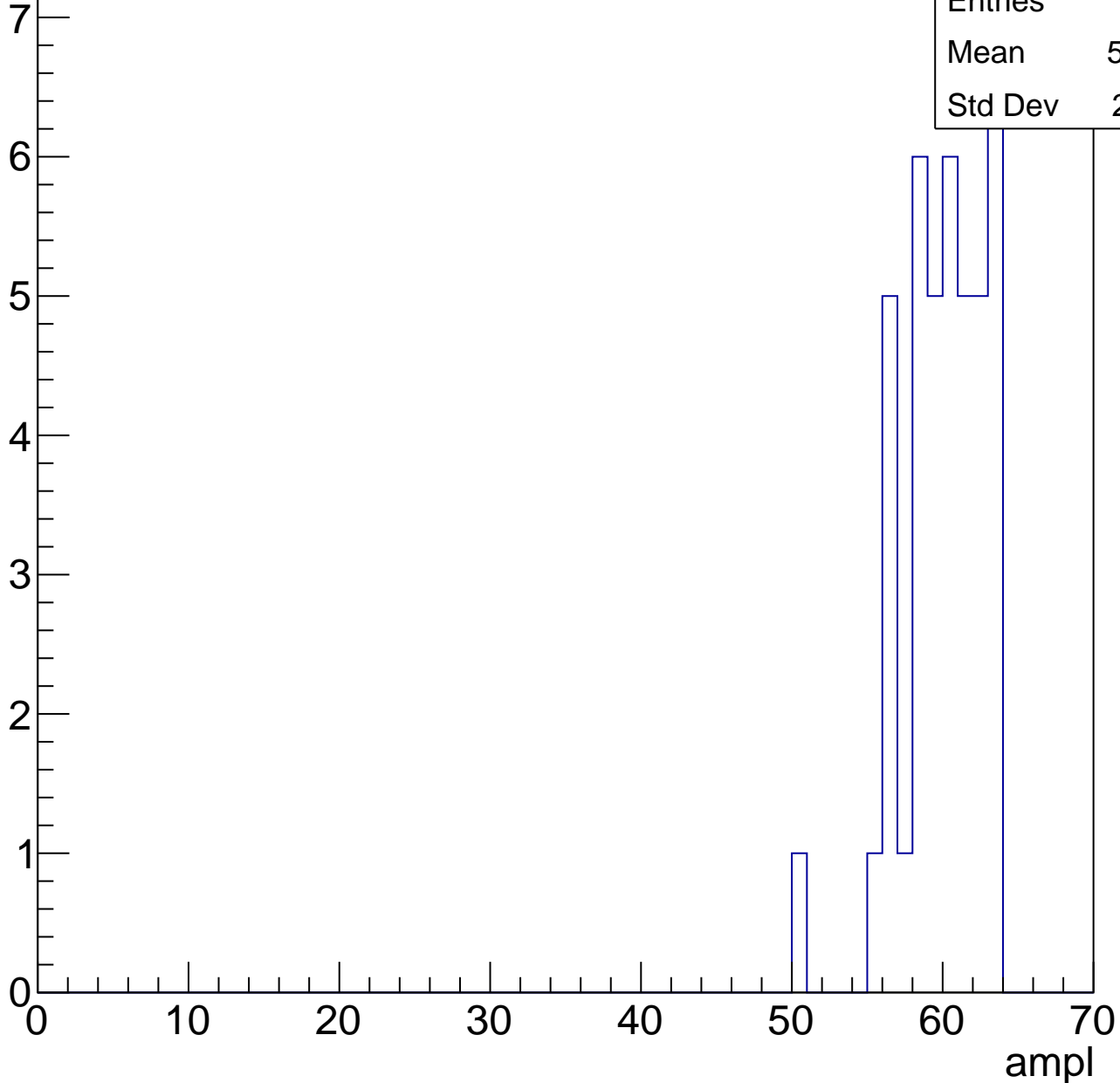


# B1L103S, U22-ch119, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	42
Mean	59.55
Std Dev	2.771

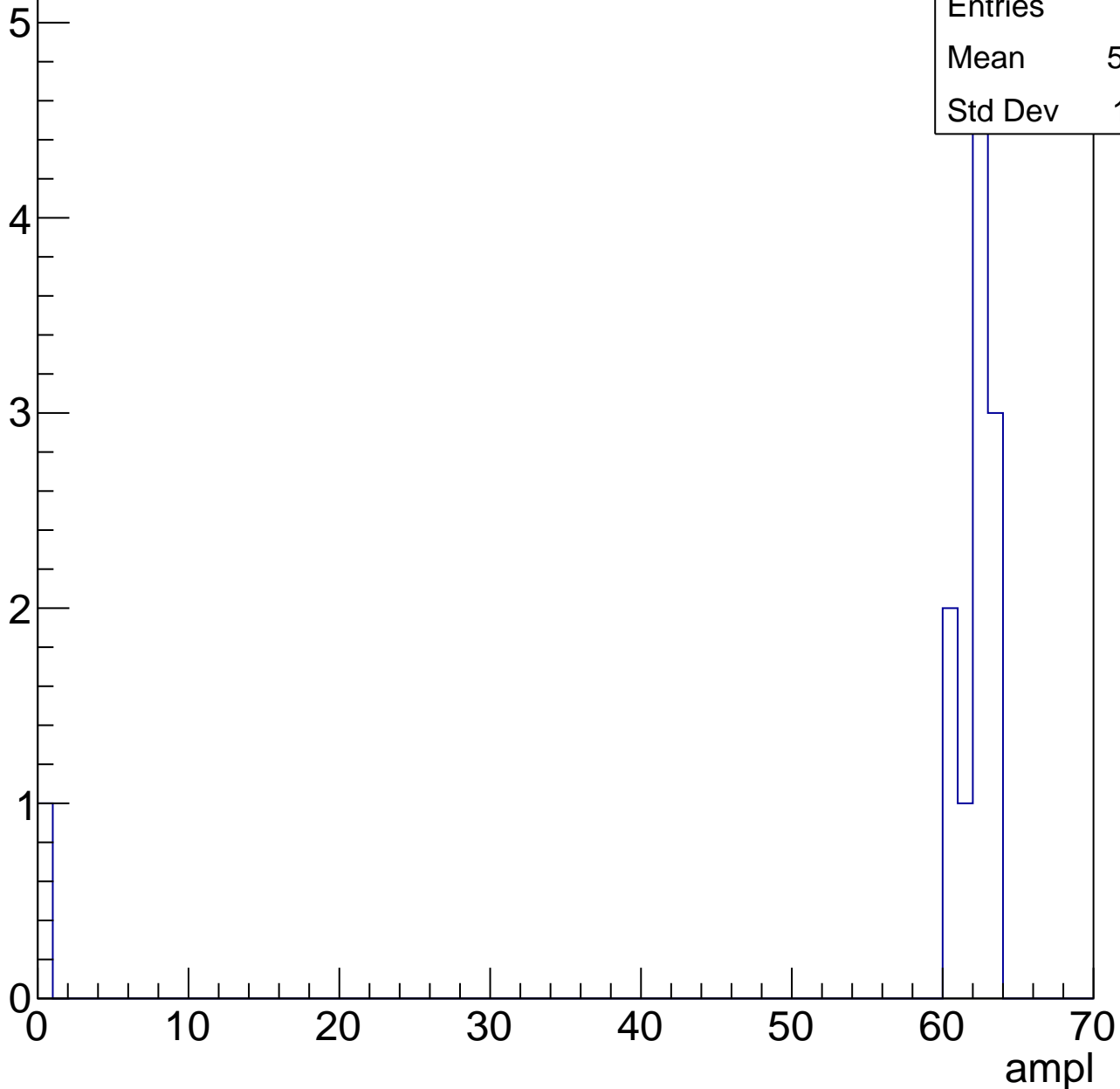


# B1L103S, U22-ch119, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	56.67
Std Dev	17.11





# B1L103S, U22-ch119, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.05
Std Dev	13.29

Entry



# B1L103S, U22-ch120, adc0

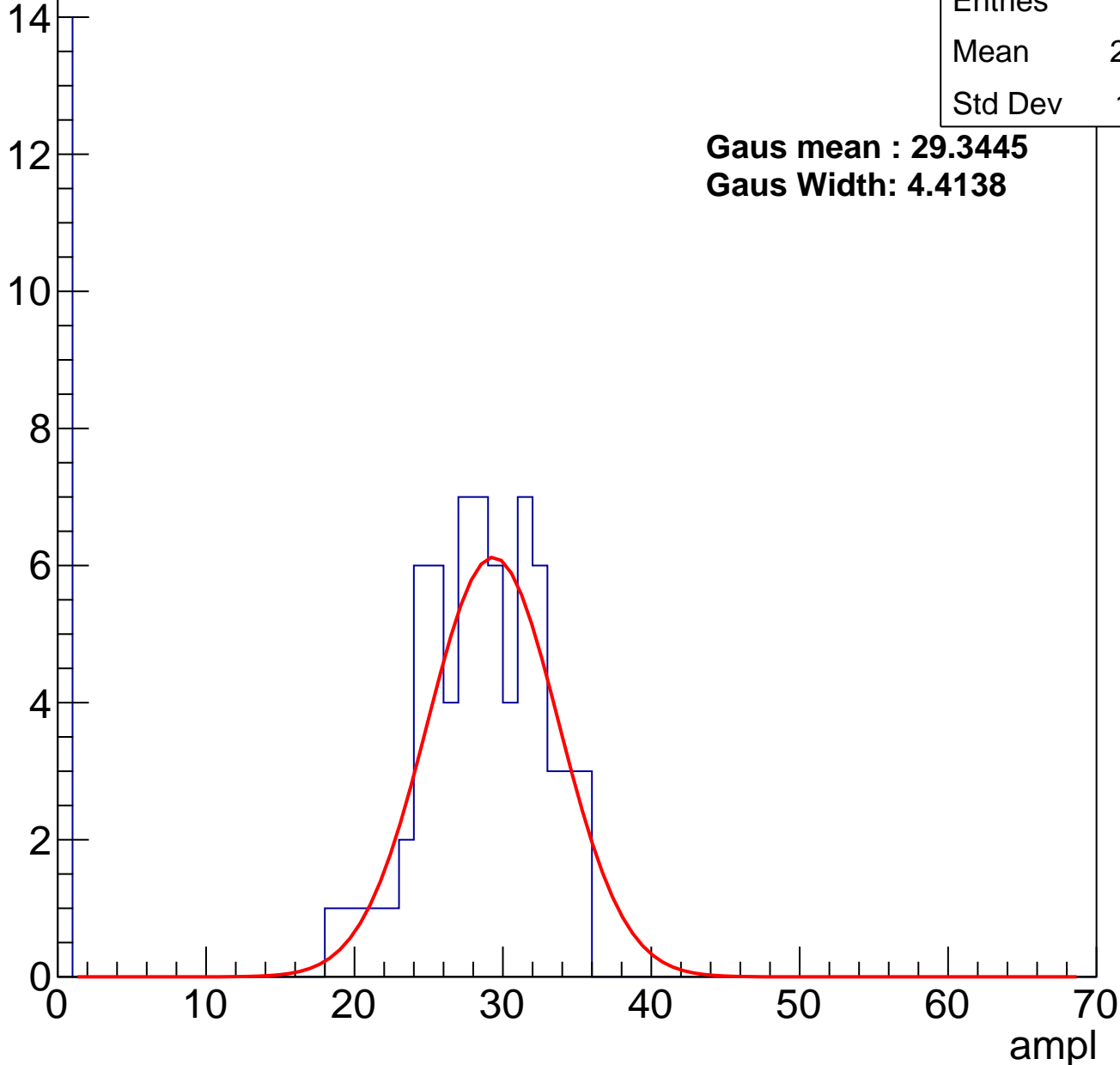
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	83
Mean	23.35
Std Dev	11.11

**Gaus mean : 29.3445**

**Gaus Width: 4.4138**

Entry



# B1L103S, U22-ch120, adc1

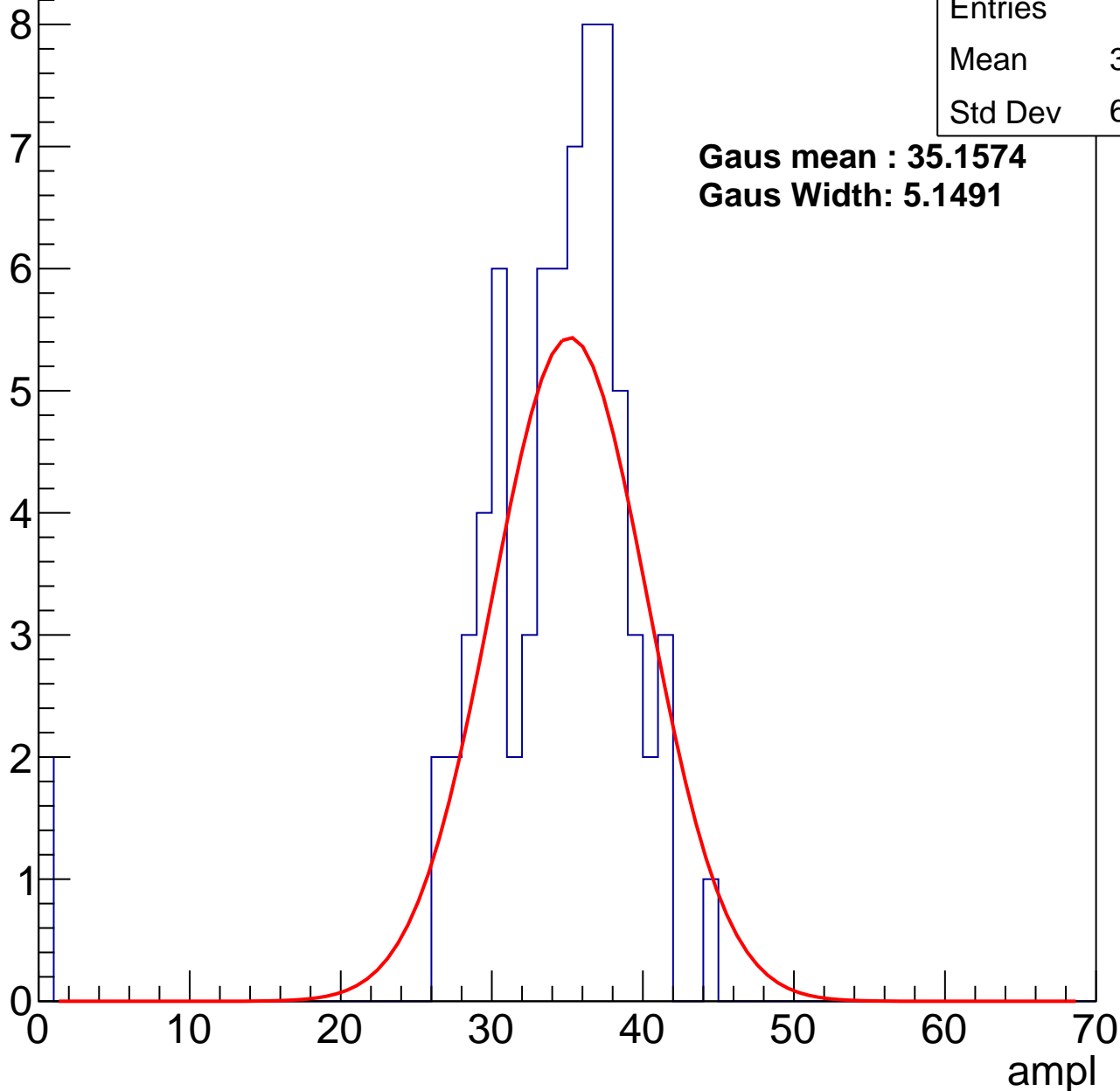
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	33.27
Std Dev	6.849

**Gaus mean : 35.1574**

**Gaus Width: 5.1491**



# B1L103S, U22-ch120, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	71
Mean	34.66
Std Dev	15.21

**Gaus mean : 42.0235**

**Gaus Width: 4.1885**

Entry

10

8

6

4

2

0

0

10

20

30

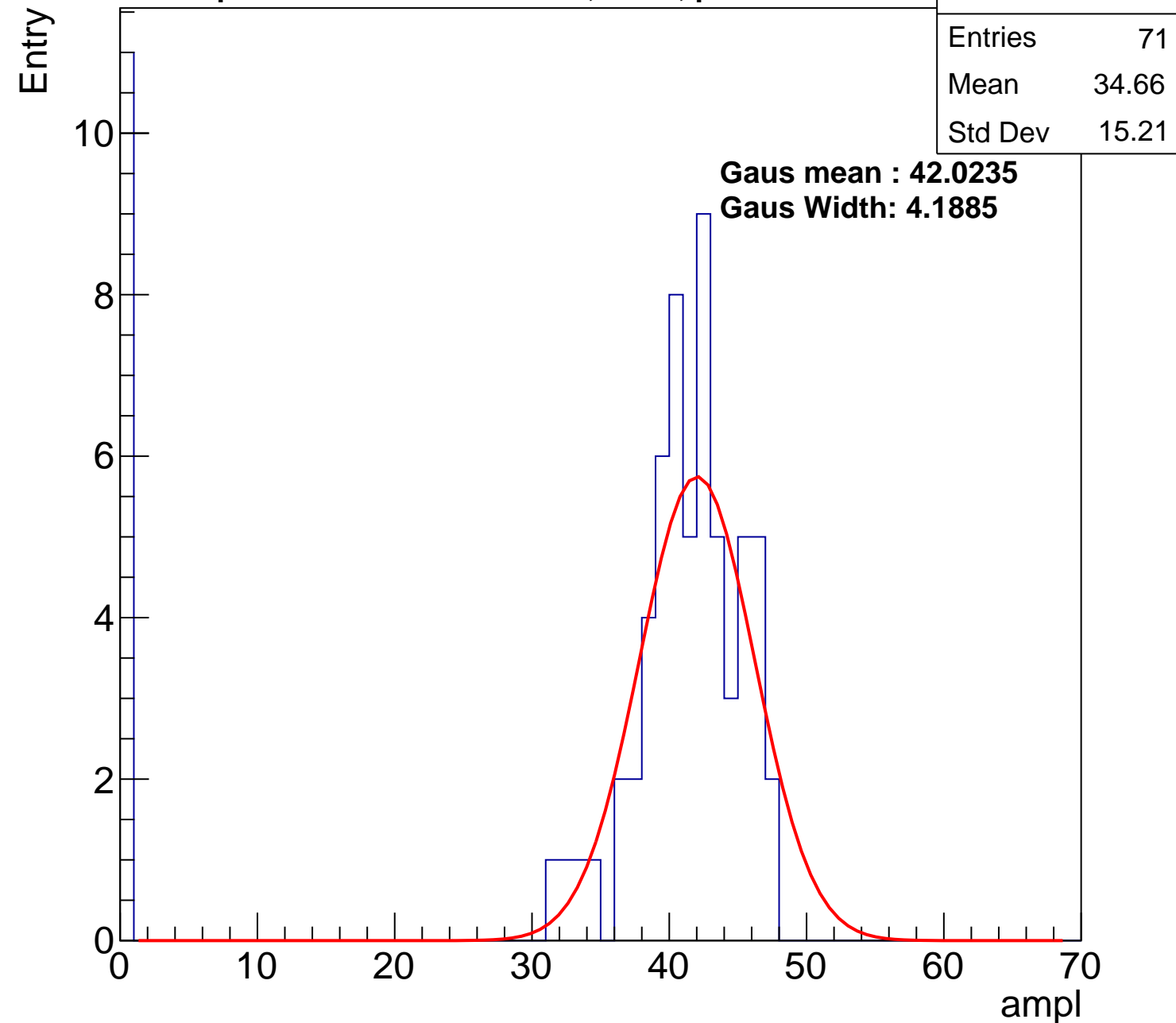
40

50

60

70

ampl

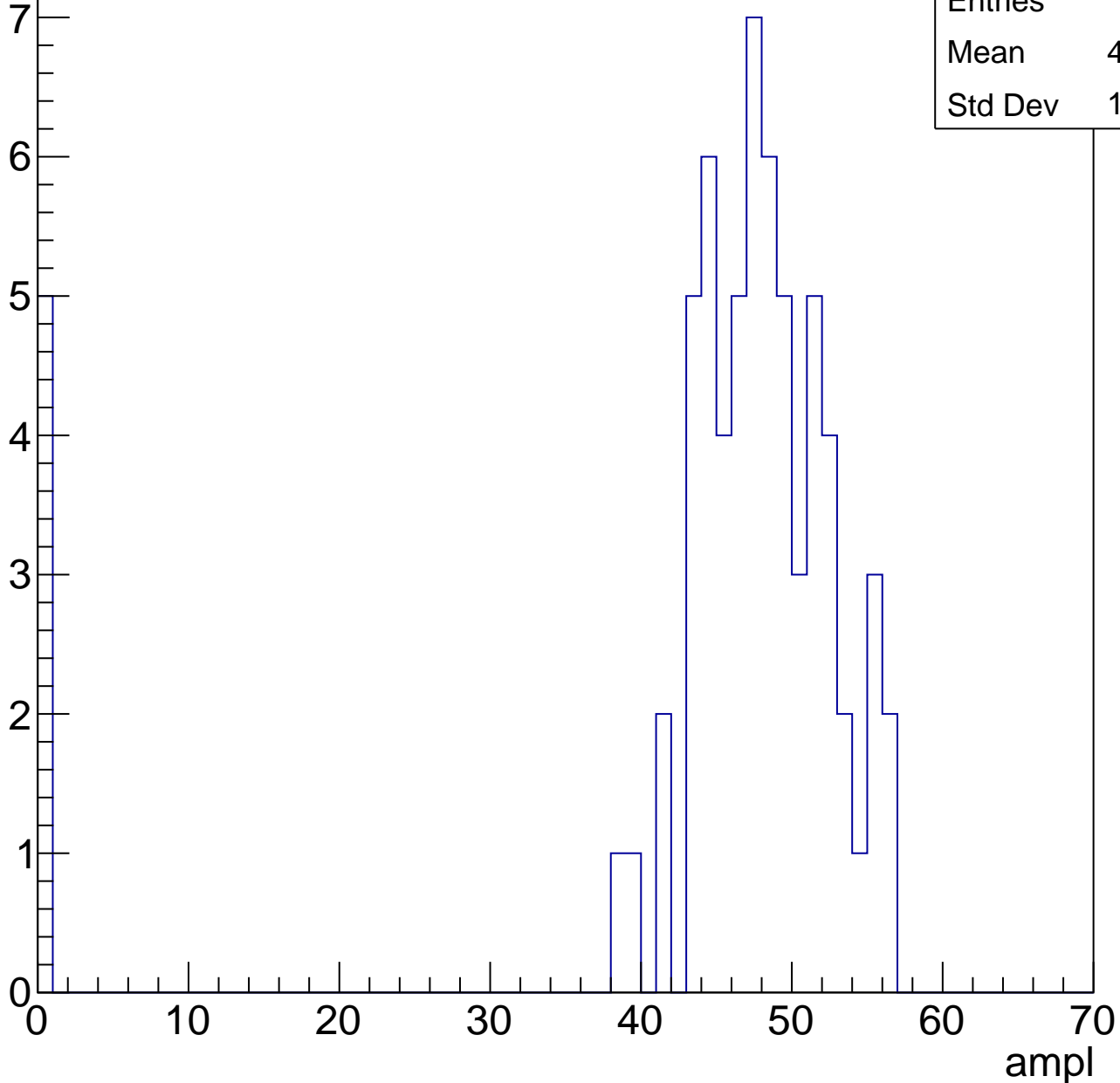


# B1L103S, U22-ch120, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	44.18
Std Dev	13.15

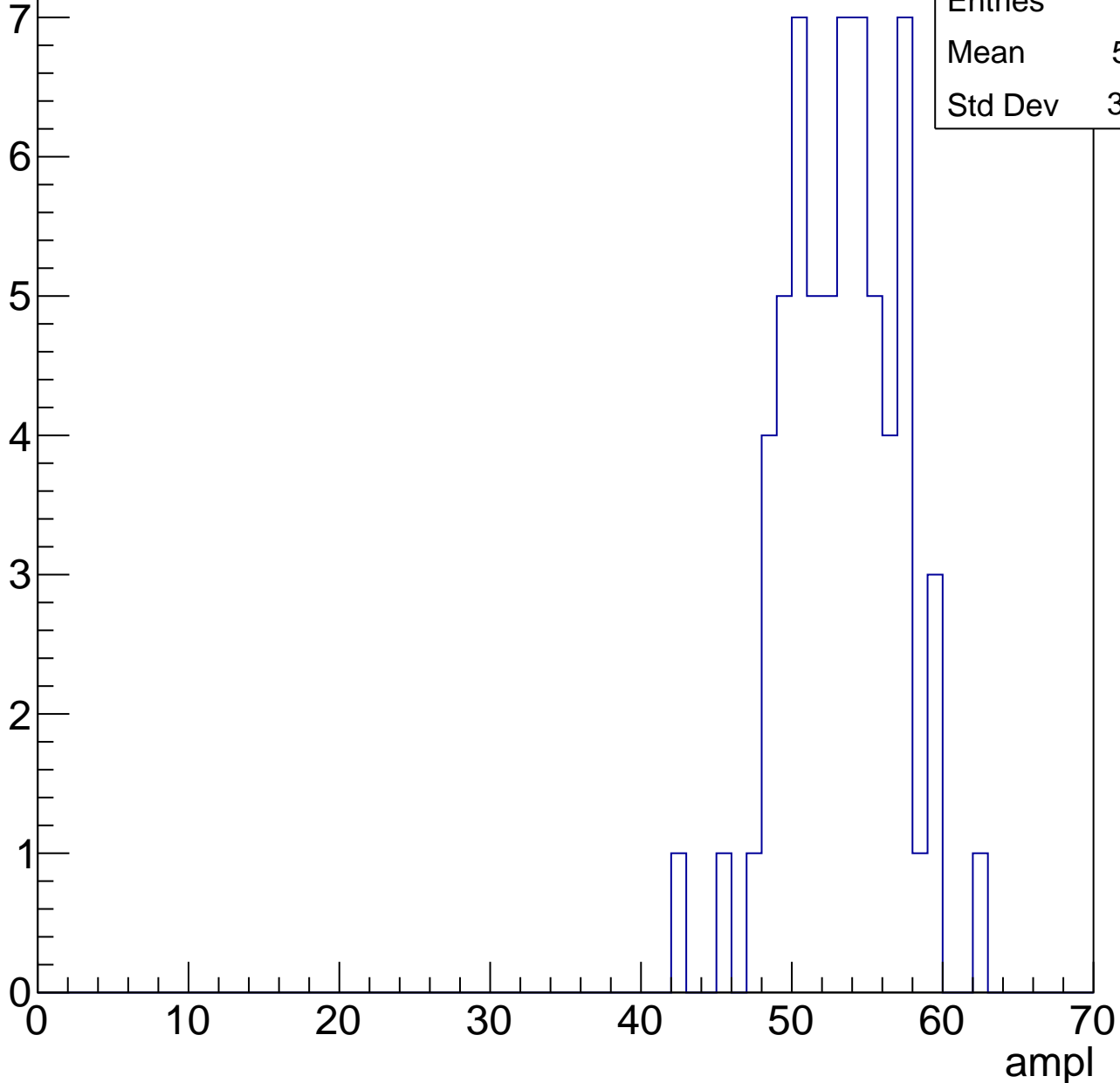


# B1L103S, U22-ch120, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	52.81
Std Dev	3.699

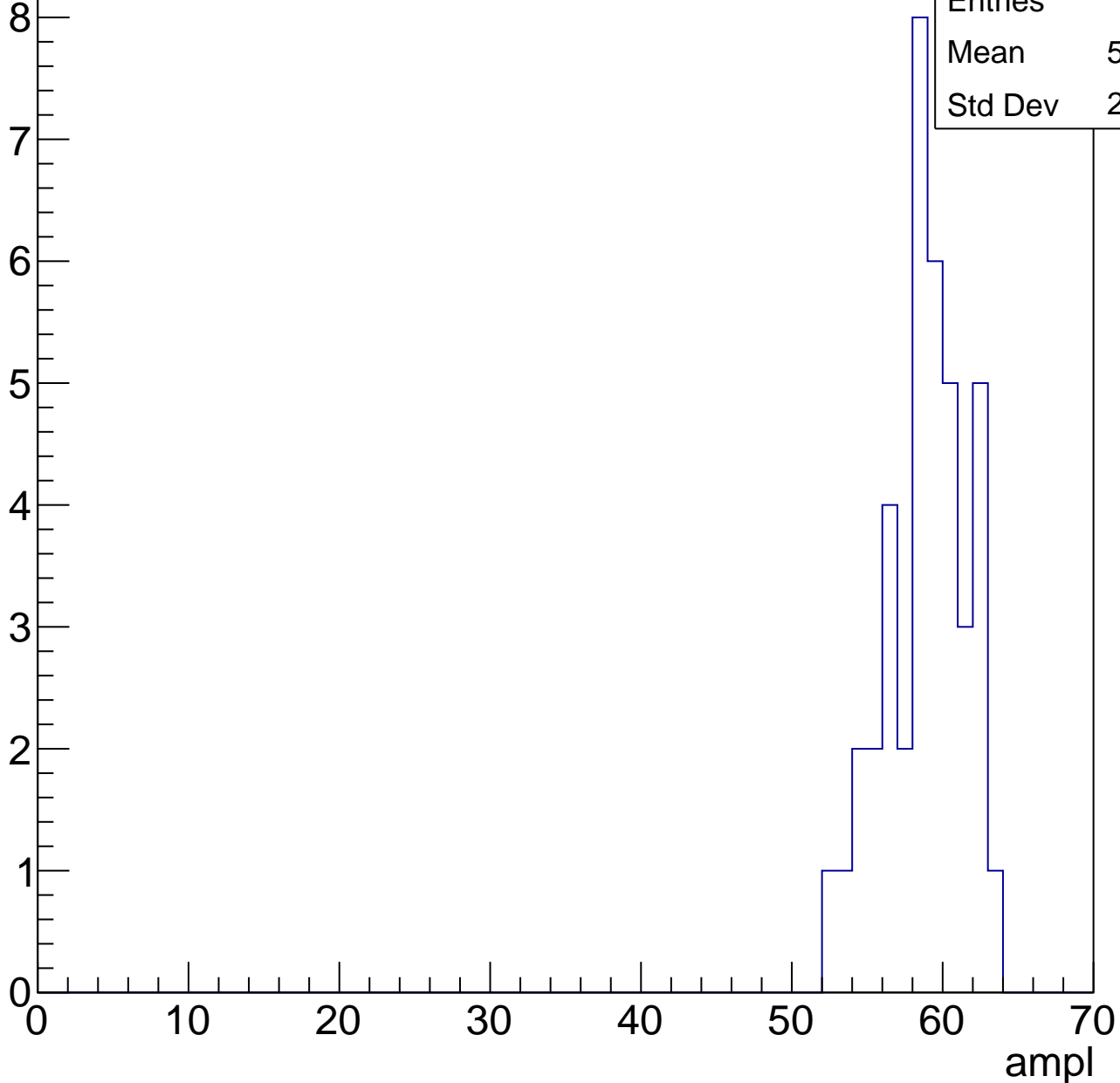


# B1L103S, U22-ch120, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	40
Mean	58.38
Std Dev	2.652

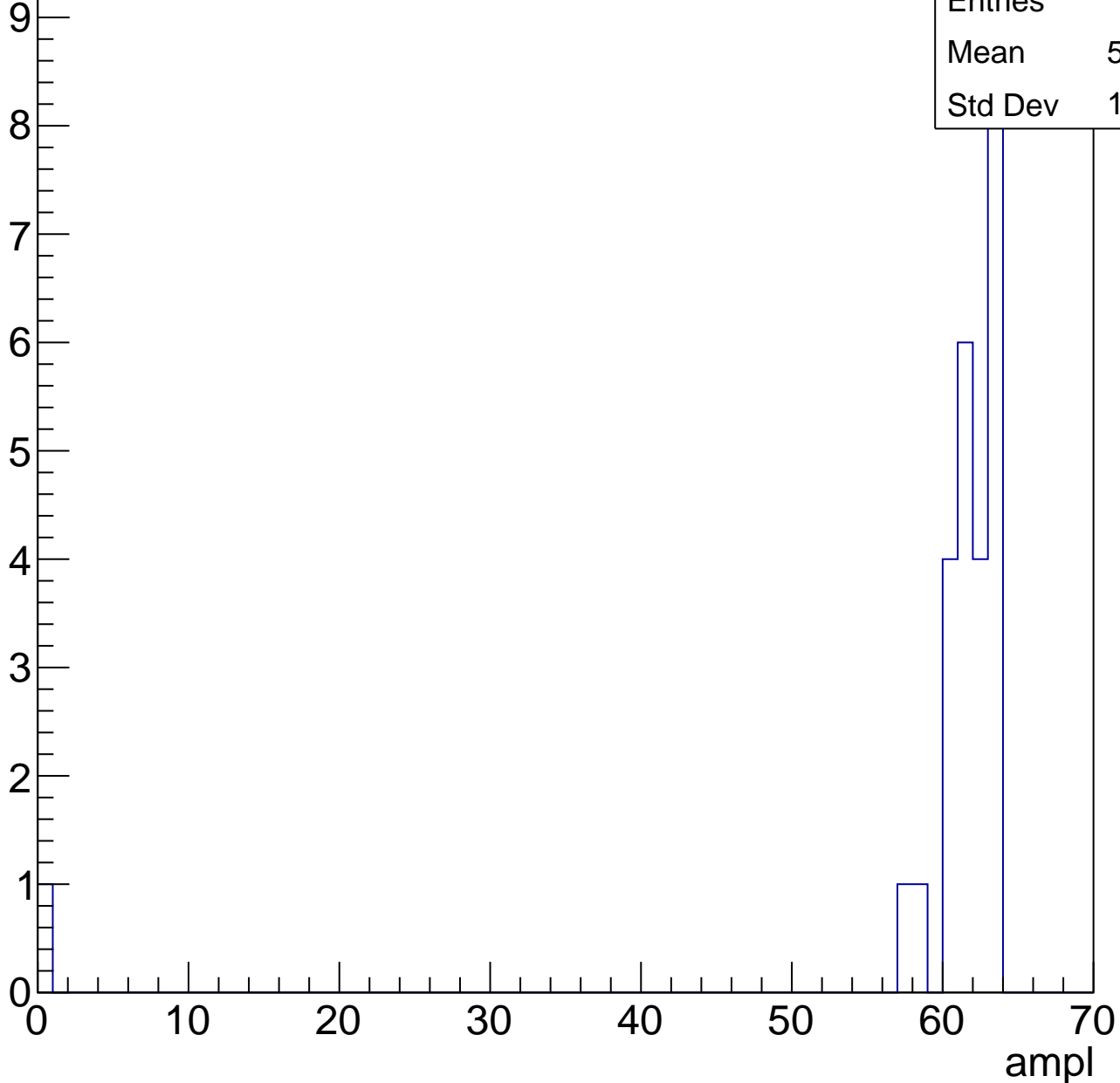


# B1L103S, U22-ch120, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	26
Mean	59.08
Std Dev	11.92



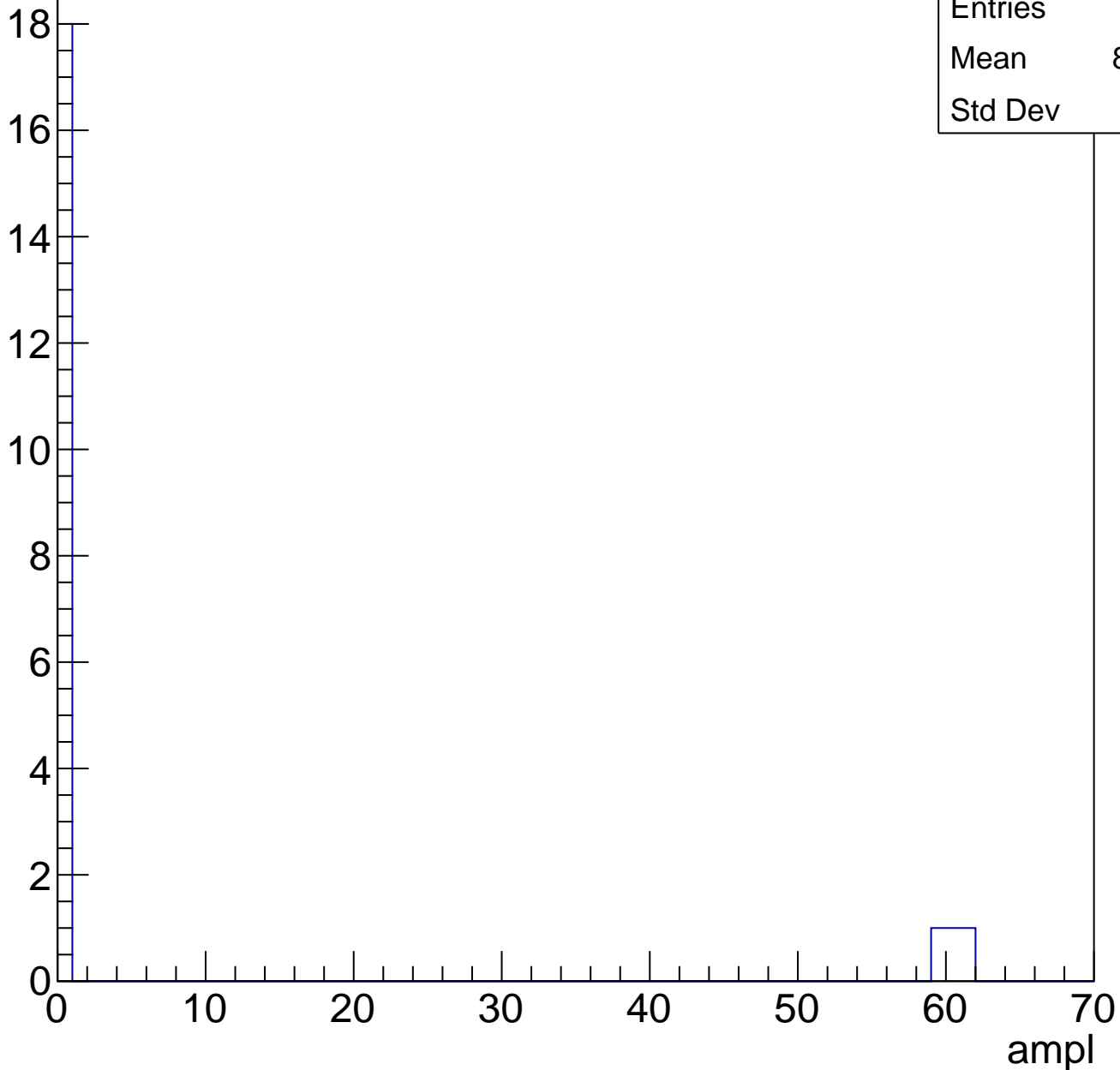


# B1L103S, U22-ch120, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	8.571
Std Dev	21

Entry



# B1L103S, U22-ch121, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	78
Mean	21.64
Std Dev	11.11

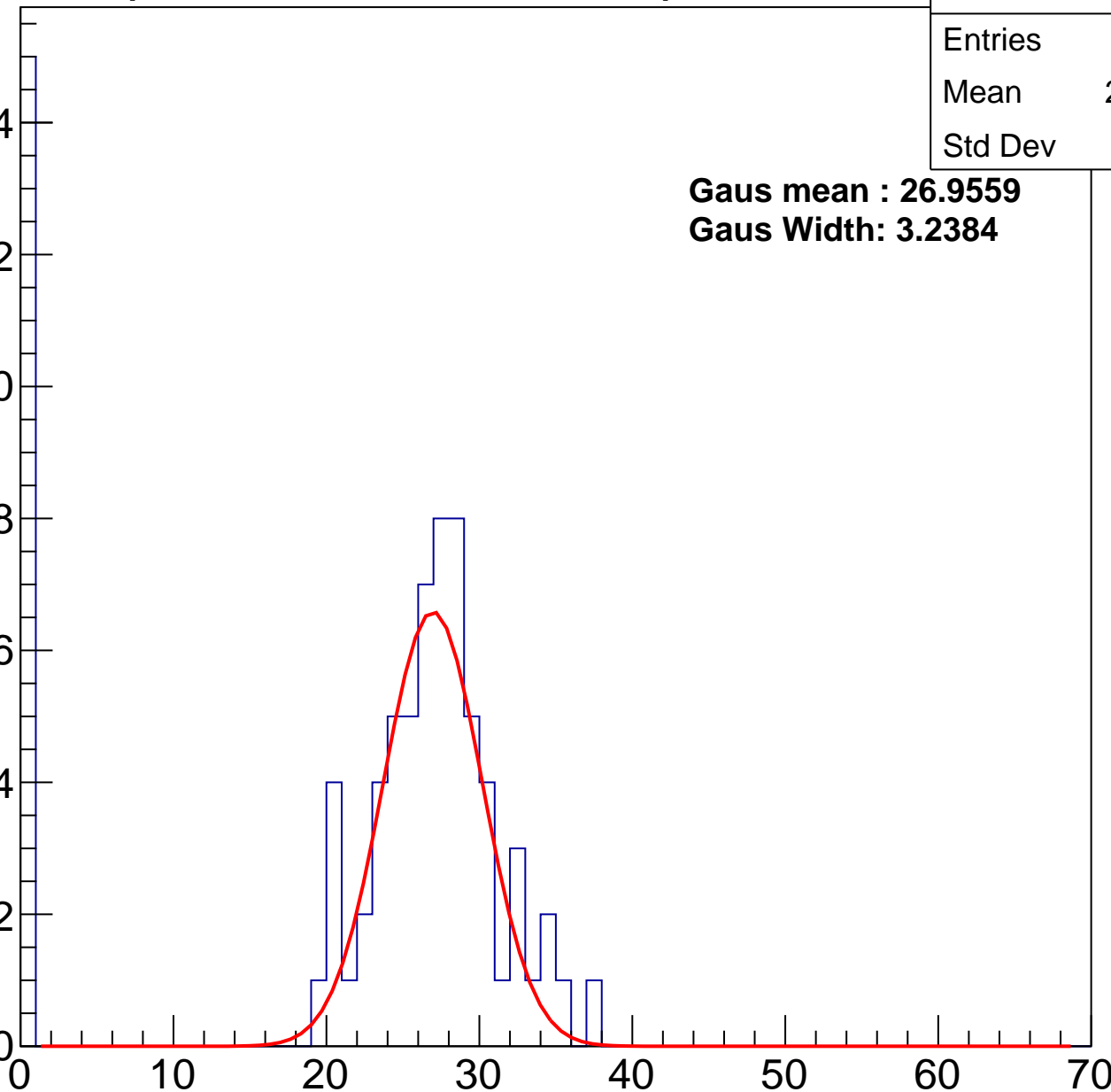
**Gaus mean : 26.9559**

**Gaus Width: 3.2384**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L103S, U22-ch121, adc1

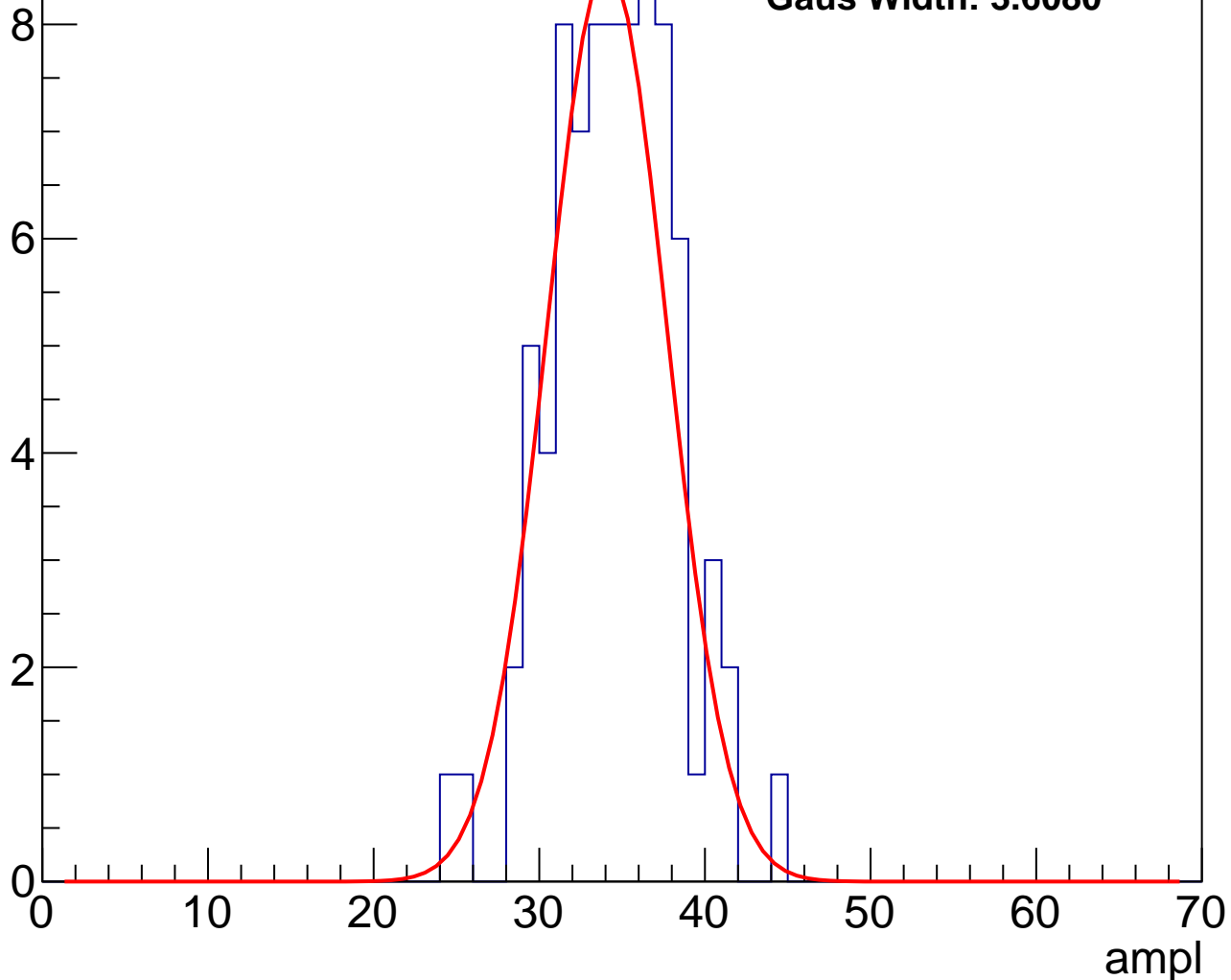
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	83
Mean	34.06
Std Dev	3.635

**Gaus mean : 34.0892**

**Gaus Width: 3.6080**



# B1L103S, U22-ch121, adc2

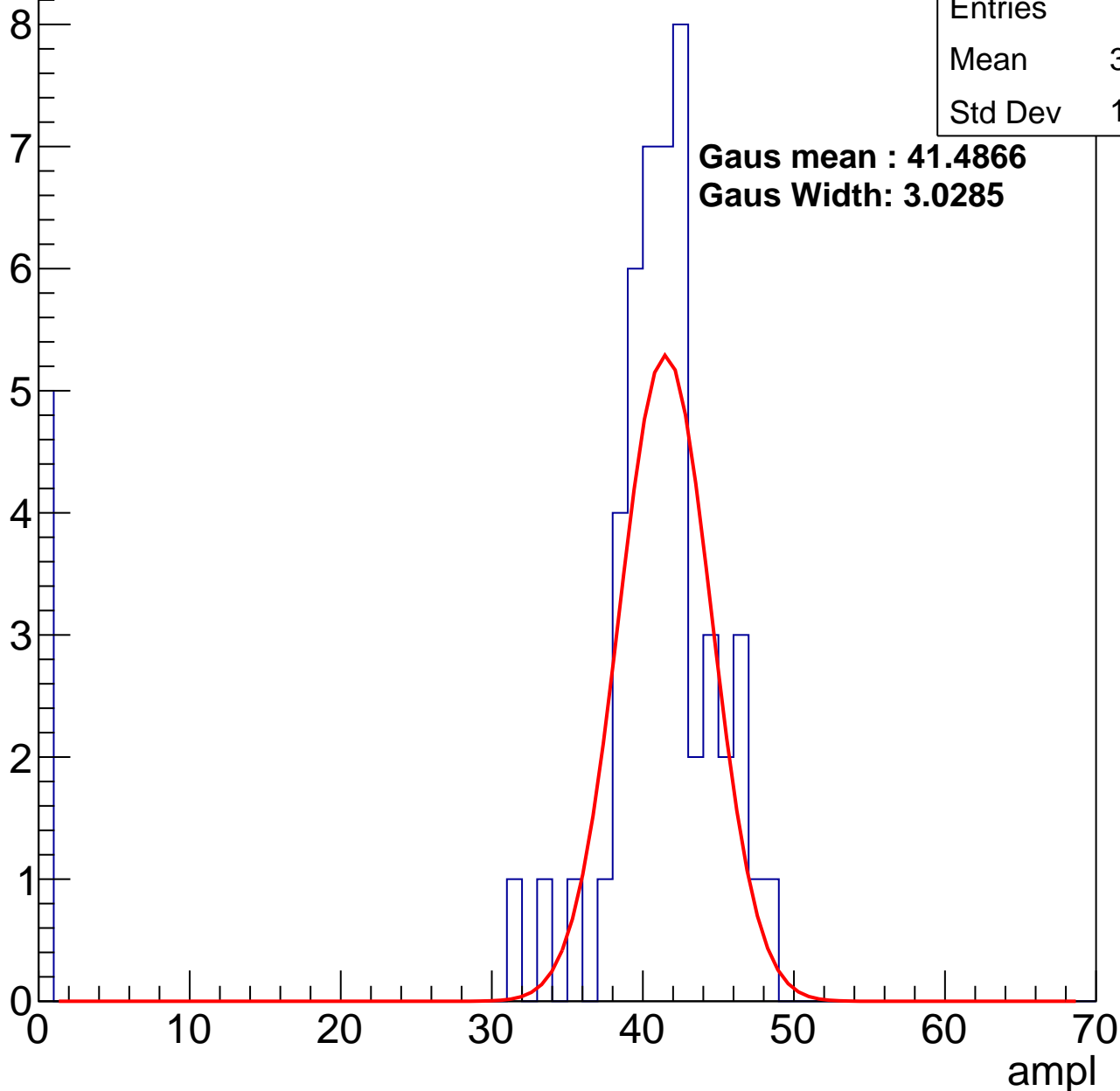
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	53
Mean	37.09
Std Dev	12.37

**Gaus mean : 41.4866**

**Gaus Width: 3.0285**

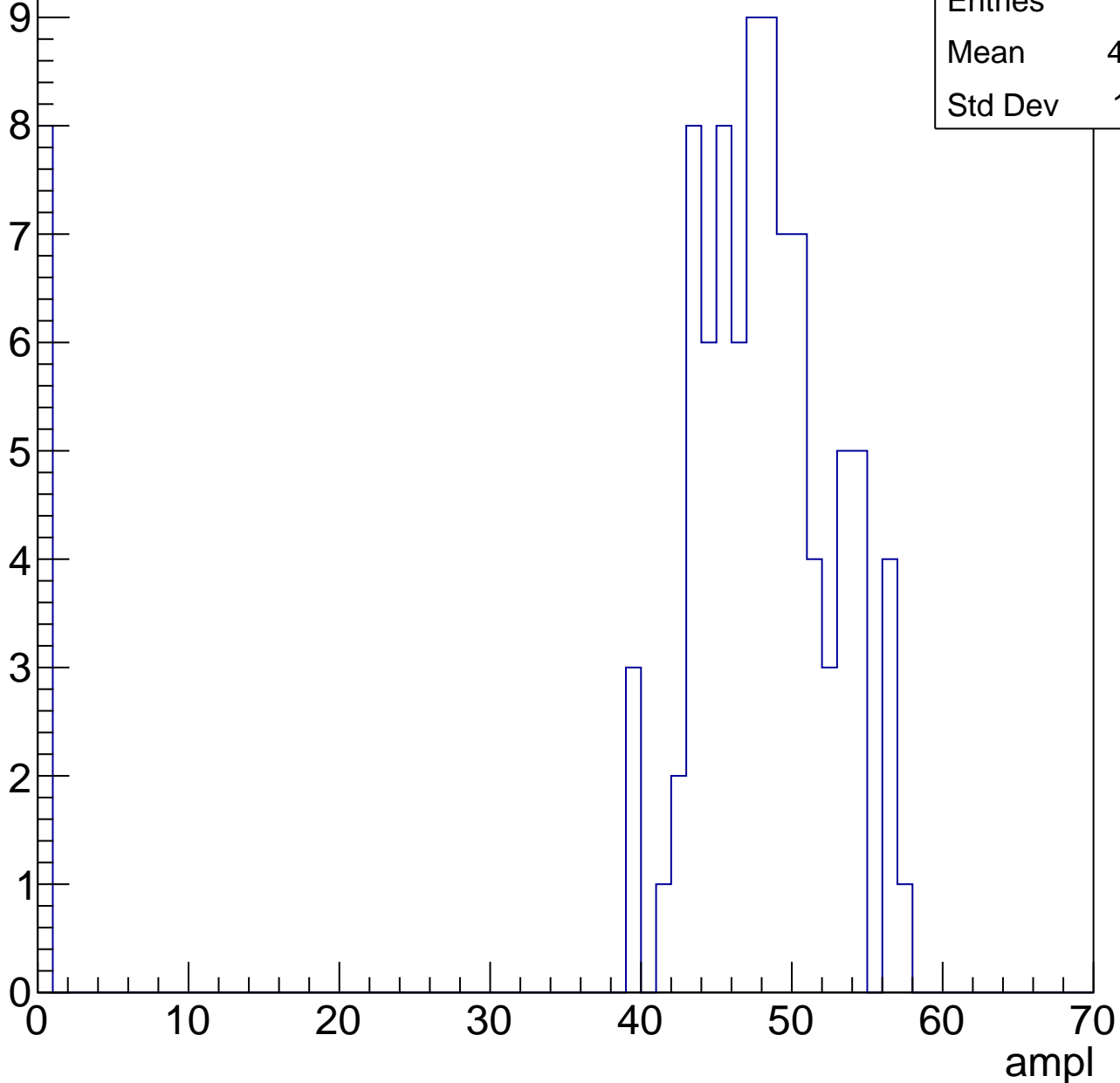


# B1L103S, U22-ch121, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	96
Mean	43.85
Std Dev	13.81

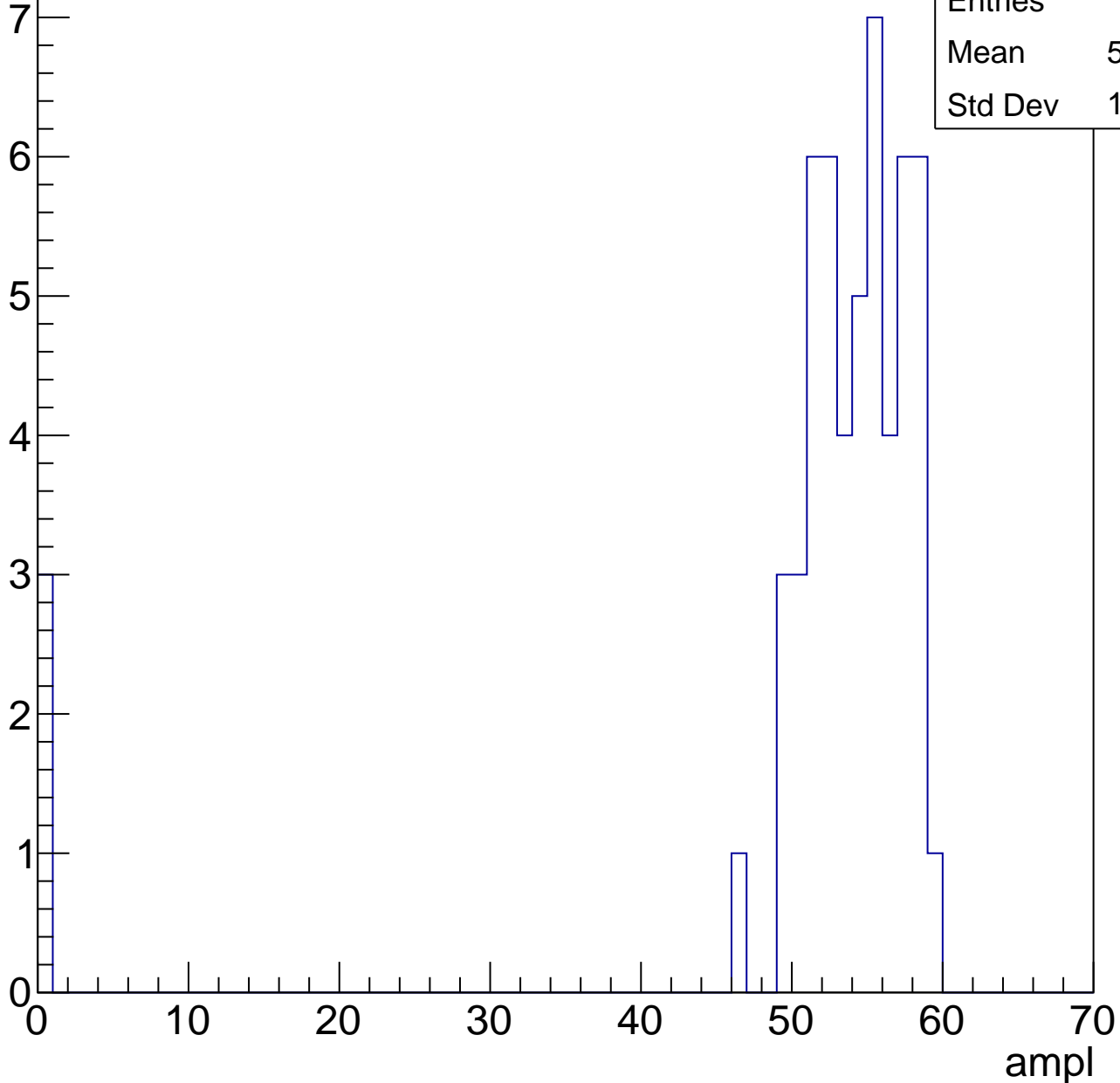


# B1L103S, U22-ch121, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	55
Mean	50.93
Std Dev	12.57

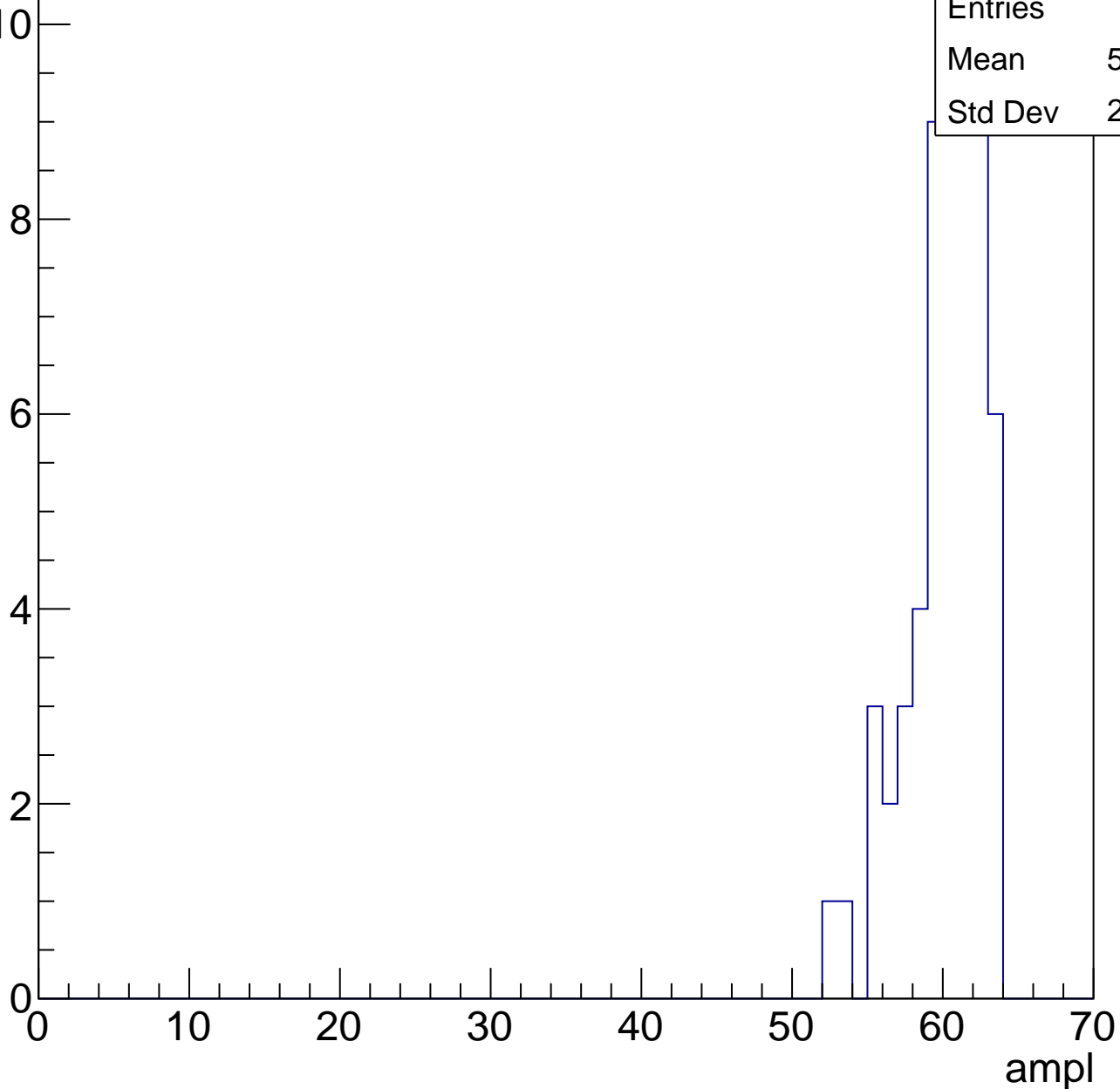


# B1L103S, U22-ch121, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	59.67
Std Dev	2.529



# B1L103S, U22-ch121, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch121, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.15
Std Dev	13.73



# B1L103S, U22-ch122, adc0

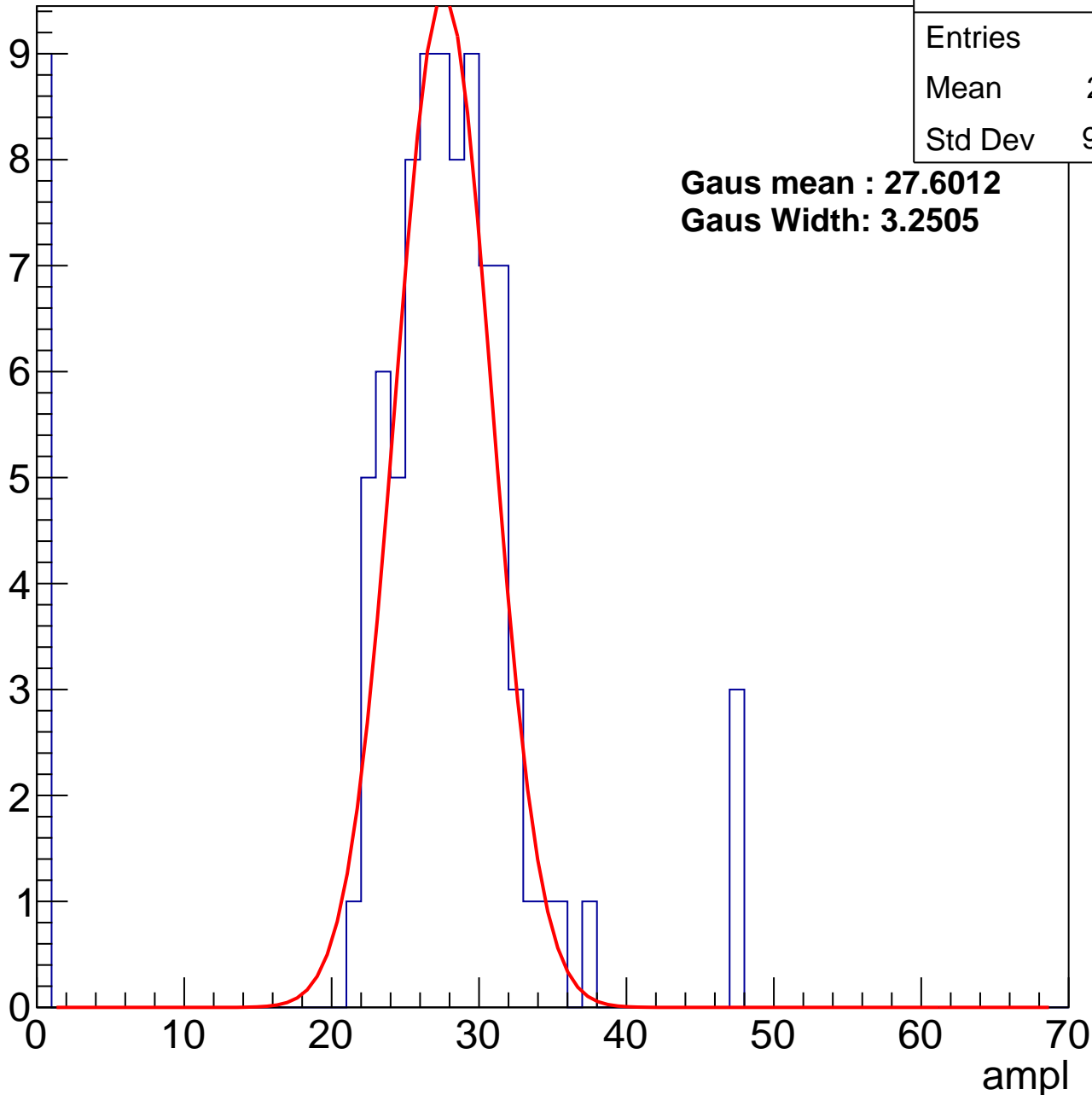
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	93
Mean	25.31
Std Dev	9.494

**Gaus mean : 27.6012**

**Gaus Width: 3.2505**



# B1L103S, U22-ch122, adc1

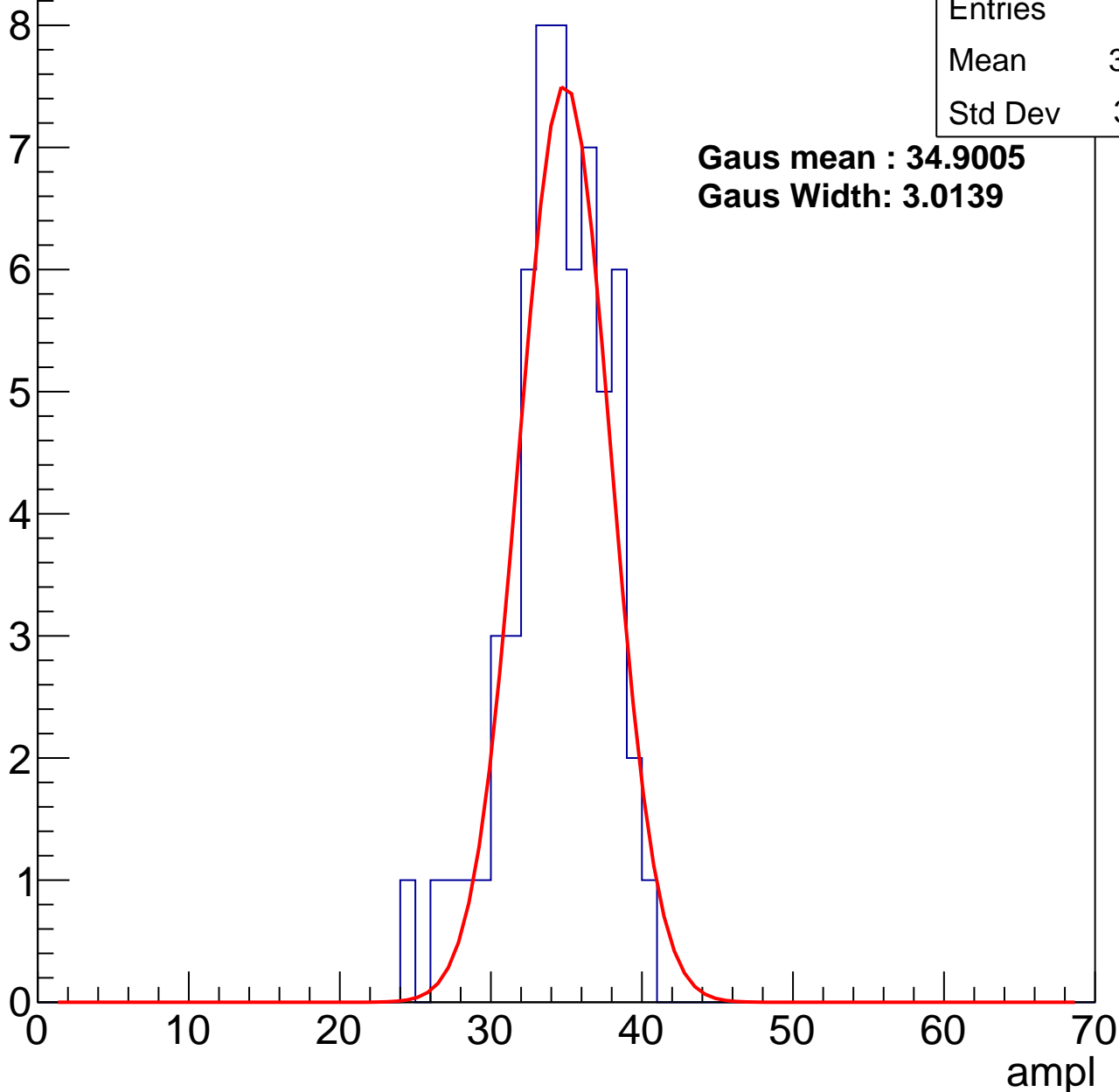
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	60
Mean	33.97
Std Dev	3.271

**Gaus mean : 34.9005**

**Gaus Width: 3.0139**



# B1L103S, U22-ch122, adc2

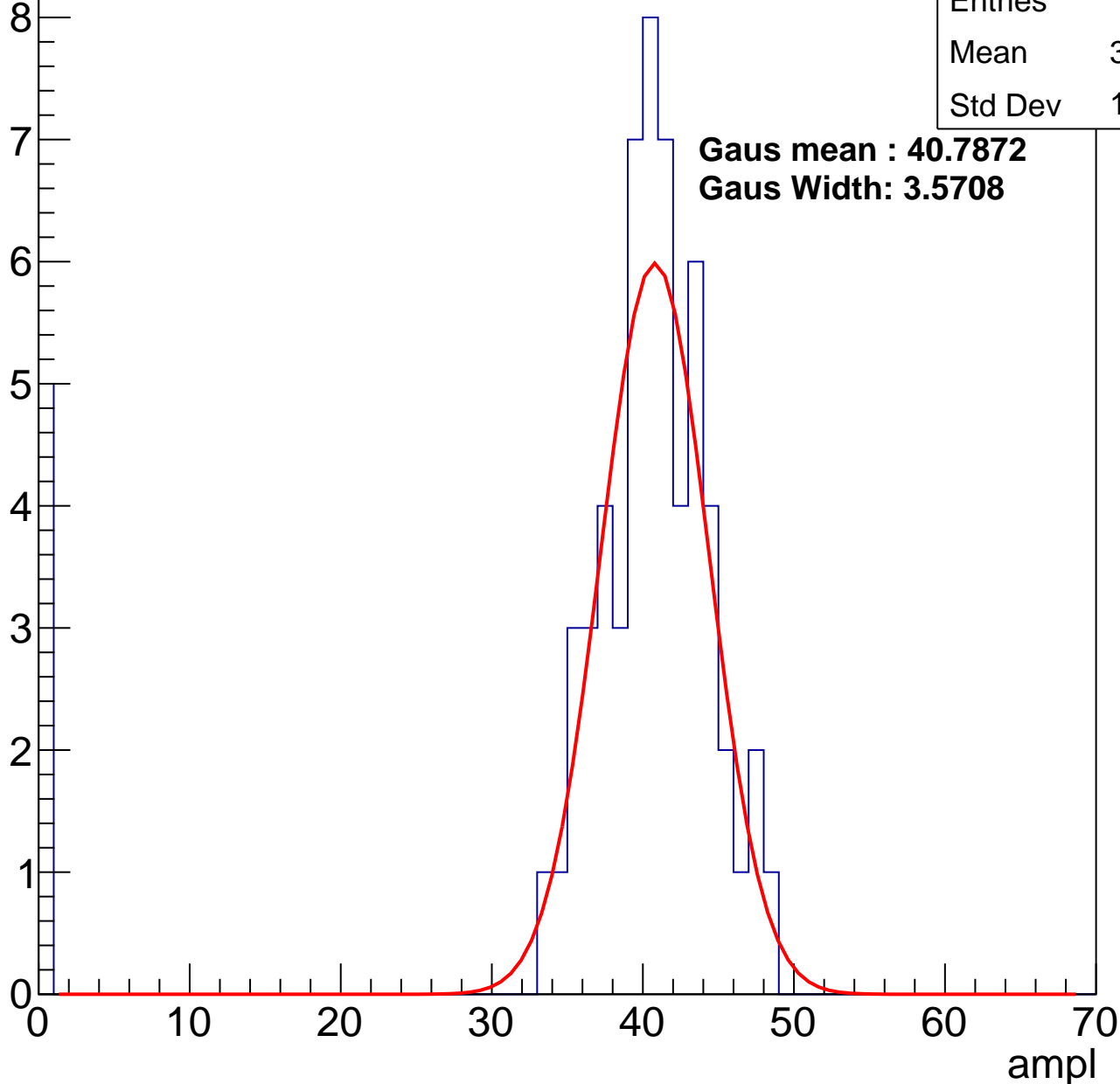
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	37.13
Std Dev	11.46

**Gaus mean : 40.7872**

**Gaus Width: 3.5708**

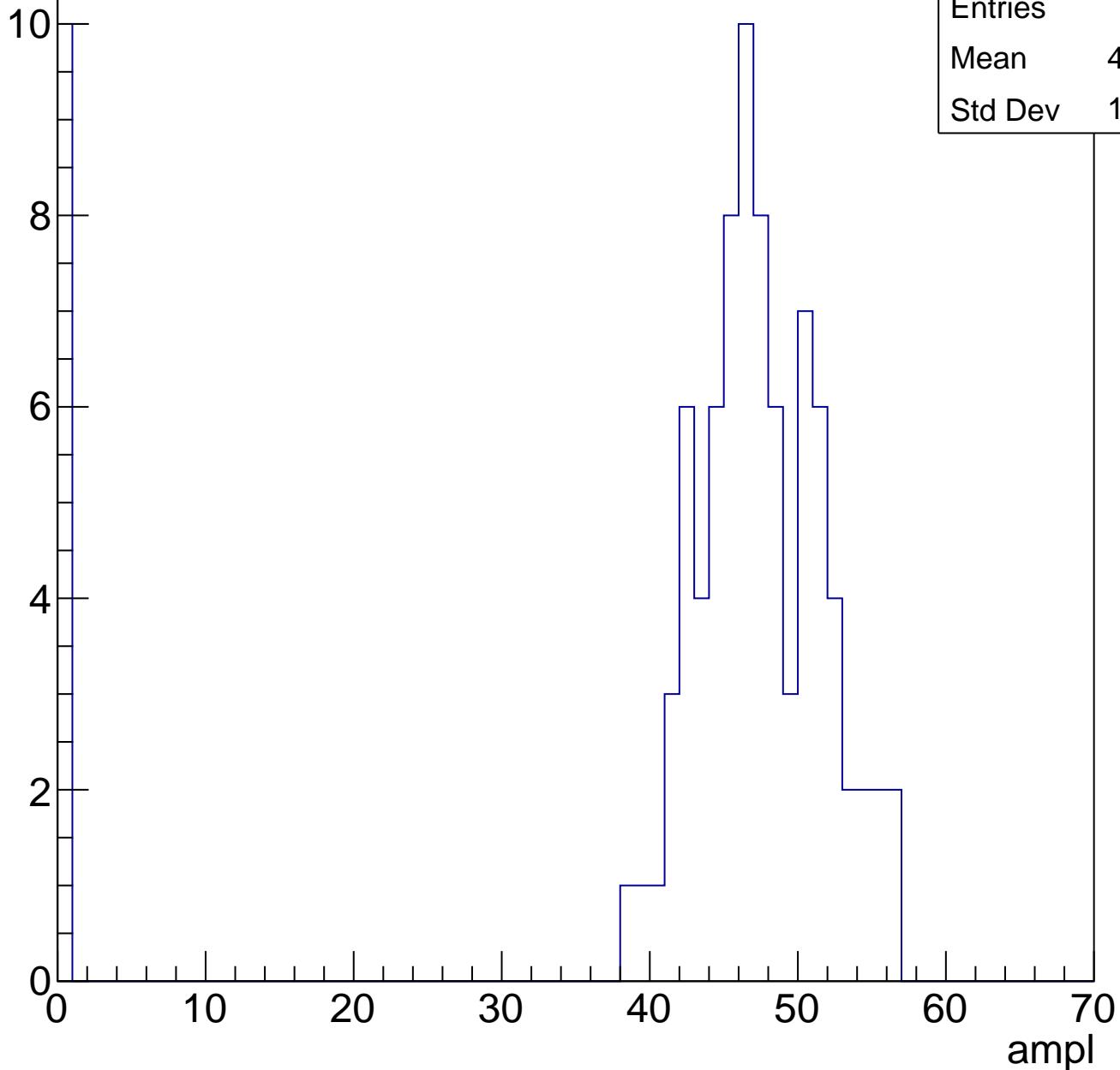


# B1L103S, U22-ch122, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	92
Mean	41.95
Std Dev	15.14

Entry

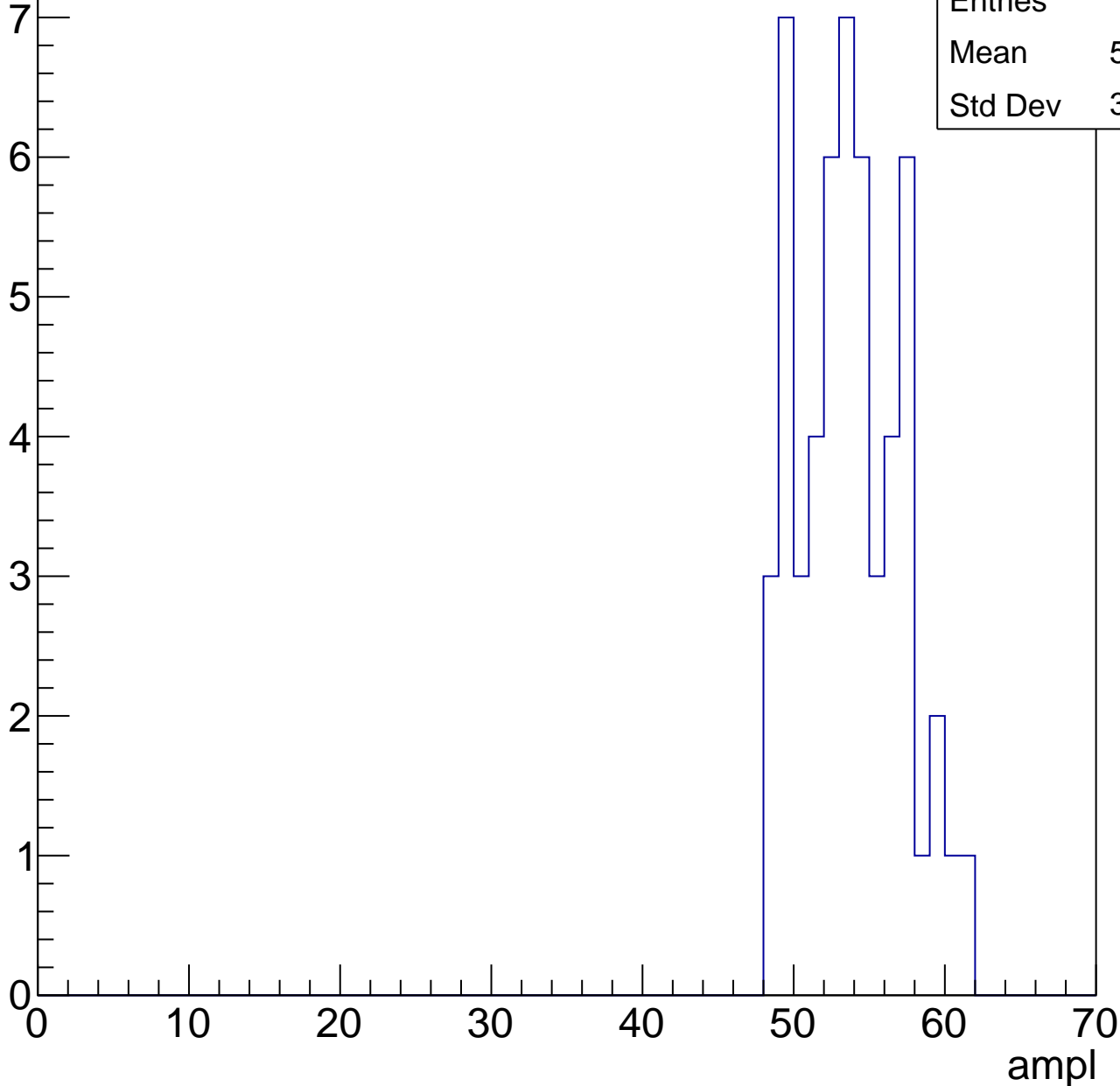


# B1L103S, U22-ch122, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	54
Mean	53.26
Std Dev	3.312

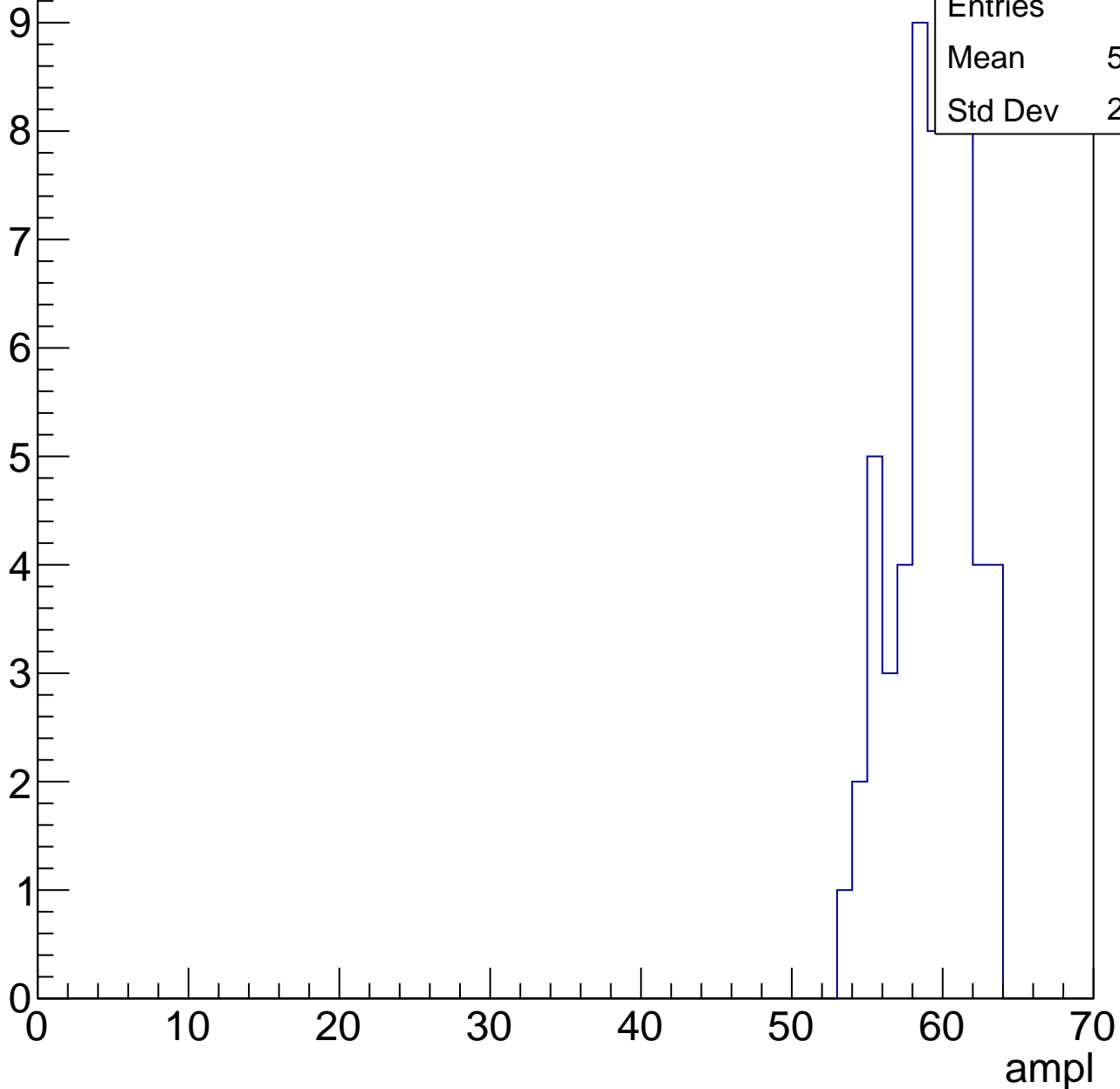


# B1L103S, U22-ch122, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	57
Mean	58.86
Std Dev	2.516

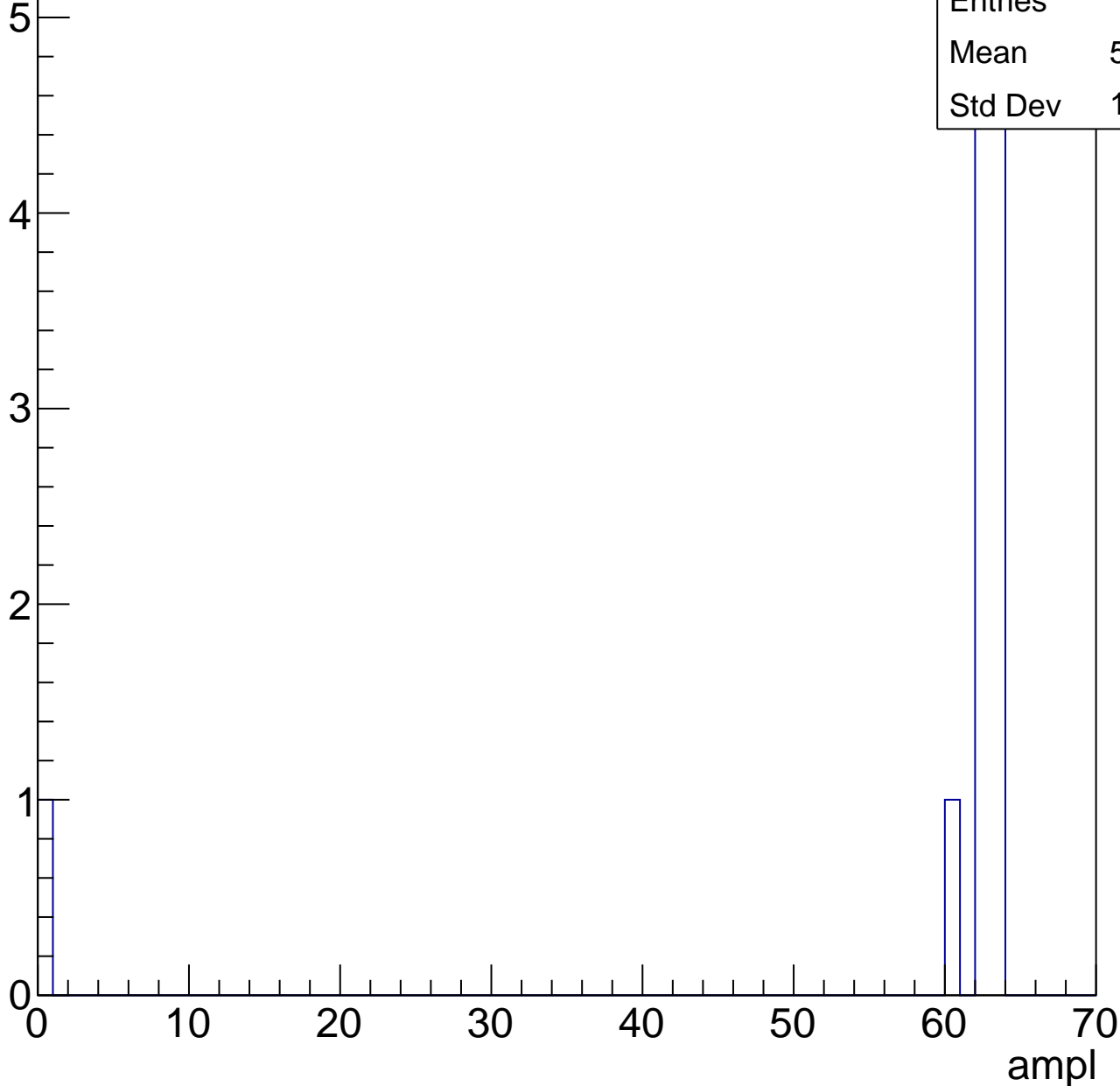


# B1L103S, U22-ch122, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	57.08
Std Dev	17.23



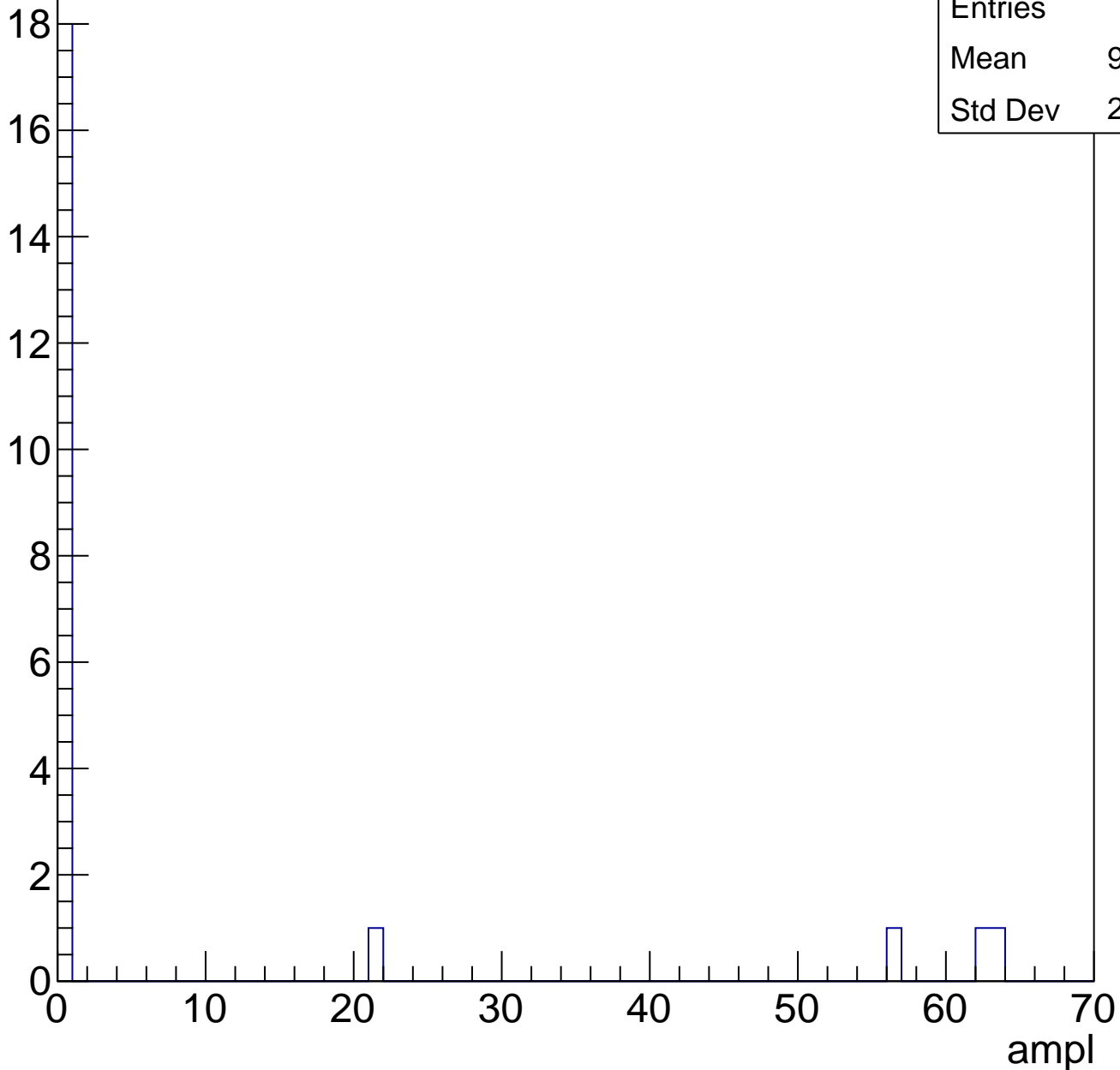


# B1L103S, U22-ch122, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	22
Mean	9.182
Std Dev	20.82

Entry



# B1L103S, U22-ch123, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	83
Mean	26.55
Std Dev	8.133

**Gaus mean : 29.1763**

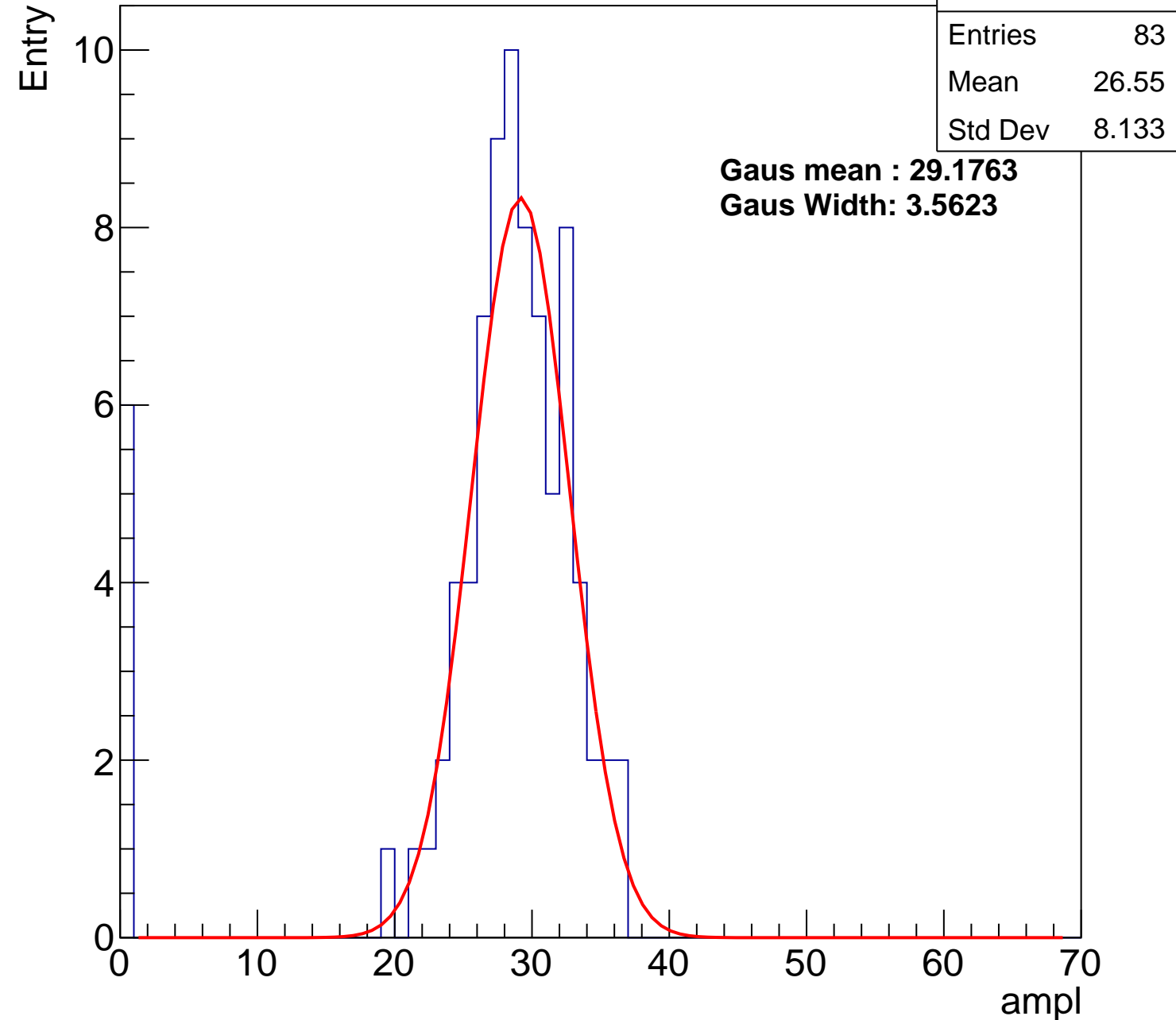
**Gaus Width: 3.5623**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch123, adc1

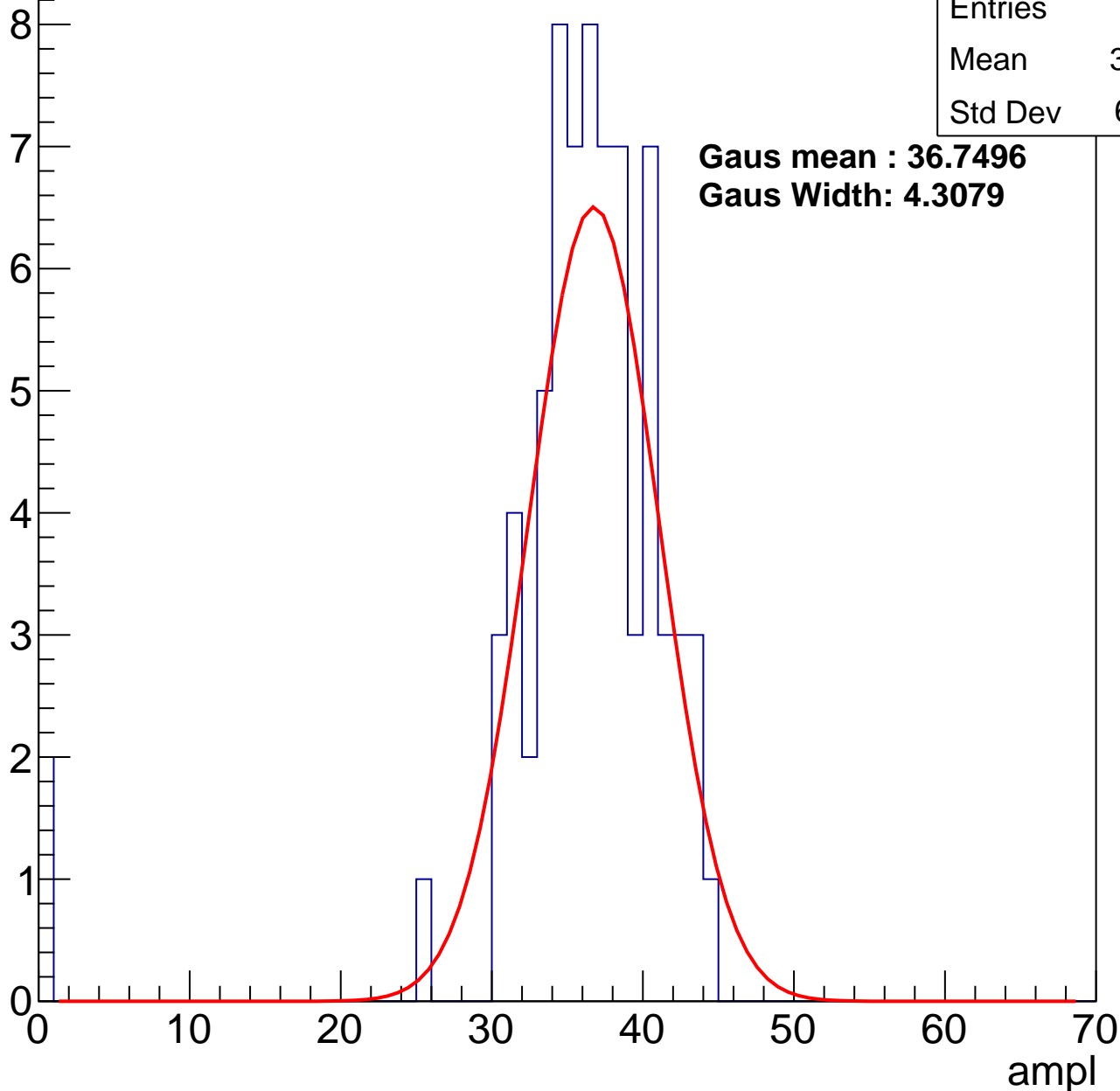
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	74
Mean	35.36
Std Dev	6.951

**Gaus mean : 36.7496**

**Gaus Width: 4.3079**



# B1L103S, U22-ch123, adc2

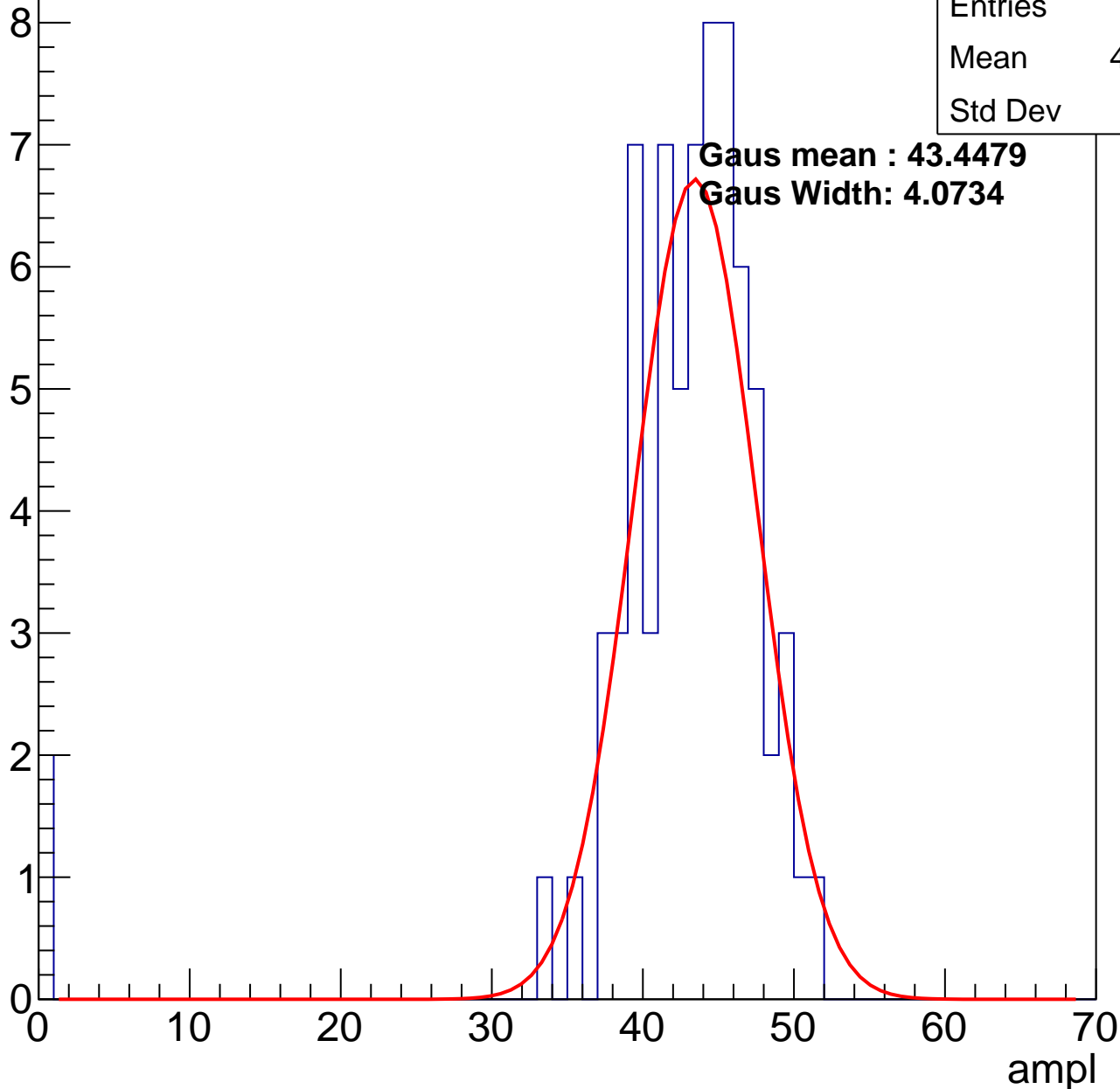
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	73
Mean	41.79
Std Dev	7.9

**Gaus mean : 43.4479**

**Gaus Width: 4.0734**



# B1L103S, U22-ch123, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	66
Mean	41.62
Std Dev	18.88

Entry

10

8

6

4

2

0

0

10

20

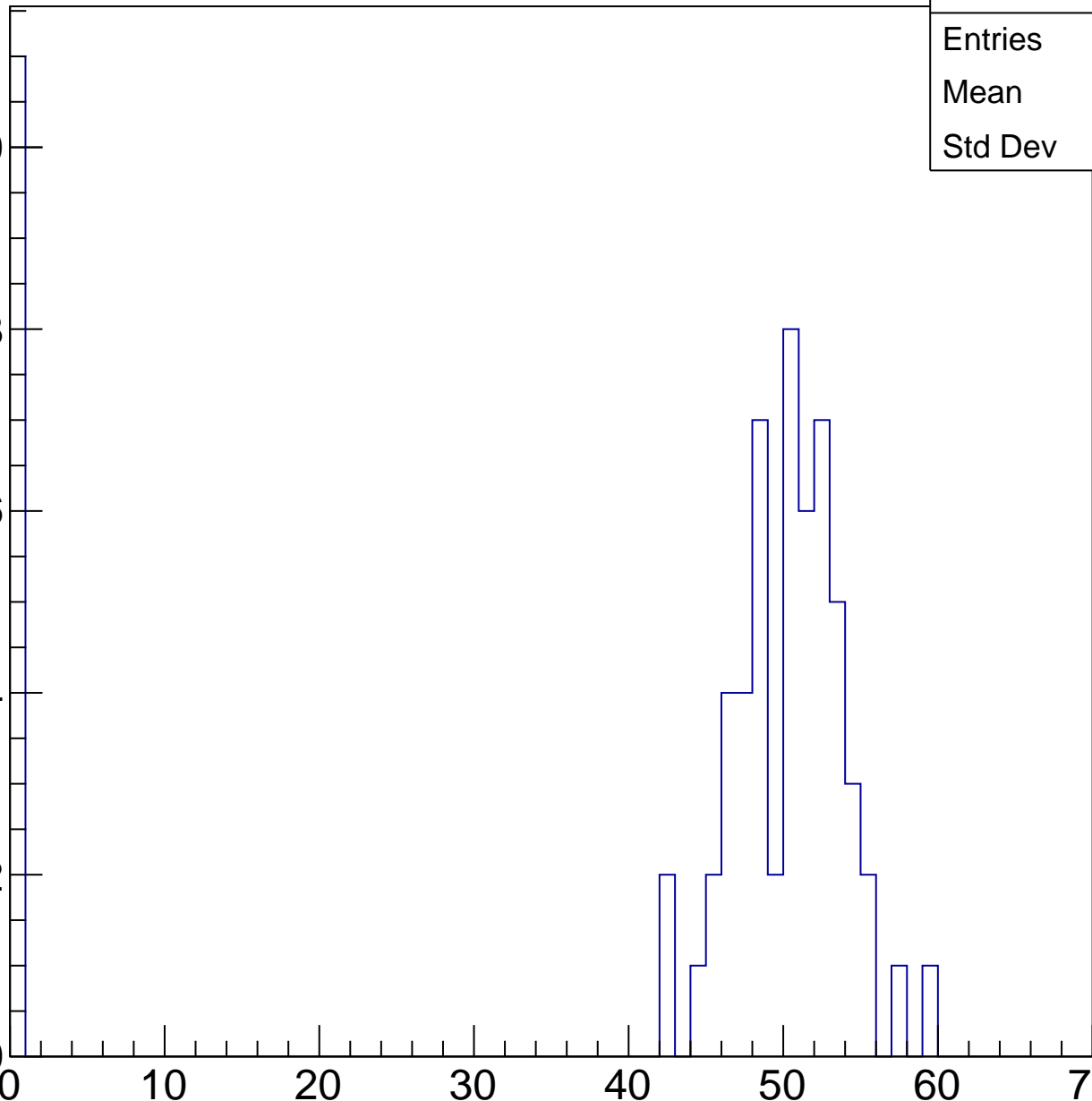
30

40

50

60

ampl

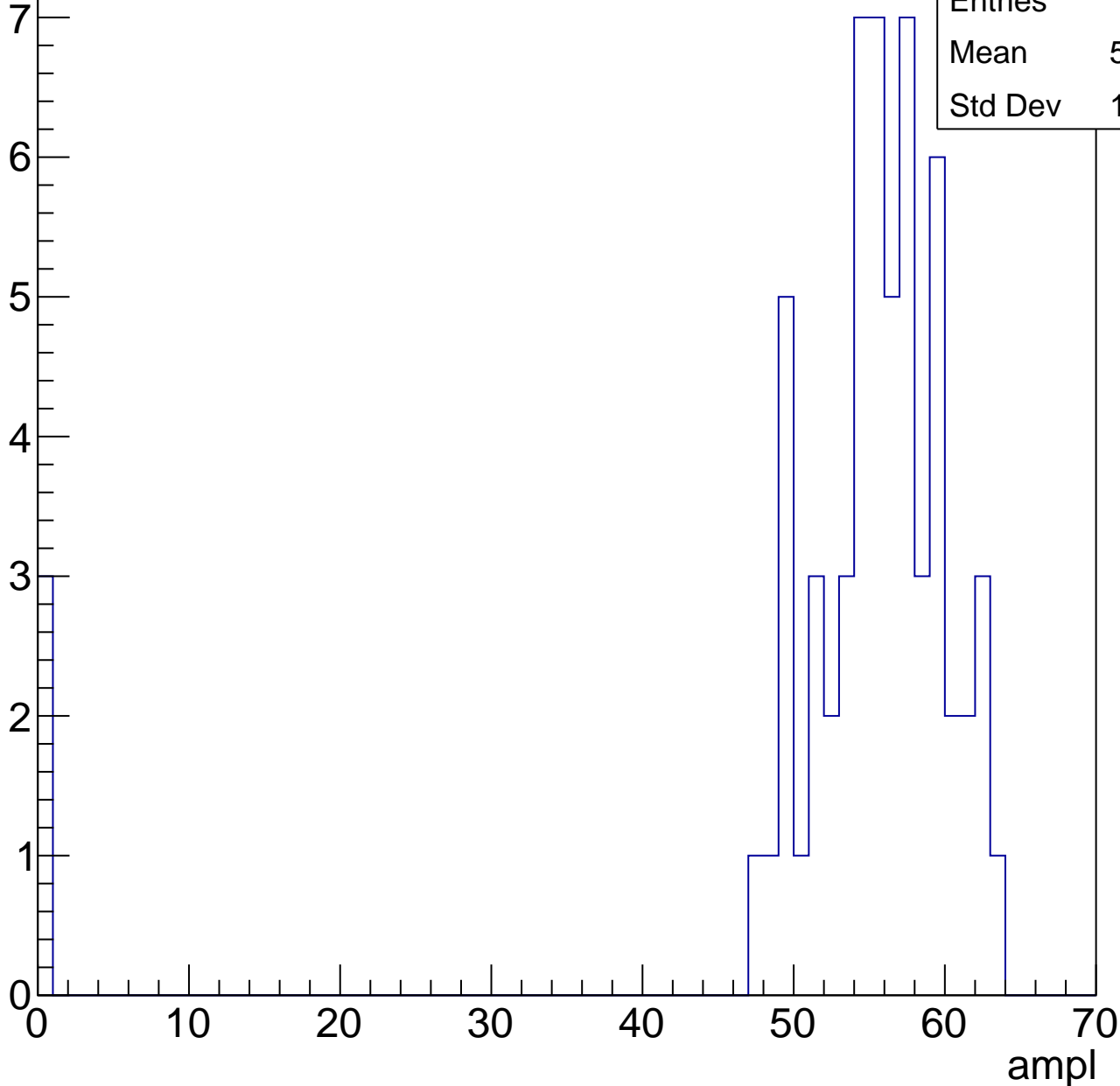


# B1L103S, U22-ch123, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	52.69
Std Dev	12.47

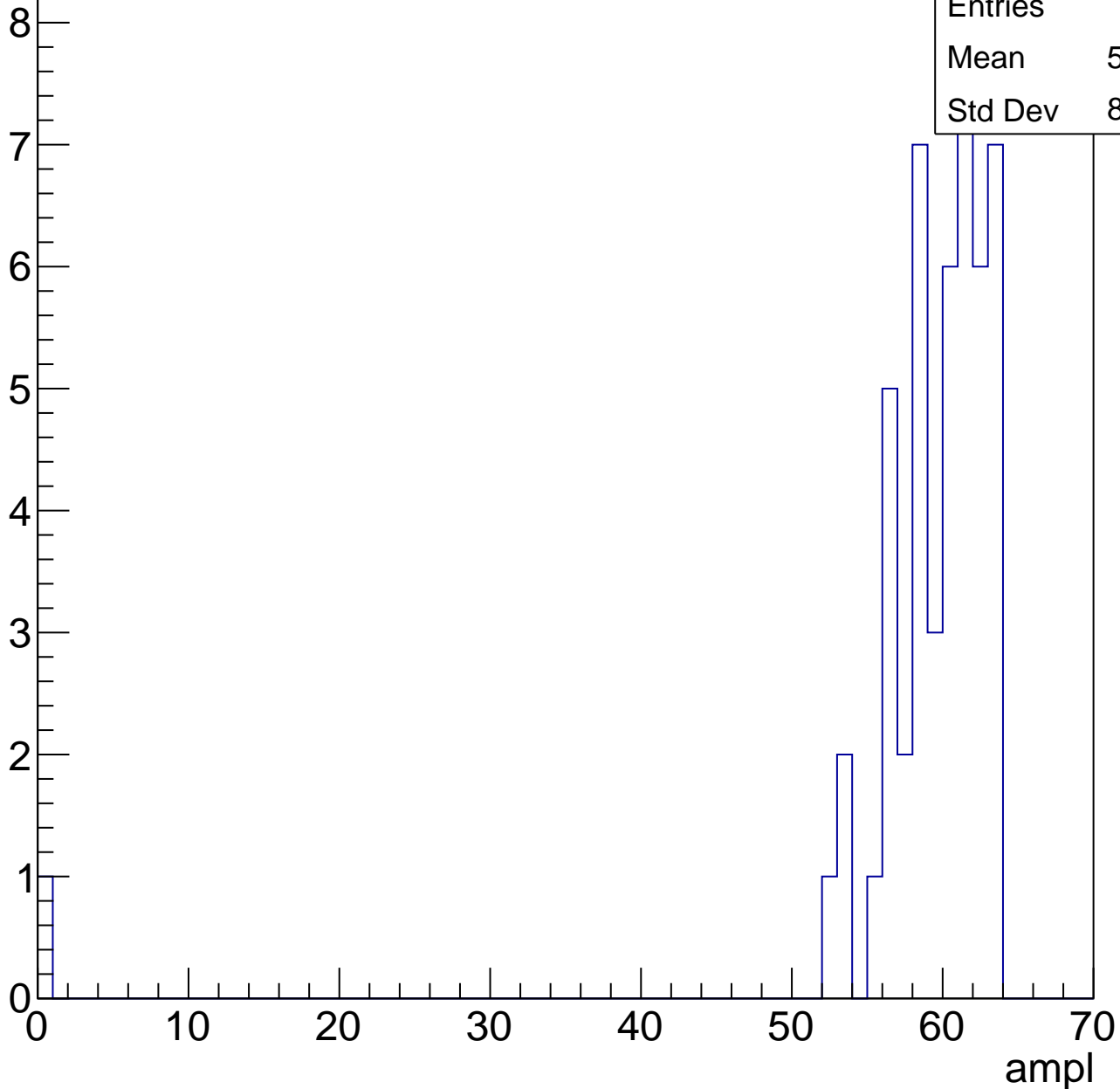


# B1L103S, U22-ch123, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	49
Mean	58.18
Std Dev	8.863



# B1L103S, U22-ch123, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

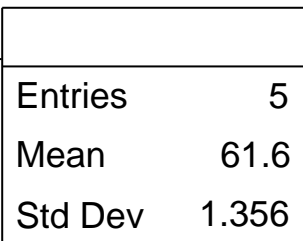
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.6
Std Dev	1.356

0 10 20 30 40 50 60 70

ampl





# B1L103S, U22-ch123, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Entries	18
Mean	0
Std Dev	0

ampl

# B1L103S, U22-ch124, adc0

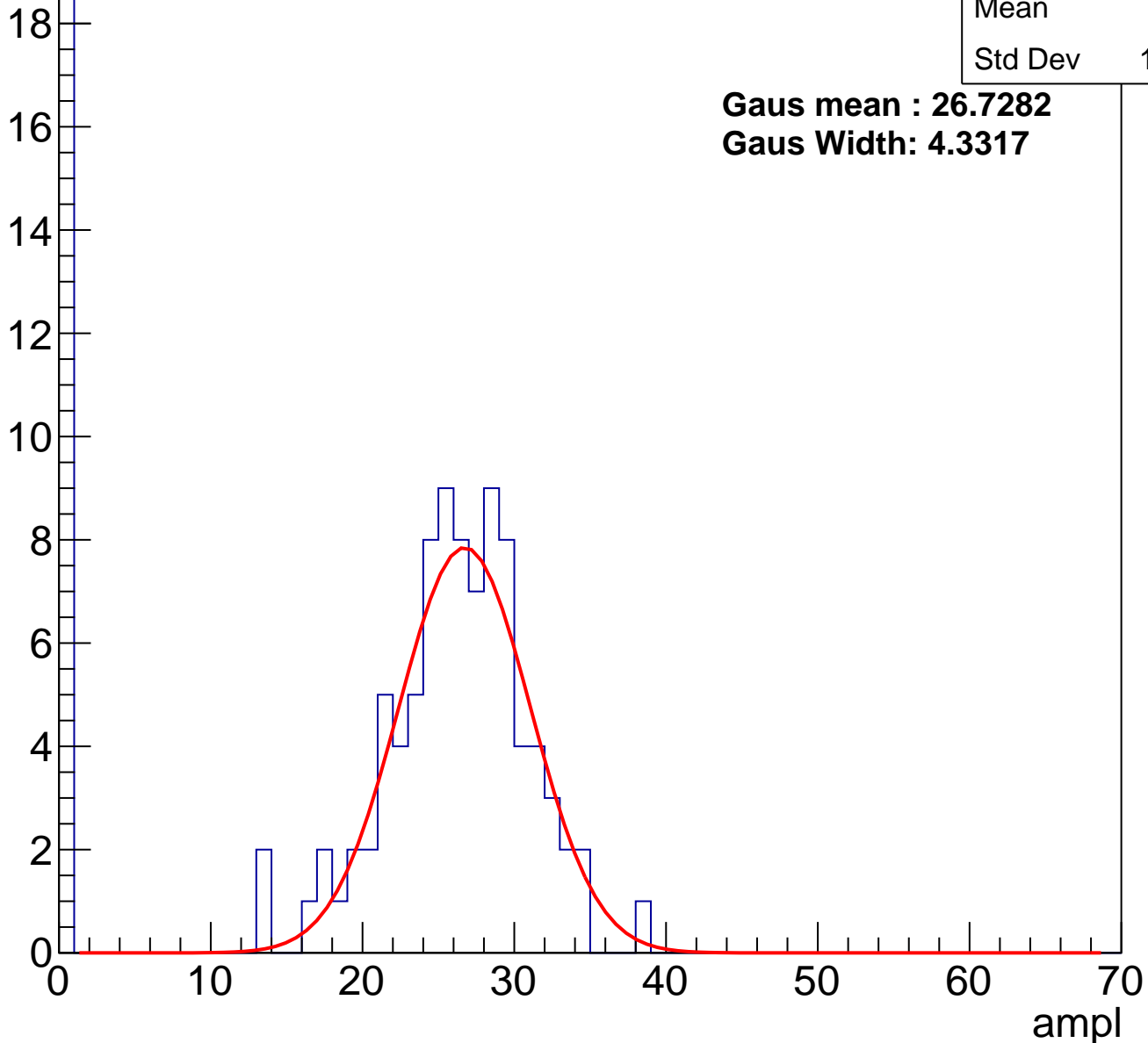
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	108
Mean	21.2
Std Dev	10.64

**Gaus mean : 26.7282**

**Gaus Width: 4.3317**

Entry



# B1L103S, U22-ch124, adc1

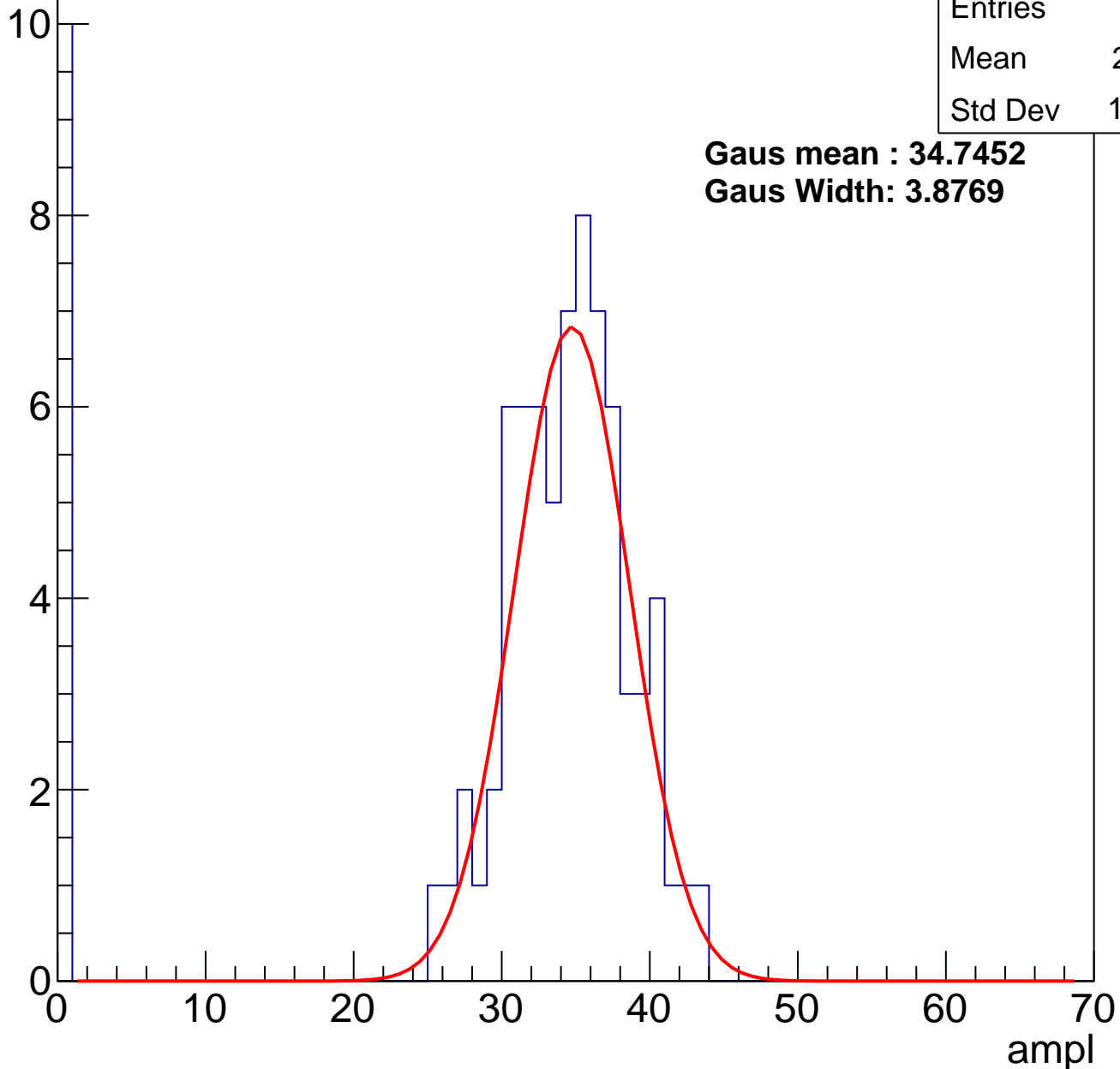
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	29.91
Std Dev	11.79

**Gaus mean : 34.7452**

**Gaus Width: 3.8769**

Entry



# B1L103S, U22-ch124, adc2

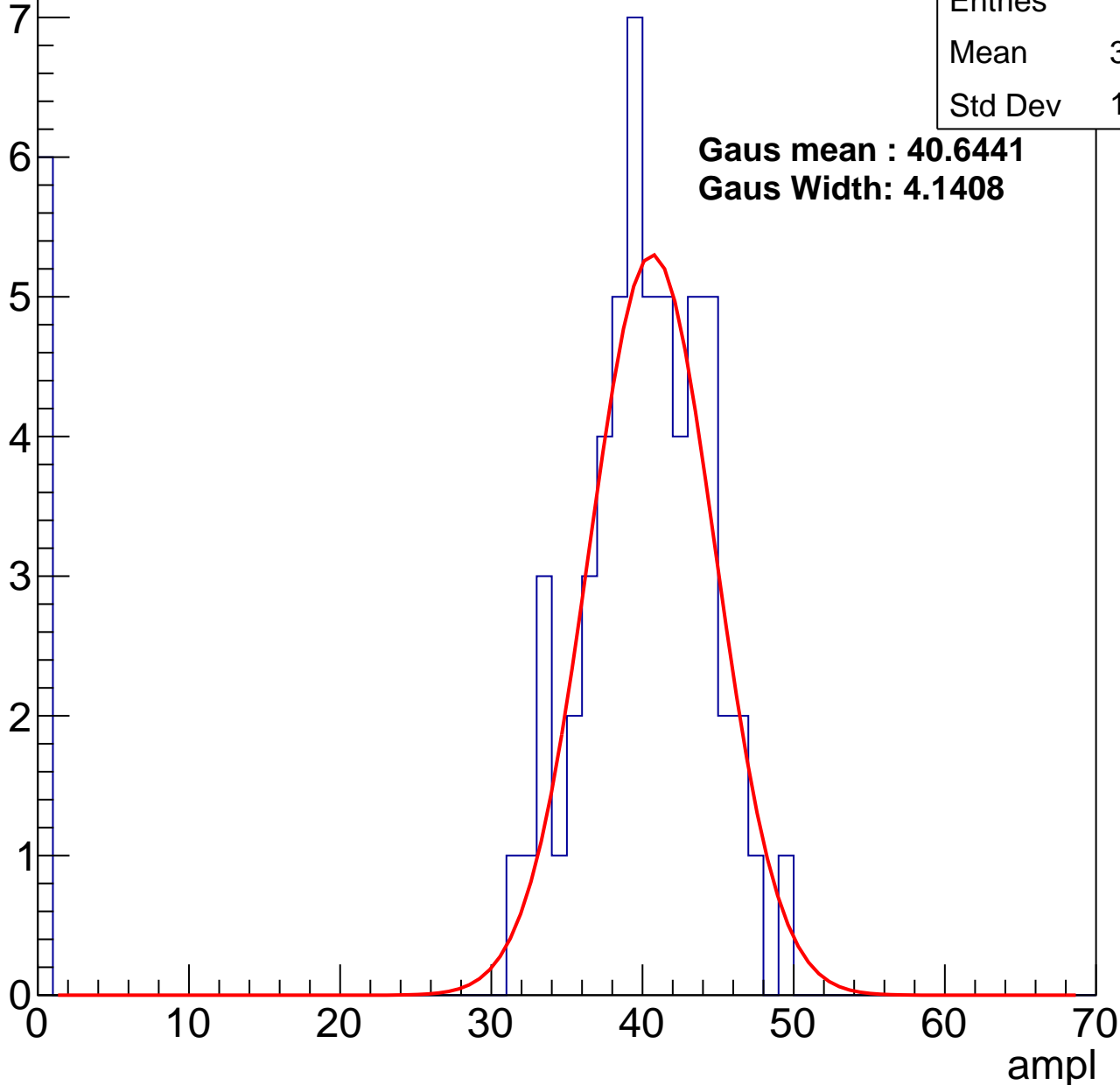
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	63
Mean	36.05
Std Dev	12.28

**Gaus mean : 40.6441**

**Gaus Width: 4.1408**

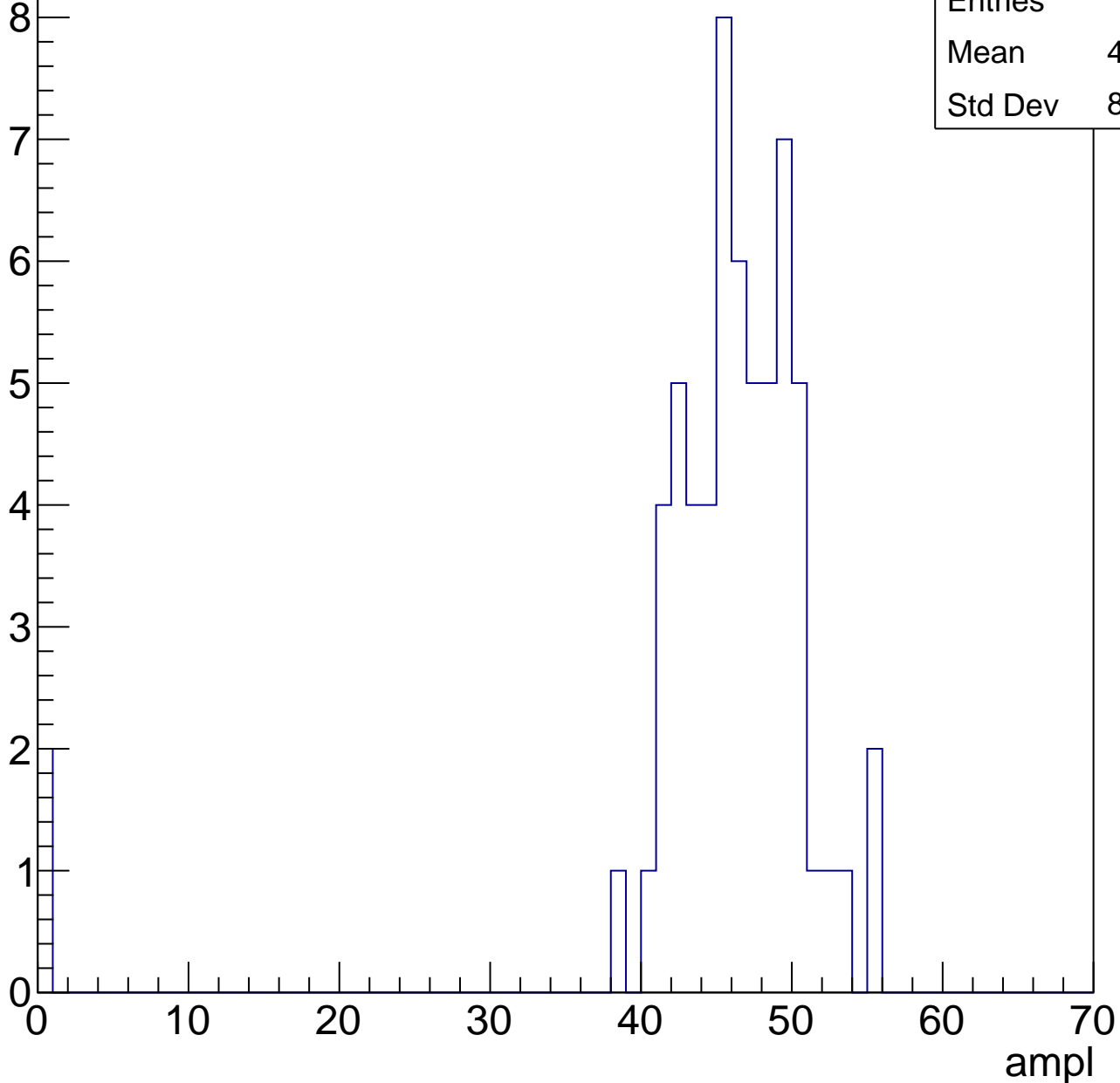


# B1L103S, U22-ch124, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	62
Mean	44.68
Std Dev	8.893

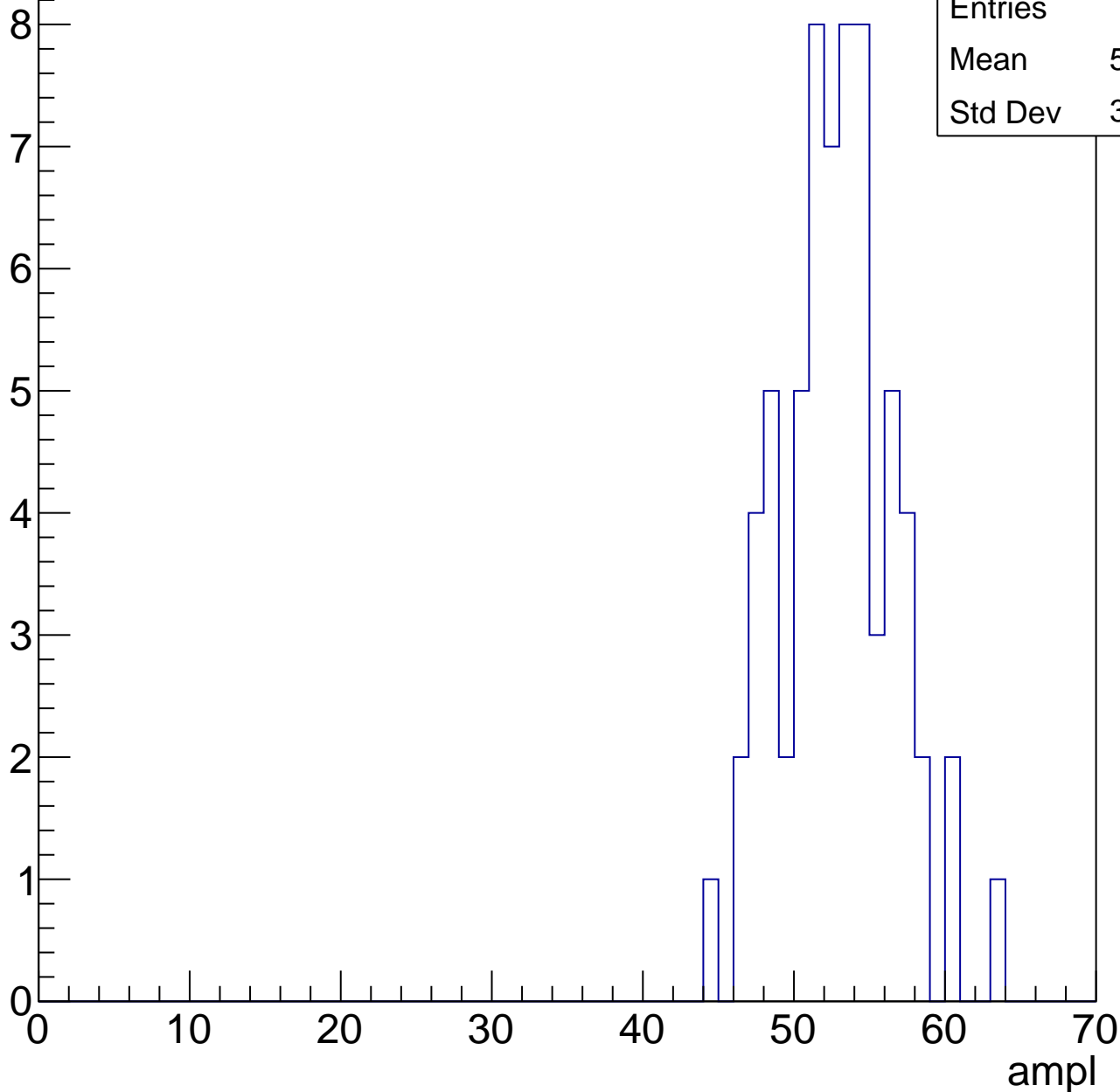


# B1L103S, U22-ch124, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	67
Mean	52.42
Std Dev	3.702

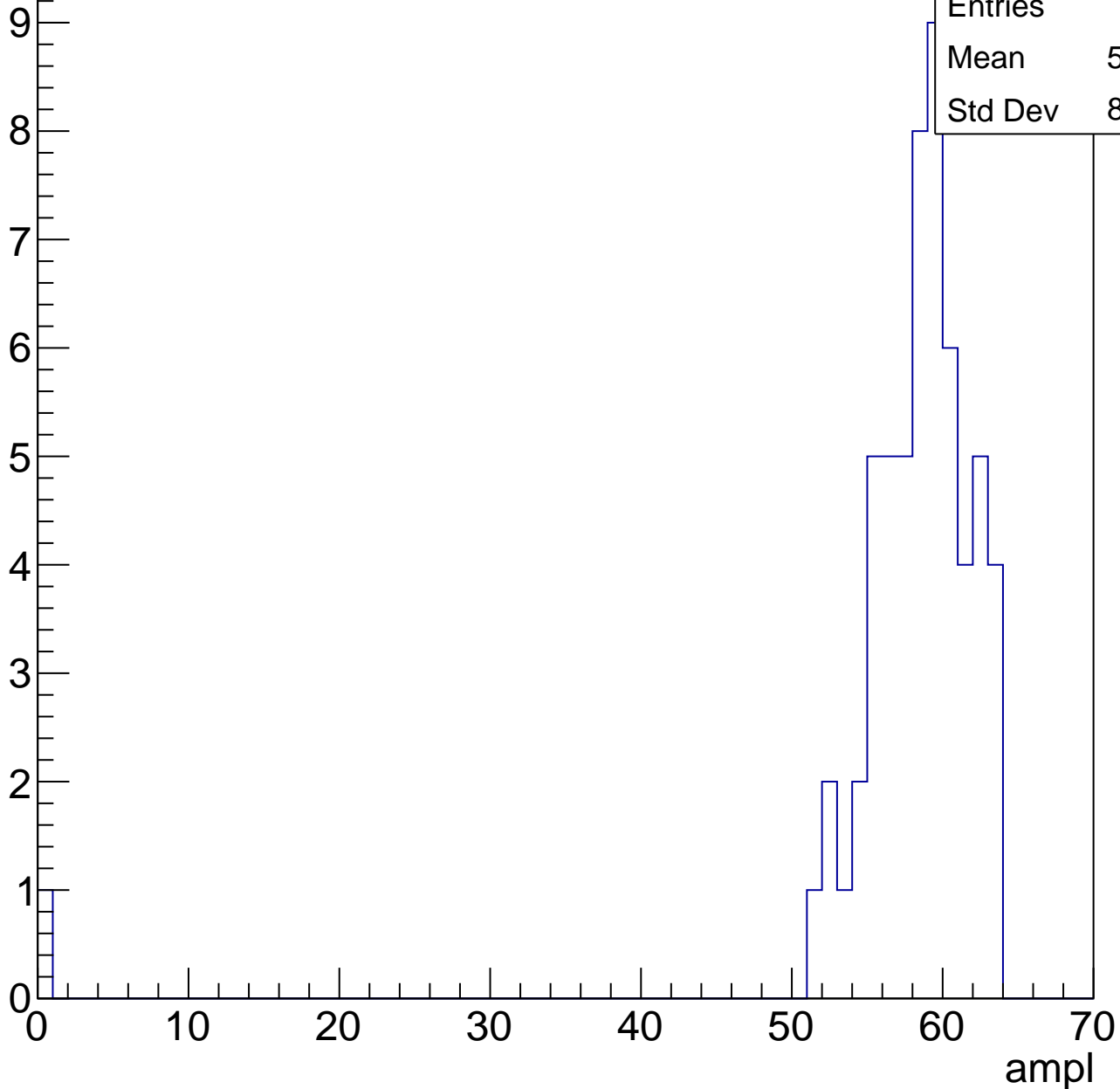


# B1L103S, U22-ch124, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	58
Mean	57.19
Std Dev	8.119

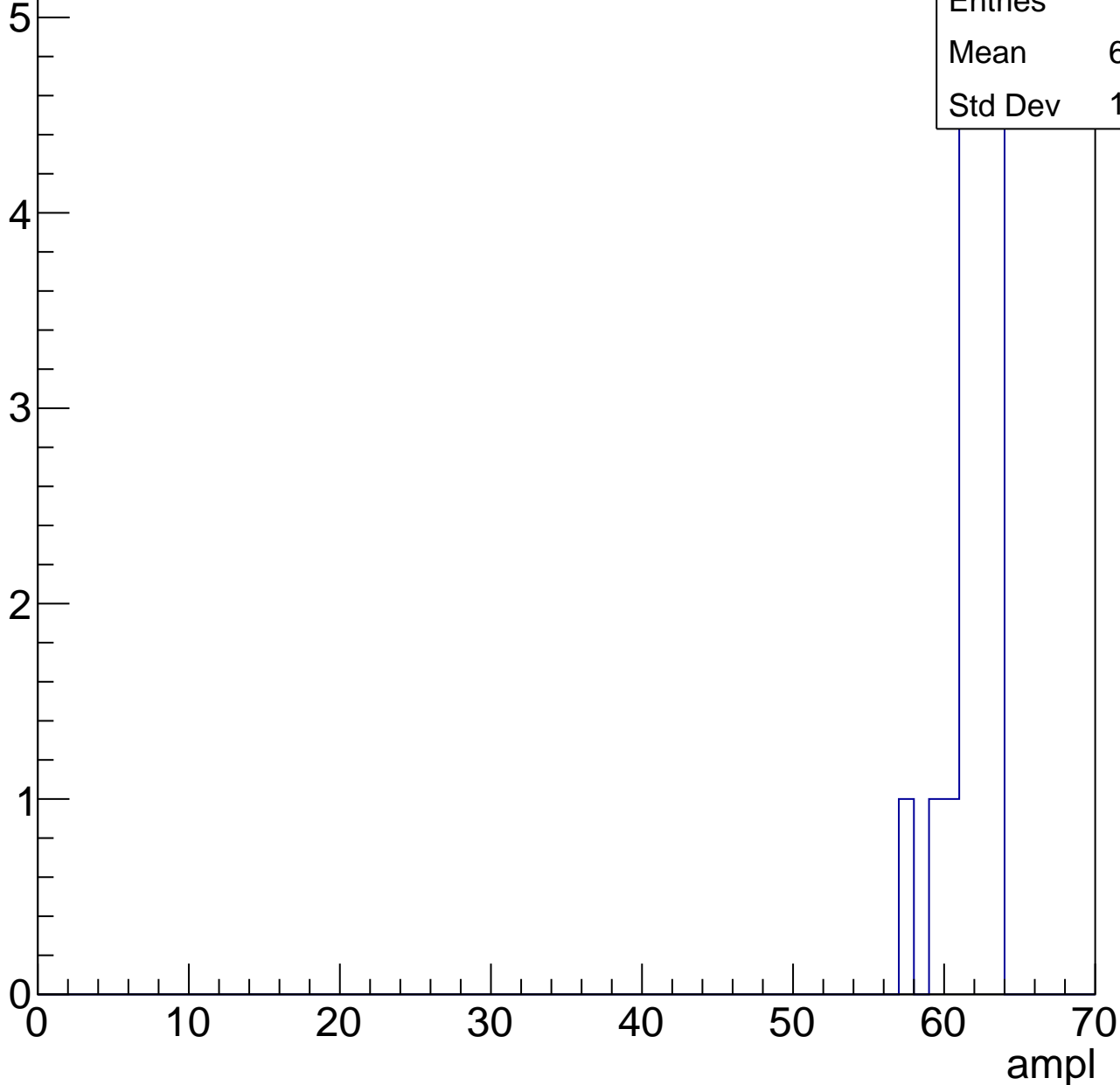


# B1L103S, U22-ch124, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	18
Mean	61.44
Std Dev	1.536

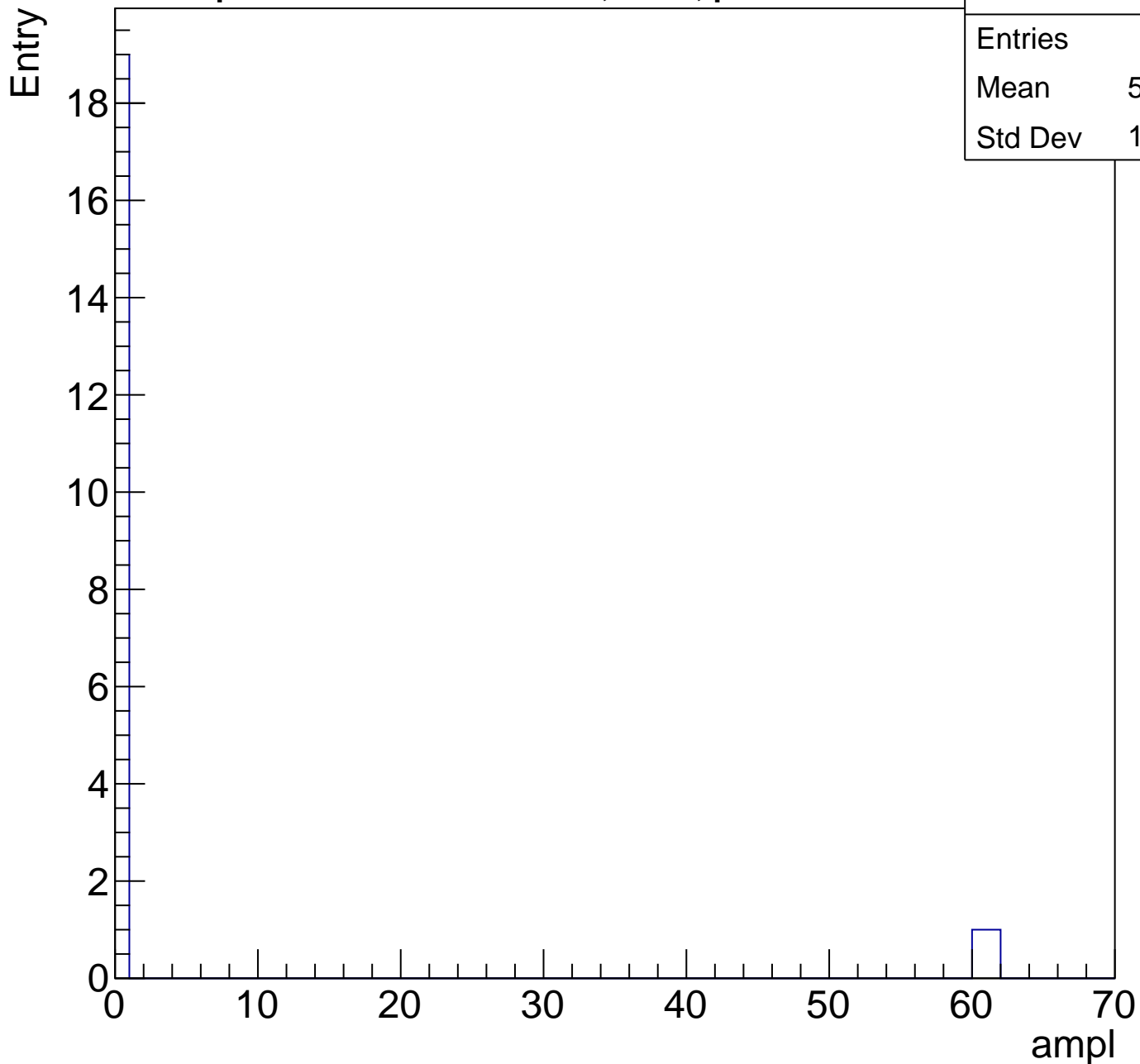




# B1L103S, U22-ch124, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	21
Mean	5.762
Std Dev	17.76



# B1L103S, U22-ch125, adc0

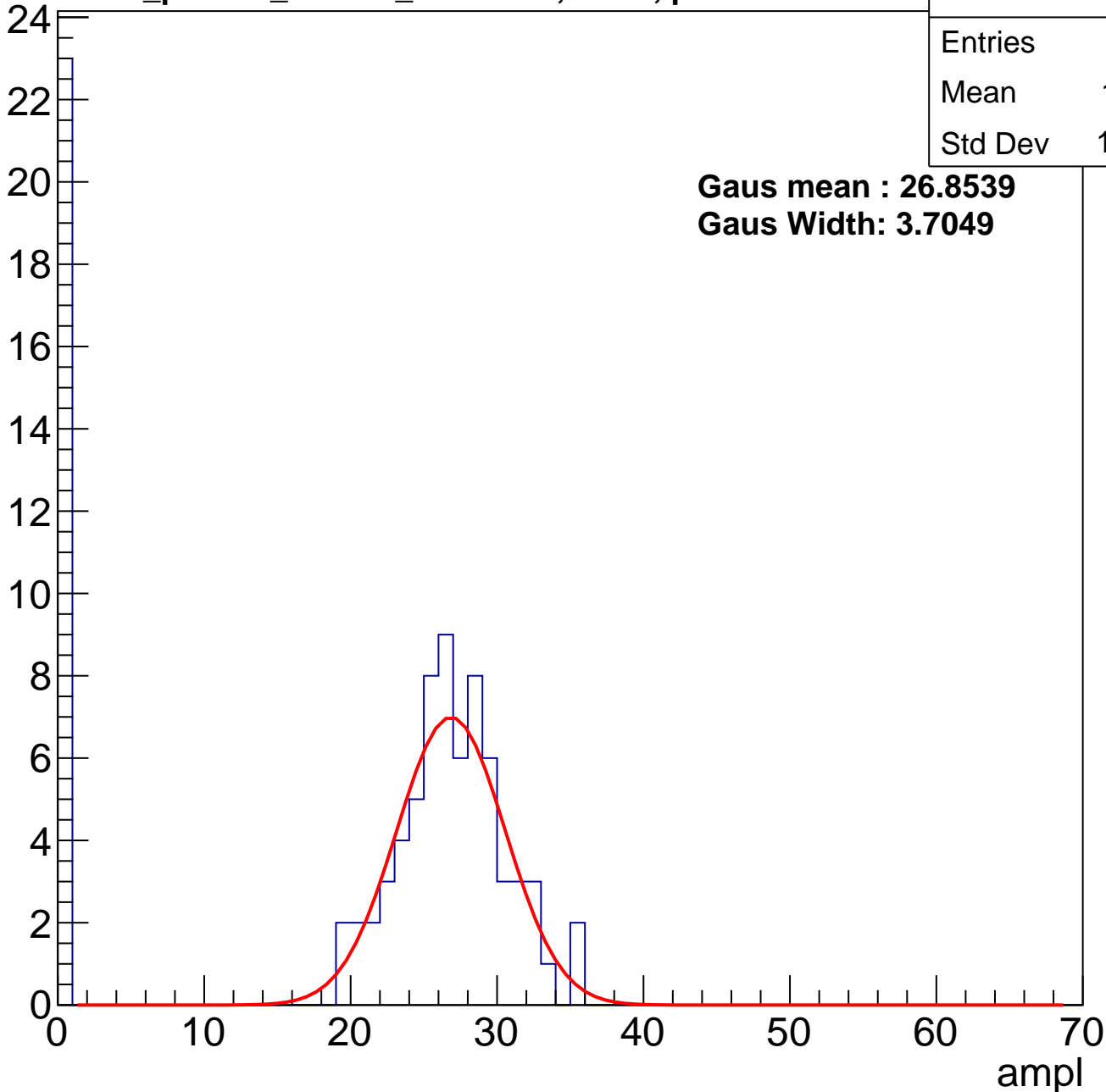
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	90
Mean	19.71
Std Dev	11.95

**Gaus mean : 26.8539**

**Gaus Width: 3.7049**

Entry



# B1L103S, U22-ch125, adc1

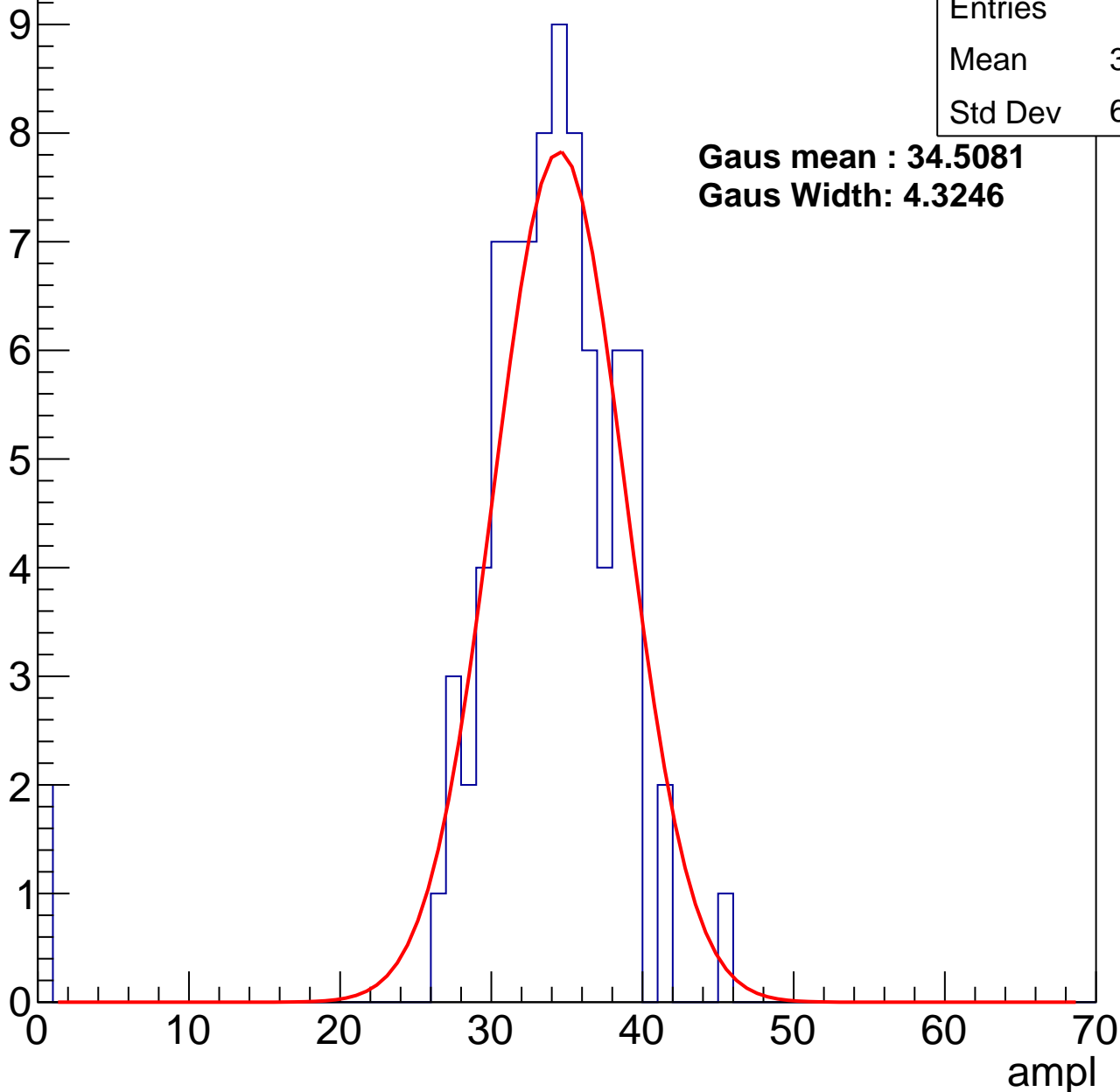
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	83
Mean	32.93
Std Dev	6.347

**Gaus mean : 34.5081**

**Gaus Width: 4.3246**



# B1L103S, U22-ch125, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	69
Mean	38.48
Std Dev	10.13

**Gaus mean : 41.6369**

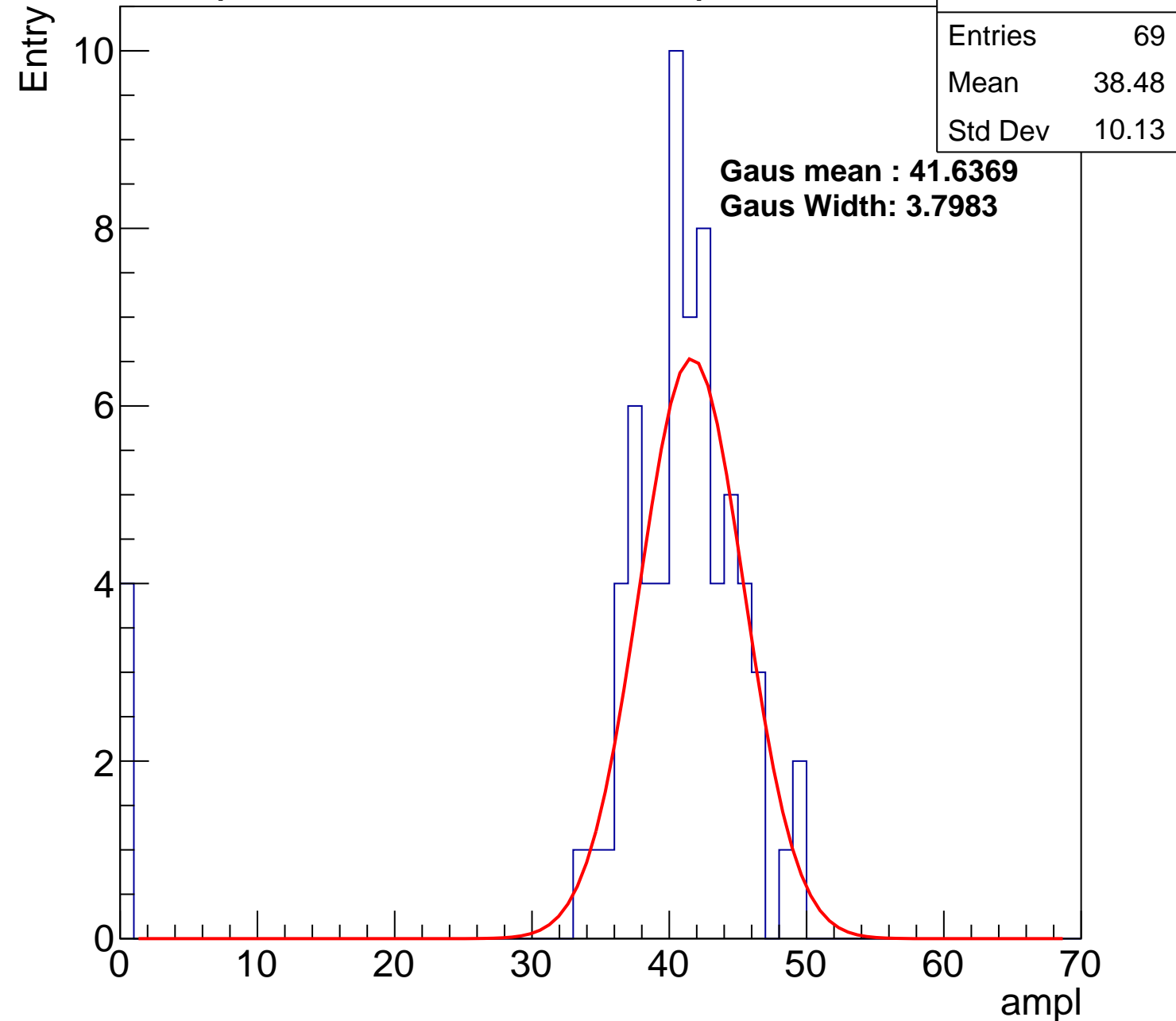
**Gaus Width: 3.7983**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

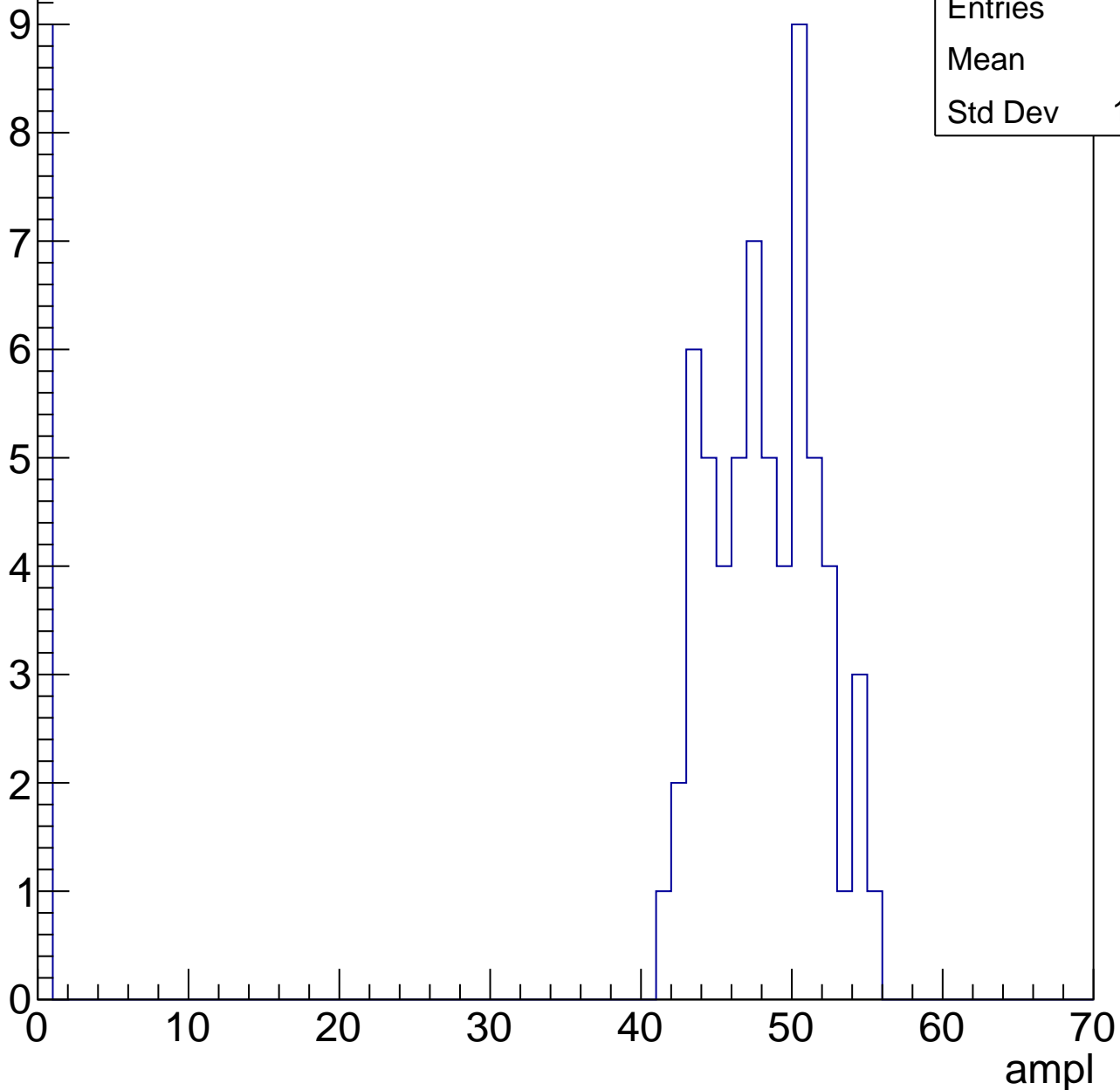


# B1L103S, U22-ch125, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

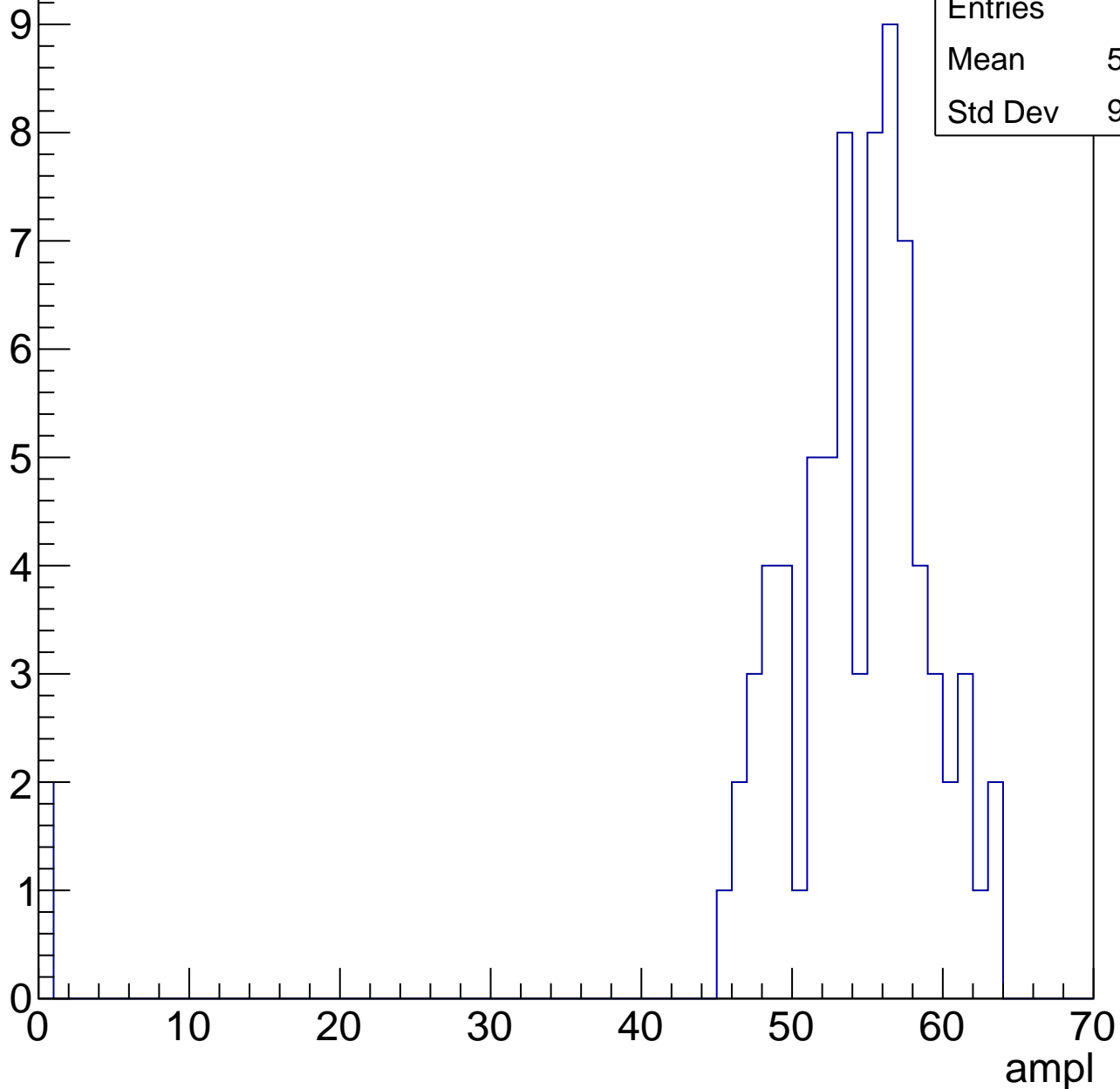
Entries	71
Mean	41.7
Std Dev	16.21



# B1L103S, U22-ch125, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

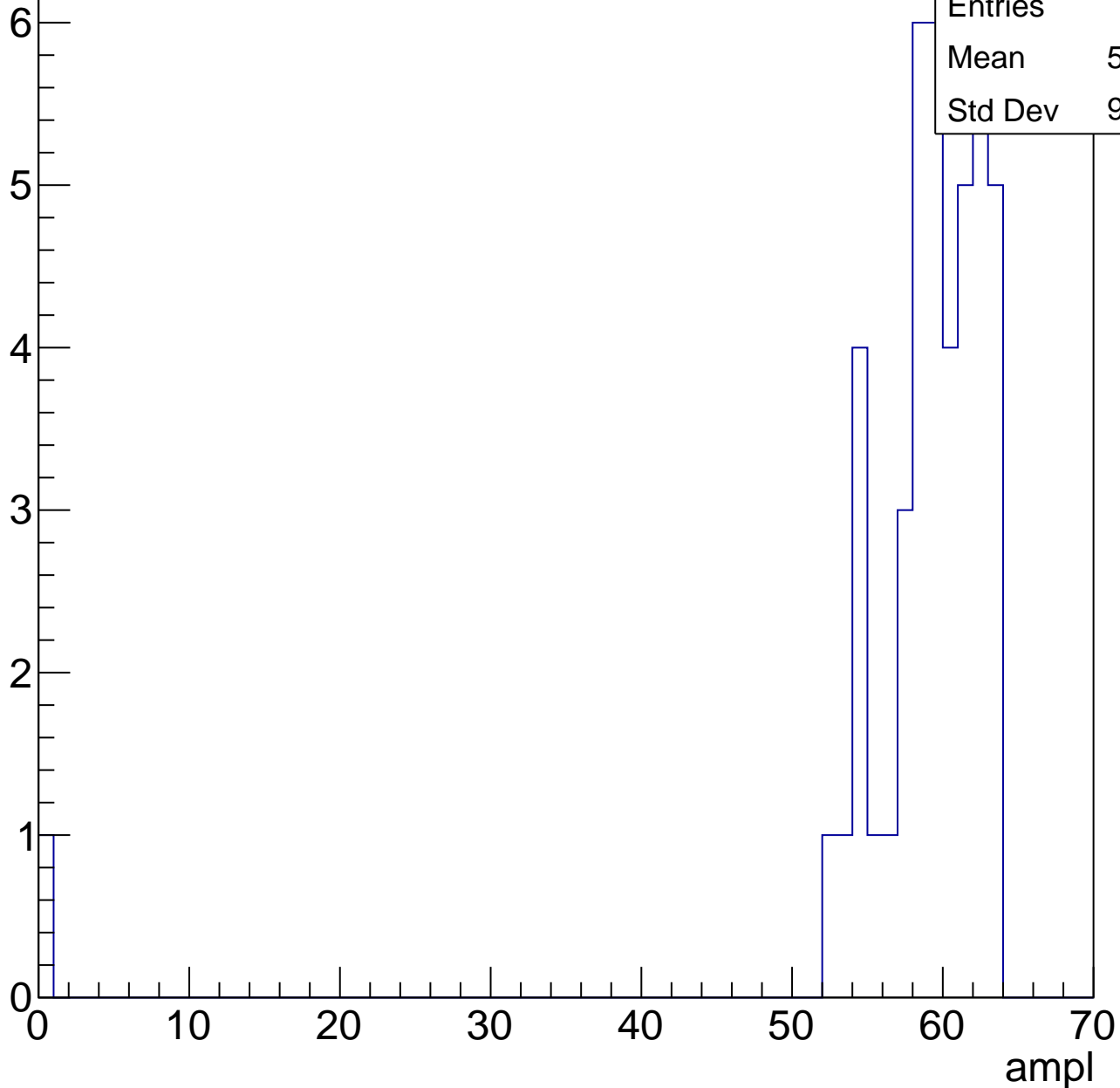


Entries	77
Mean	52.73
Std Dev	9.594

# B1L103S, U22-ch125, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

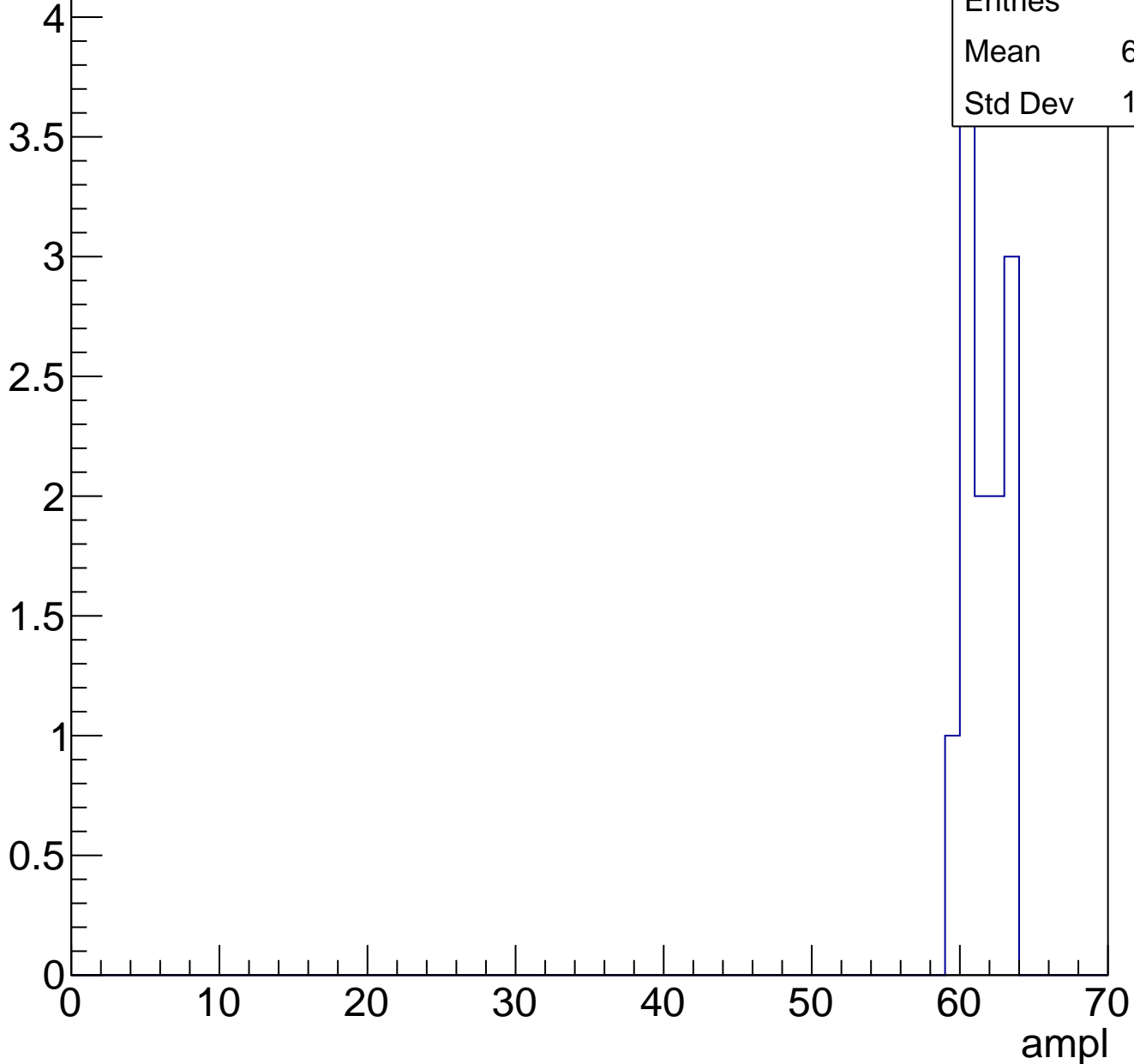
Entry



# B1L103S, U22-ch125, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry





# B1L103S, U22-ch125, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	20
Mean	3.1
Std Dev	13.51

Entry



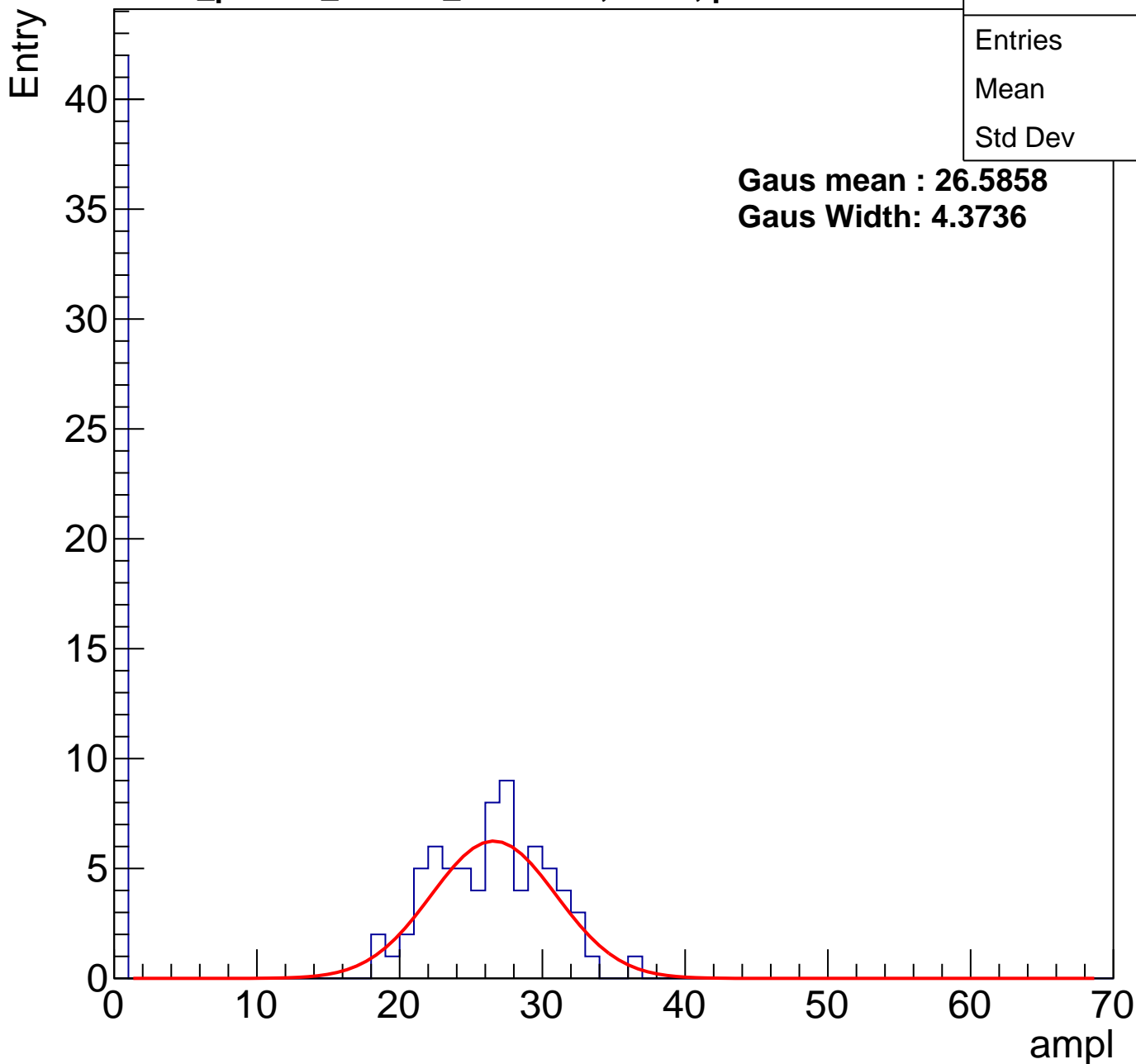
# B1L103S, U22-ch126, adc0

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	113
Mean	16.31
Std Dev	12.91

**Gaus mean : 26.5858**

**Gaus Width: 4.3736**



# B1L103S, U22-ch126, adc1

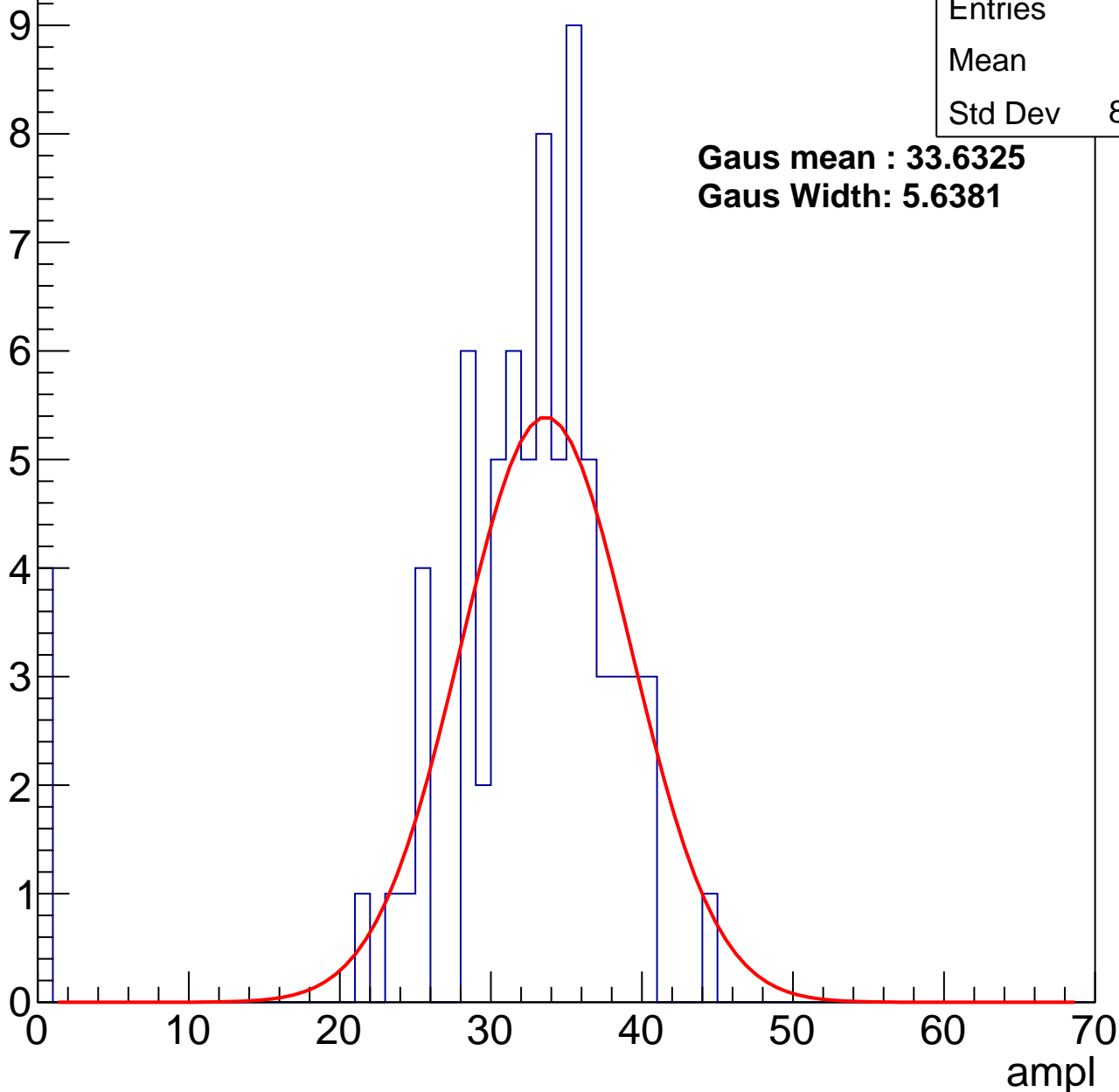
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	75
Mean	31
Std Dev	8.546

**Gaus mean : 33.6325**

**Gaus Width: 5.6381**



# B1L103S, U22-ch126, adc2

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	81
Mean	34.33
Std Dev	14.1

**Gaus mean : 40.7169**

**Gaus Width: 4.1314**

Entry

10

8

6

4

2

0

0

10

20

30

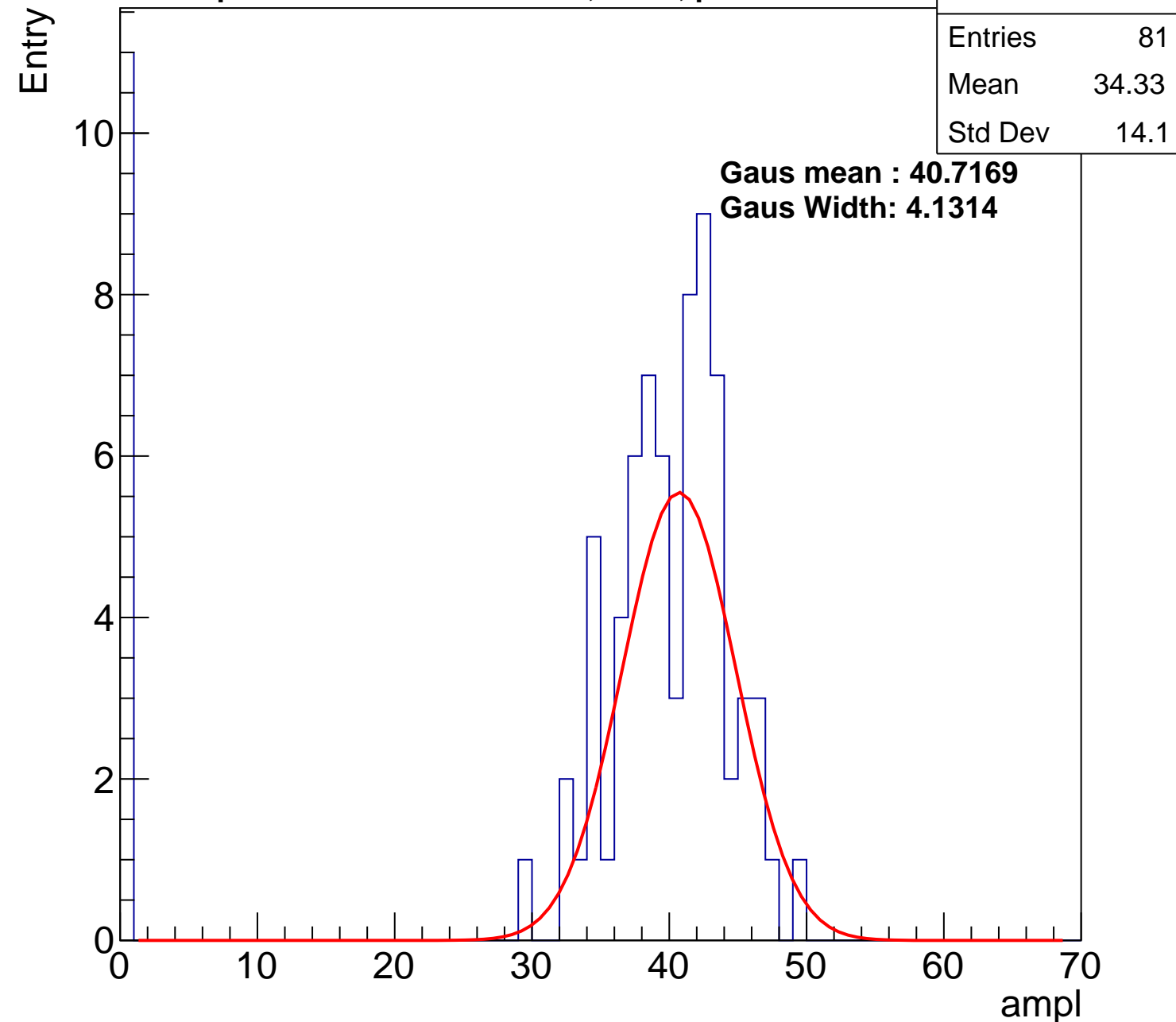
40

50

60

70

ampl

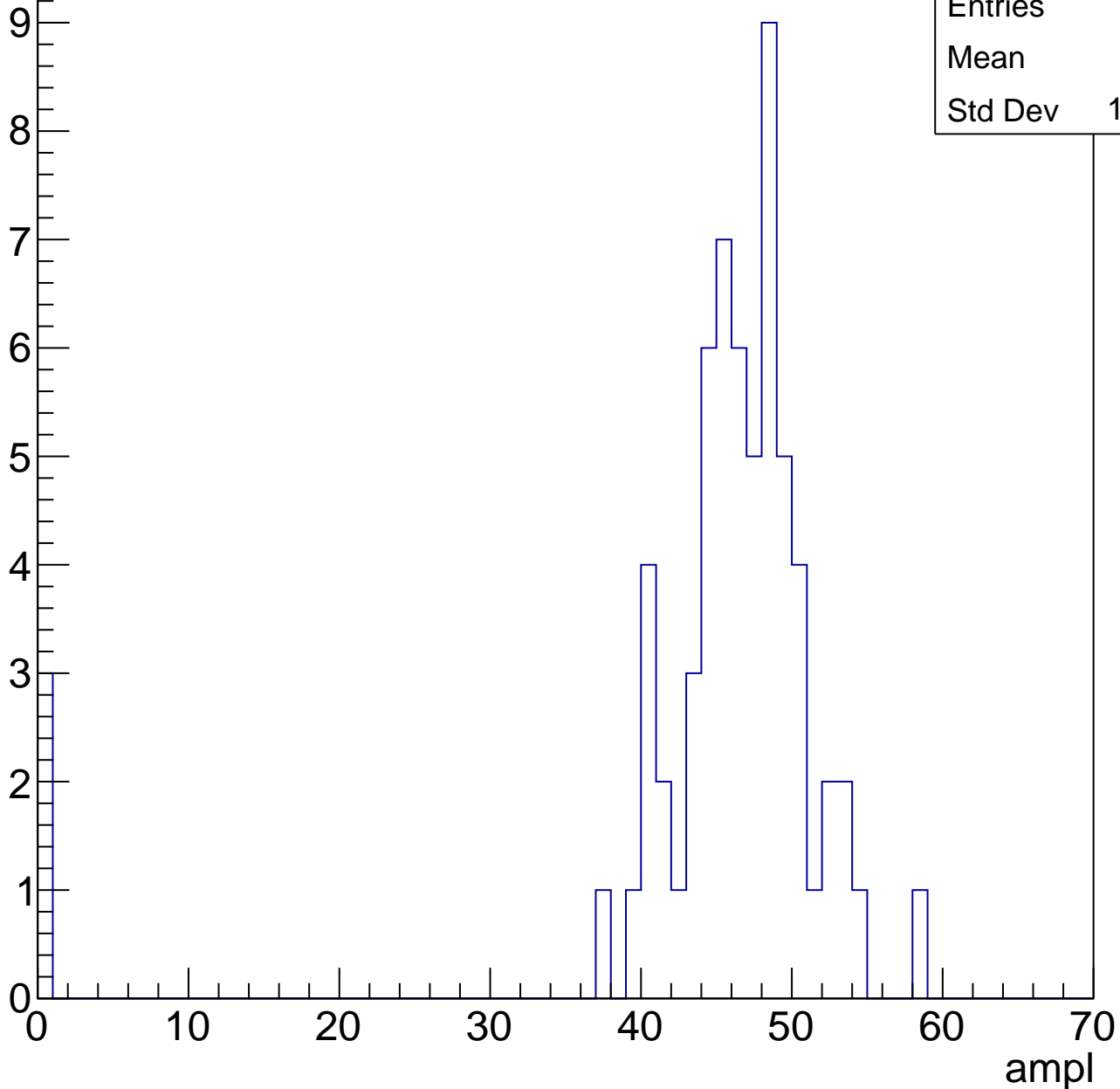


# B1L103S, U22-ch126, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	64
Mean	44.2
Std Dev	10.53



# B1L103S, U22-ch126, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

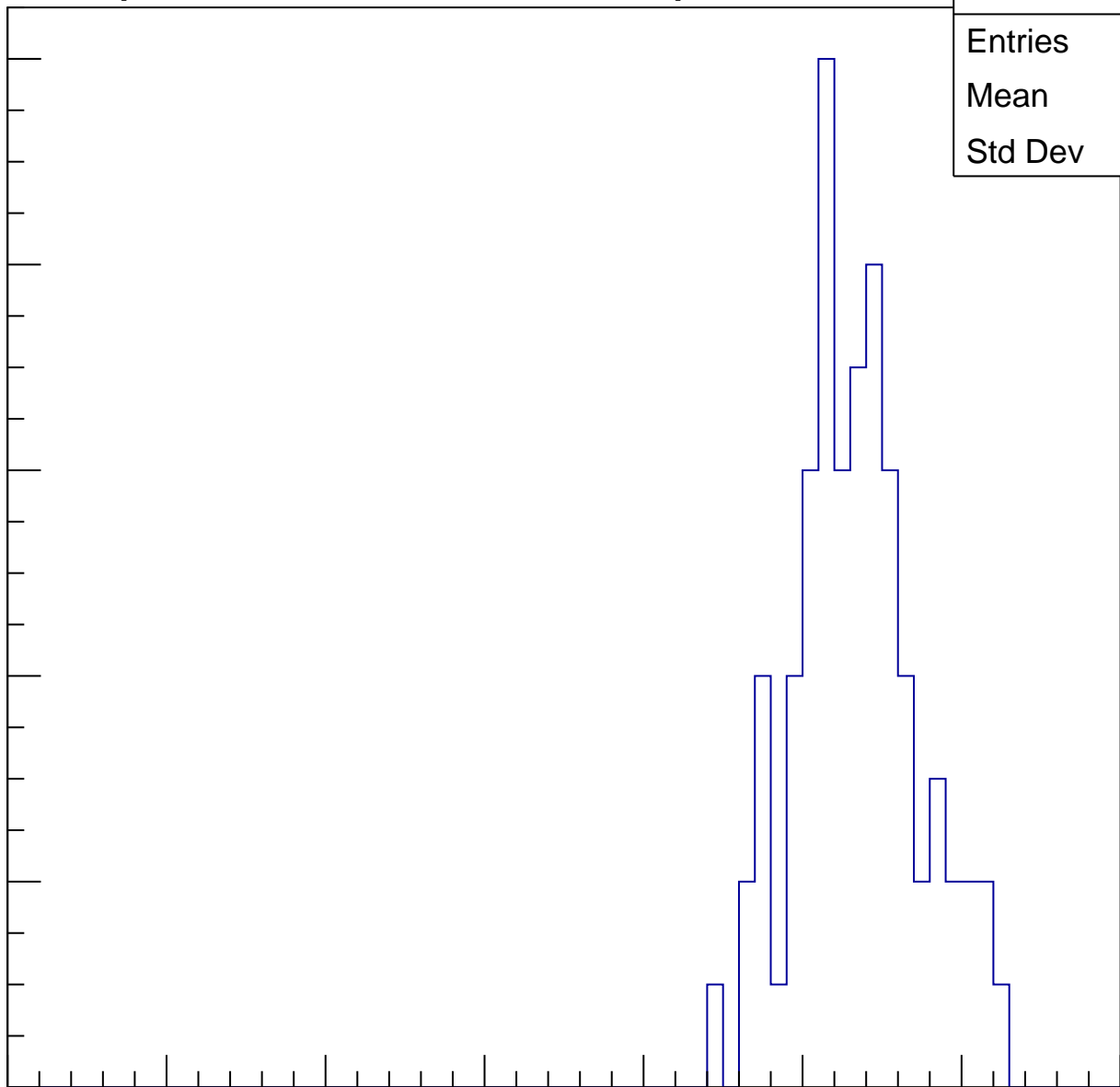
Entries	71
Mean	52.92
Std Dev	3.874

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

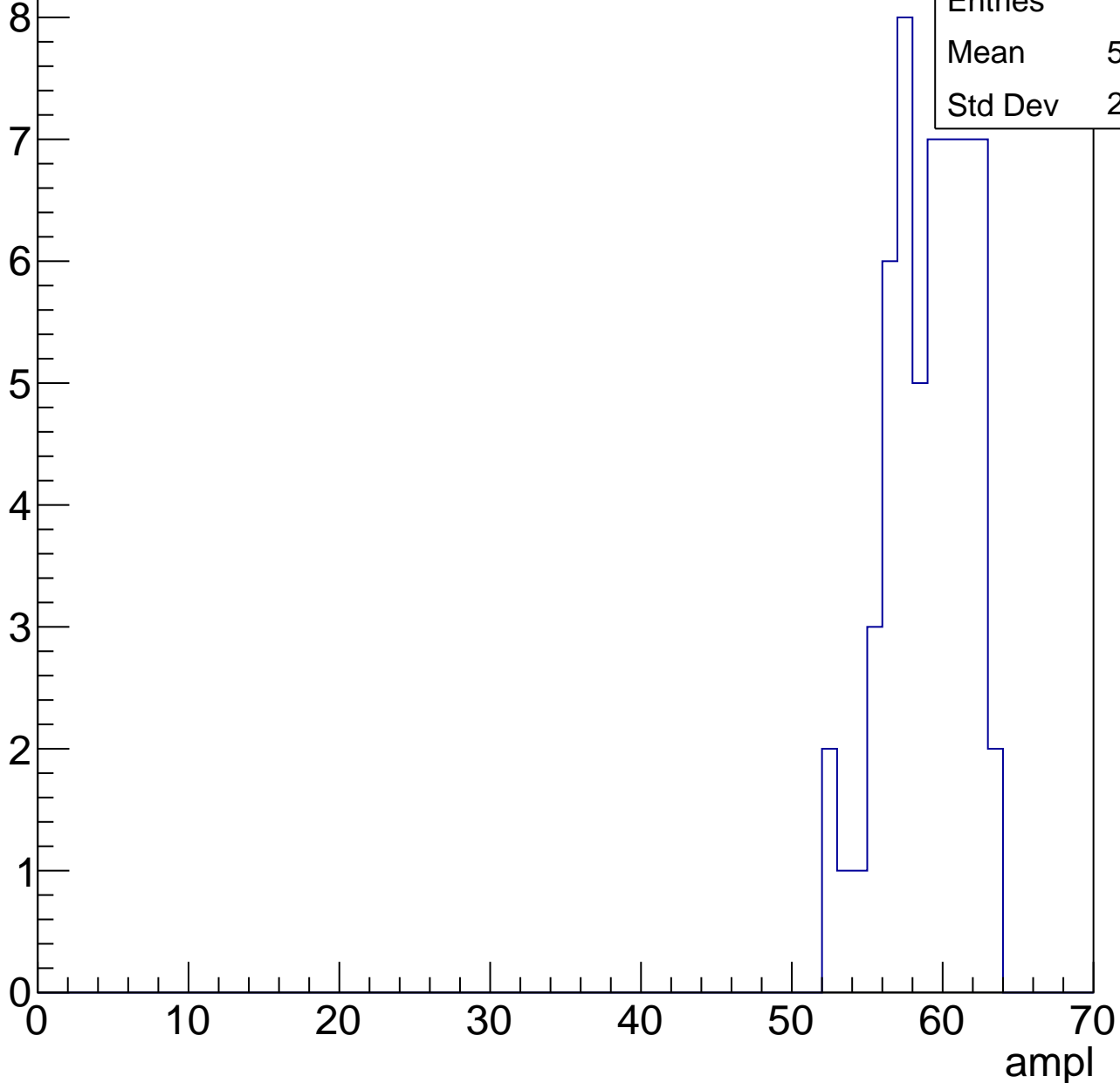


# B1L103S, U22-ch126, adc5

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	56
Mean	58.54
Std Dev	2.725

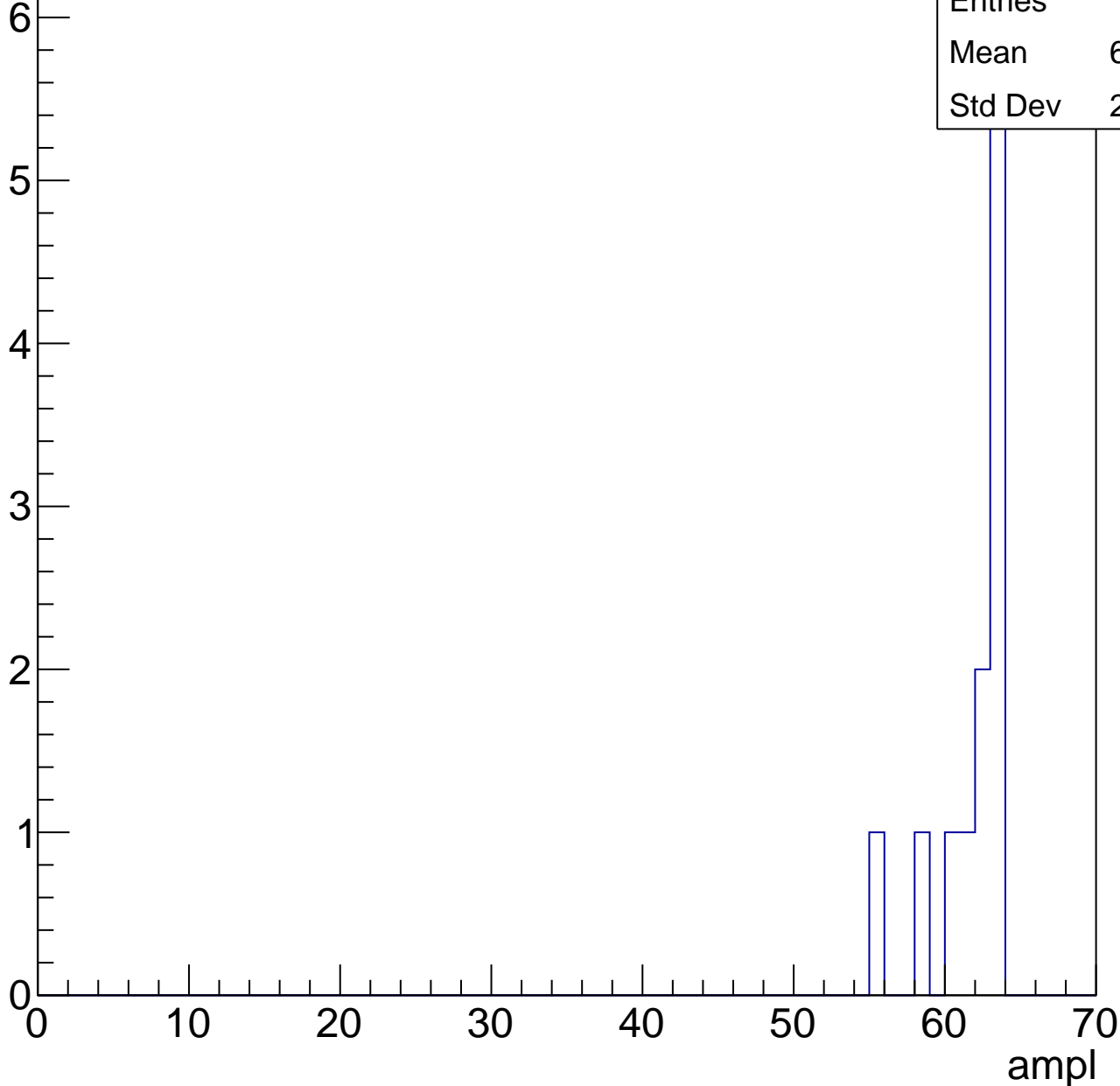


# B1L103S, U22-ch126, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	12
Mean	61.33
Std Dev	2.427



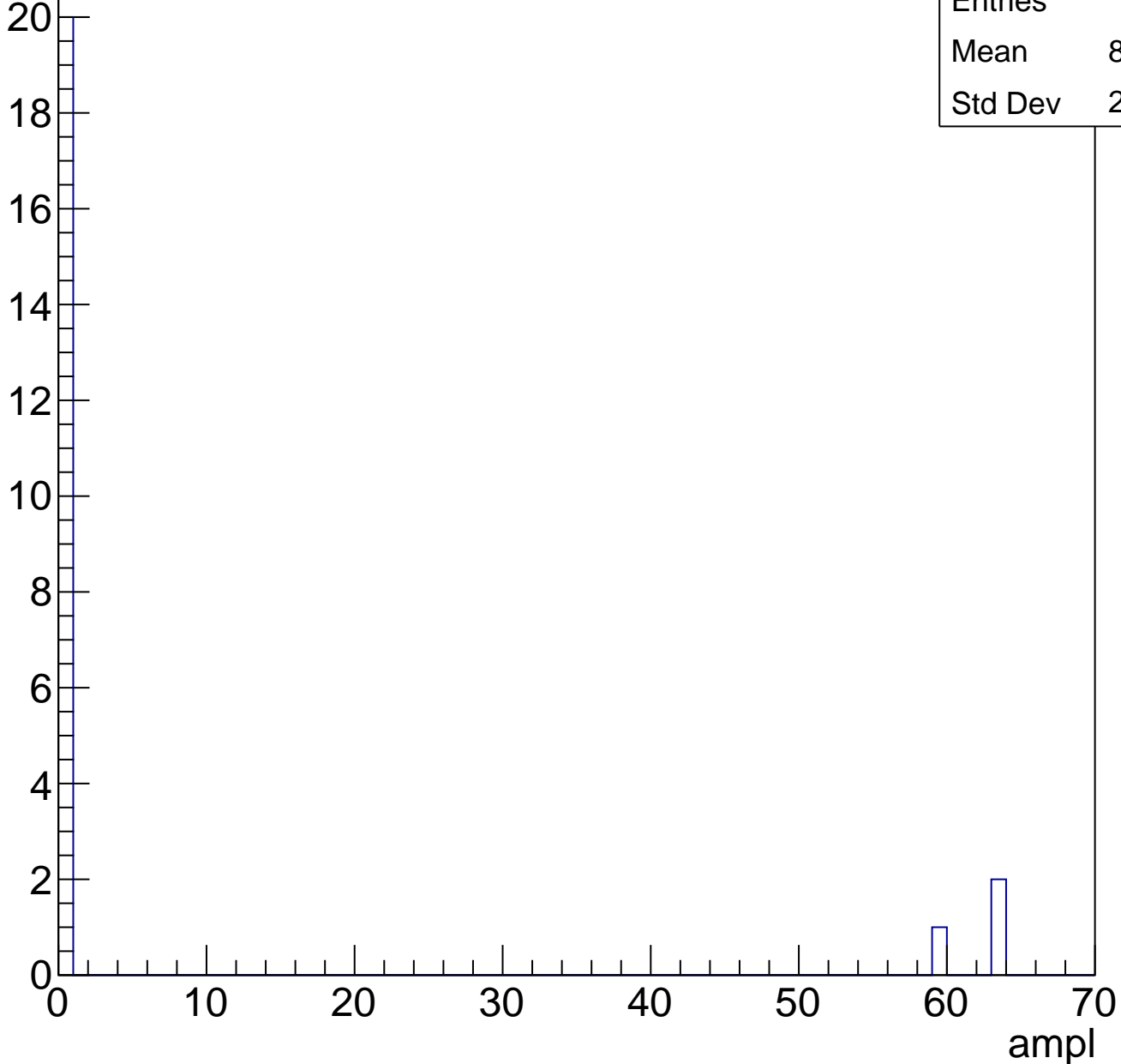


# B1L103S, U22-ch126, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	23
Mean	8.043
Std Dev	20.78

Entry



# B1L103S, U22-ch127, adc0

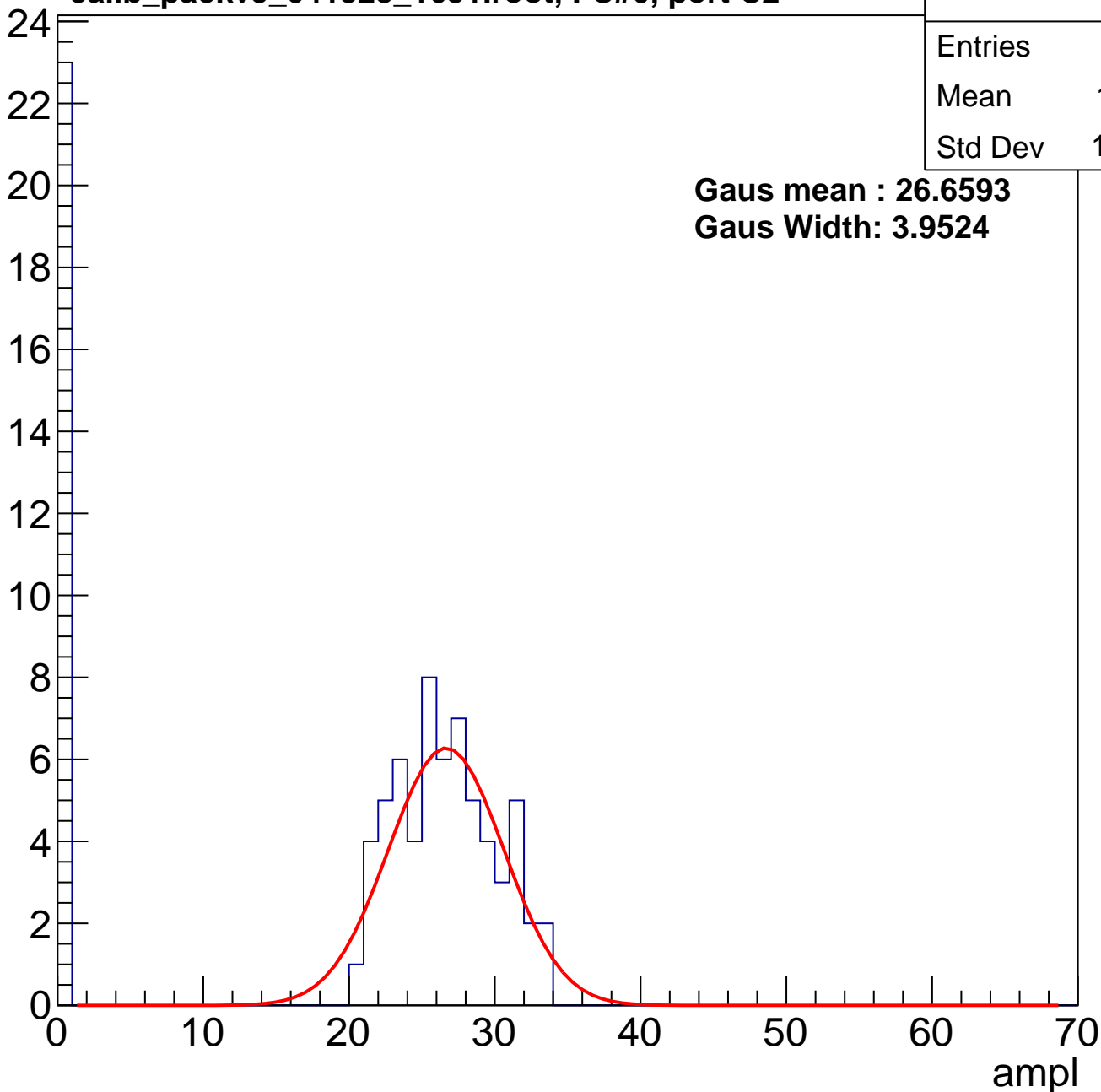
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	85
Mean	19.11
Std Dev	11.98

**Gaus mean : 26.6593**

**Gaus Width: 3.9524**

Entry



# B1L103S, U22-ch127, adc1

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	73
Mean	31.37
Std Dev	7.575

**Gaus mean : 33.5672**

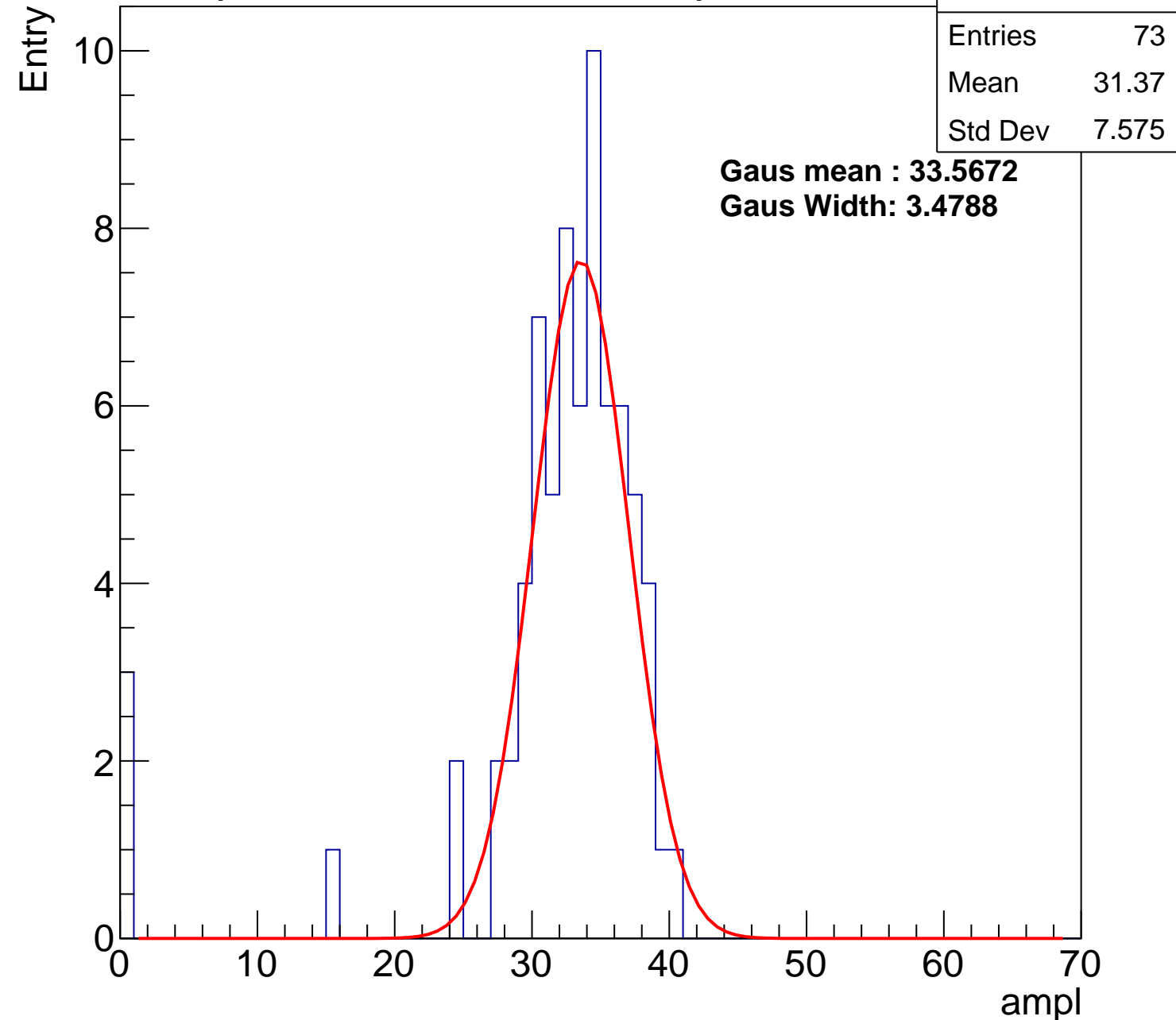
**Gaus Width: 3.4788**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L103S, U22-ch127, adc2

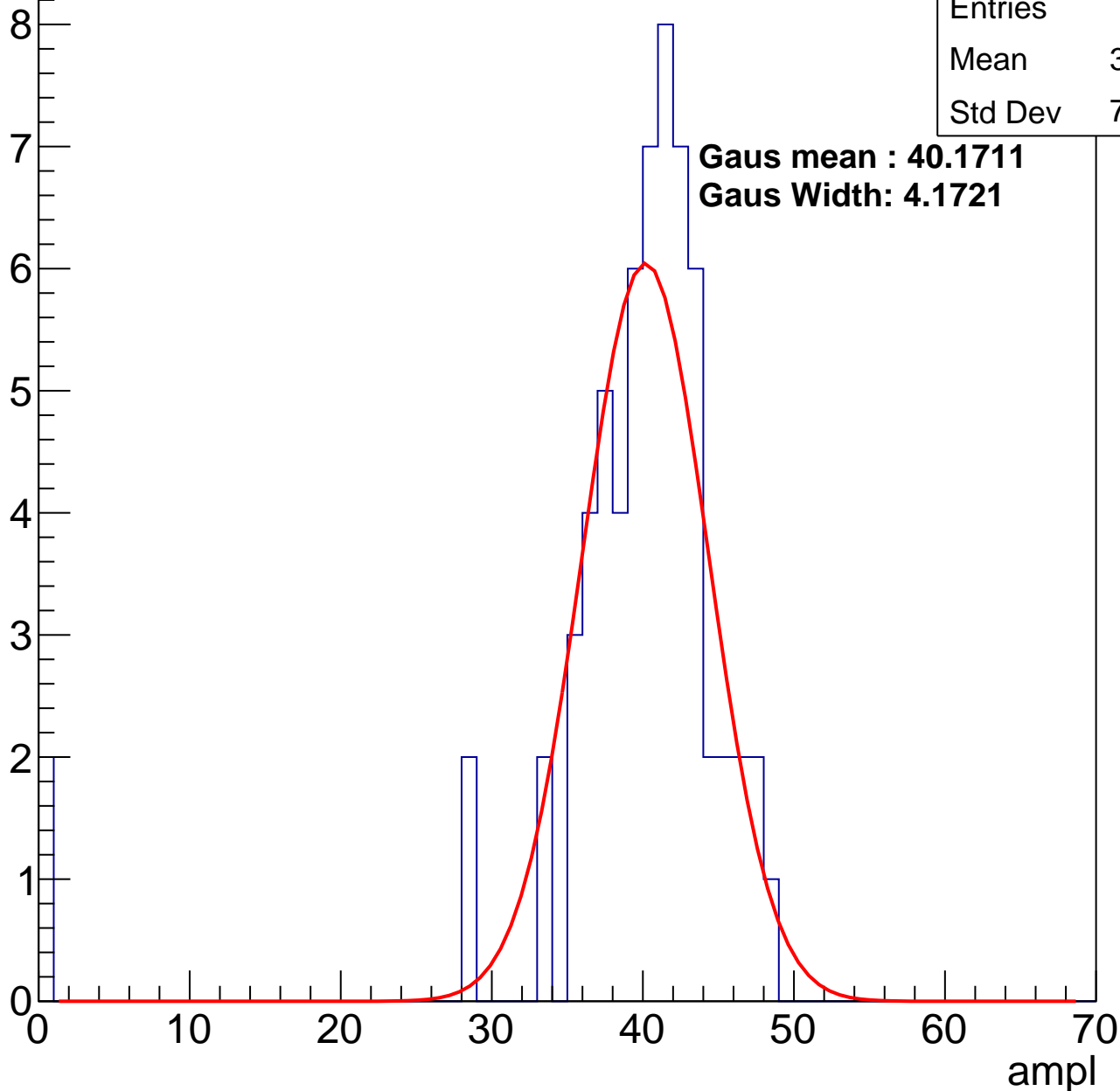
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	65
Mean	38.68
Std Dev	7.927

**Gaus mean : 40.1711**

**Gaus Width: 4.1721**

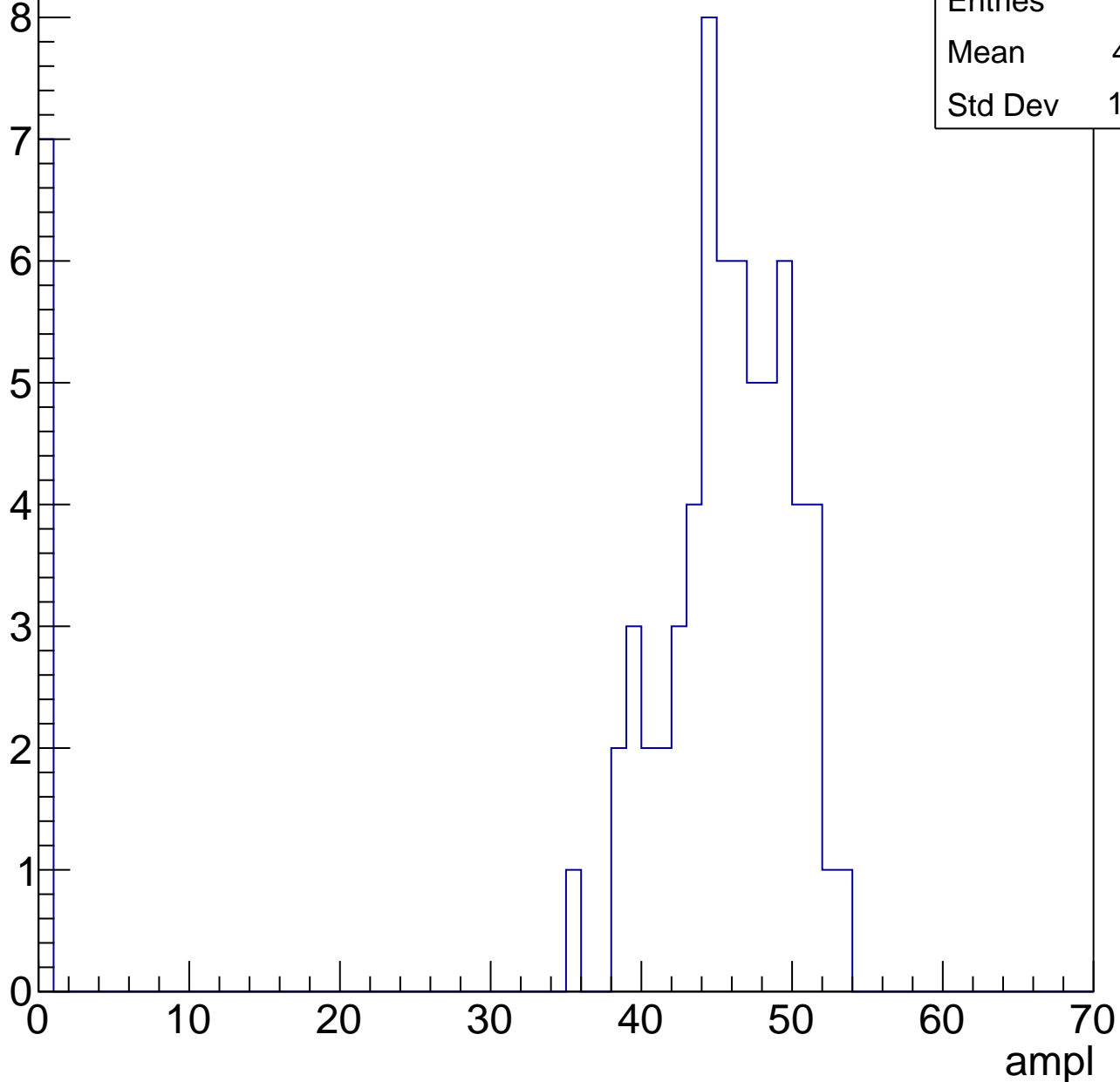


# B1L103S, U22-ch127, adc3

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	70
Mean	40.91
Std Dev	14.13

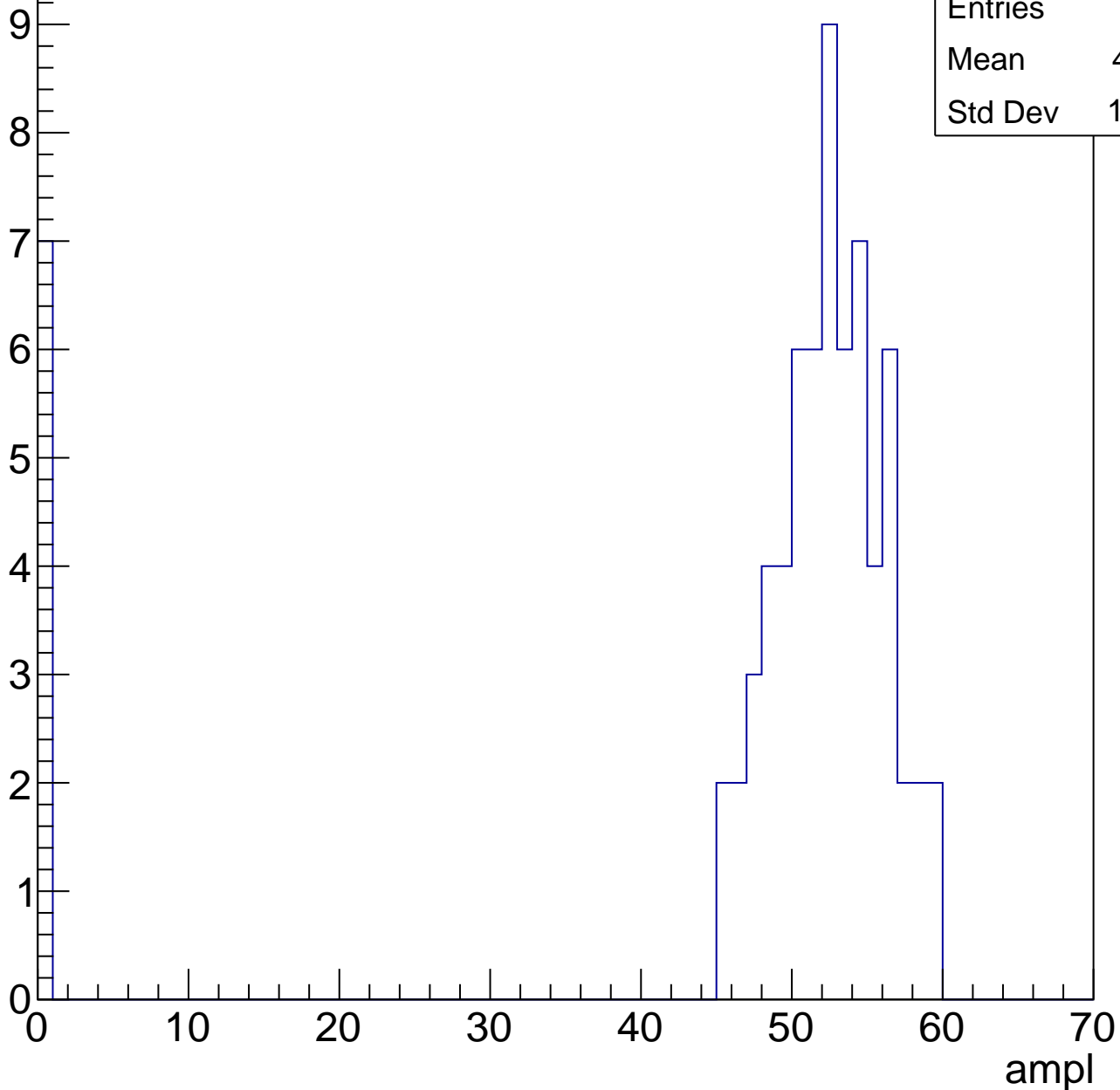


# B1L103S, U22-ch127, adc4

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	72
Mean	47.01
Std Dev	15.77



# B1L103S, U22-ch127, adc5

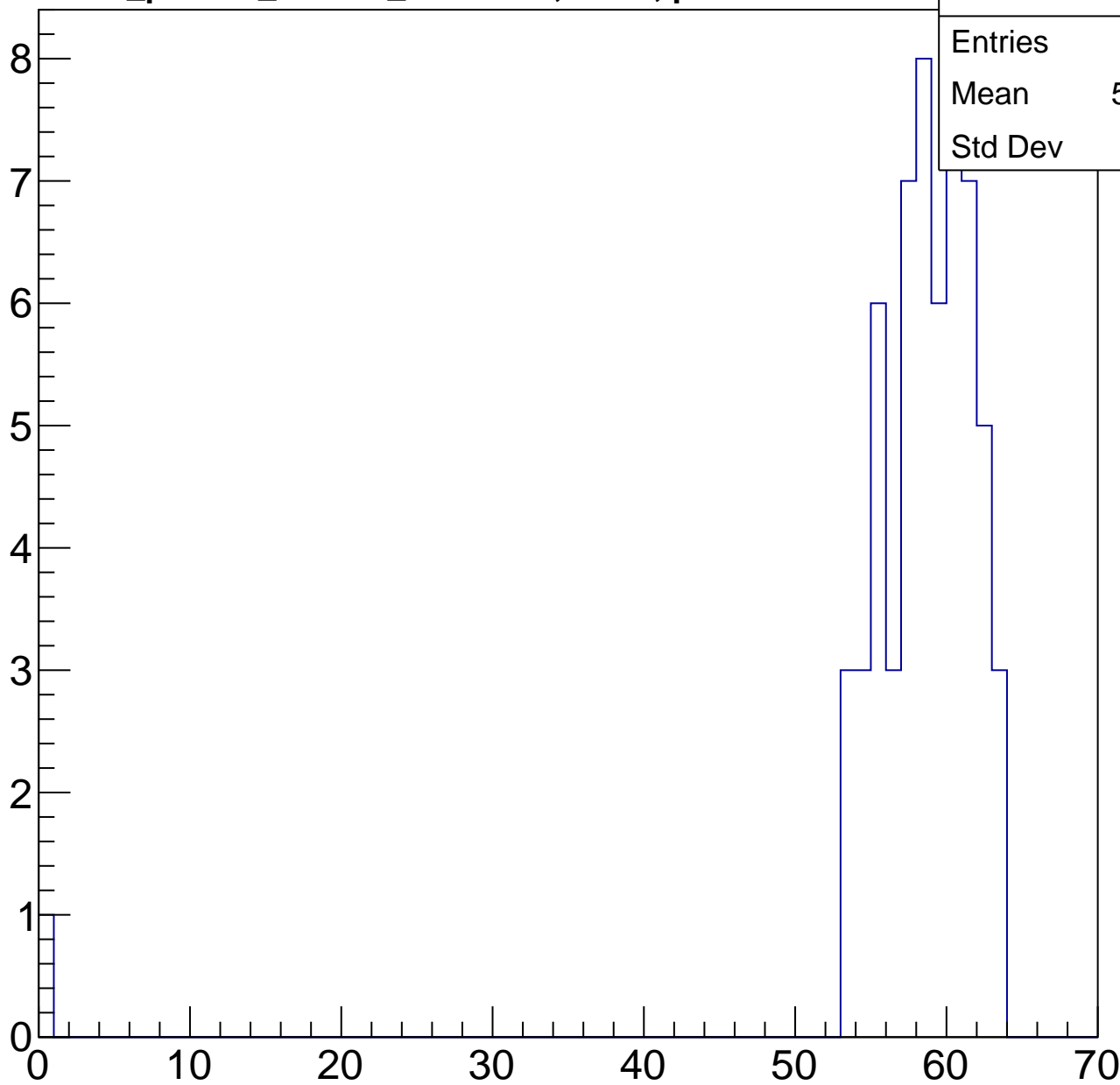
calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	60
Mean	57.37
Std Dev	7.95

ampl

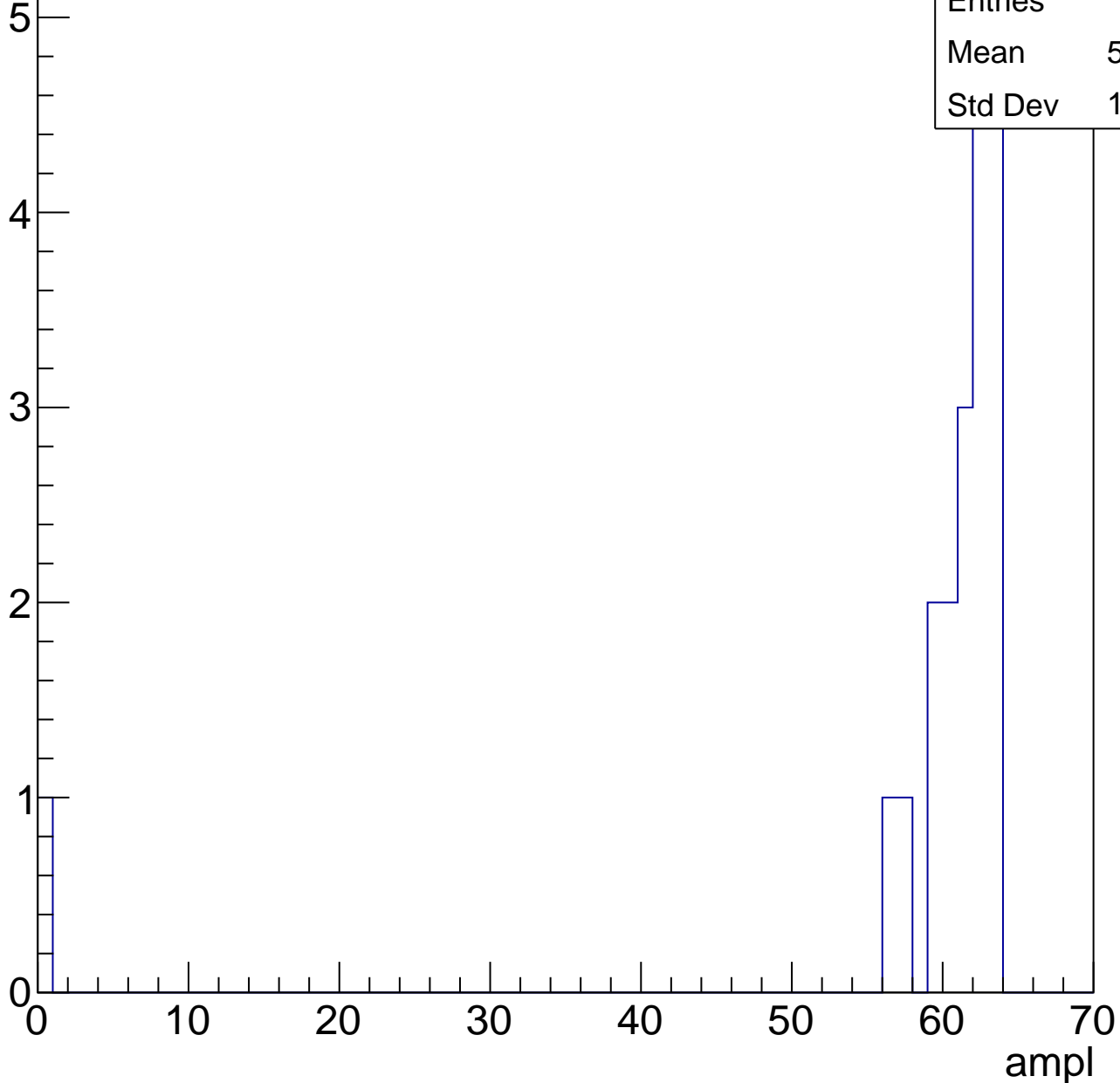


# B1L103S, U22-ch127, adc6

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entry

Entries	20
Mean	57.95
Std Dev	13.44



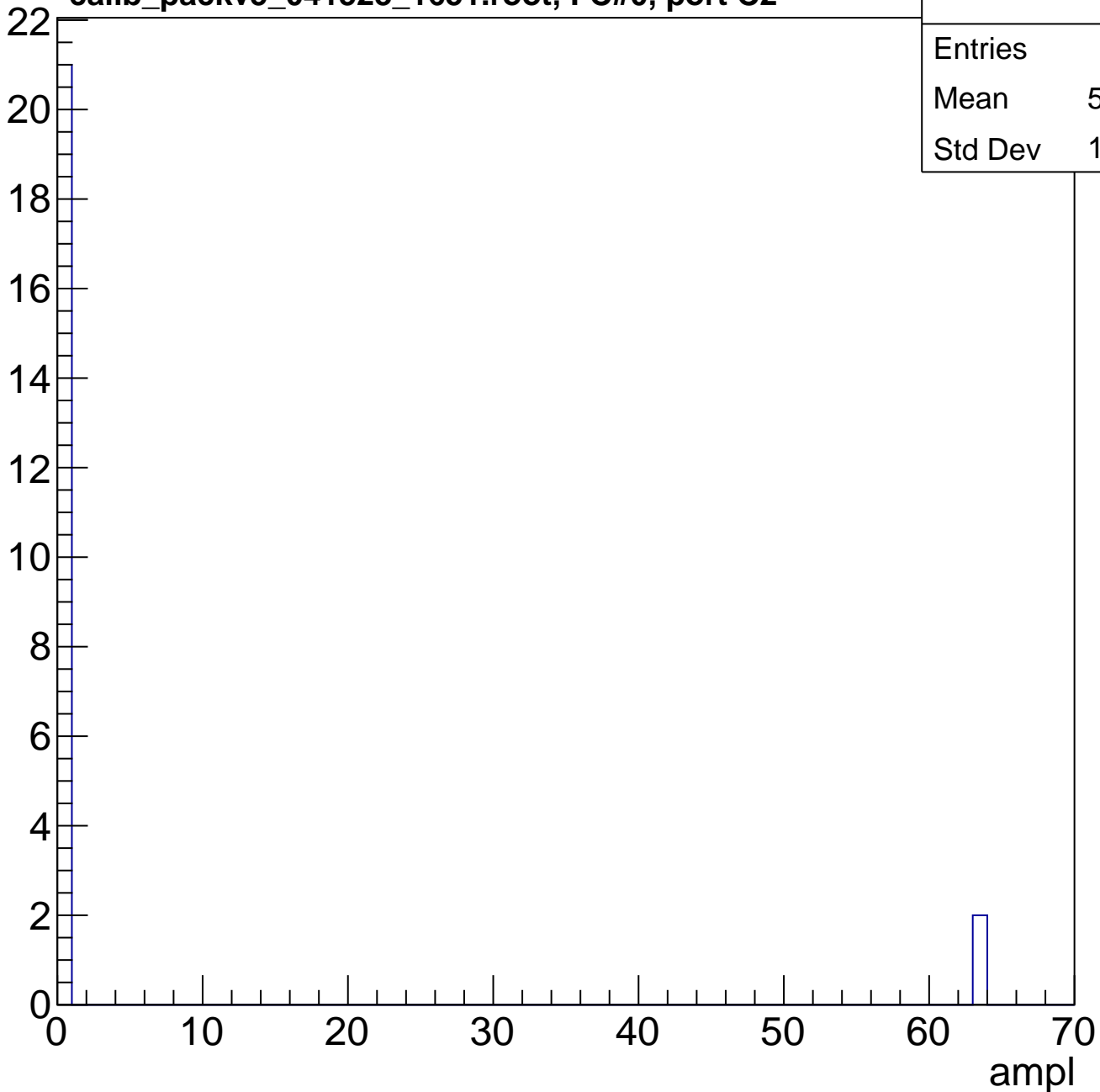


# B1L103S, U22-ch127, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	23
Mean	5.478
Std Dev	17.75

Entry



# B1L103S, U22-ch127, adc7

calib\_packv5\_041523\_1651.root, FC#0, port C2

Entries	23
Mean	5.478
Std Dev	17.75

