B0L100S, U22-ch0

calib_packv5_042523_0143.root, FC#6, port A1
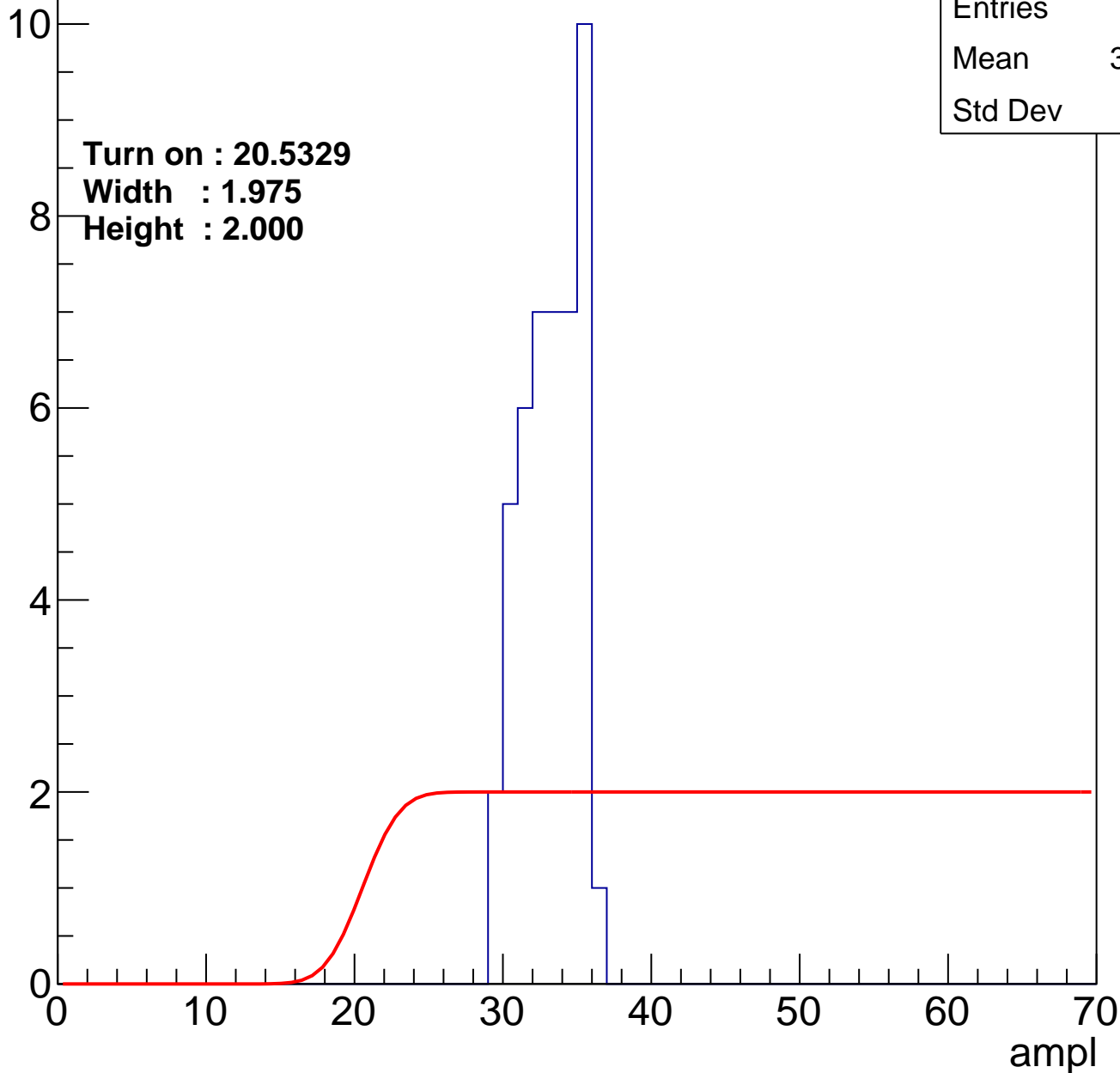
Entries	45
Mean	32.73
Std Dev	1.89

Turn on : 20.5329
Width   : 1.975
Height : 2.000

B0L100S, U22-ch1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch6

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl
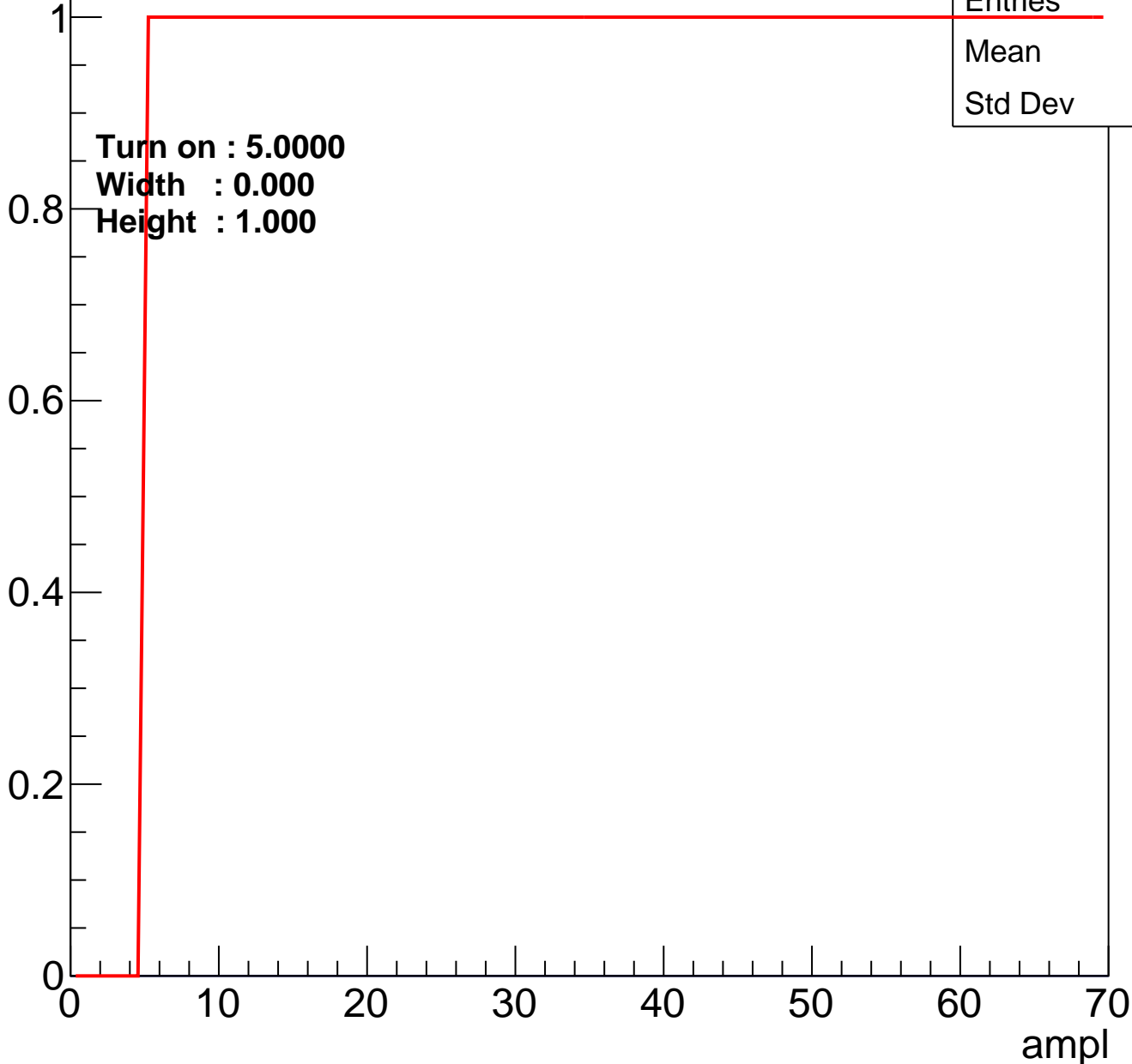
B0L100S, U22-ch7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch8

calib_packv5_042523_0143.root, FC#6, port A1

Entries	327
Mean	46.95
Std Dev	9.918

Turn on : 32.2200
Width   : 2.323
Height : 9.999
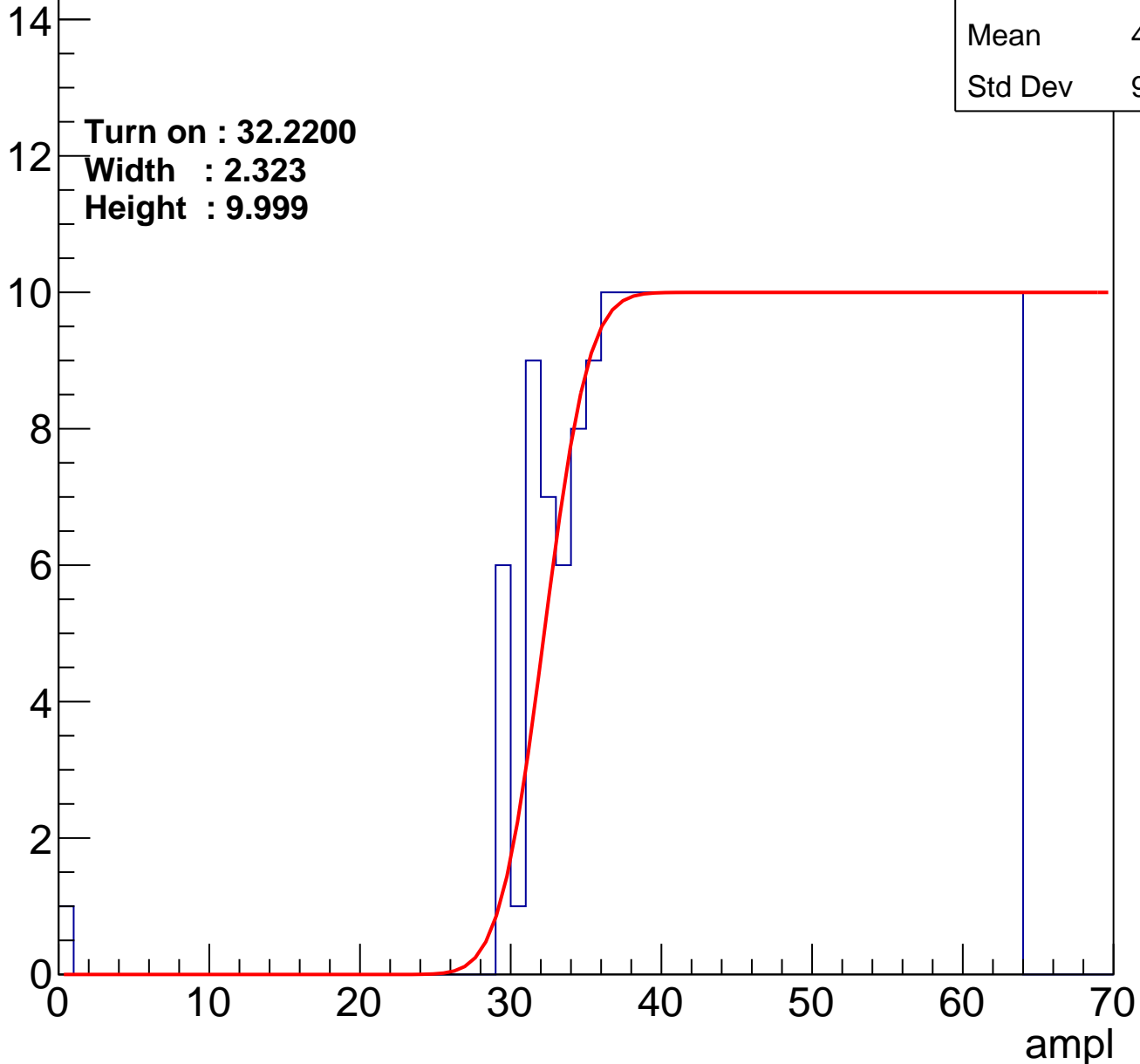
B0L100S, U22-ch9

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	25.44
Std Dev	6.633
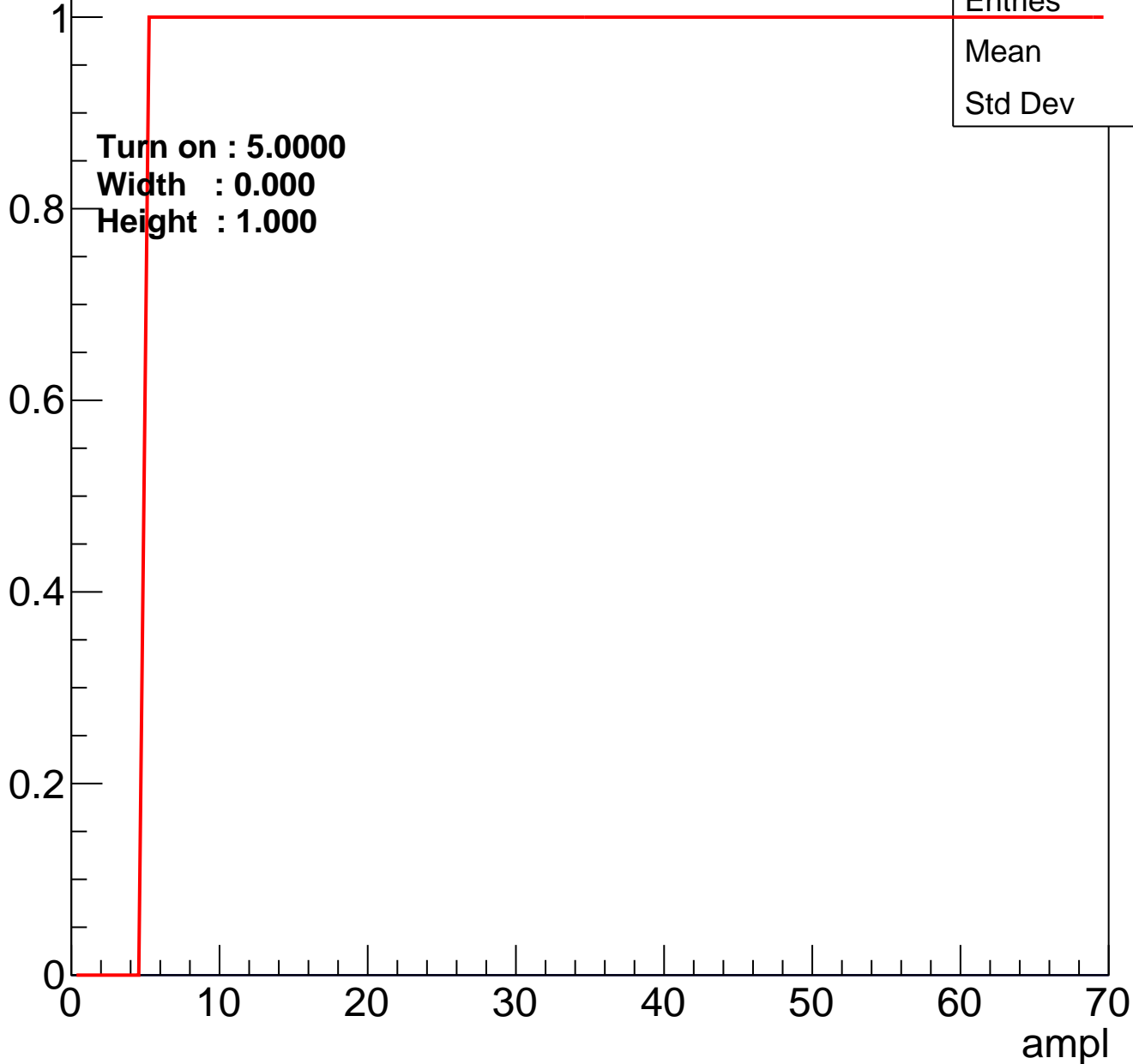
Turn on : 25.4999
Width   : 0.308
Height : 2.000

Entry

ampl
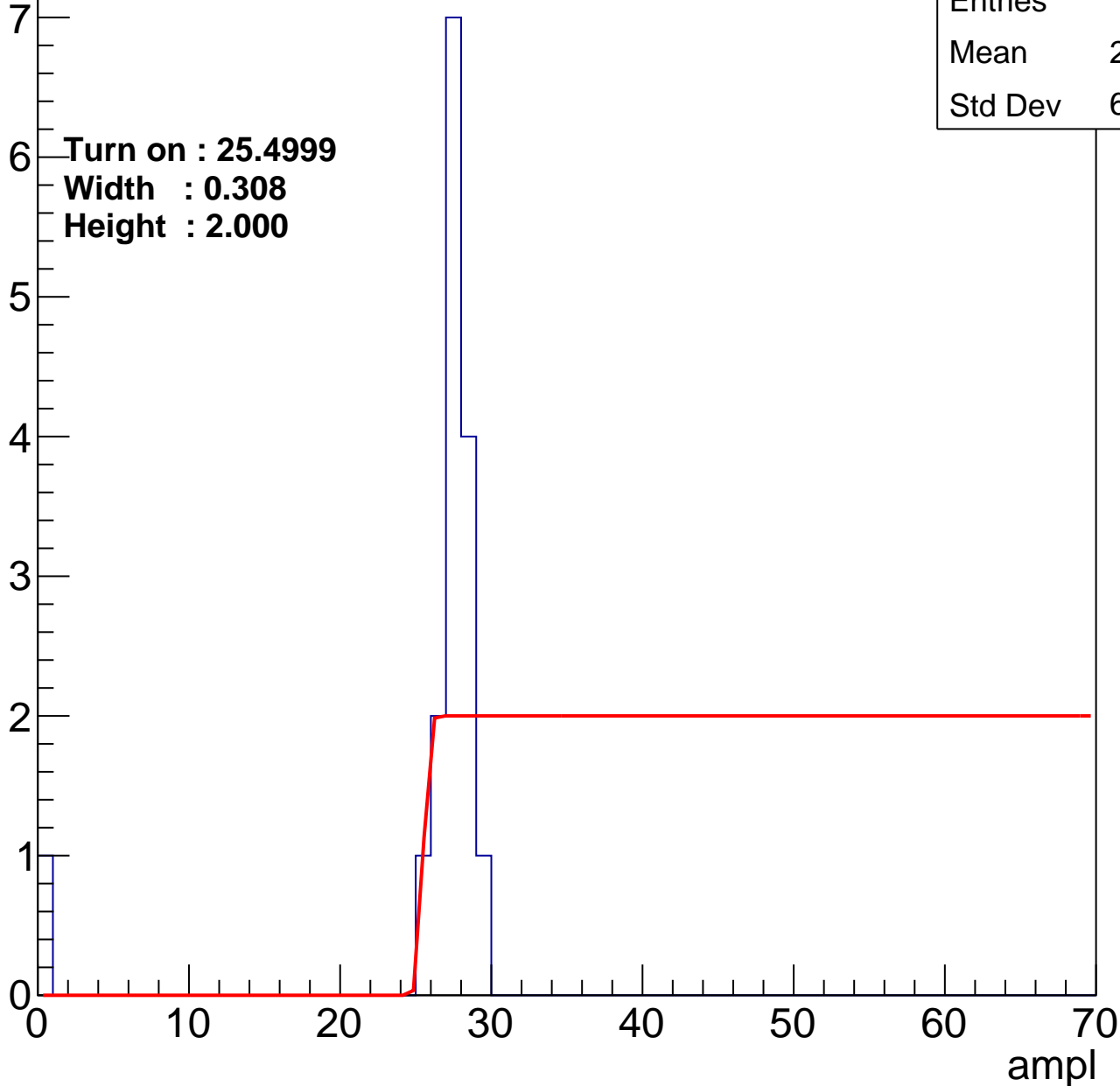
B0L100S, U22-ch10

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

B0L100S, U22-ch11

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch12

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch13

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch14

calib_packv5_042523_0143.root, FC#6, port A1

Entries	19
Mean	30.05
Std Dev	0.8255
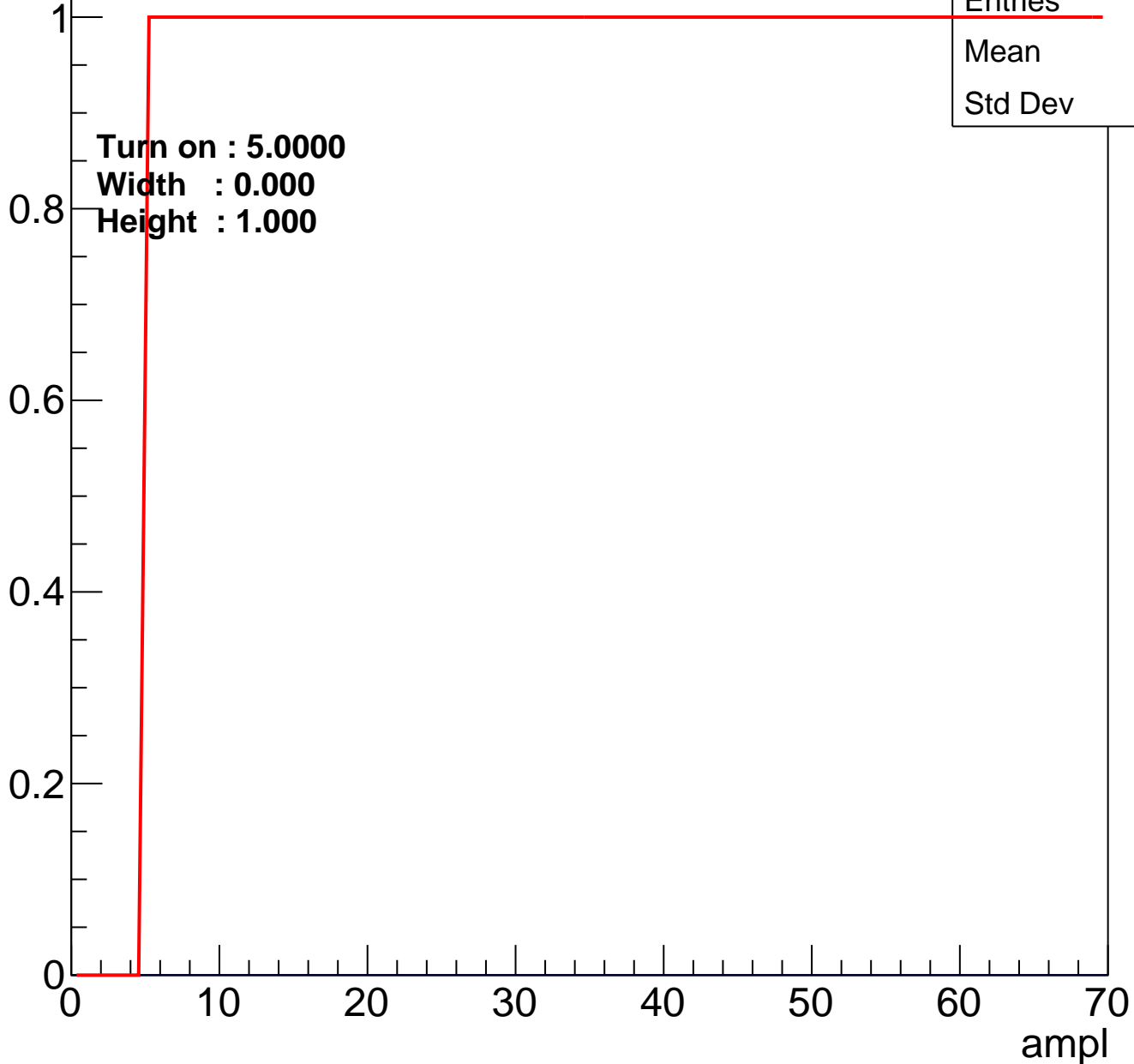
Turn on : 19.7384
Width   : 1.788
Height : 2.000

ampl
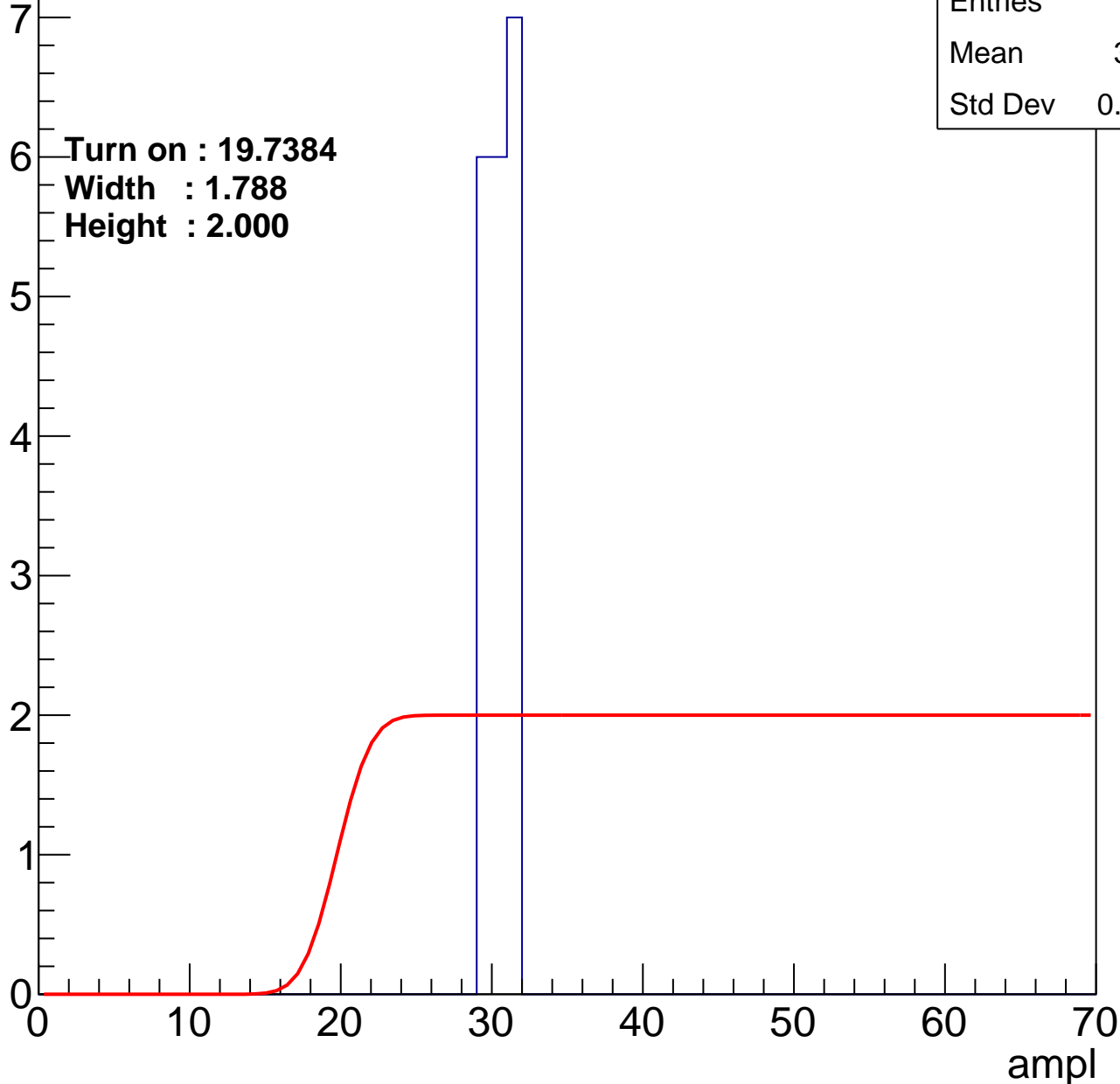
Entry

B0L100S, U22-ch15

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch16

calib_packv5_042523_0143.root, FC#6, port A1
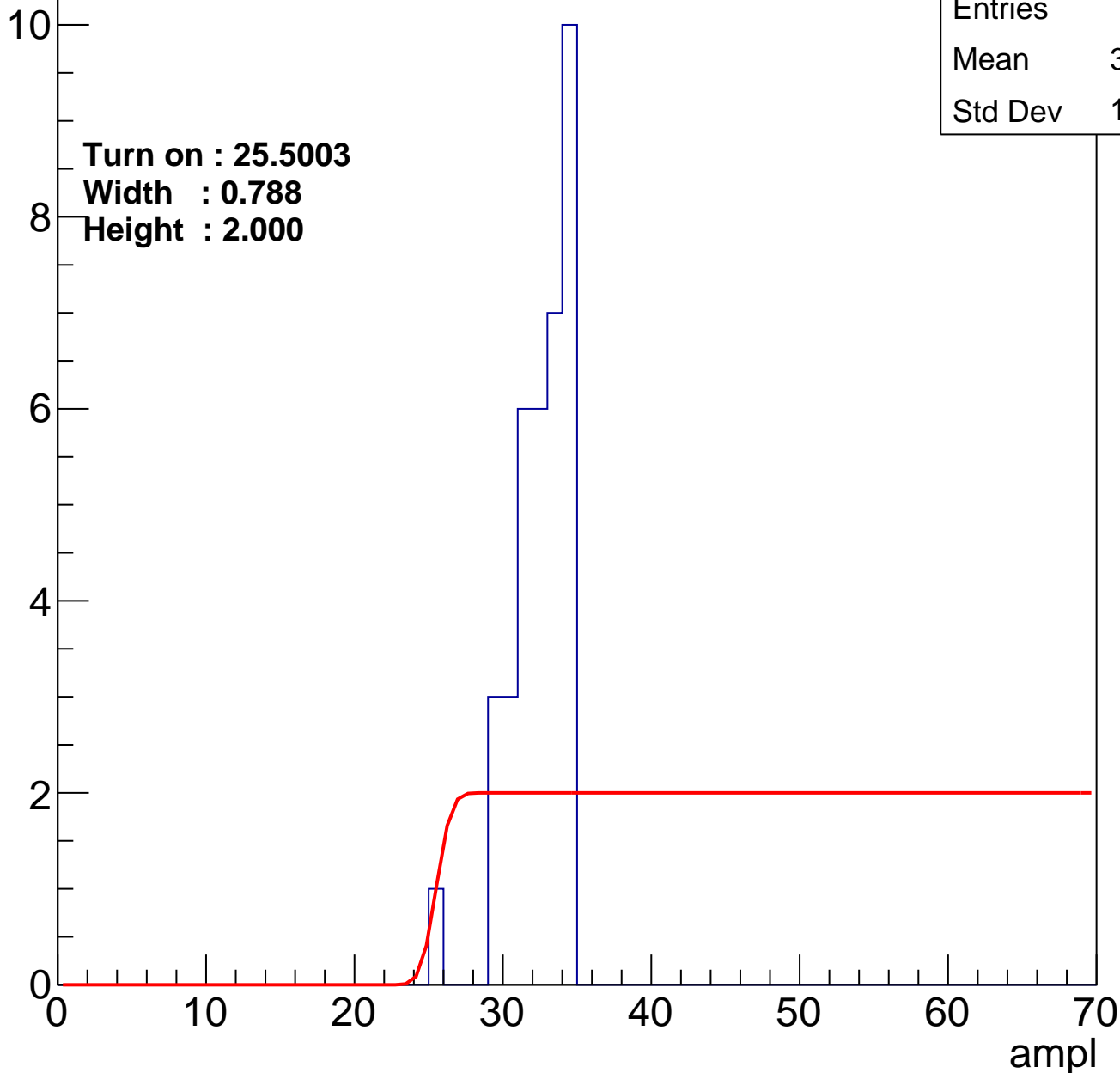
Entries	36
Mean	31.97
Std Dev	1.979

Turn on : 25.5003
Width   : 0.788
Height : 2.000

B0L100S, U22-ch17
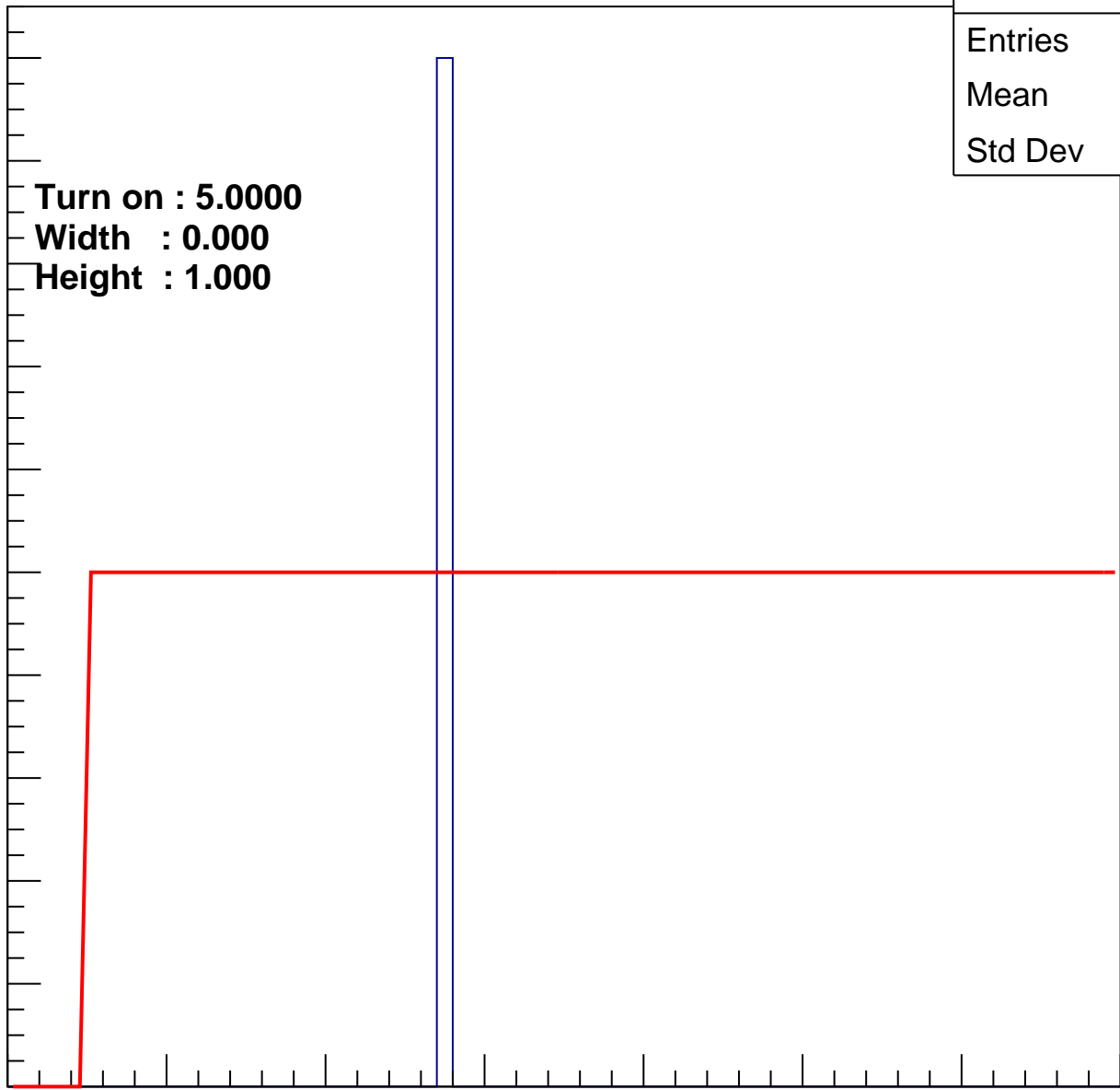
calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	27
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch18

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	29.33
Std Dev	0.4714

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
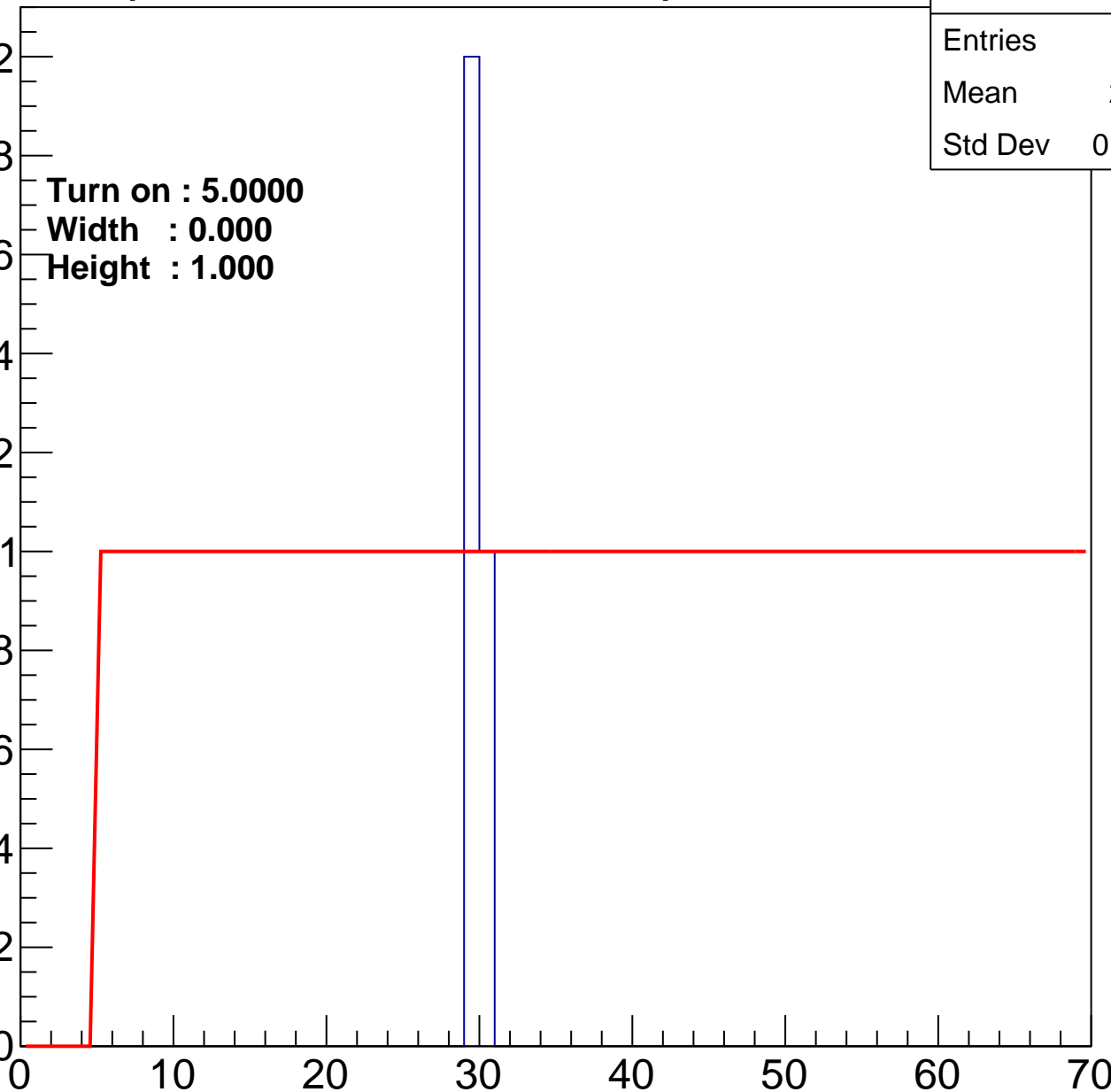
B0L100S, U22-ch19

calib_packv5_042523_0143.root, FC#6, port A1

Entries		10
Mean		30.4
Std Dev		0.4899

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
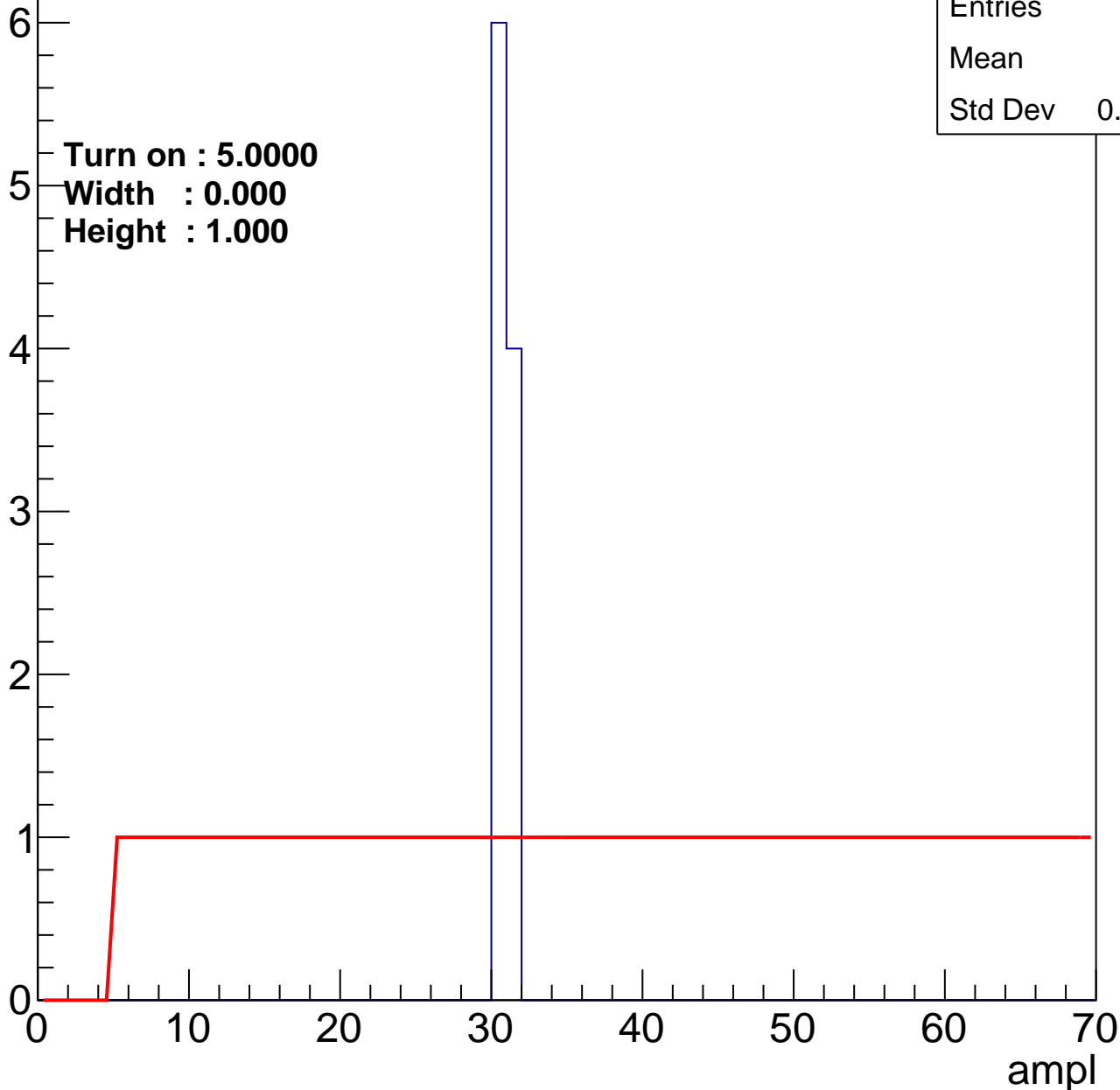
B0L100S, U22-ch20

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

ampl
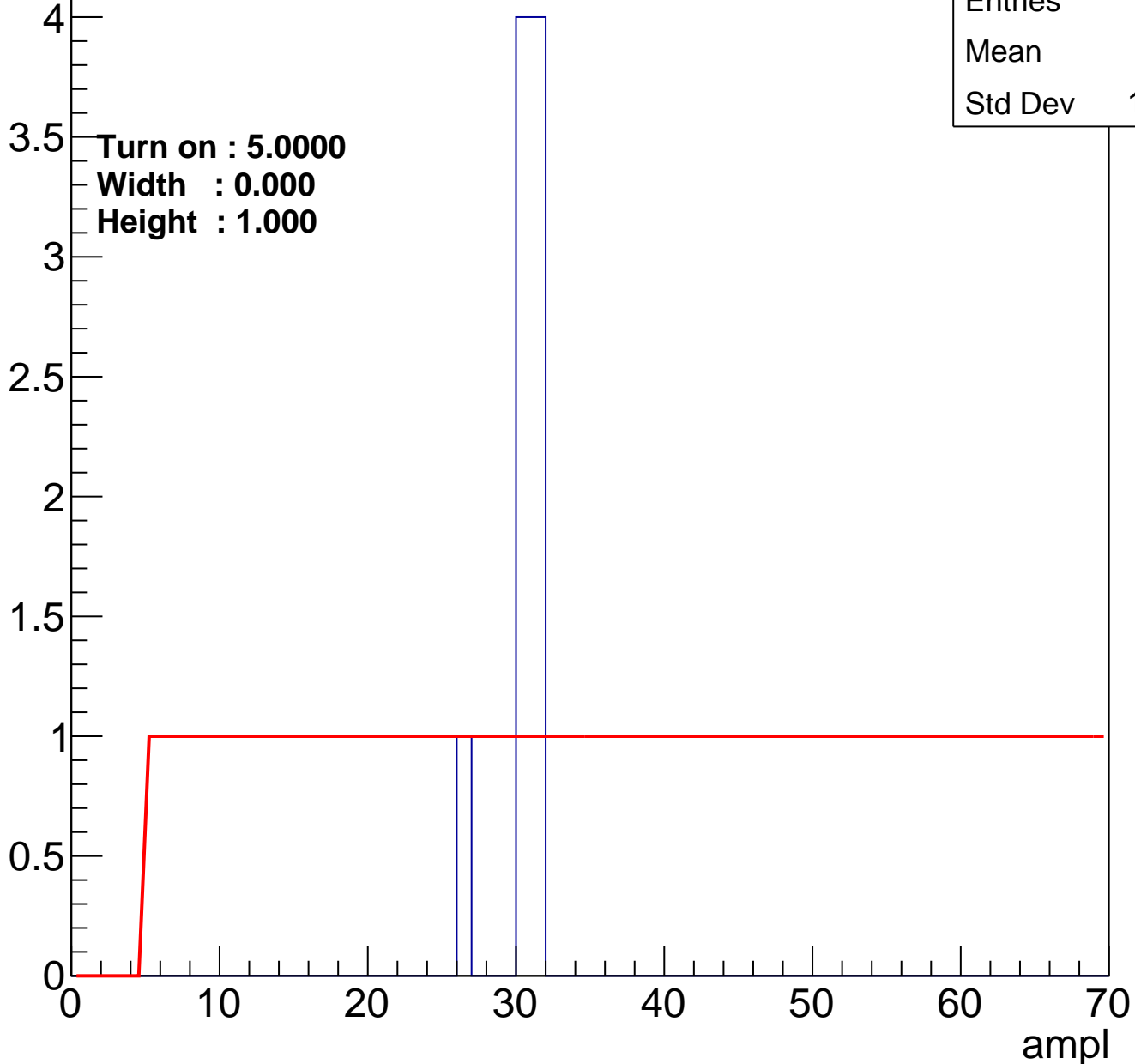
B0L100S, U22-ch22

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	29
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
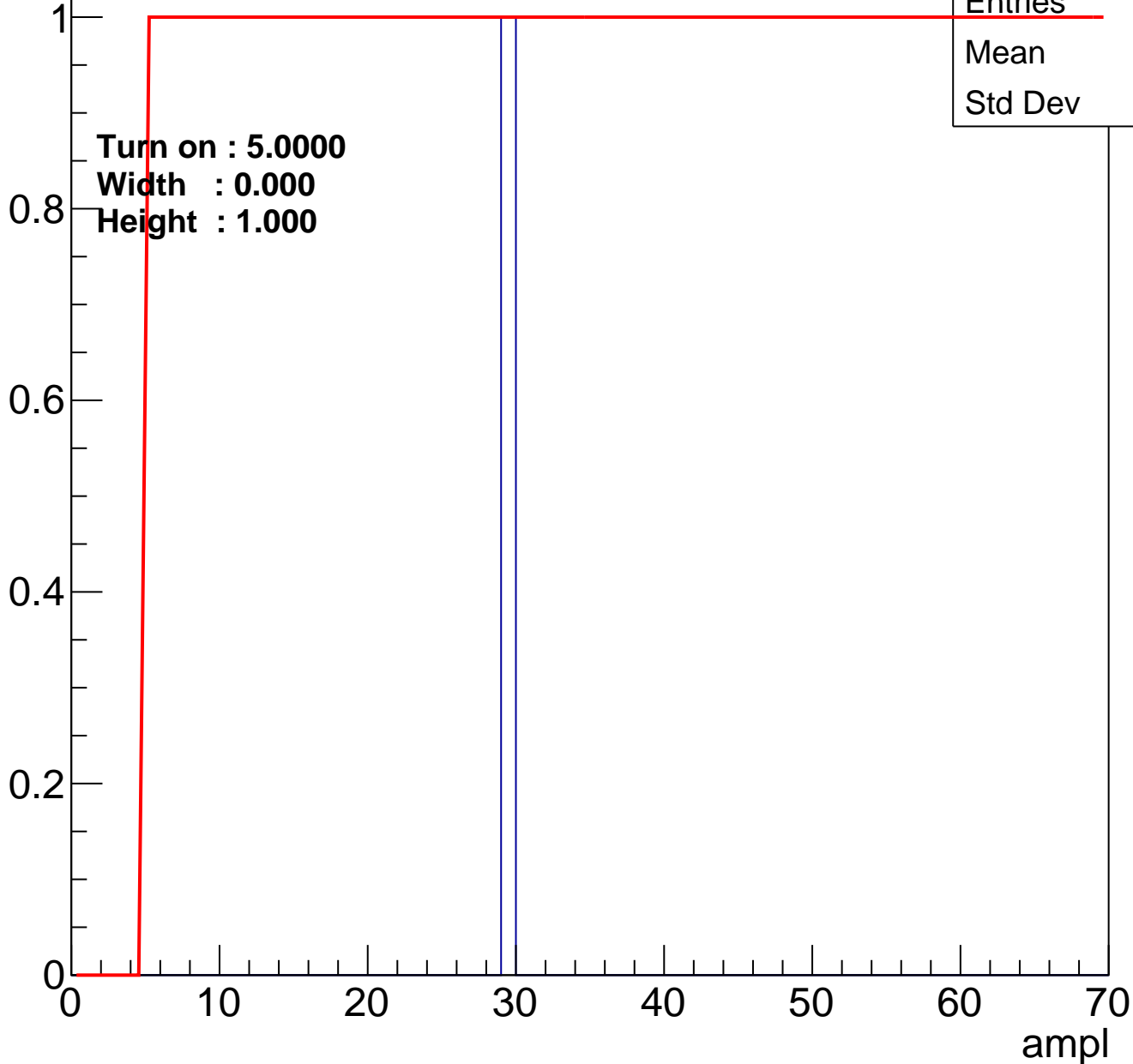
B0L100S, U22-ch23

calib_packv5_042523_0143.root, FC#6, port A1

Entries	35
Mean	31.03
Std Dev	1.781

Turn on : 26.5001
Width   : 0.290
Height : 2.000

Entry

ampl
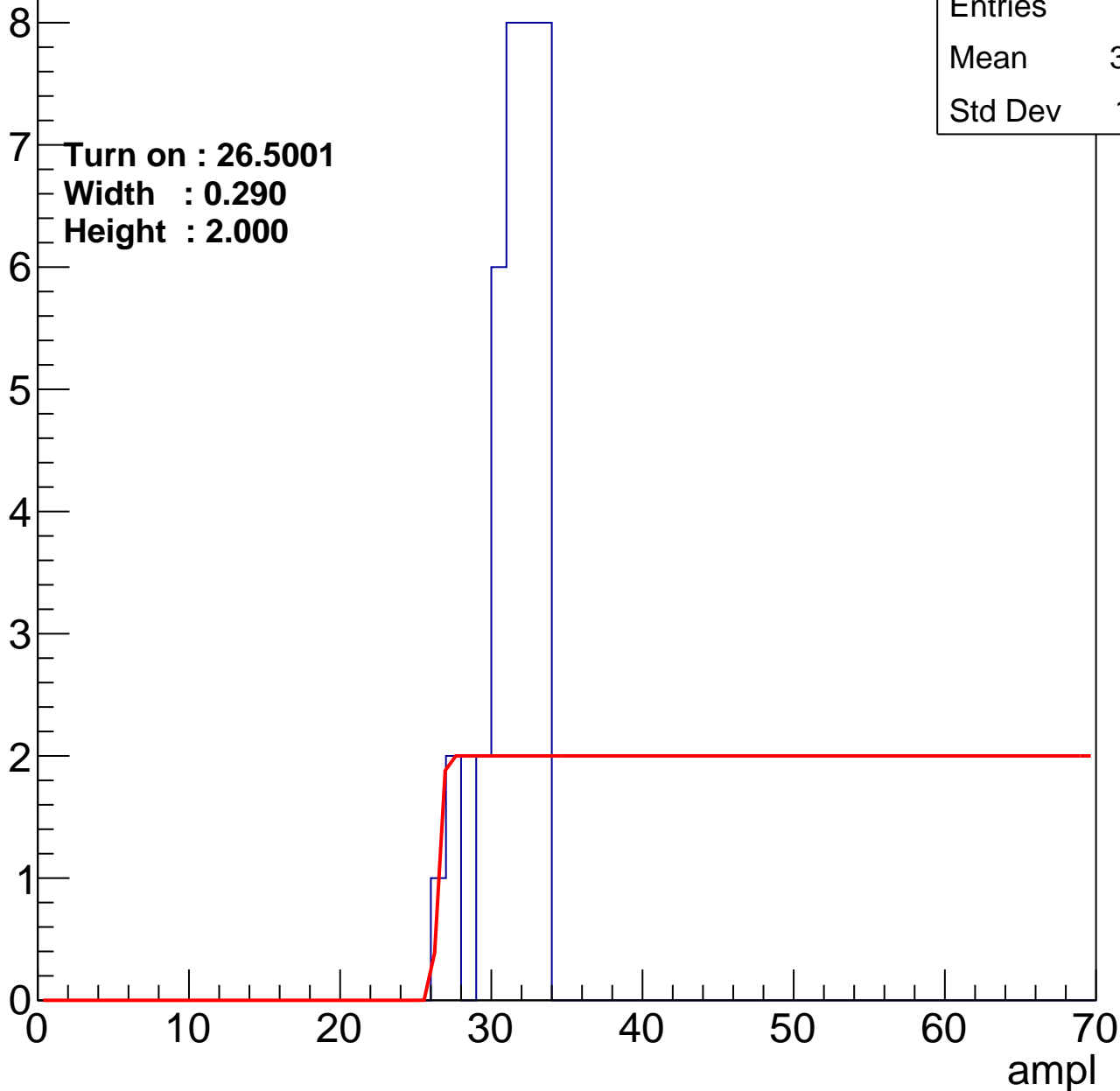
B0L100S, U22-ch24

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch25

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch26

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch27

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch28

calib_packv5_042523_0143.root, FC#6, port A1

Entries	29
Mean	32.17
Std Dev	1.391

Turn on : 17.2572
Width   : 2.337
Height : 2.000

Entry

ampl
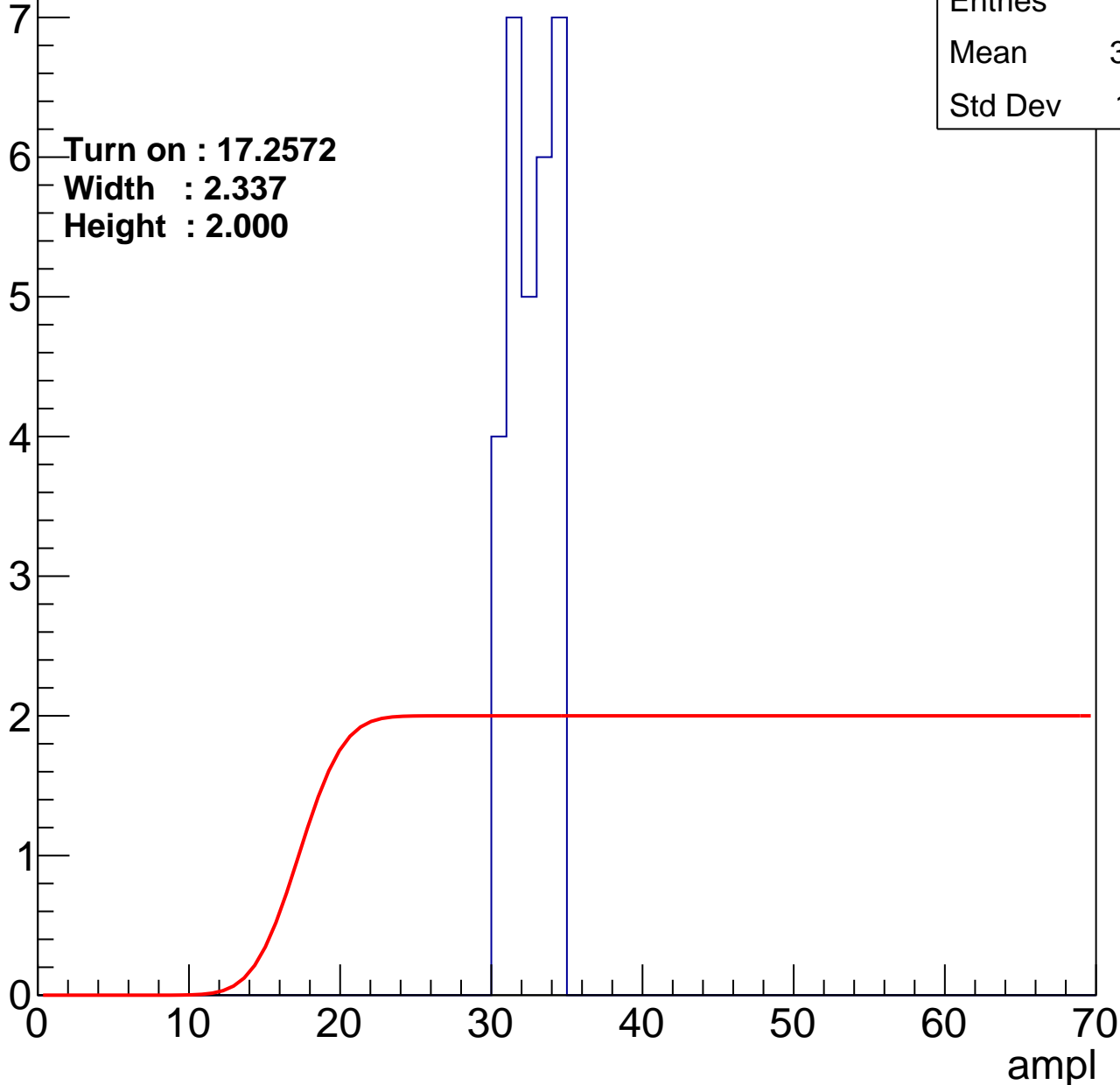
B0L100S, U22-ch29

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U22-ch30

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch31

calib_packv5_042523_0143.root, FC#6, port A1

Entries	23
Mean	31.17
Std Dev	0.9161

Turn on : 16.9066
Width   : 2.392
Height : 2.000
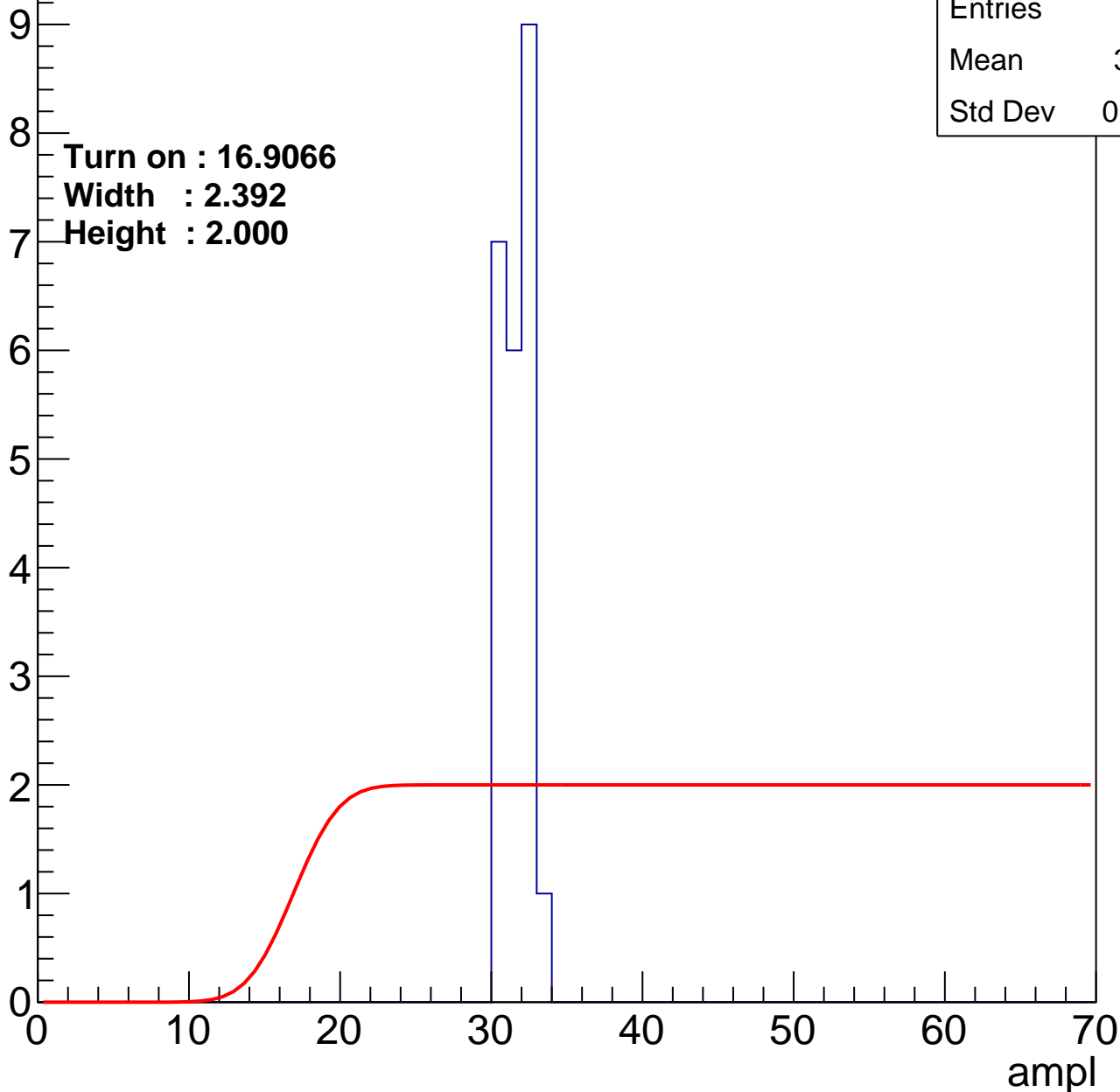
B0L100S, U22-ch32

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	24
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
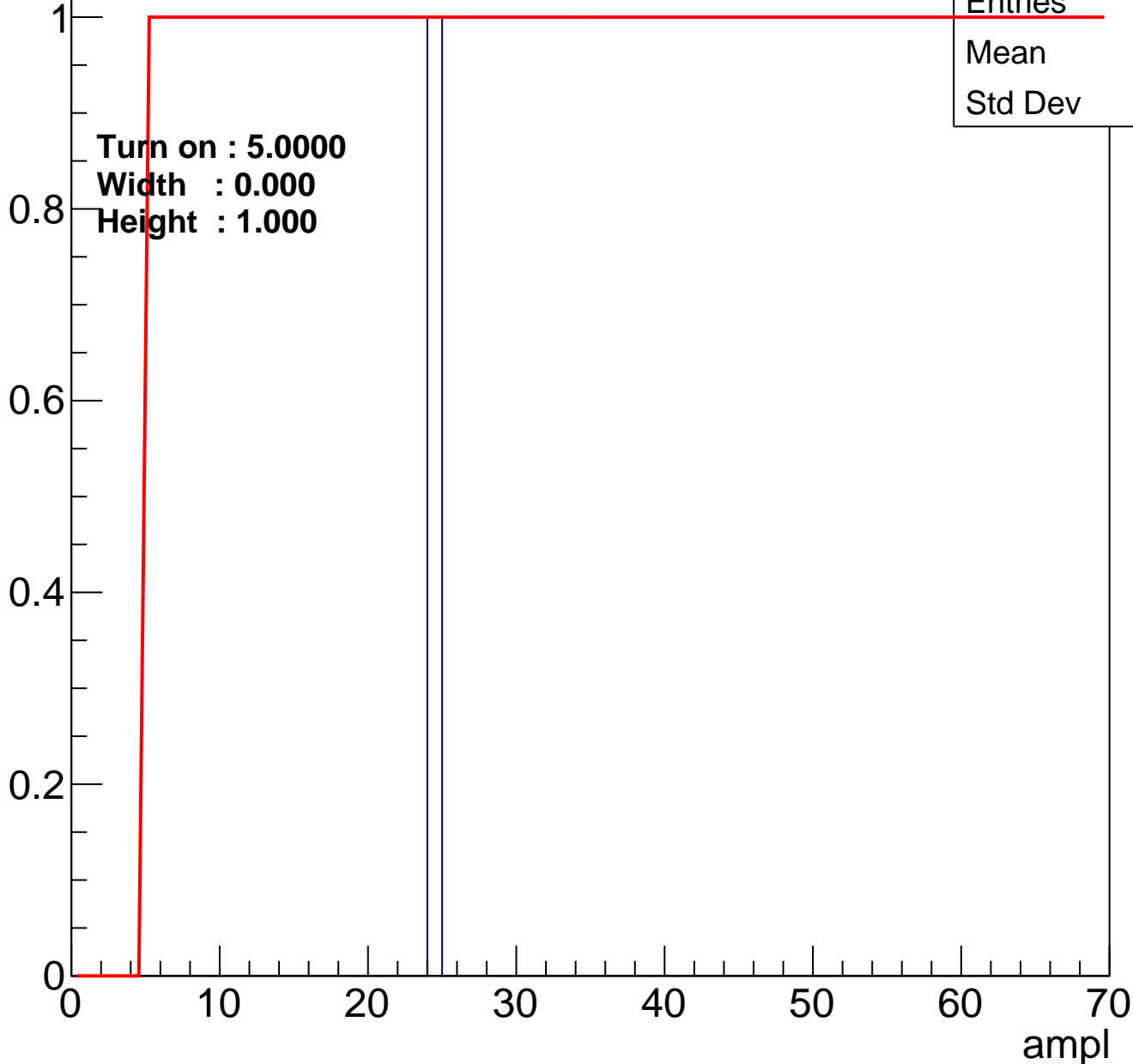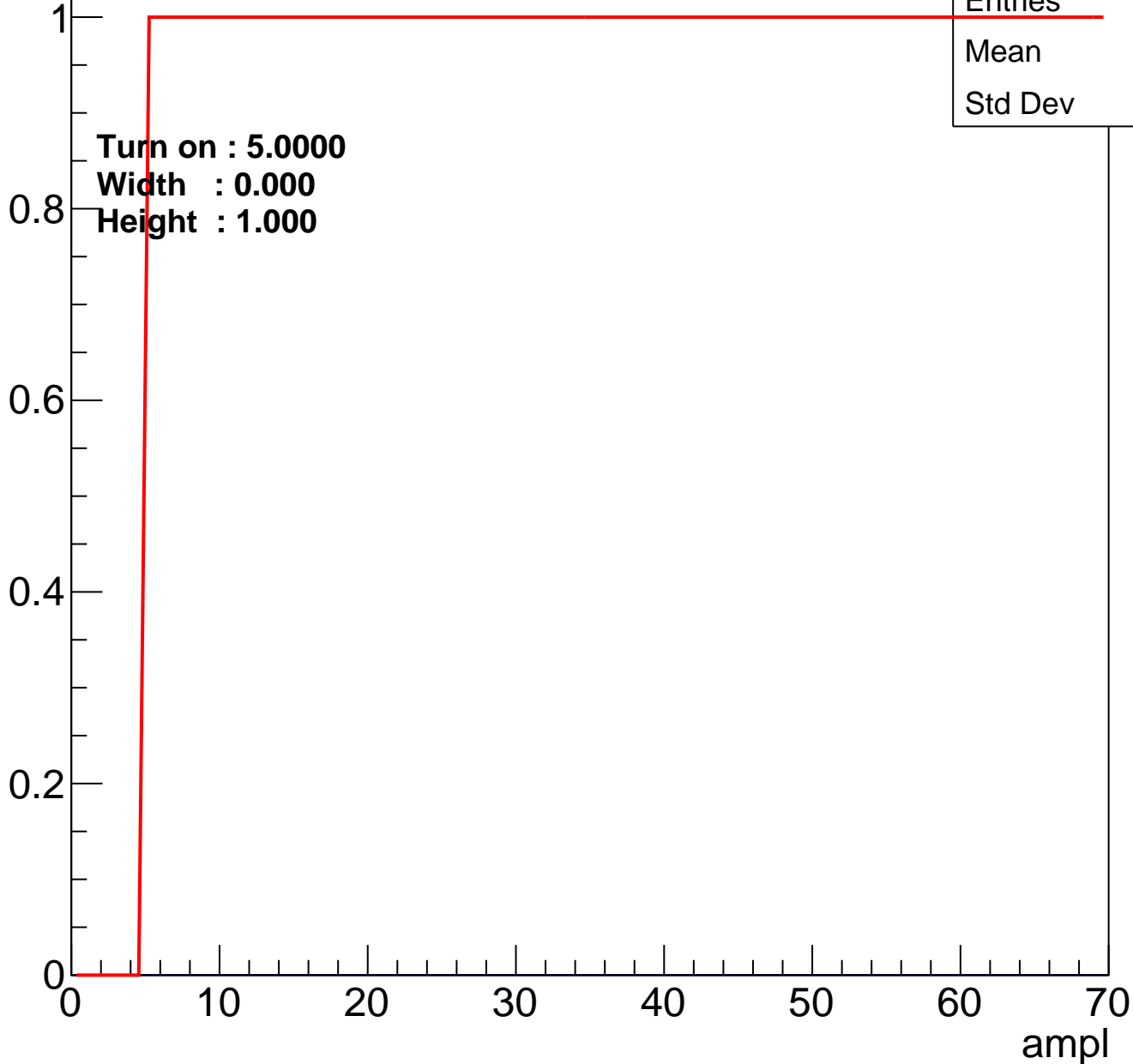
B0L100S, U22-ch33

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch34

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	6.25
Std Dev	10.83

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
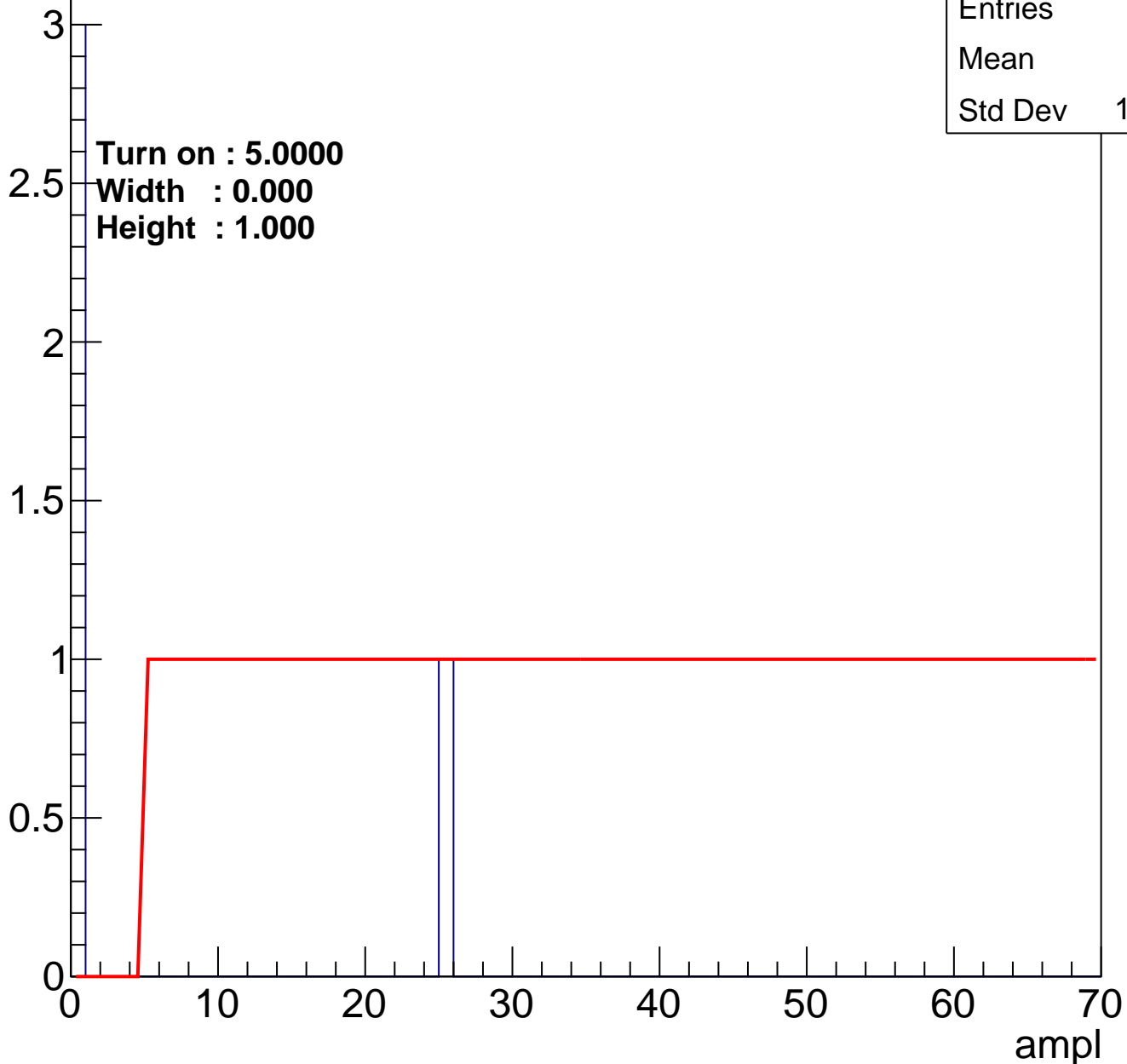
B0L100S, U22-ch35

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	1
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch36

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

B0L100S, U22-ch37

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch38

calib_packv5_042523_0143.root, FC#6, port A1

Entries	46
Mean	33.02
Std Dev	1.775

Turn on : 16.5292
Width   : 2.167
Height : 2.000
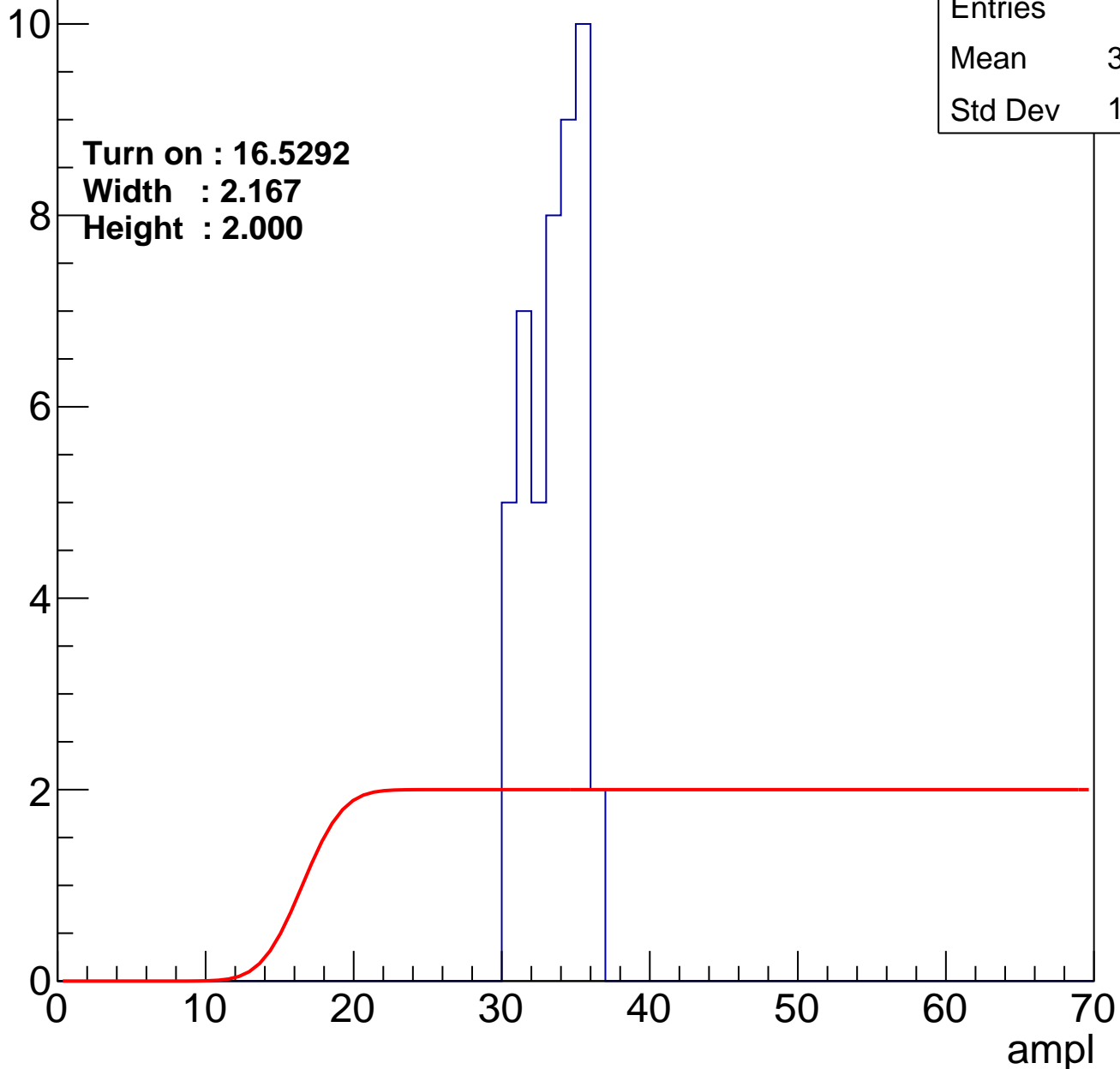
B0L100S, U22-ch39

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch40

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch41

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch42

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U22-ch43

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch44

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

B0L100S, U22-ch45

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch46

calib_packv5_042523_0143.root, FC#6, port A1

Entries	28
Mean	30.96
Std Dev	2.921

Turn on : 15.7228
Width   : 1.594
Height : 2.000

Entry

ampl
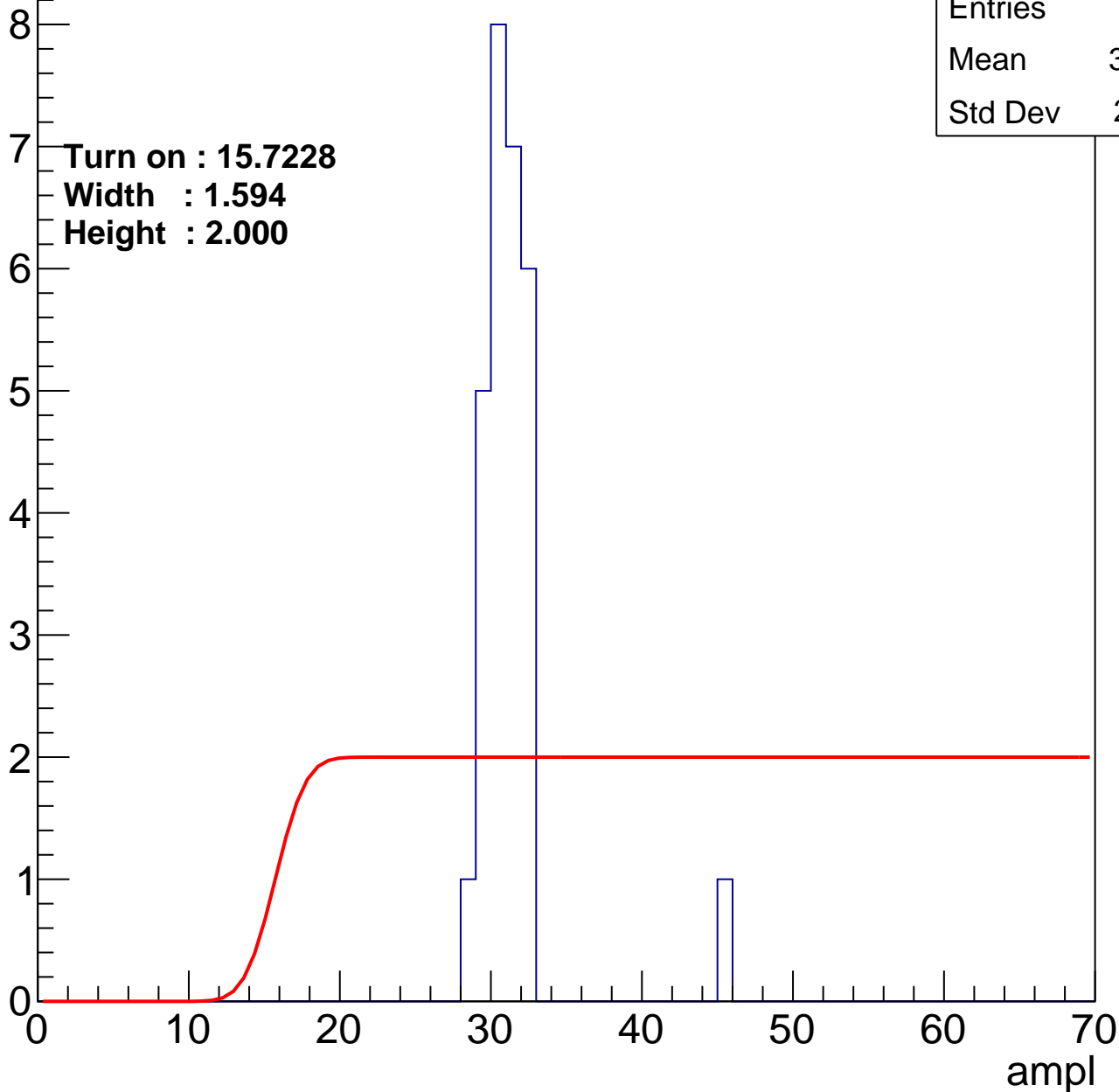
B0L100S, U22-ch47

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch48

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch49

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

B0L100S, U22-ch50

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	29.86
Std Dev	0.6389

Turn on : 21.3433
Width : 1.529
Height : 2.000
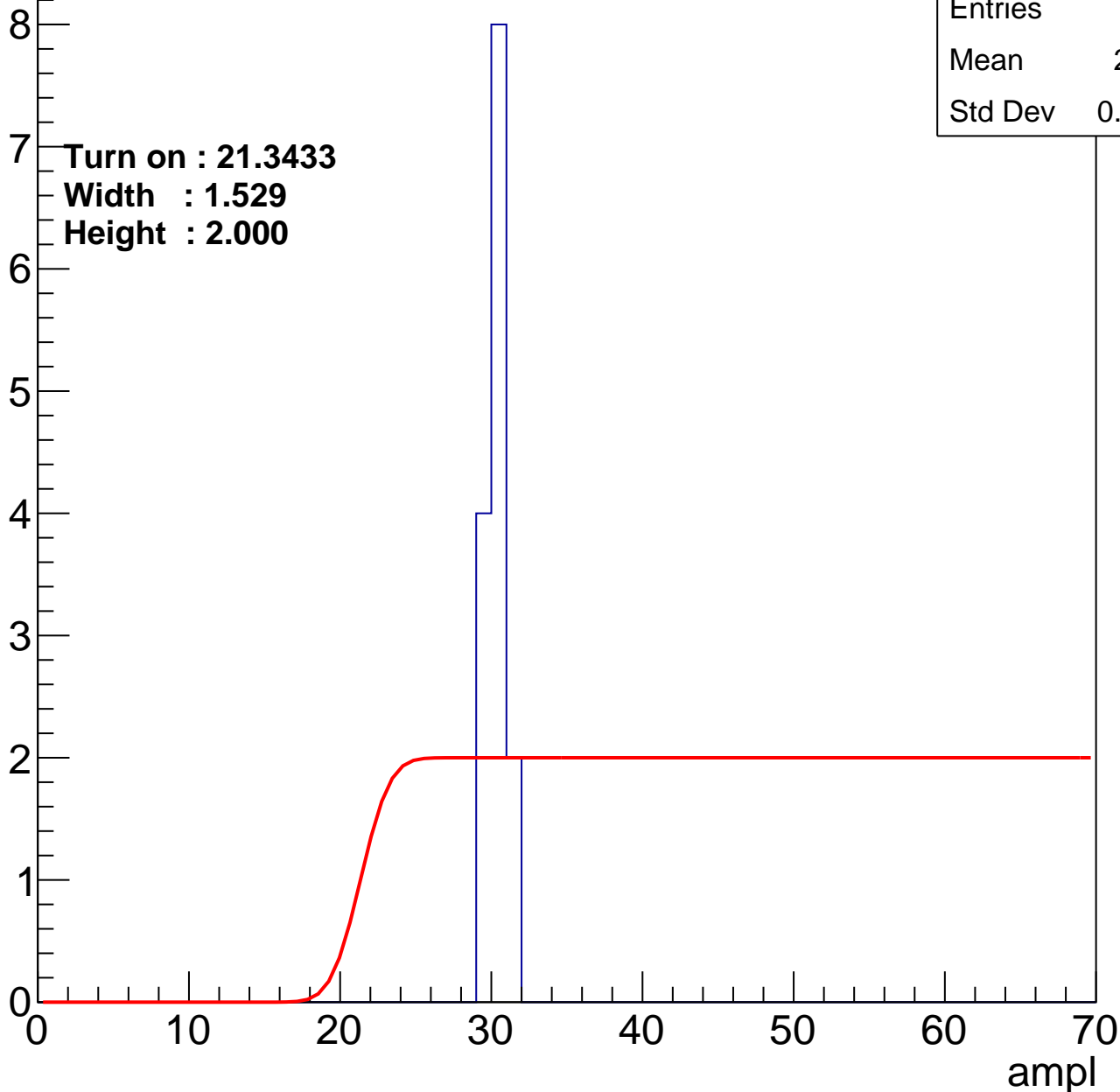
B0L100S, U22-ch51

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	25
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
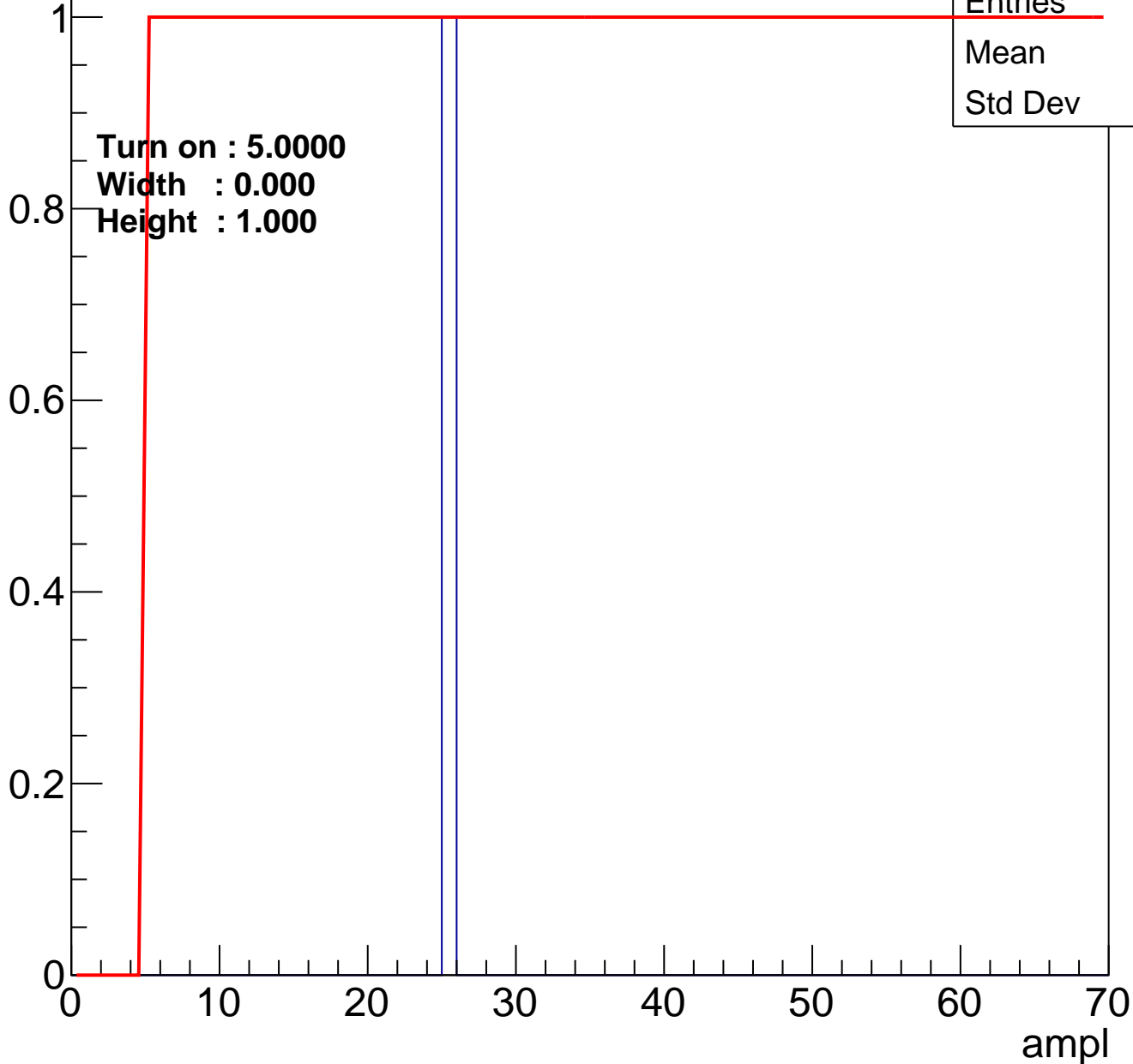
B0L100S, U22-ch52

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch53

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch54

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Turn on : 24.8495
Width   : 3.099
Height : 2.000

Entries	11
Mean	28.73
Std Dev	1.601

ampl
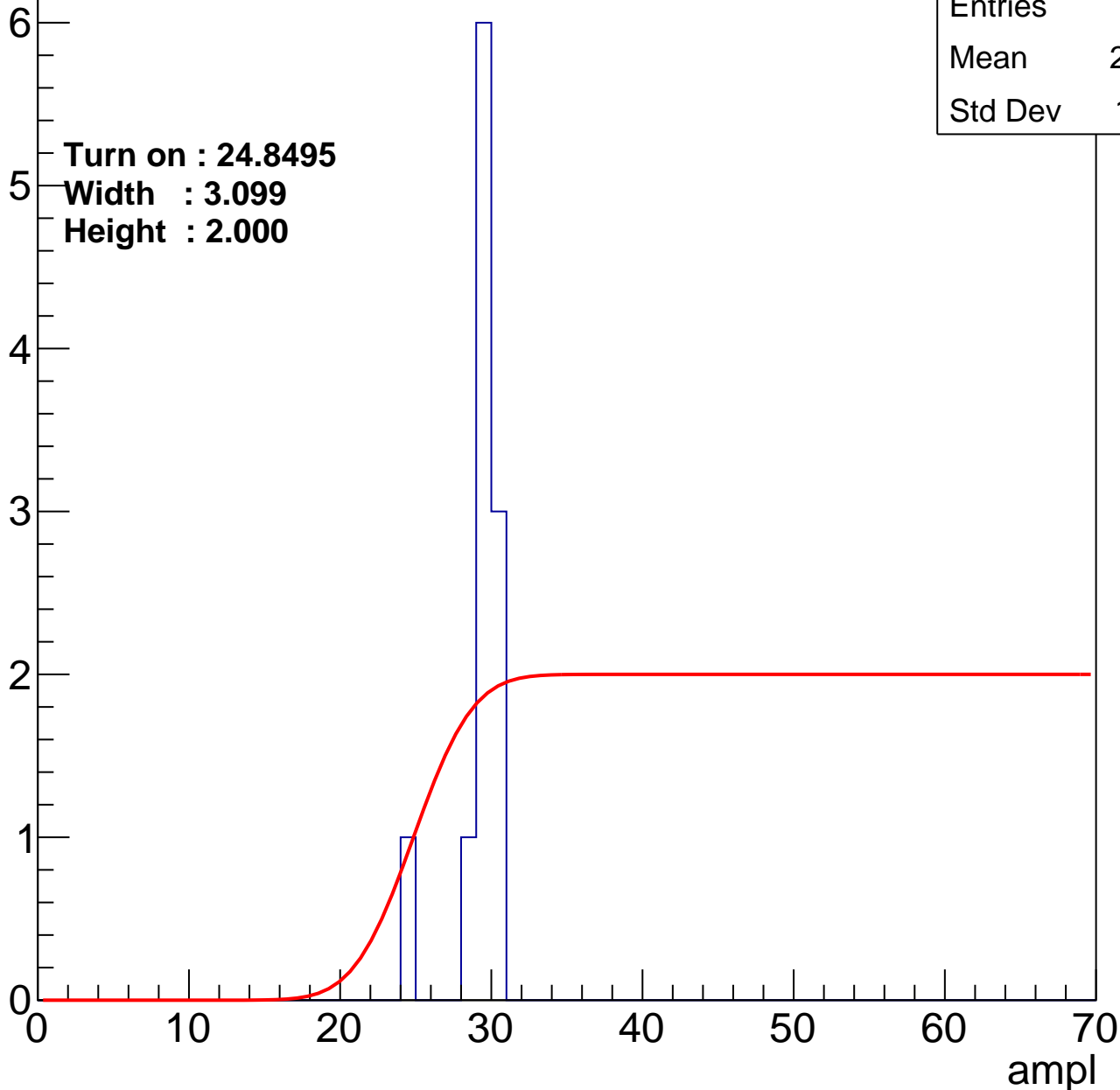
B0L100S, U22-ch55

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch56

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch57

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	29.33
Std Dev	0.4714

Turn on : 5.0000
Width   : 0.000
Height : 1.000
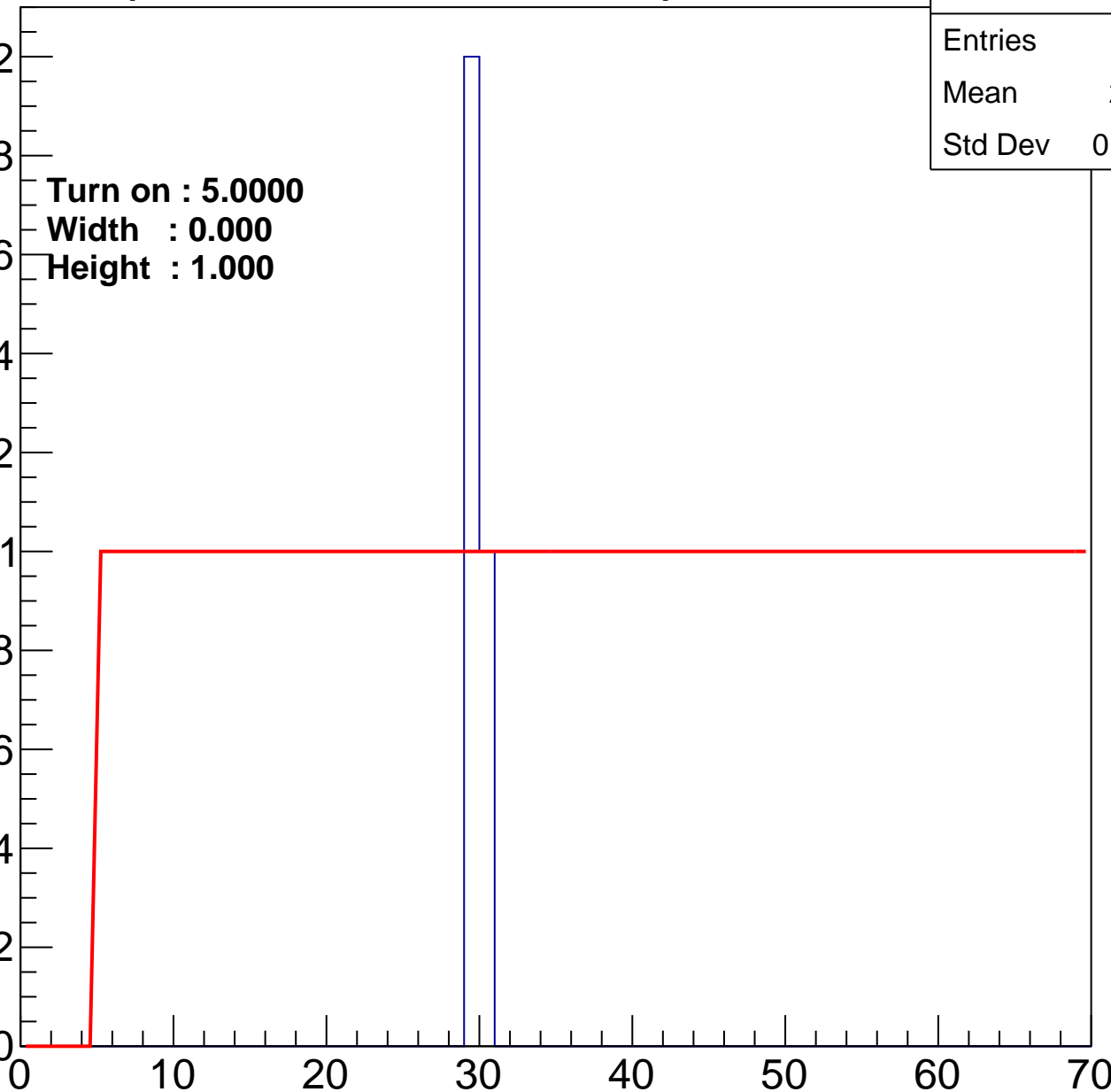
B0L100S, U22-ch58

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
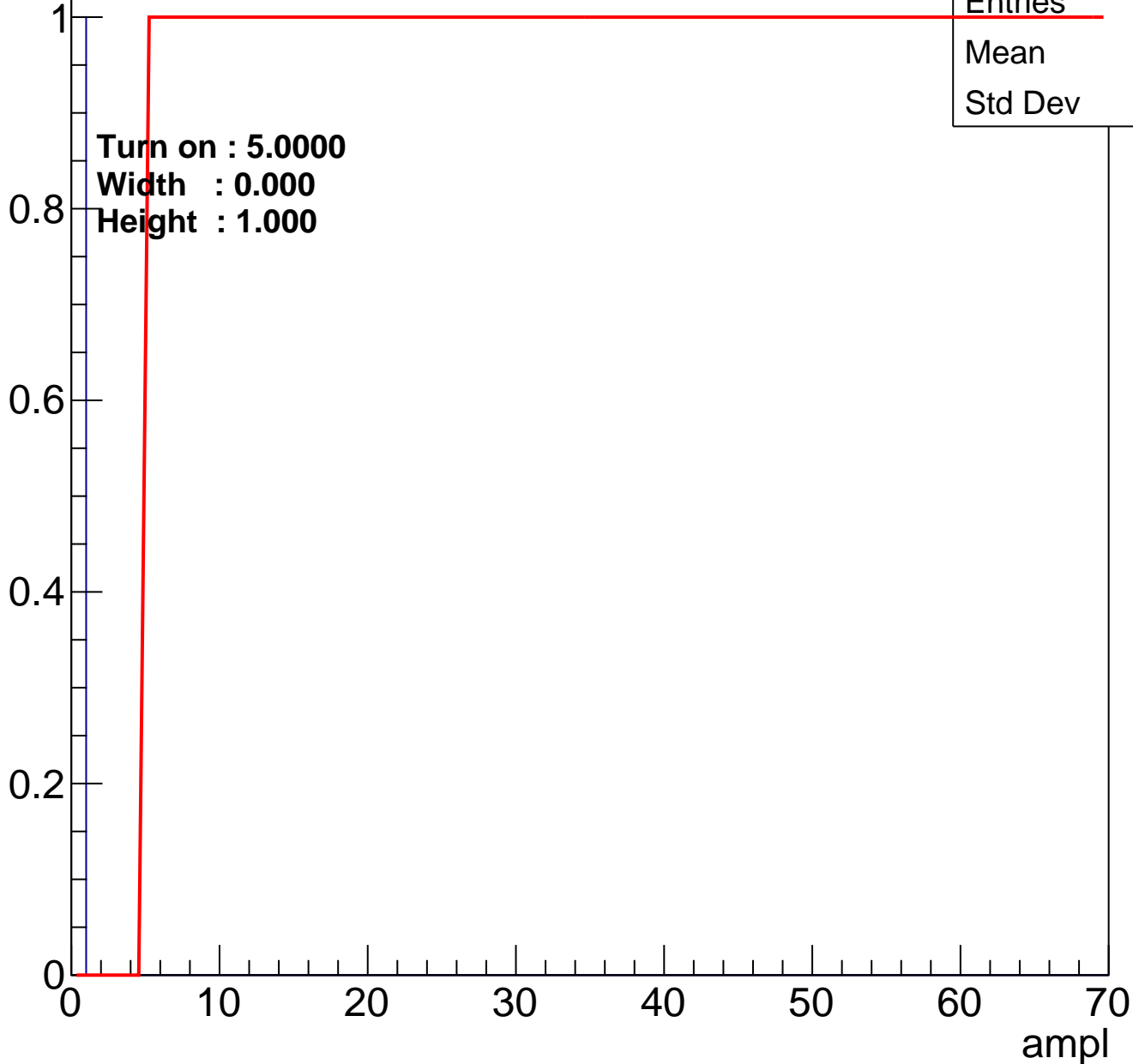
B0L100S, U22-ch59

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch60

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch61

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	25
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl
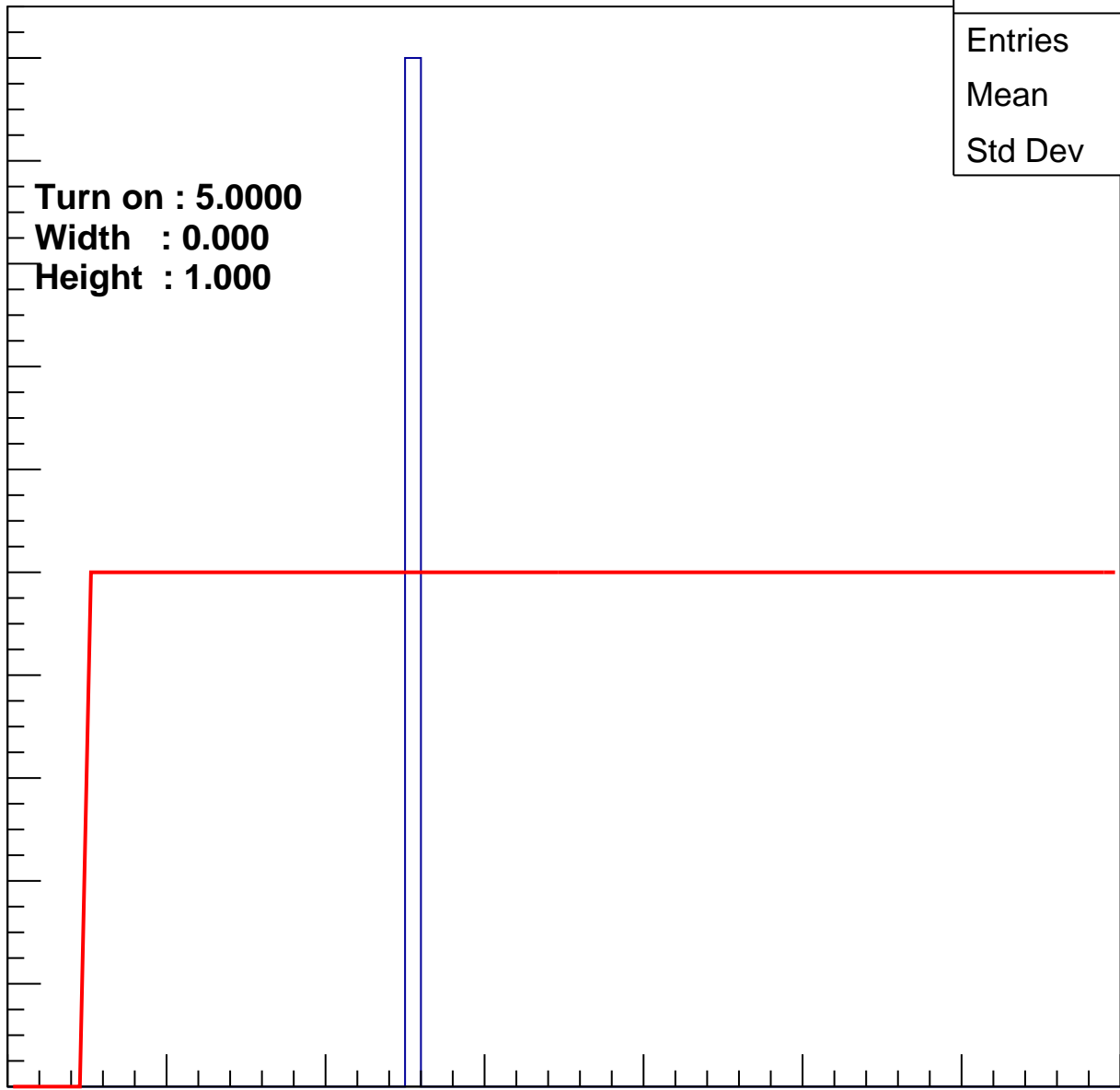
B0L100S, U22-ch62

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch63

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

B0L100S, U22-ch64

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

B0L100S, U22-ch65

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch66

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch67

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch68

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch69

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	30
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch70

calib_packv5_042523_0143.root, FC#6, port A1

Entries	54
Mean	33.61
Std Dev	2.172

Turn on : 29.5000
Width   : 0.189
Height : 2.000
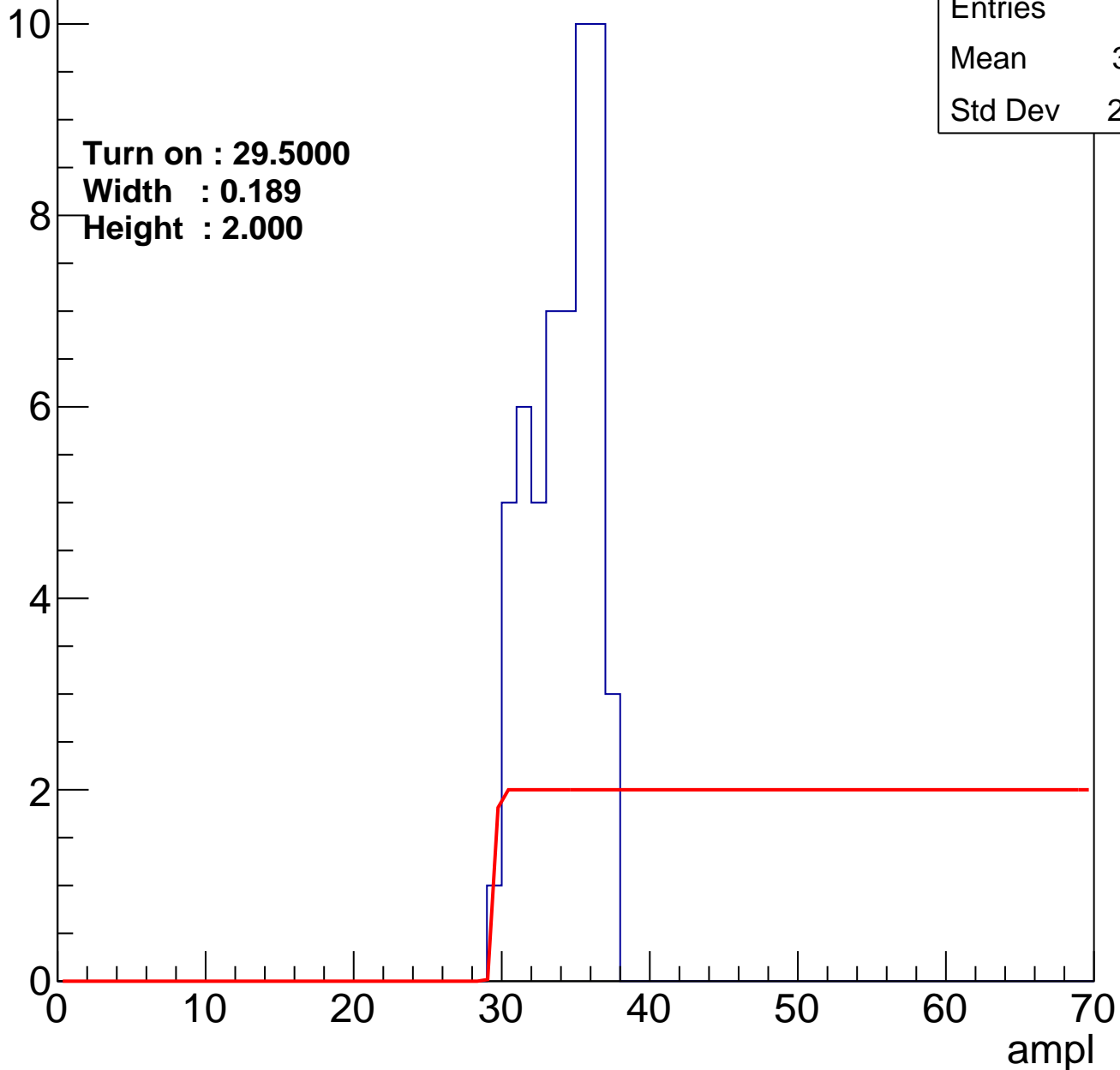
B0L100S, U22-ch71

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch72

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch73

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch74

calib_packv5_042523_0143.root, FC#6, port A1

Entries	8
Mean	29
Std Dev	1.581

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
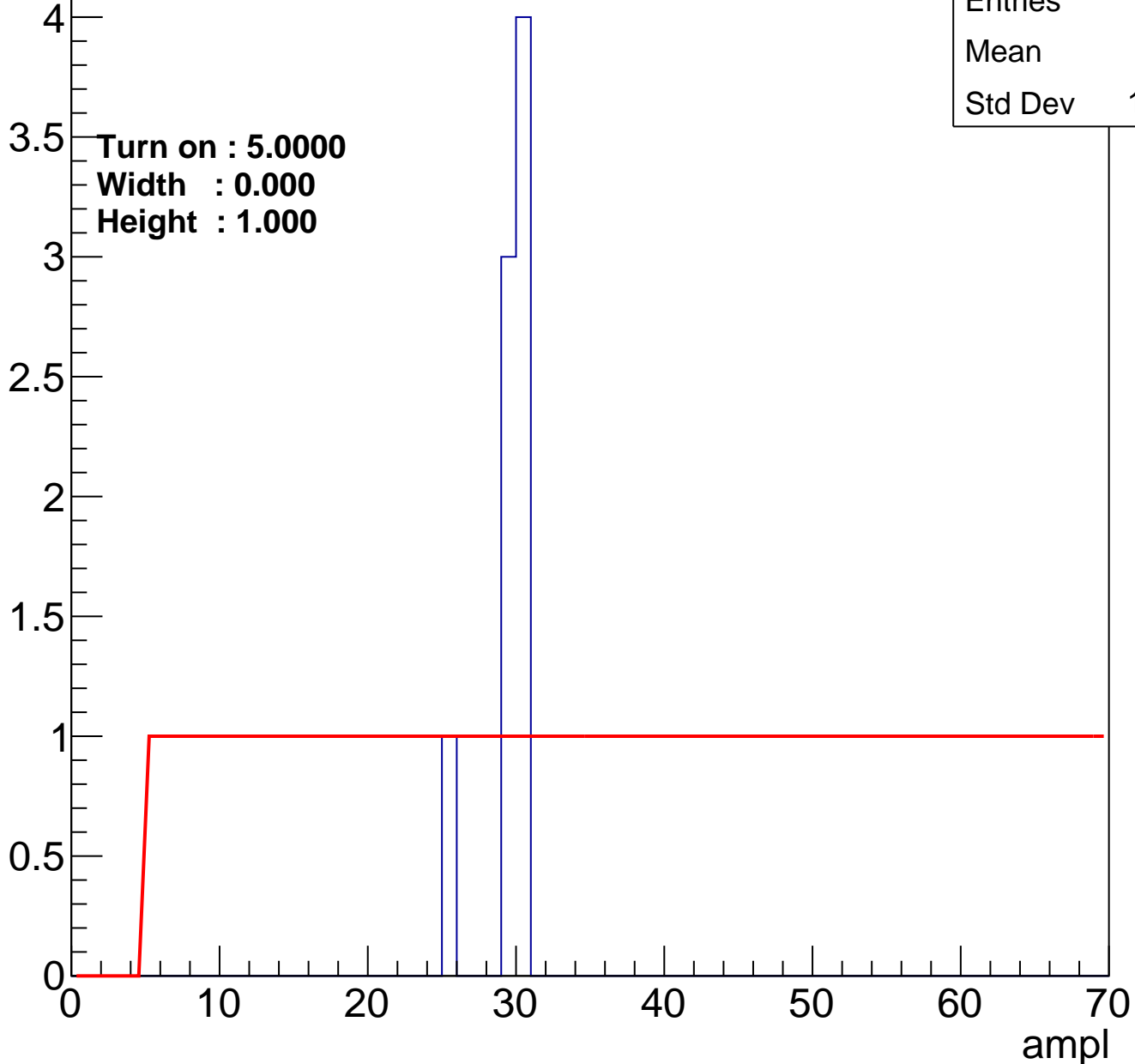
B0L100S, U22-ch75

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch76

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch77

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000
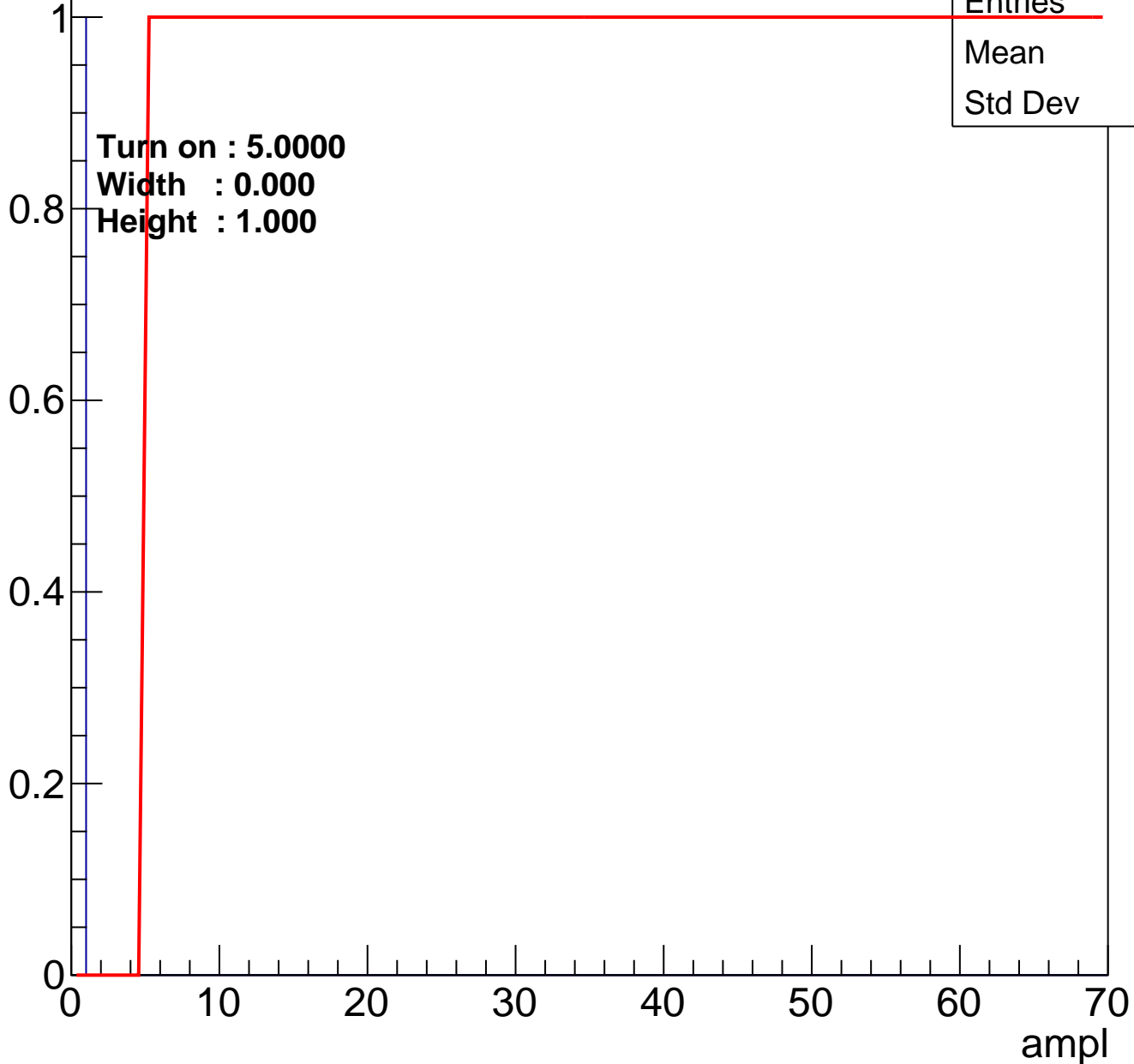
	Entries	0
	Mean	0
	Std Dev	0

B0L100S, U22-ch78

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch79

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	1
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch80

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch81

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch82

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch83

calib_packv5_042523_0143.root, FC#6, port A1

Entries	36
Mean	32.31
Std Dev	1.33

Turn on : 17.8385
Width   : 2.450
Height : 2.000
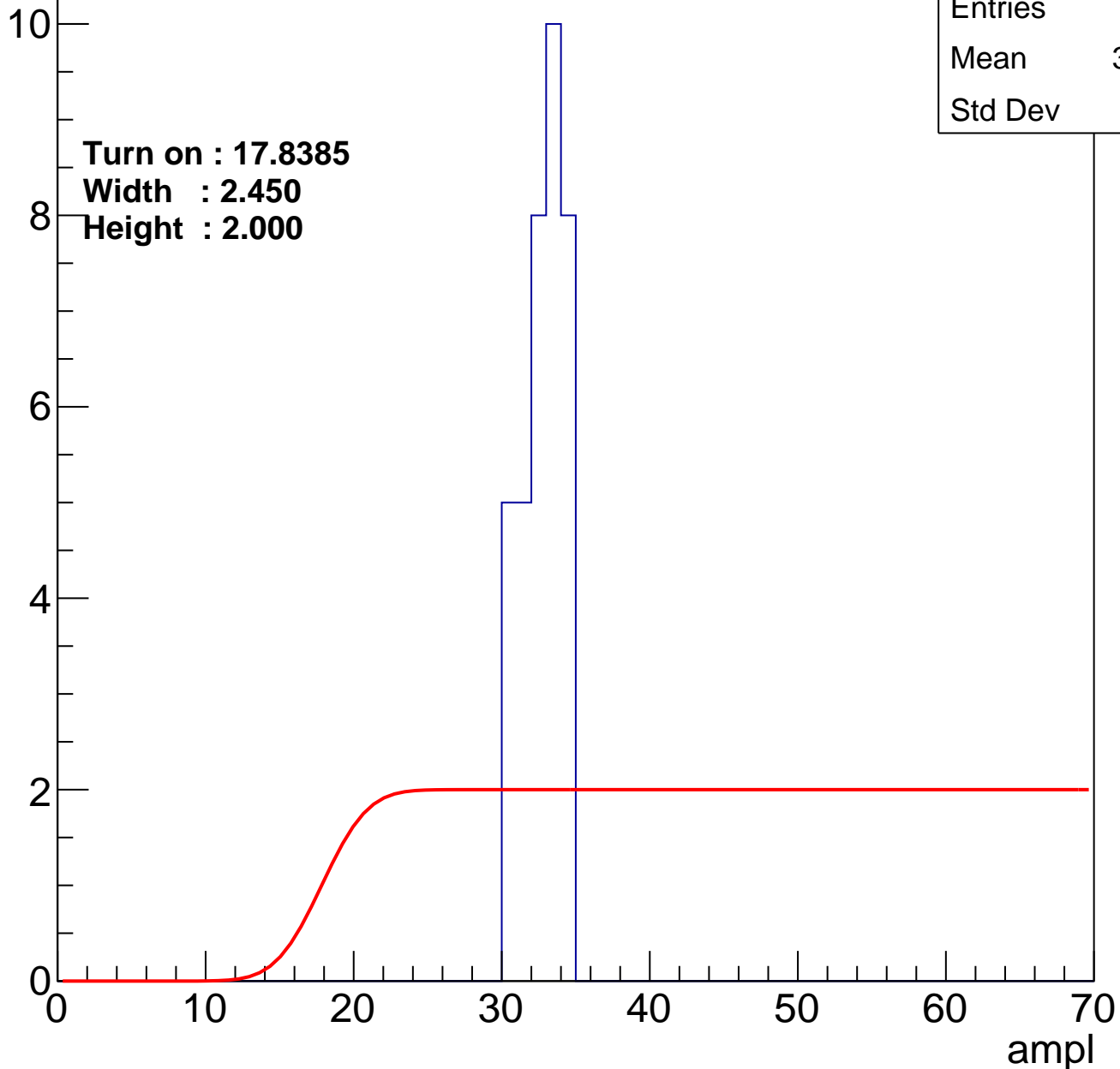
B0L100S, U22-ch84

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch85

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch86

calib_packv5_042523_0143.root, FC#6, port A1

Entries	30
Mean	30.87
Std Dev	1.31

Turn on : 19.9211
Width   : 1.835
Height : 2.000
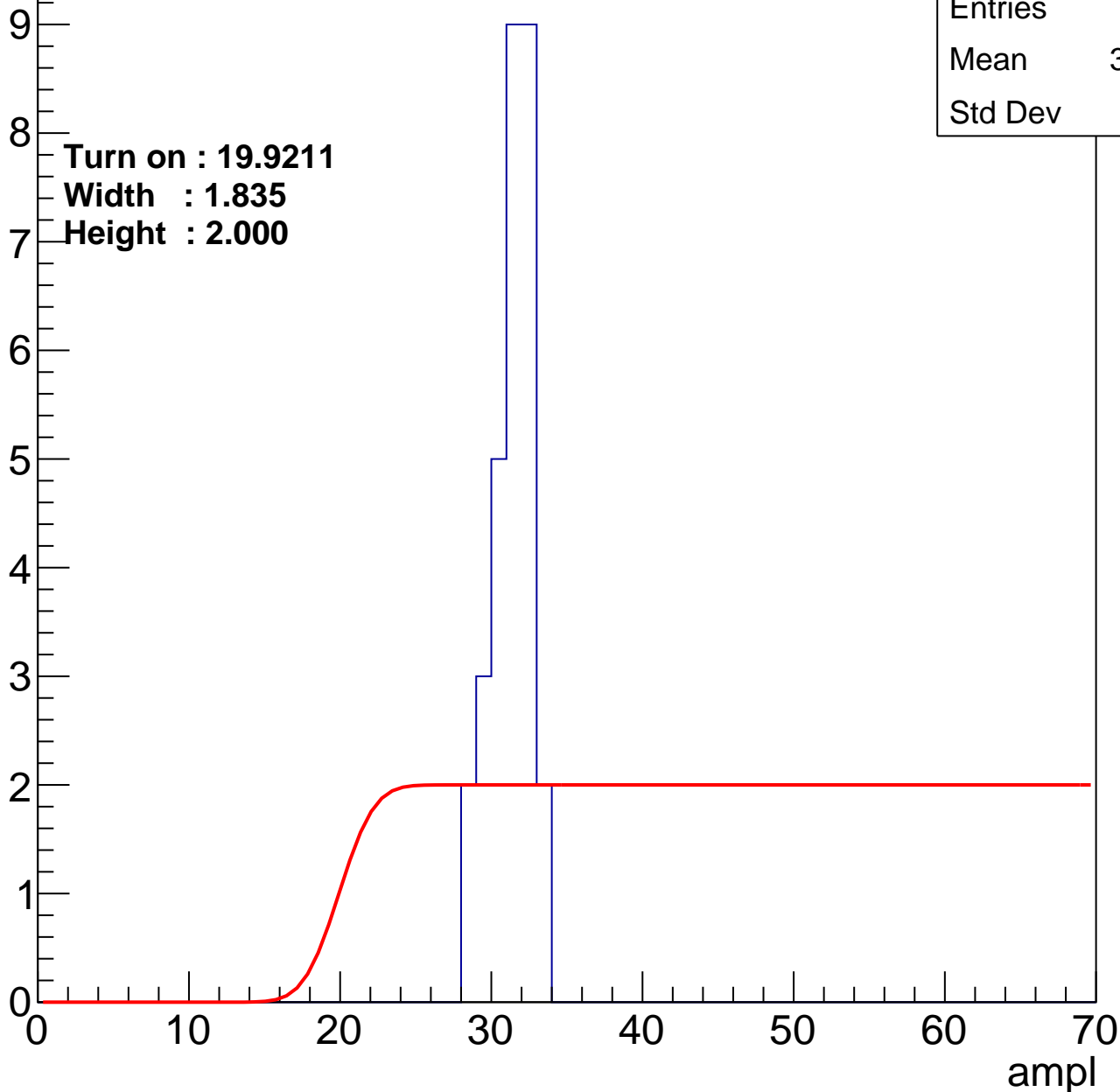
B0L100S, U22-ch87

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	1
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
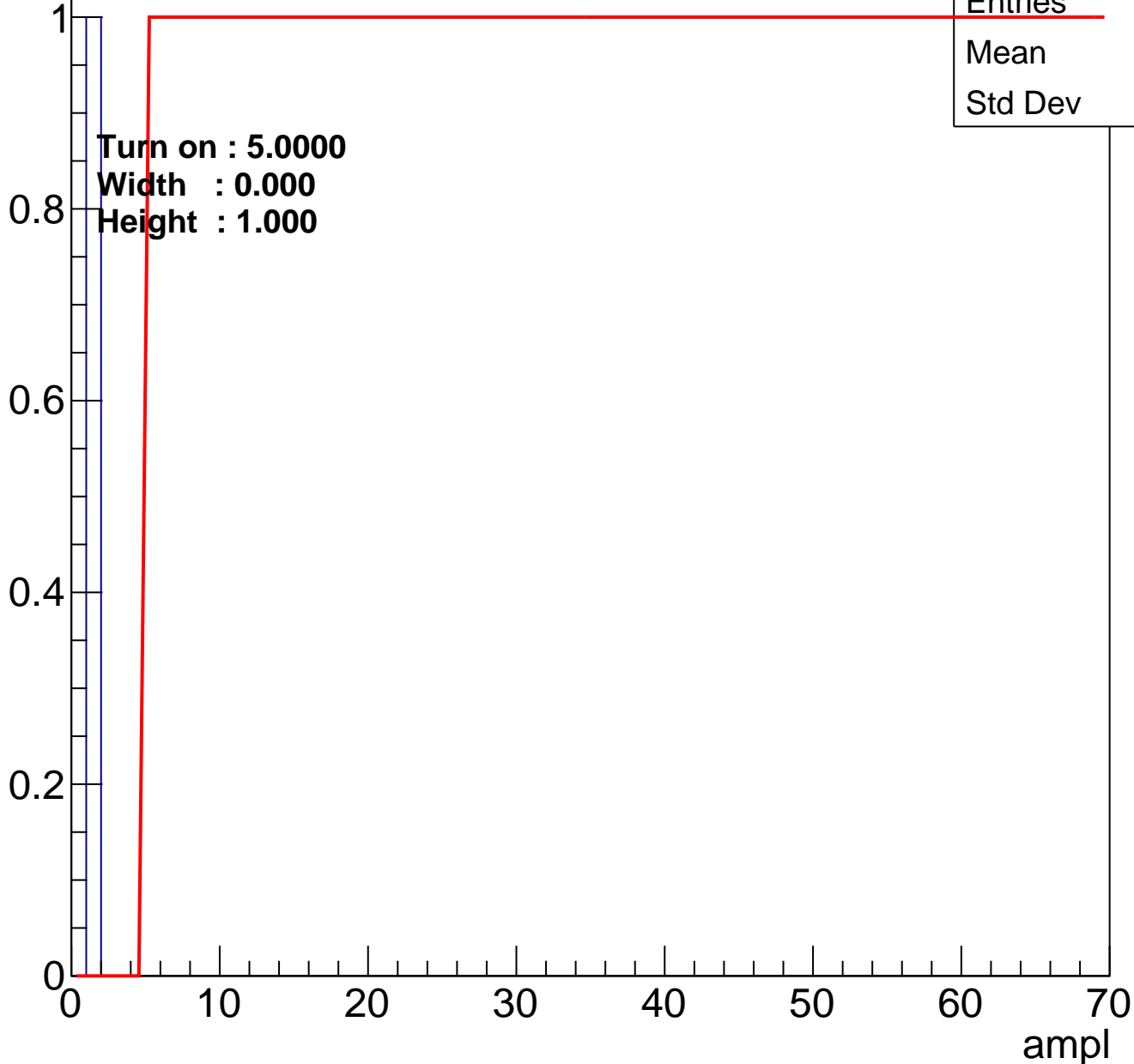
B0L100S, U22-ch88

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	29
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000
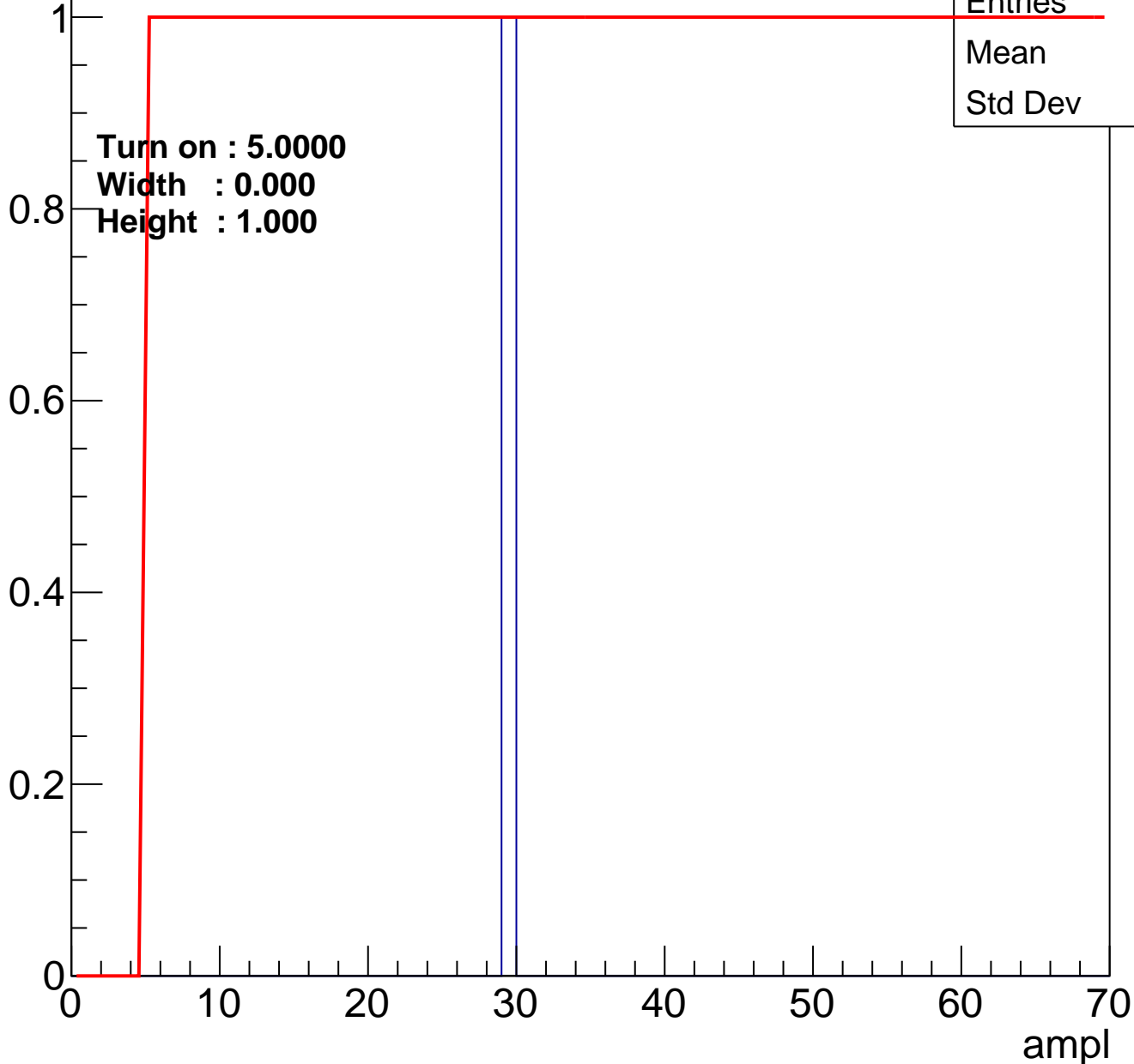
Entry

ampl

B0L100S, U22-ch89

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	25
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000
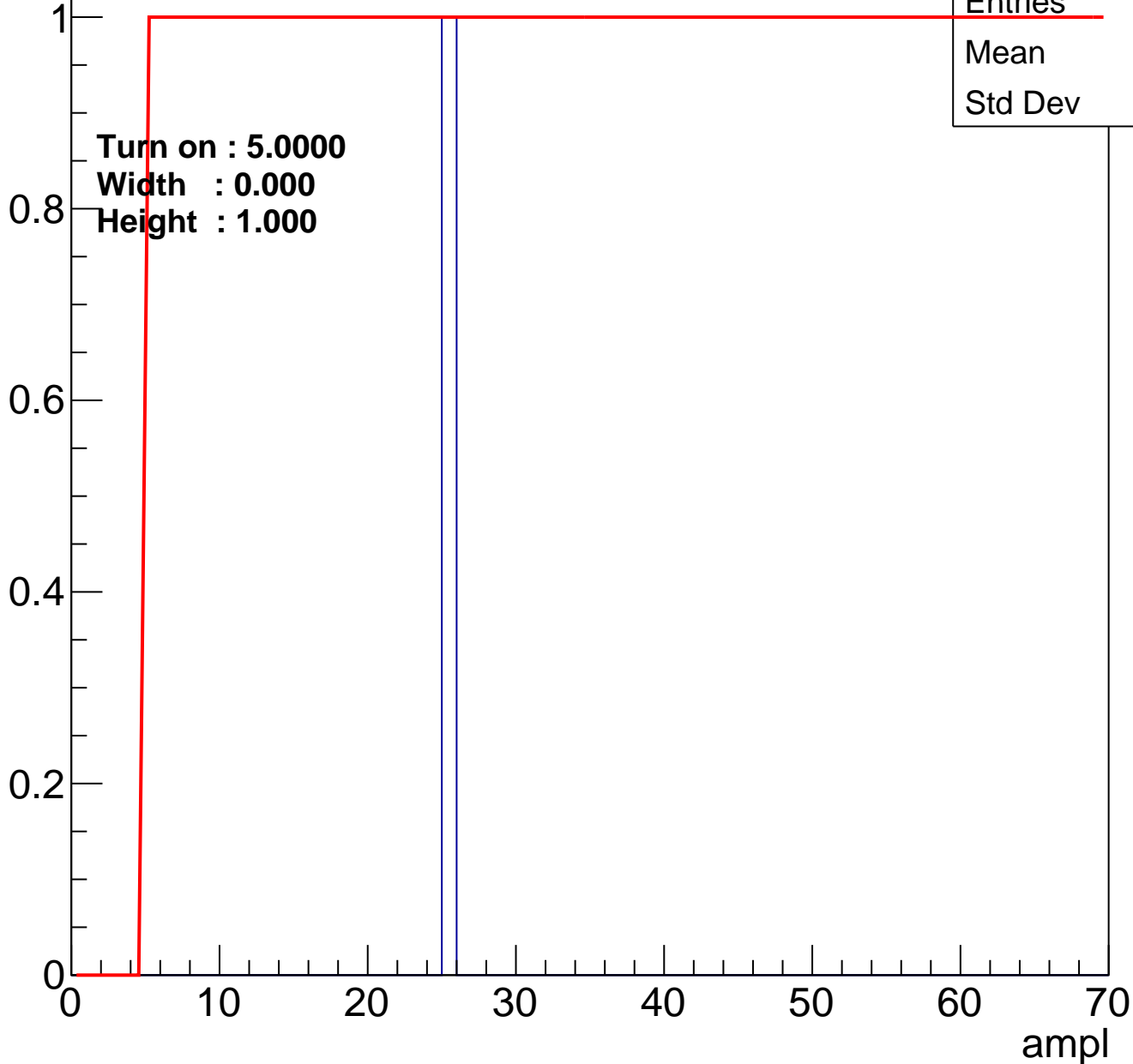
Entry

ampl

B0L100S, U22-ch90

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	29.8
Std Dev	0.4

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
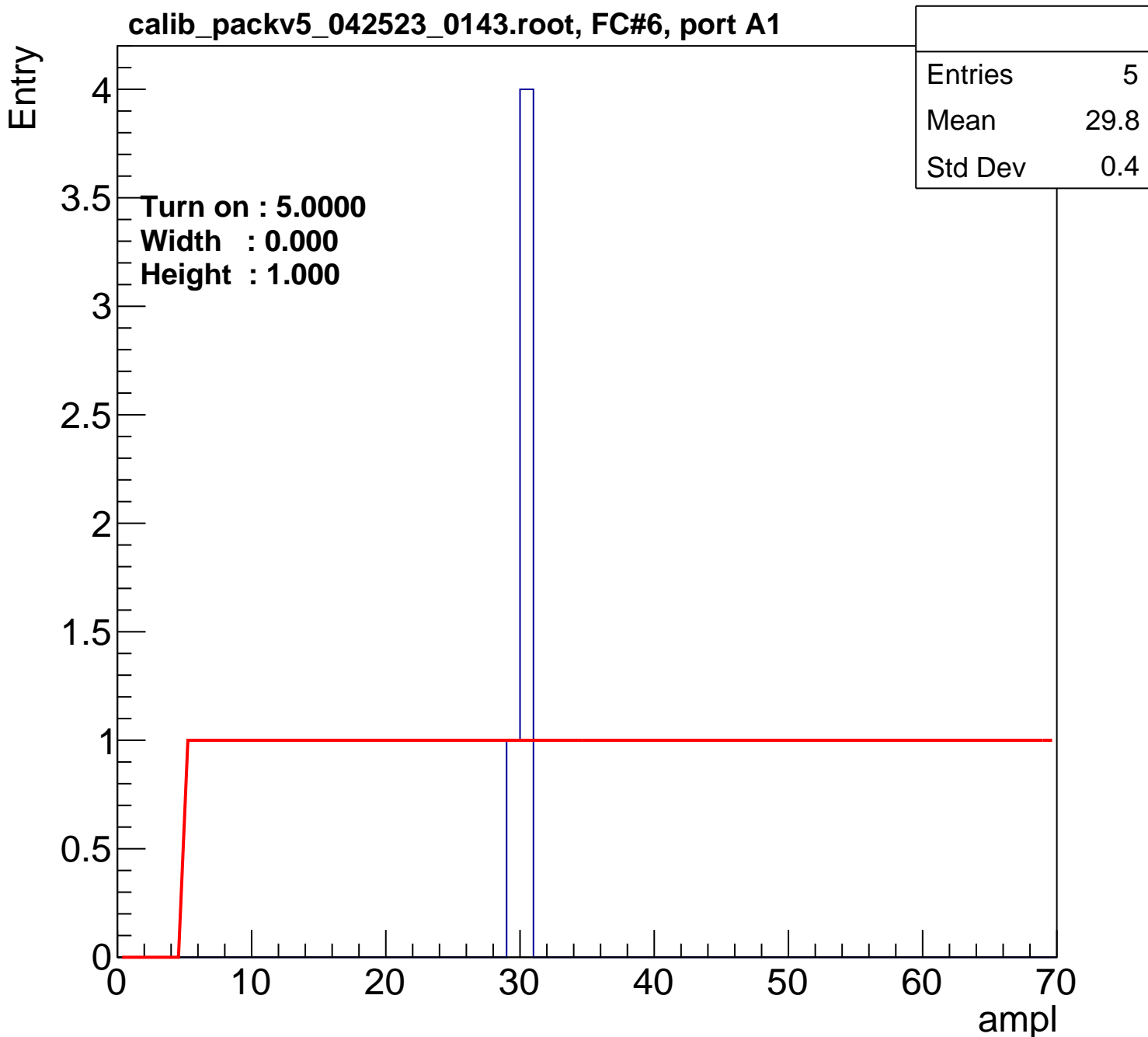
B0L100S, U22-ch91

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch92

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch93

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	26
Std Dev	1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl
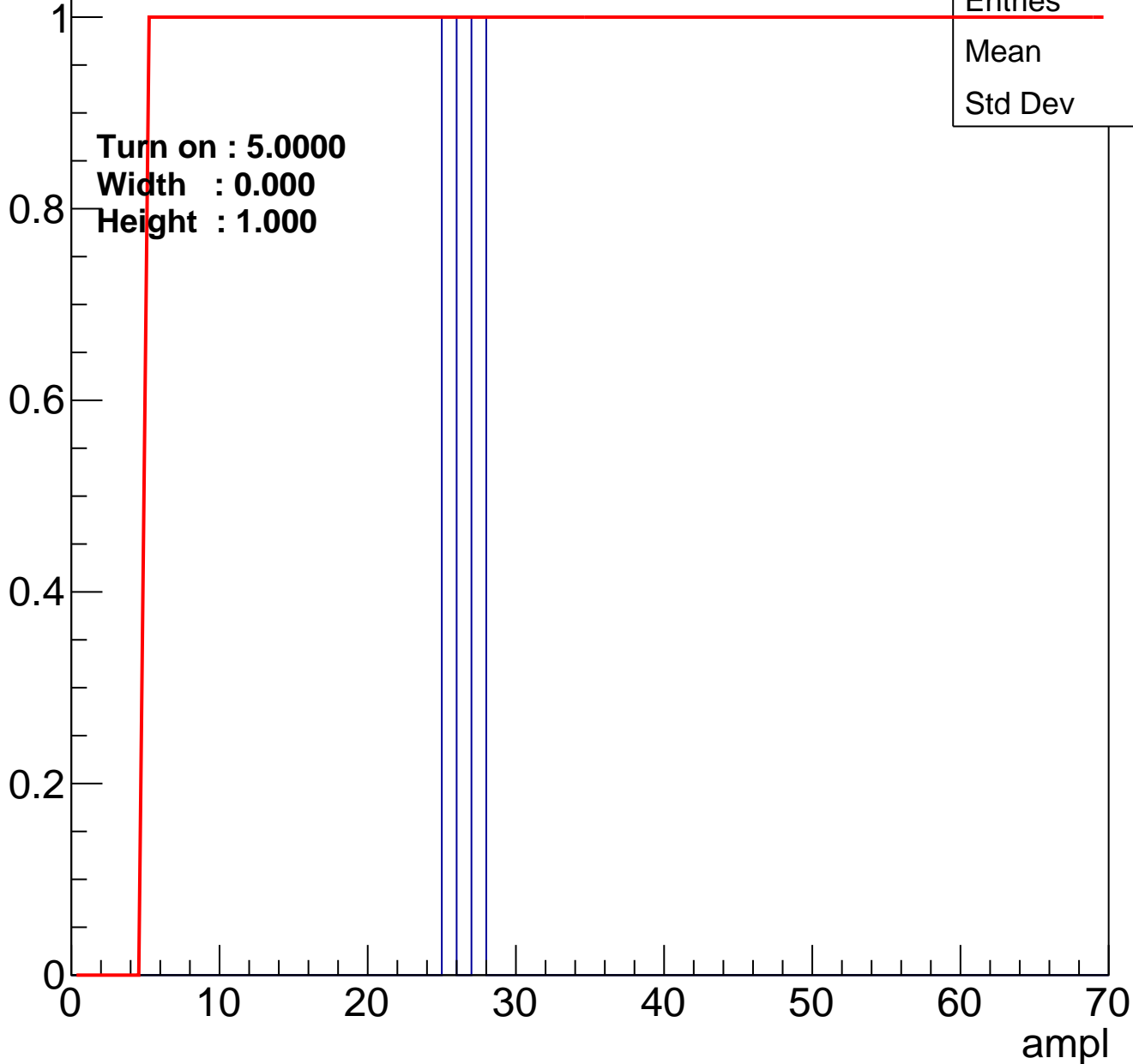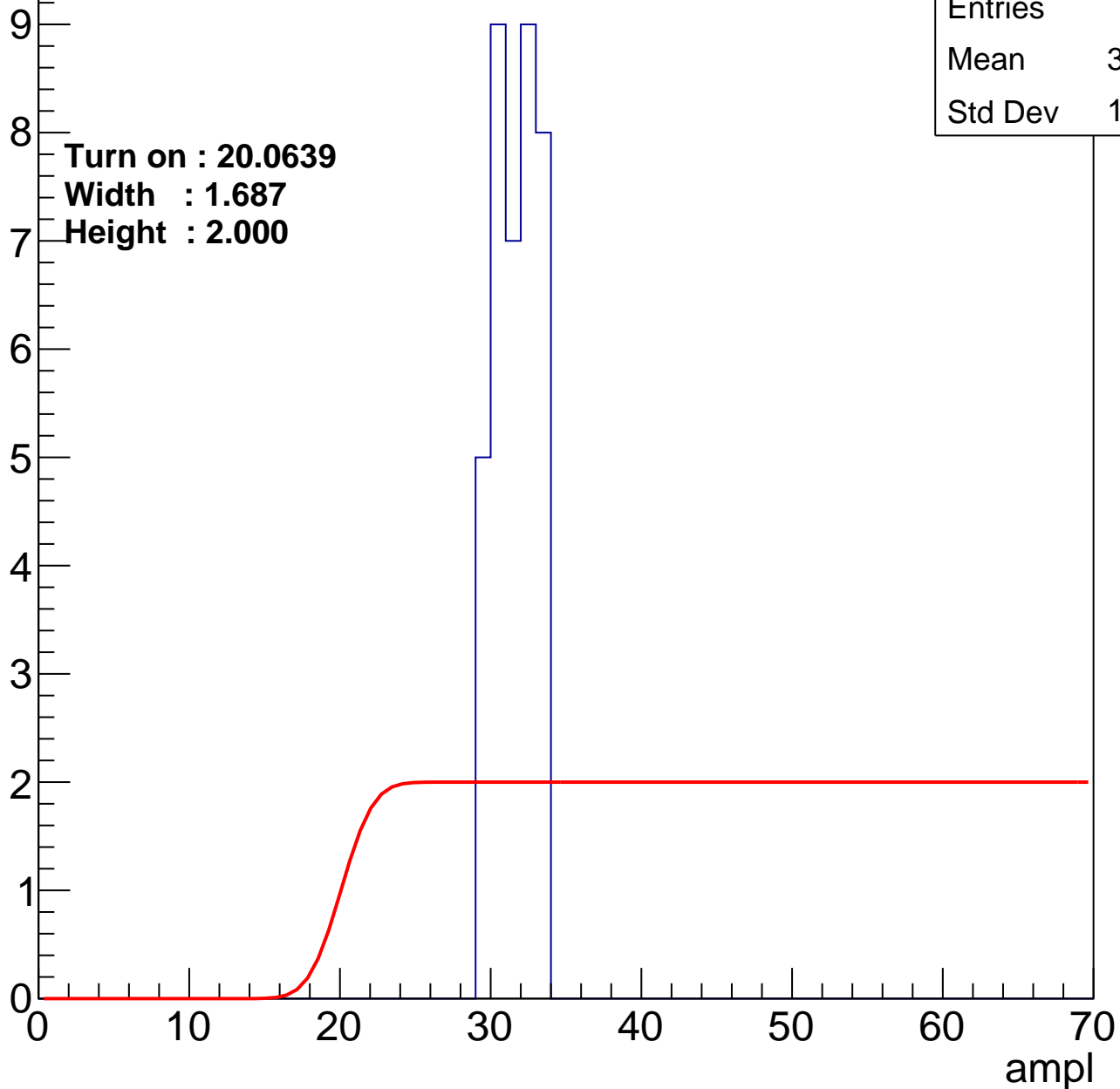
B0L100S, U22-ch94

Entries	38
Mean	31.16
Std Dev	1.348

Turn on : 20.0639
Width   : 1.687
Height : 2.000

B0L100S, U22-ch95

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000
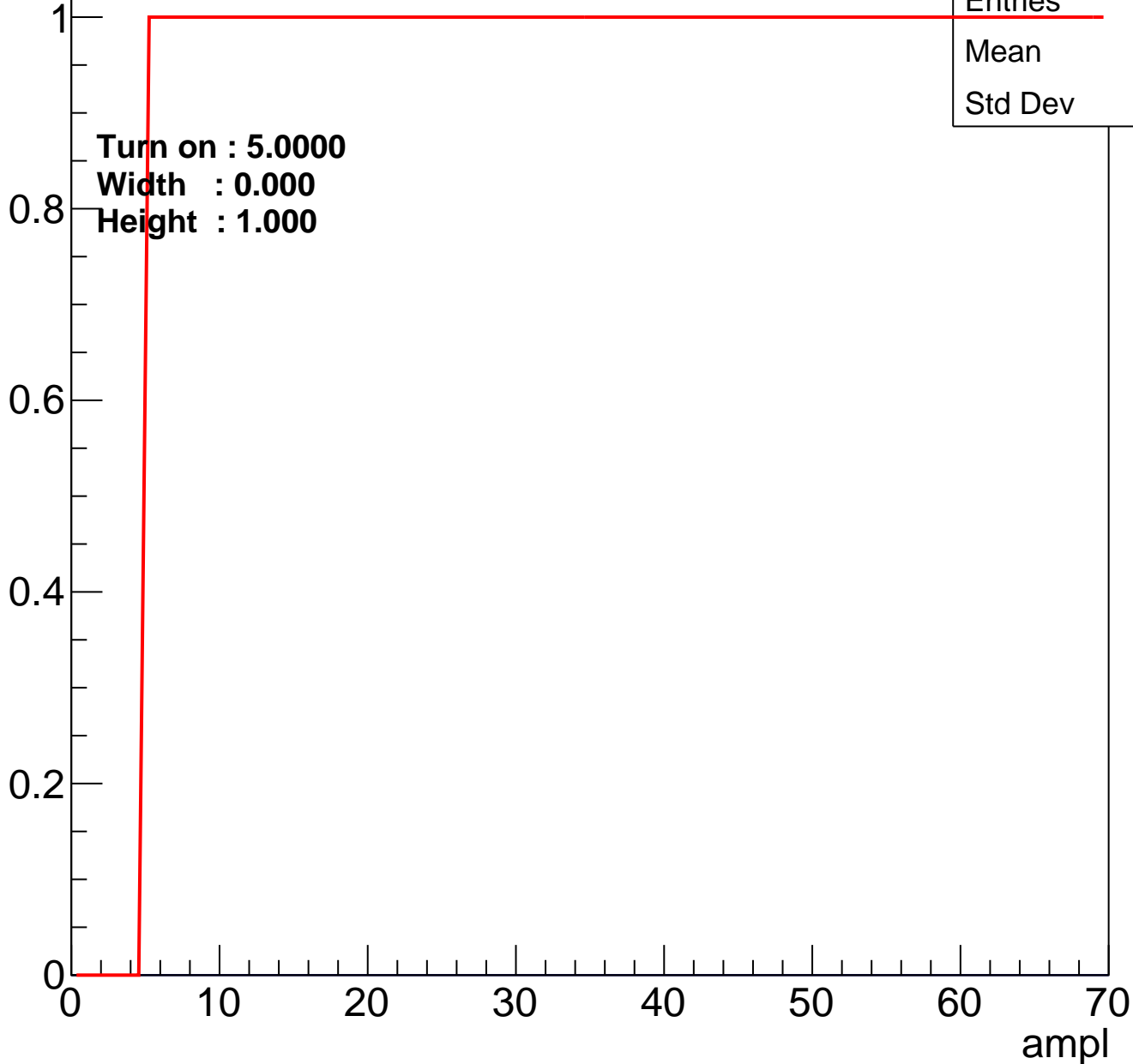
Entries	0
Mean	0
Std Dev	0

B0L100S, U22-ch96

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	9
Mean	30.22
Std Dev	0.6285

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl
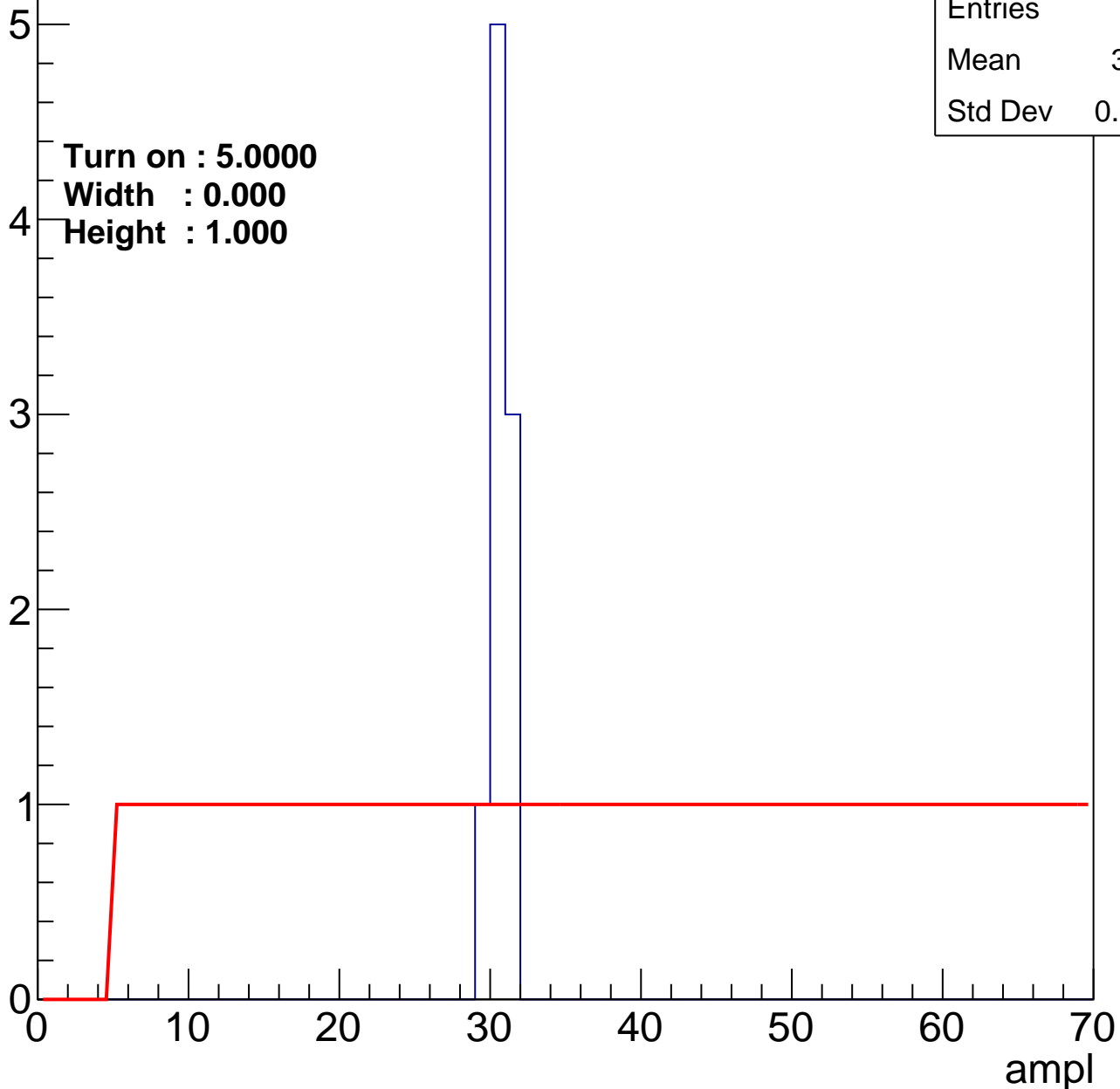
B0L100S, U22-ch97

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Turn on : 19.9104
Width   : 1.955
Height : 2.000

Entries	39
Mean	31.15
Std Dev	1.251

ampl
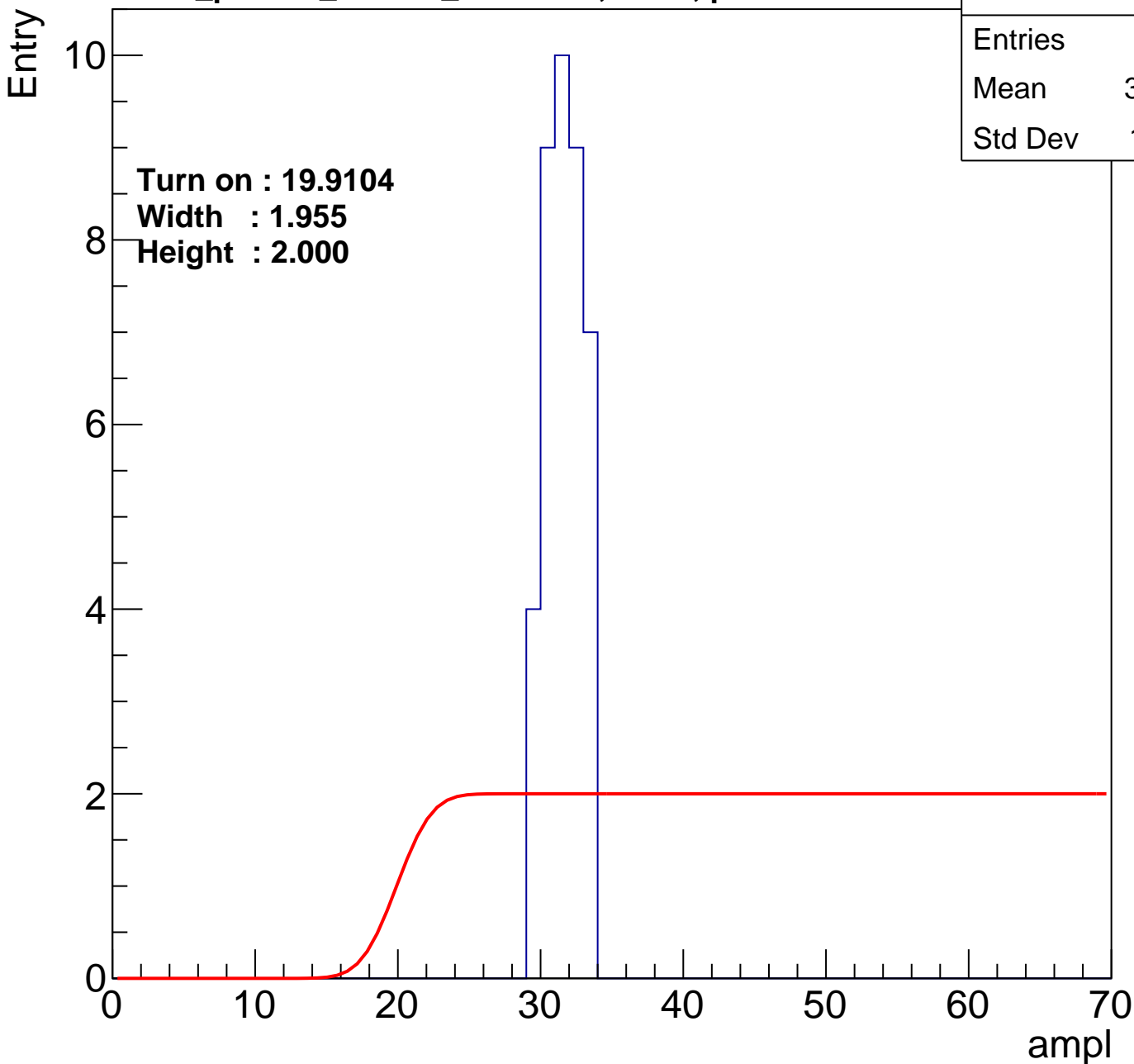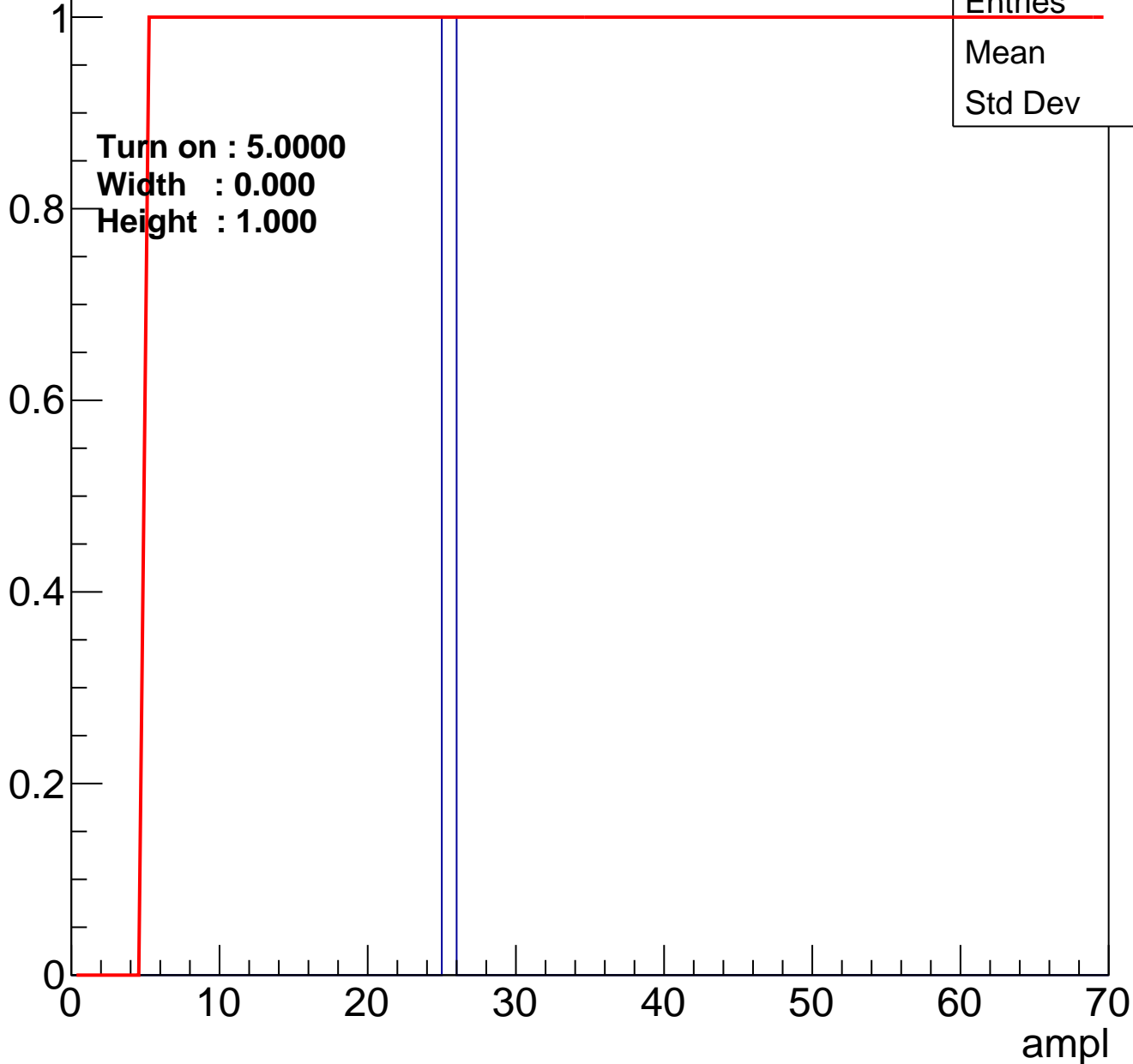
B0L100S, U22-ch99

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch100

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch101

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch102

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch103

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch104

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	11
Mean	29.82
Std Dev	0.8332

Turn on : 21.9281
Width   : 1.462
Height : 2.000

ampl
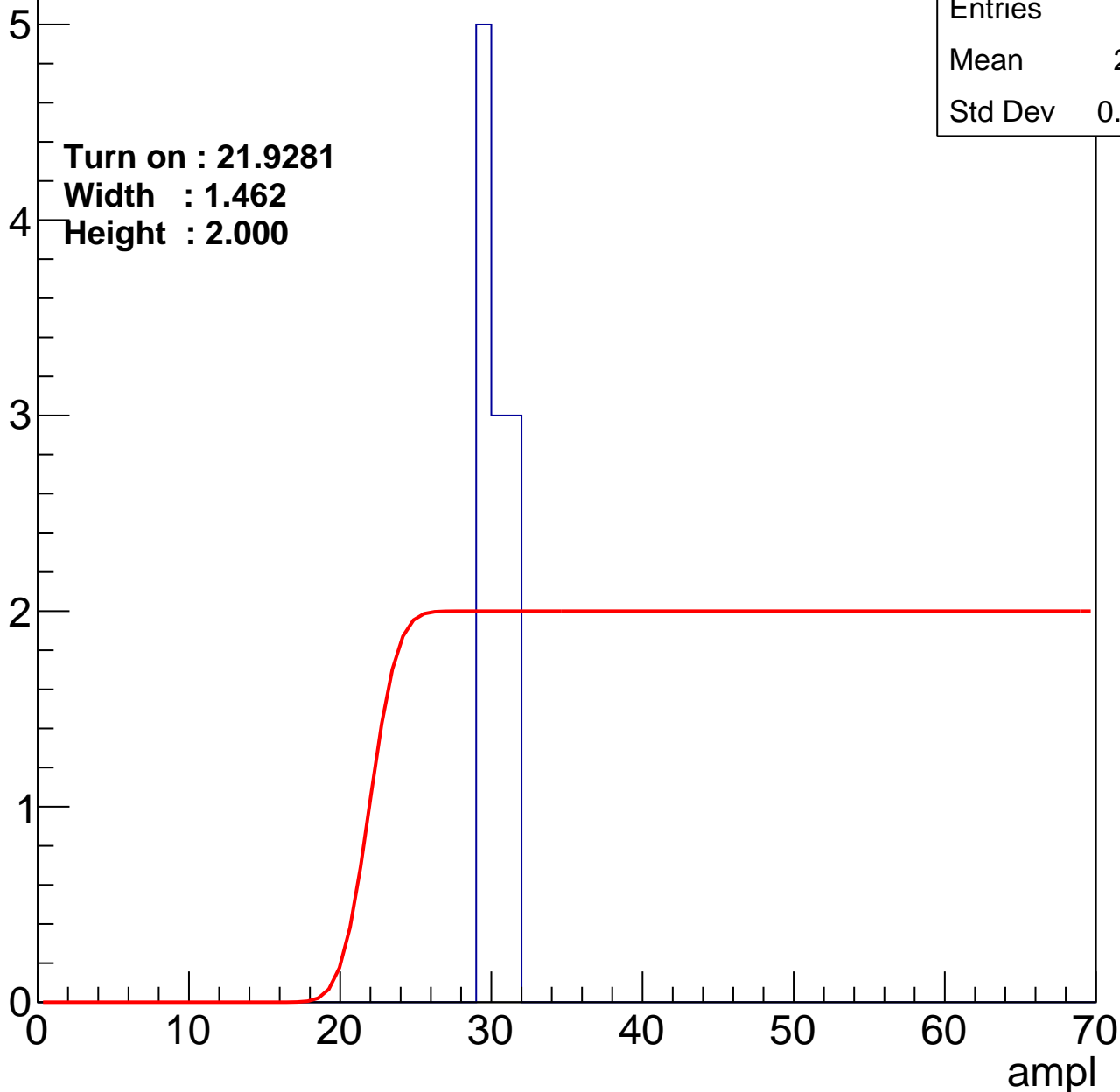
B0L100S, U22-ch105

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl
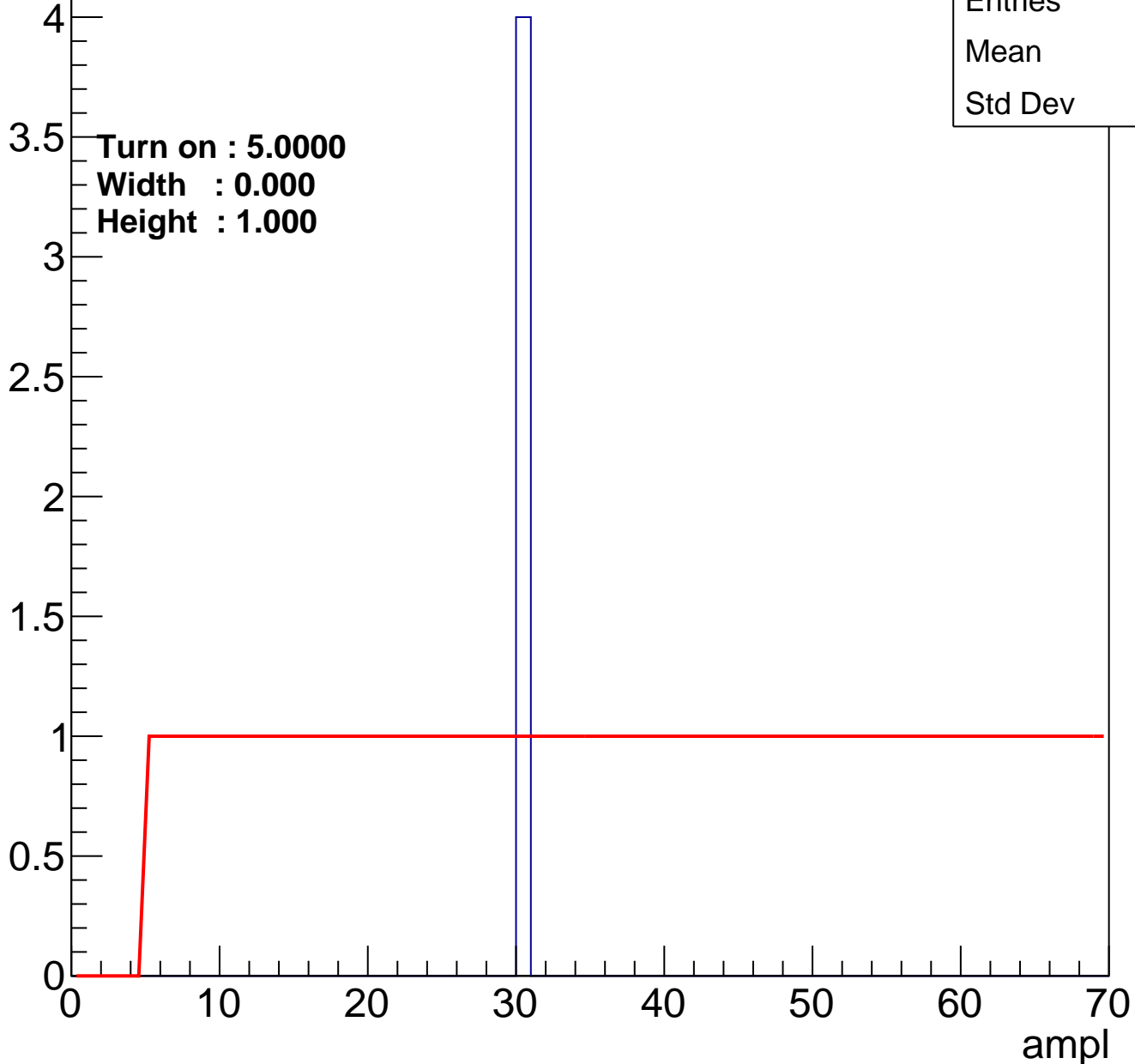
Entries	4
Mean	30
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

B0L100S, U22-ch106

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch107

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch108

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch109

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch110

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch111

calib_packv5_042523_0143.root, FC#6, port A1

Entries	18
Mean	30.56
Std Dev	1.012

Turn on : 17.3504
Width   : 2.547
Height : 2.000
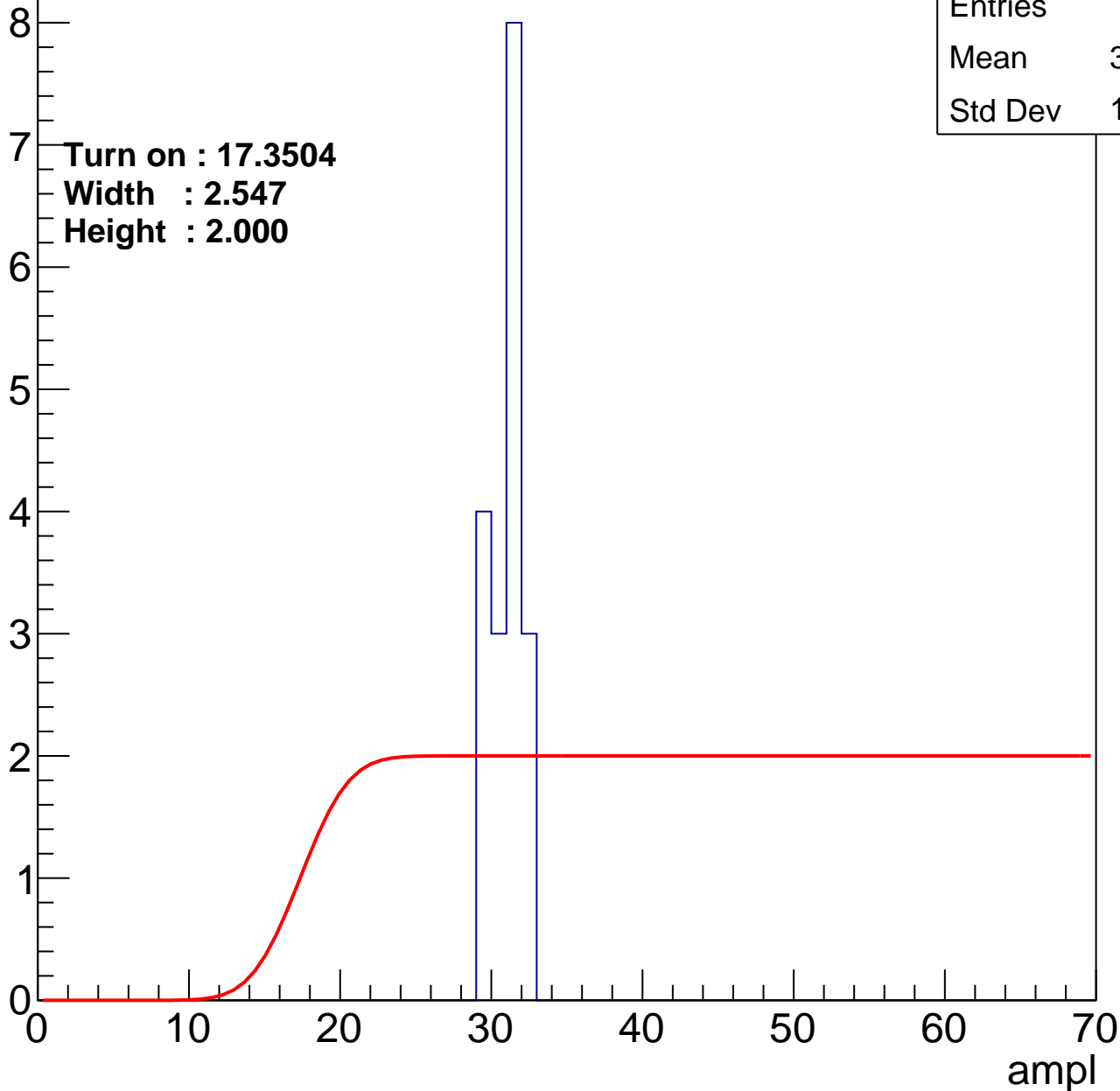
B0L100S, U22-ch112

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch113

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

ampl

B0L100S, U22-ch114

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch115

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl
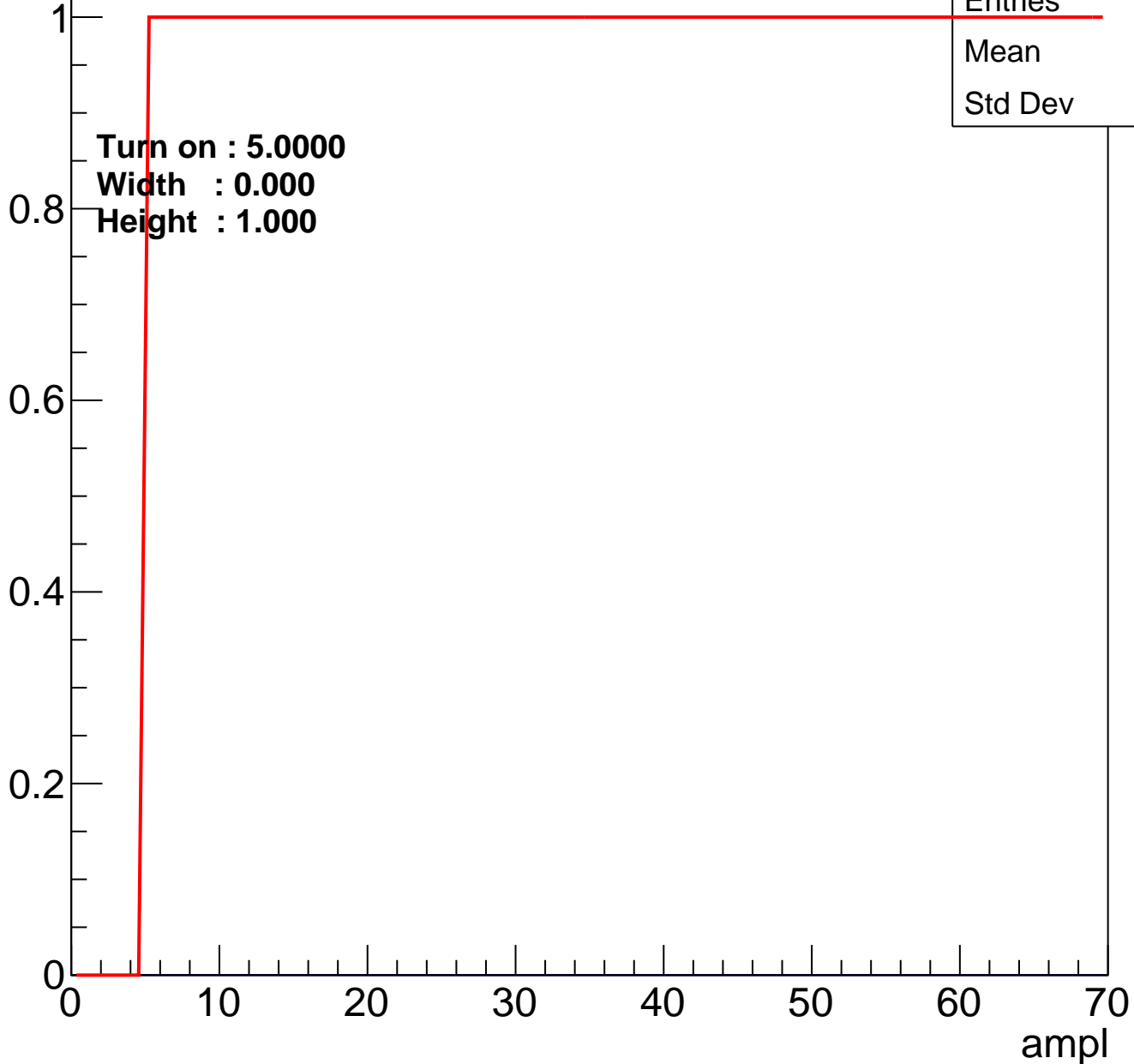
B0L100S, U22-ch116

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	1
Mean	0
Std Dev	0

ampl

B0L100S, U22-ch117

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch118

calib_packv5_042523_0143.root, FC#6, port A1

Entries	20
Mean	31.8
Std Dev	0.9274

Turn on : 17.0843
Width   : 2.224
Height : 2.000
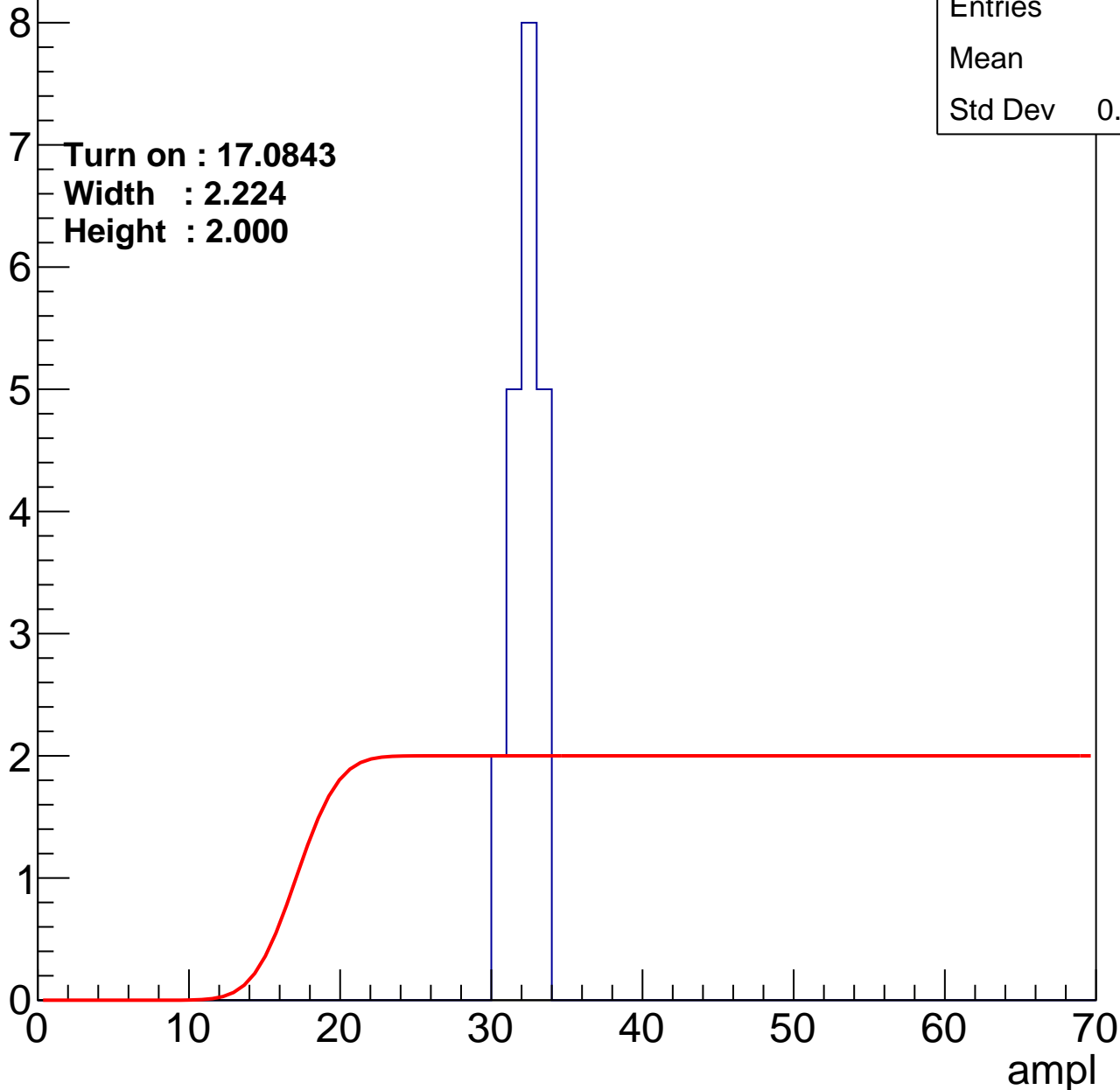
B0L100S, U22-ch119

calib_packv5_042523_0143.root, FC#6, port A1

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U22-ch120

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
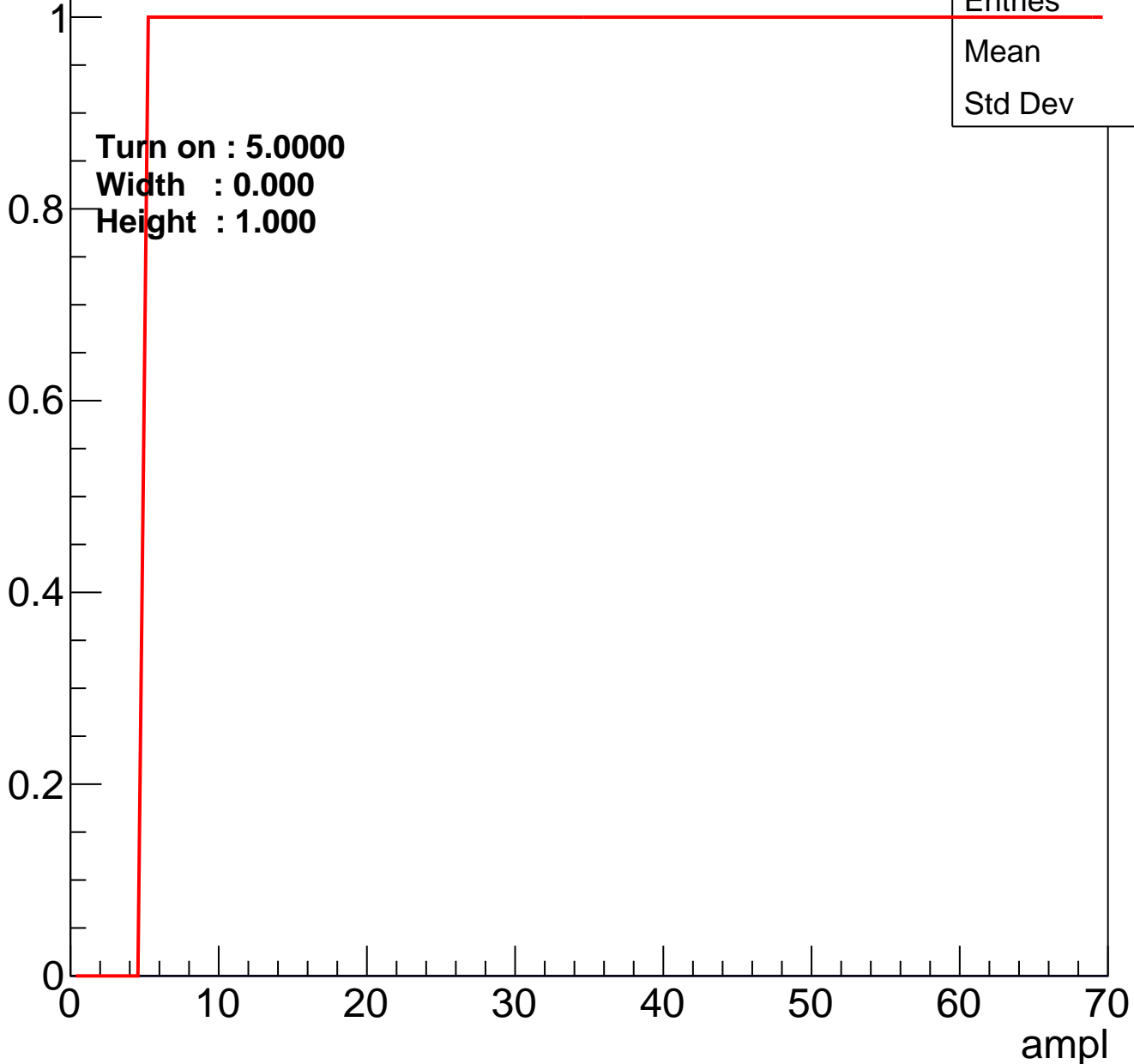
B0L100S, U22-ch121

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch122

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl
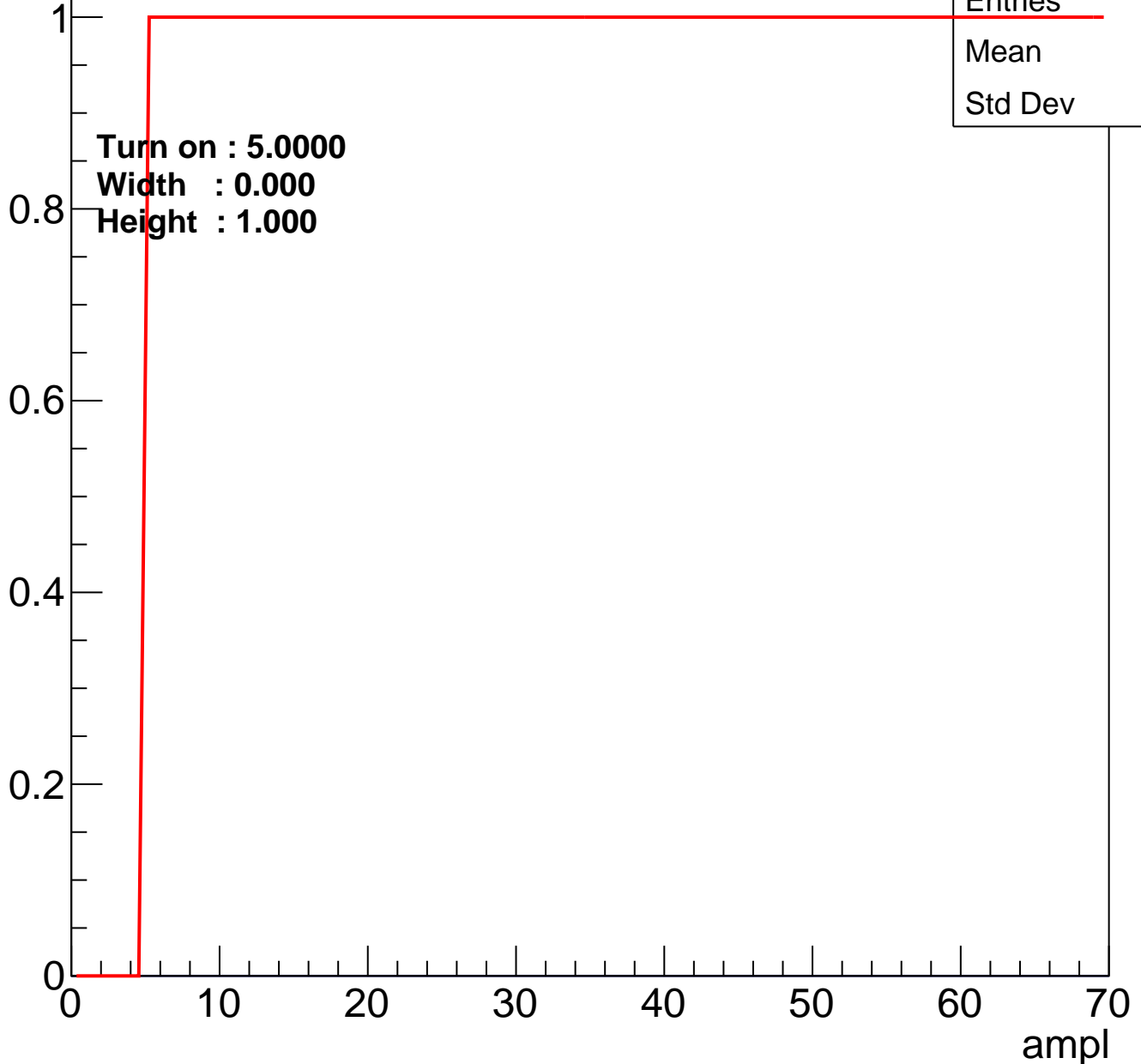
B0L100S, U22-ch123

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch124

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch125

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch126

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width   : 0.000
Height : 1.000

Entry

ampl

B0L100S, U22-ch127

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Turn on : 5.0000
Width : 0.000
Height : 1.000

Entry

ampl