

B1L100S, U23-ch0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	395
Mean	43.44
Std Dev	12.02

Turn on : 24.7172

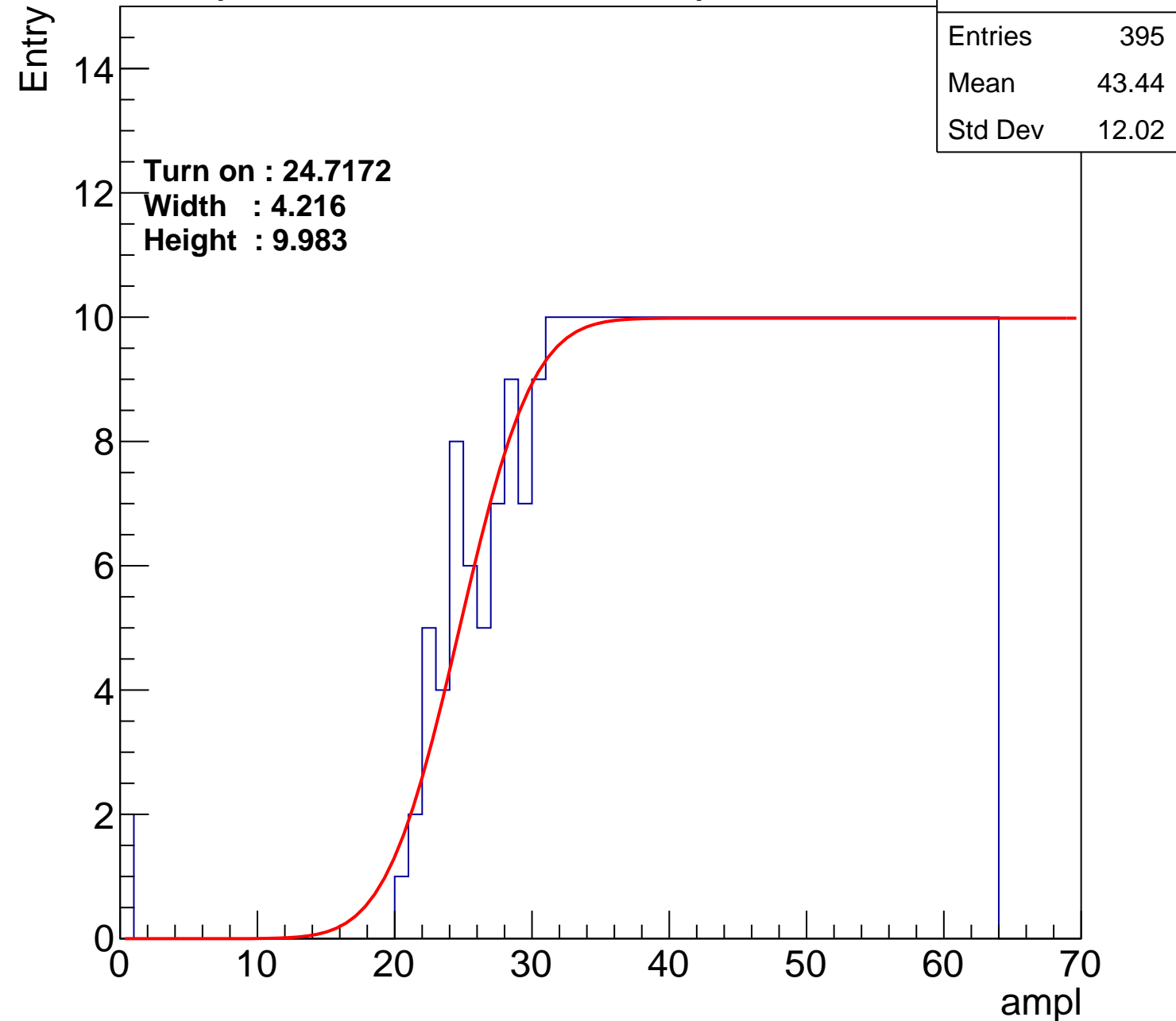
Width : 4.216

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch1

calib_packv5_042523_0143.root, FC#4, port A2

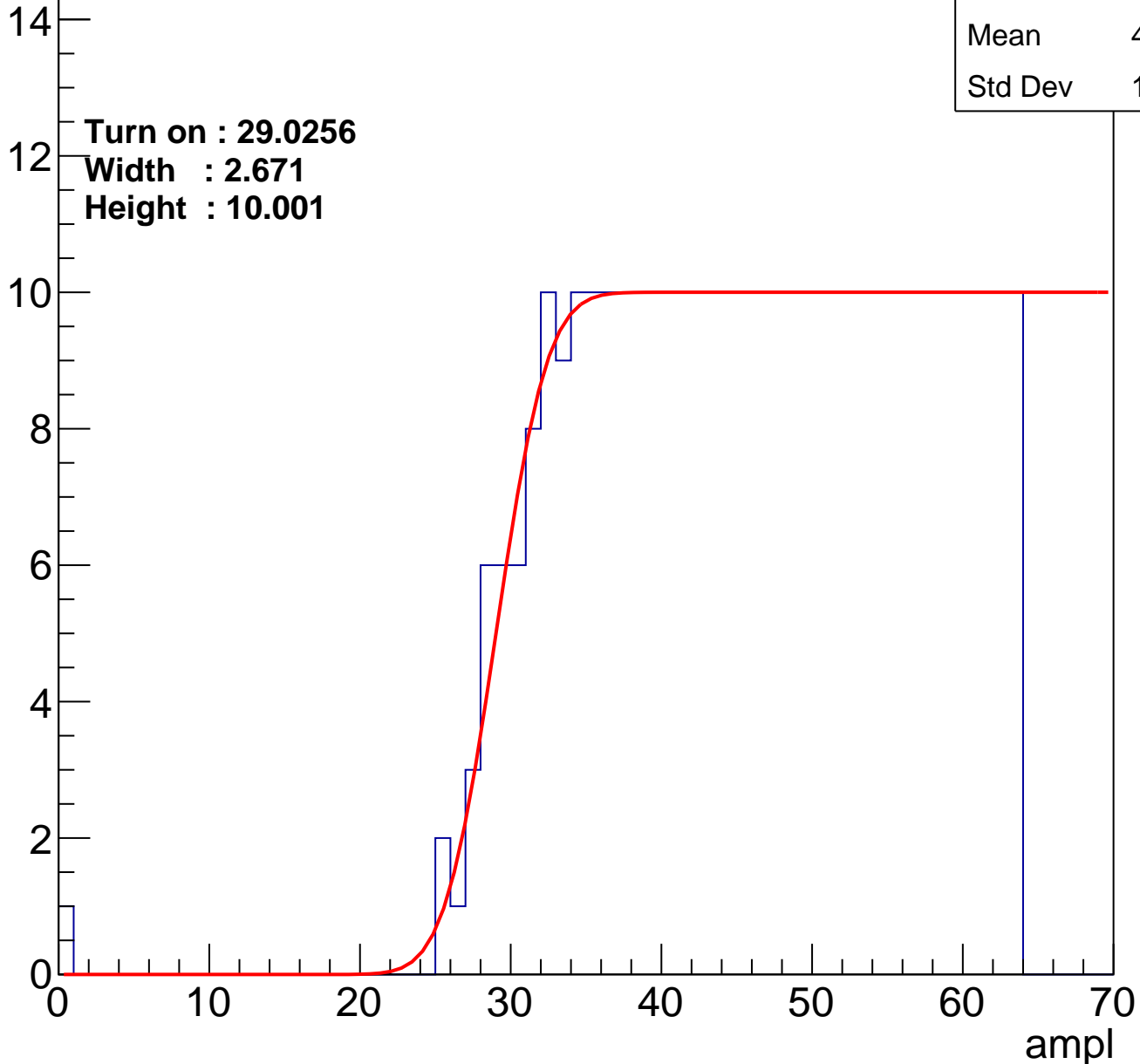
Entries	352
Mean	45.72
Std Dev	10.57

Turn on : 29.0256

Width : 2.671

Height : 10.001

Entry



B1L100S, U23-ch2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.55
Std Dev	11.75

Turn on : 24.7703

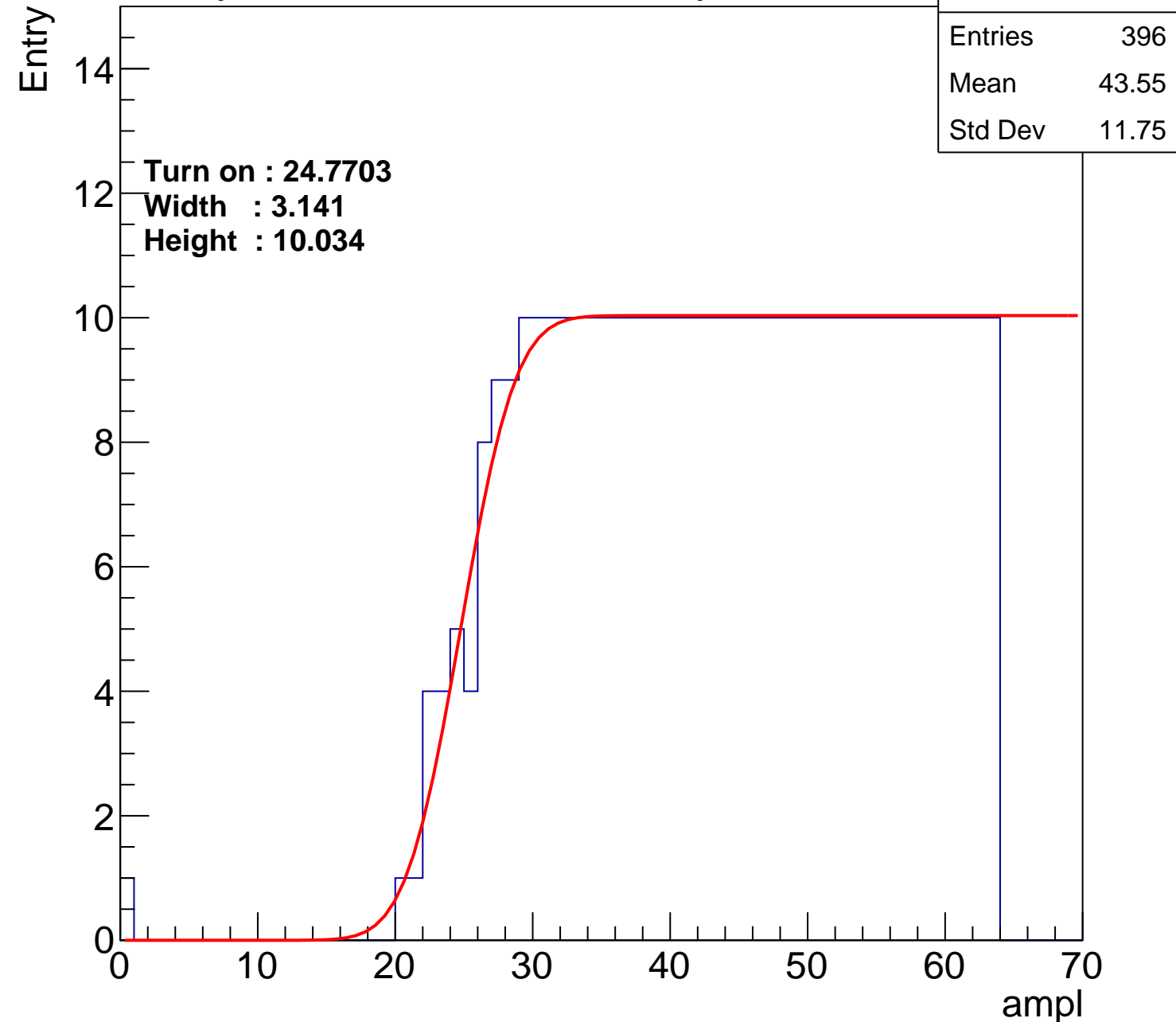
Width : 3.141

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.7347

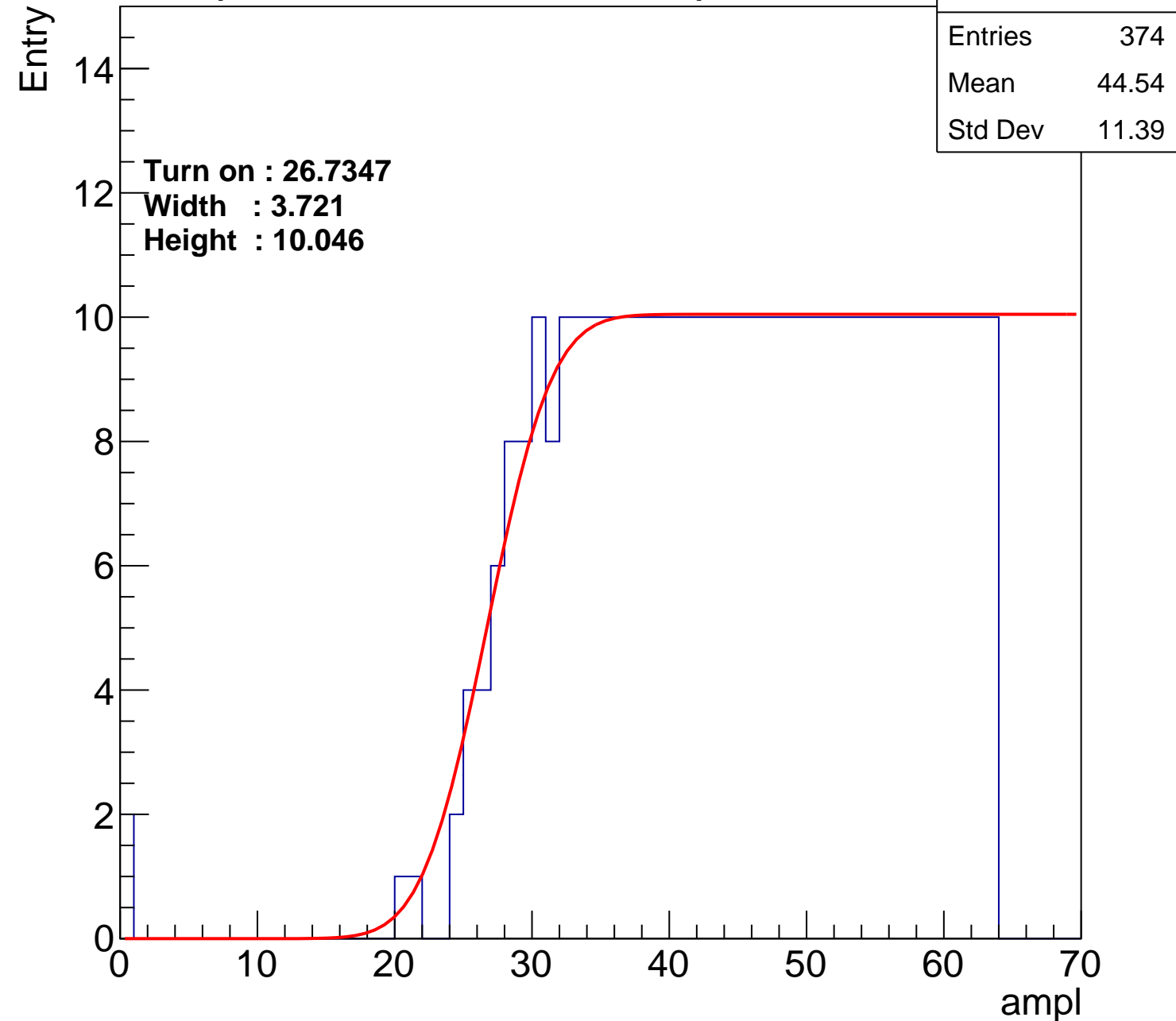
Width : 3.721

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch4

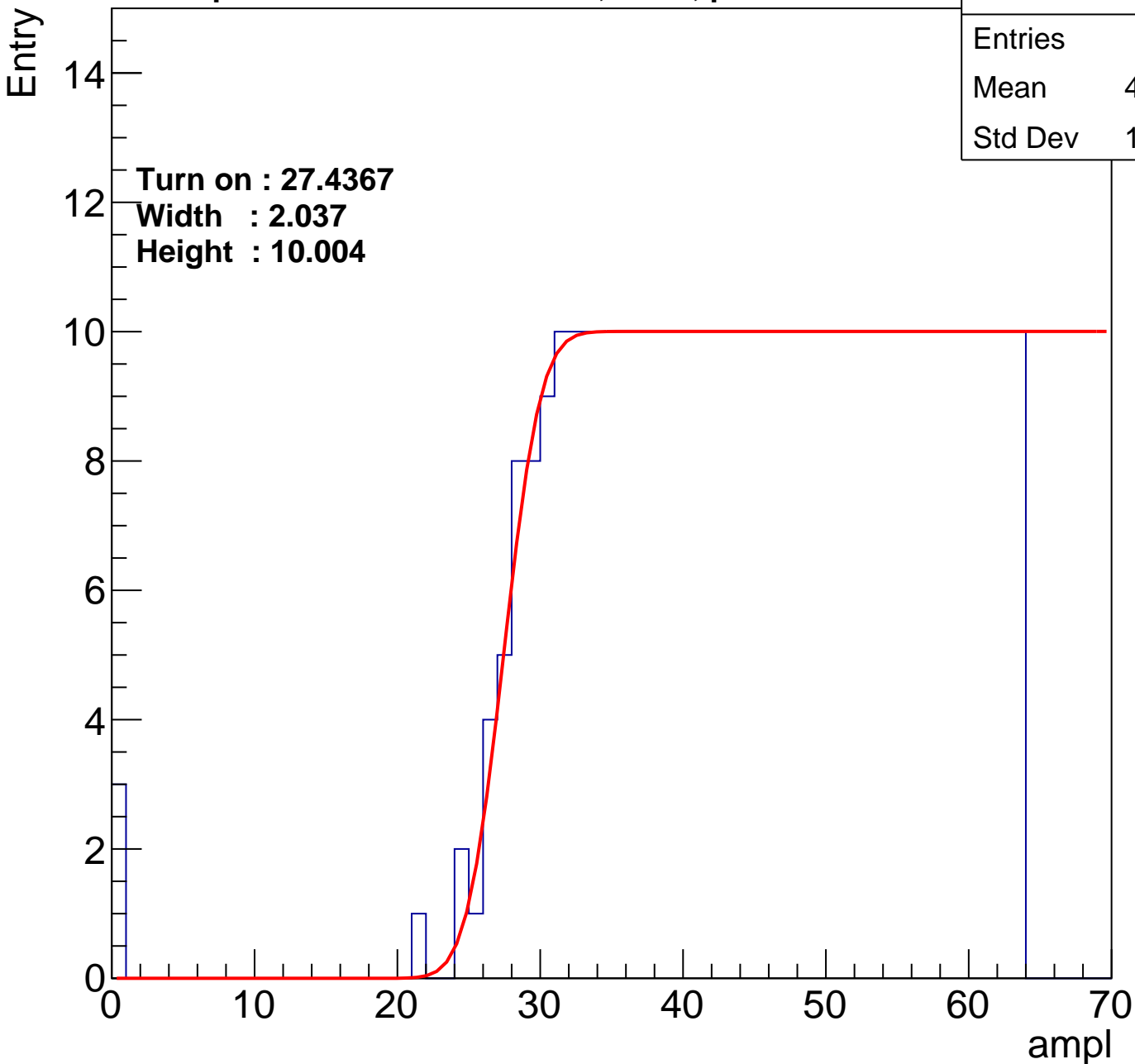
calib_packv5_042523_0143.root, FC#4, port A2

Turn on : 27.4367

Width : 2.037

Height : 10.004

Entries	371
Mean	44.66
Std Dev	11.44



B1L100S, U23-ch5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
---------	-----

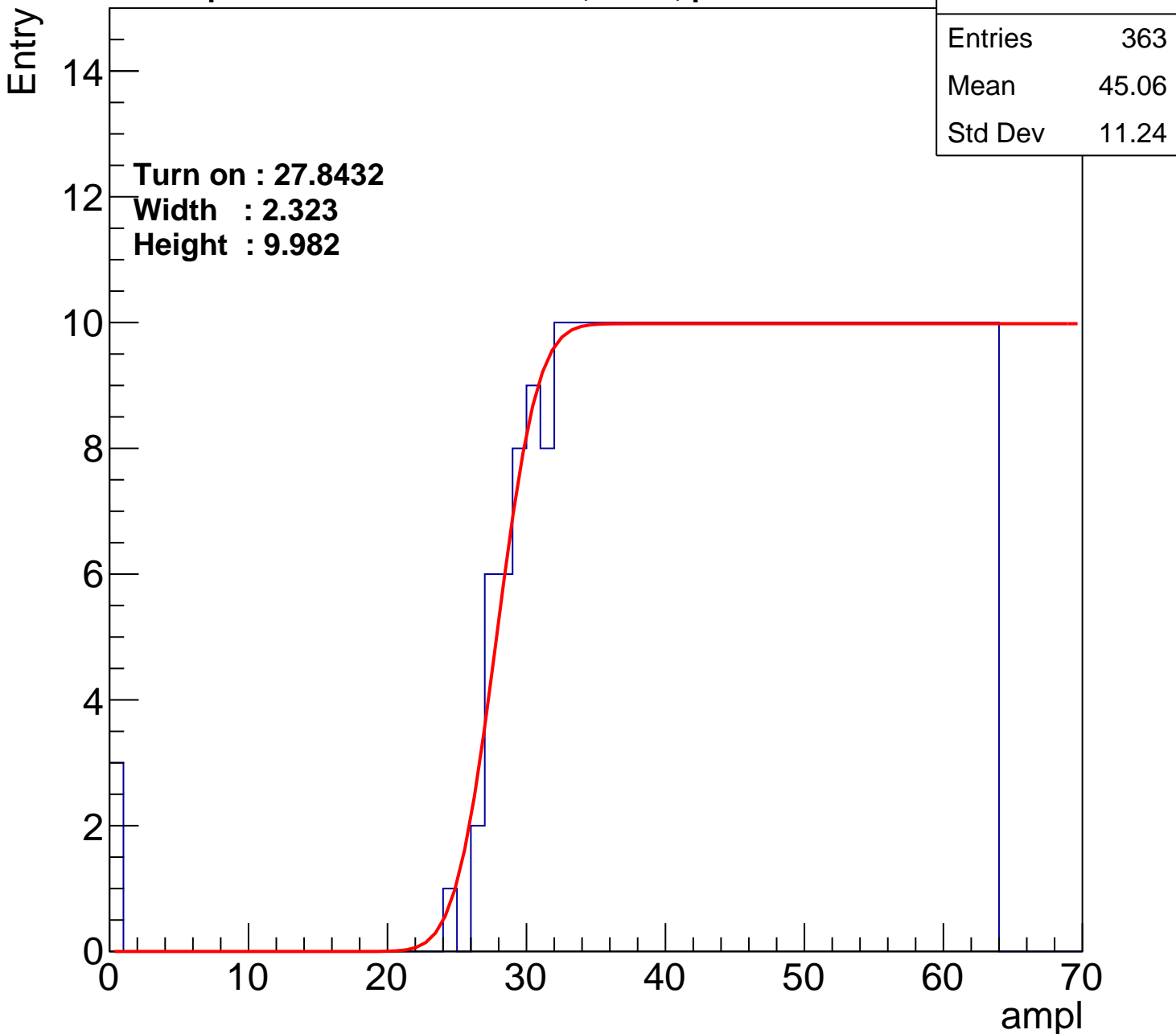
Mean	45.06
------	-------

Std Dev	11.24
---------	-------

Turn on : 27.8432

Width : 2.323

Height : 9.982



B1L100S, U23-ch6

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.26
Std Dev	11.64

Turn on : 26.3443

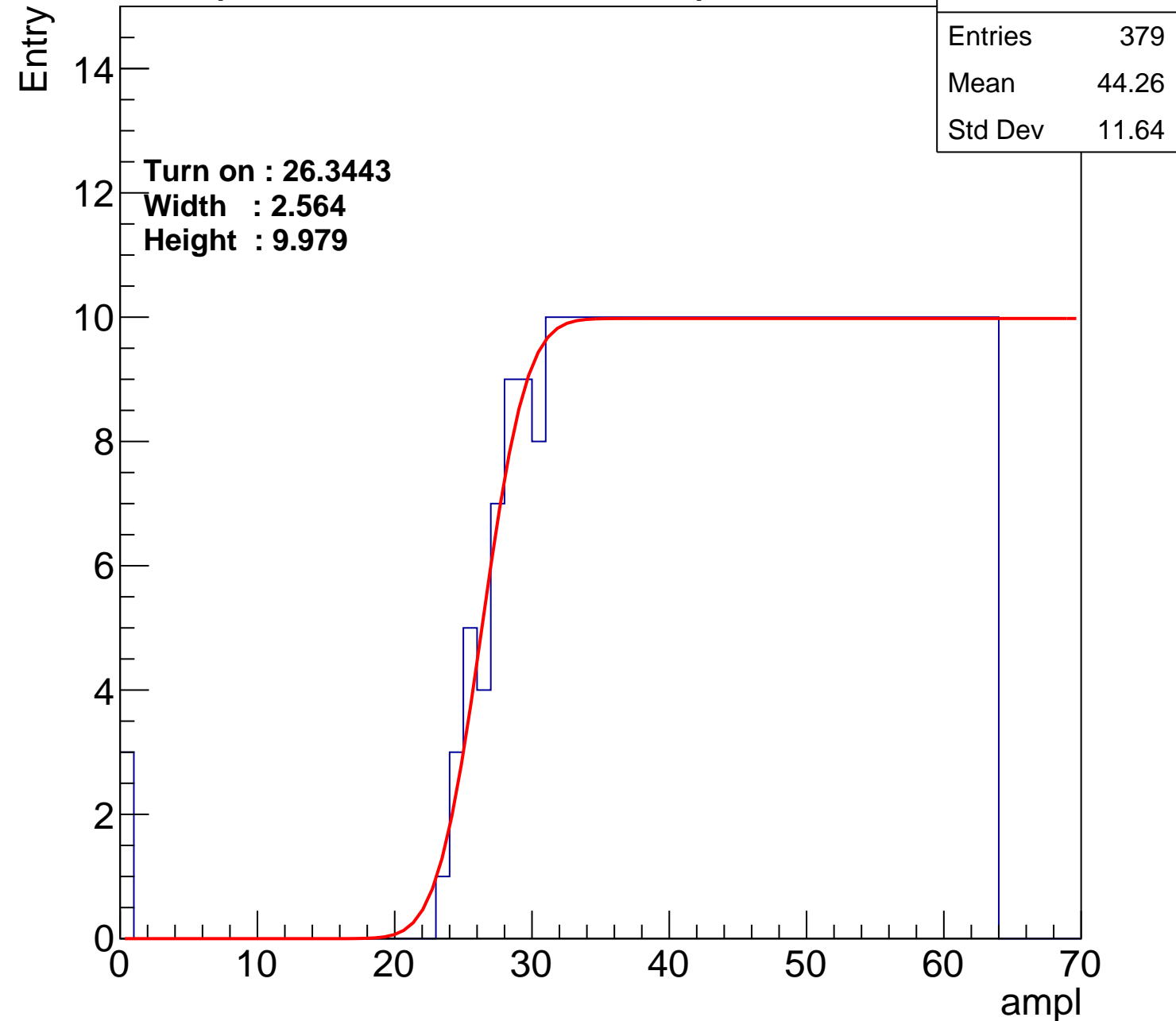
Width : 2.564

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch7

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.6963

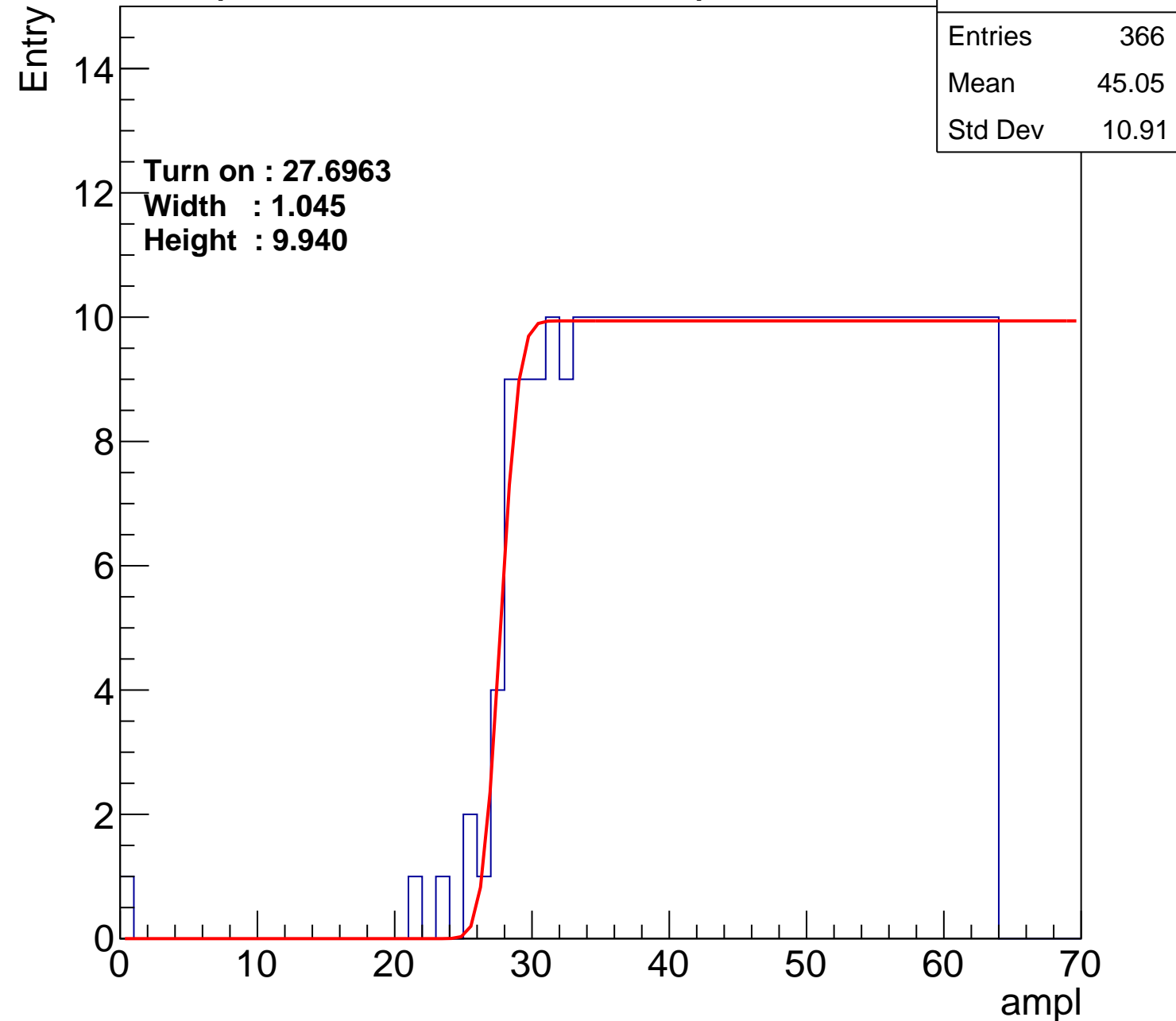
Width : 1.045

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch8

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.04
Std Dev	12.09

Turn on : 27.4806

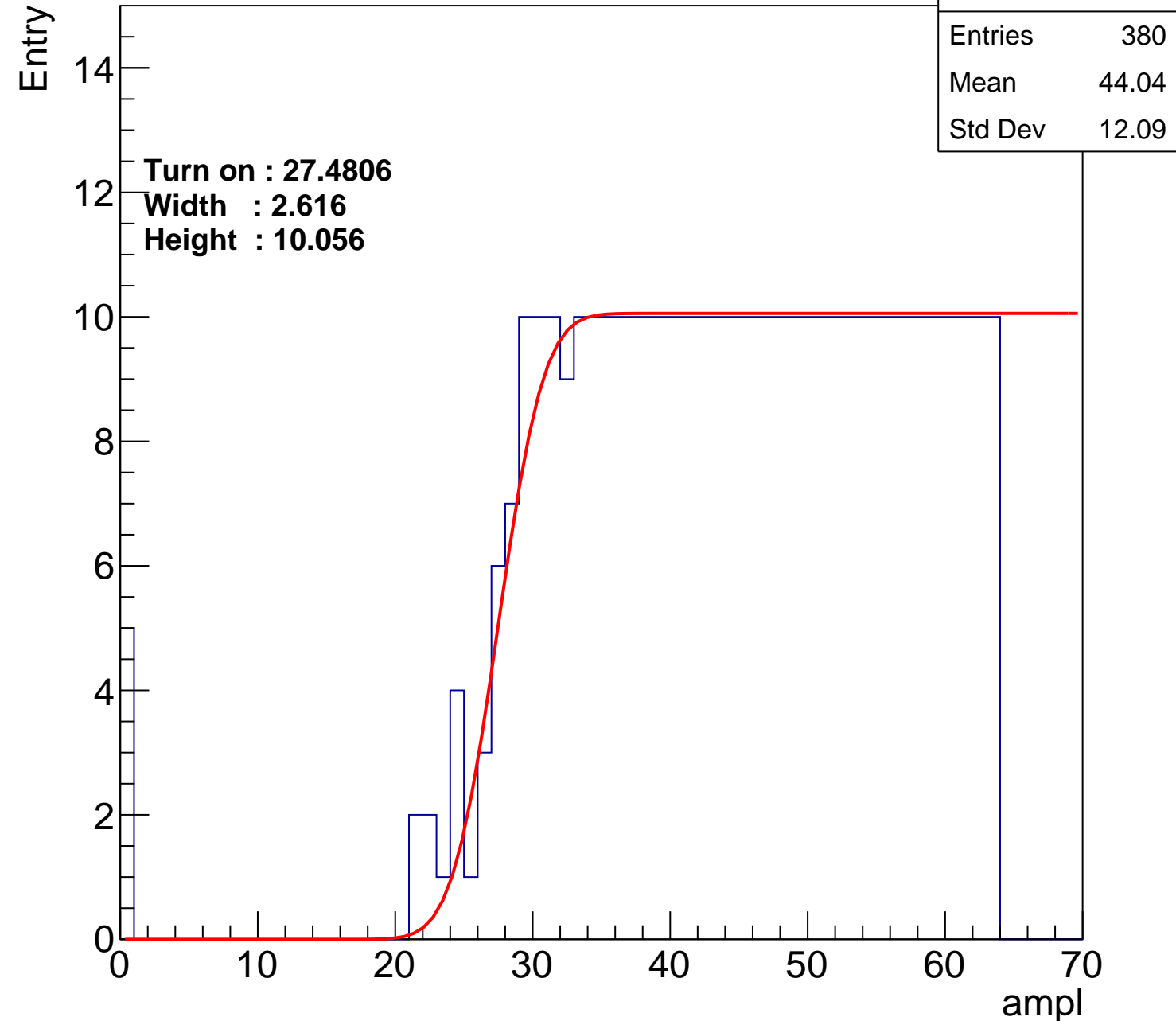
Width : 2.616

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch9

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.77
Std Dev	11.62

Turn on : 28.1210

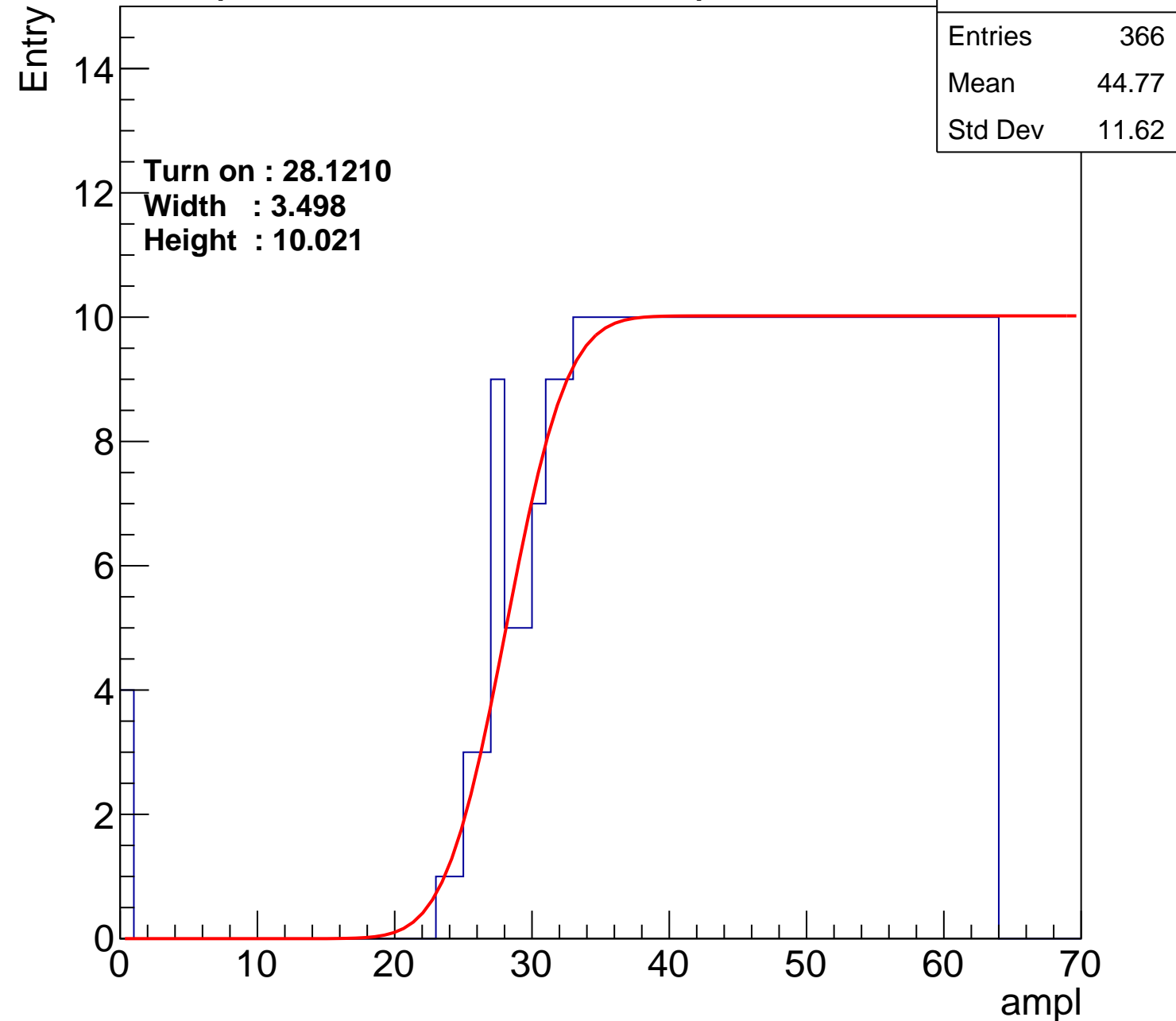
Width : 3.498

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch10

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.52
Std Dev	11.36

Turn on : 26.7080

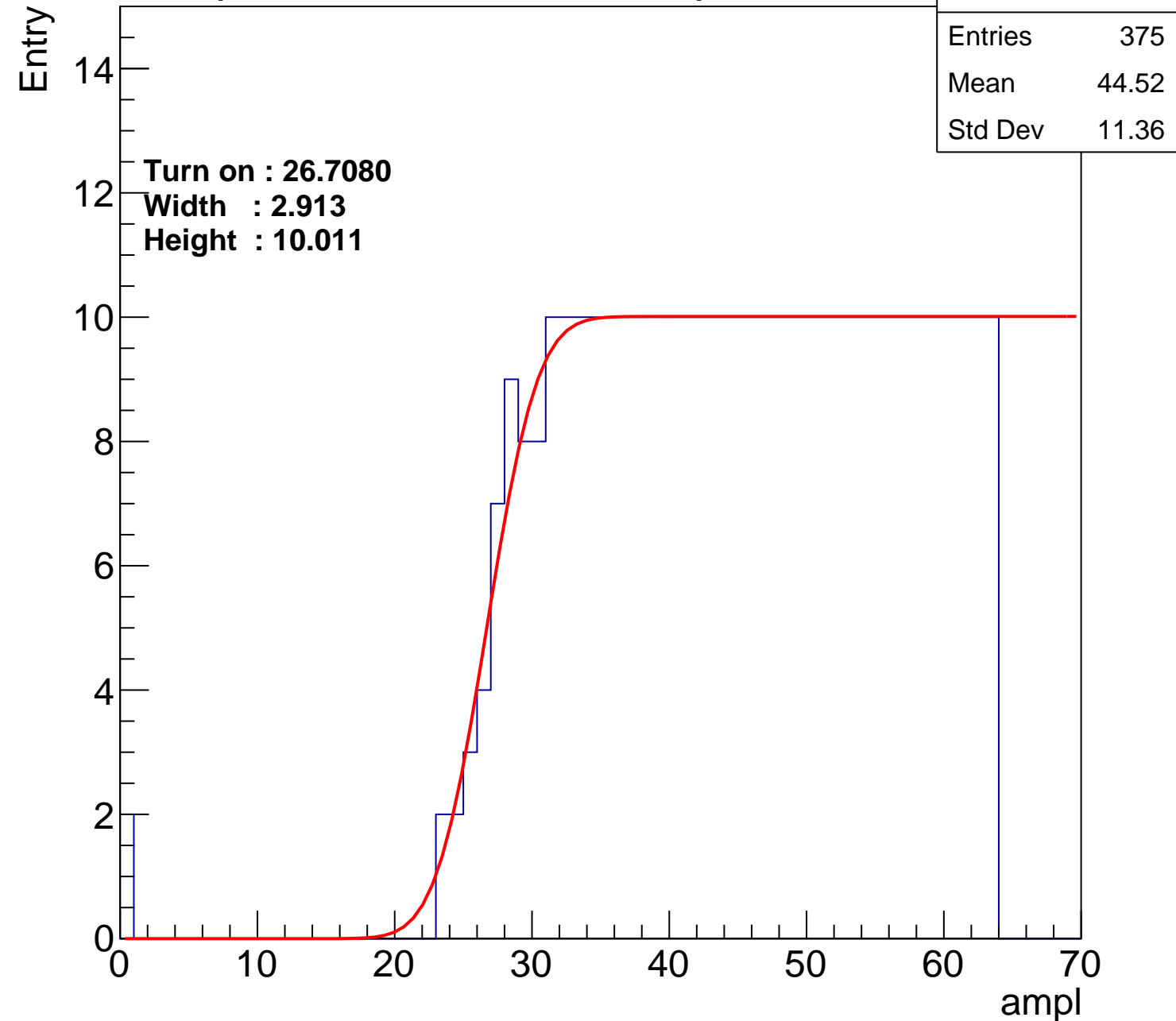
Width : 2.913

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch11

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.15
Std Dev	10.86

Turn on : 28.3317

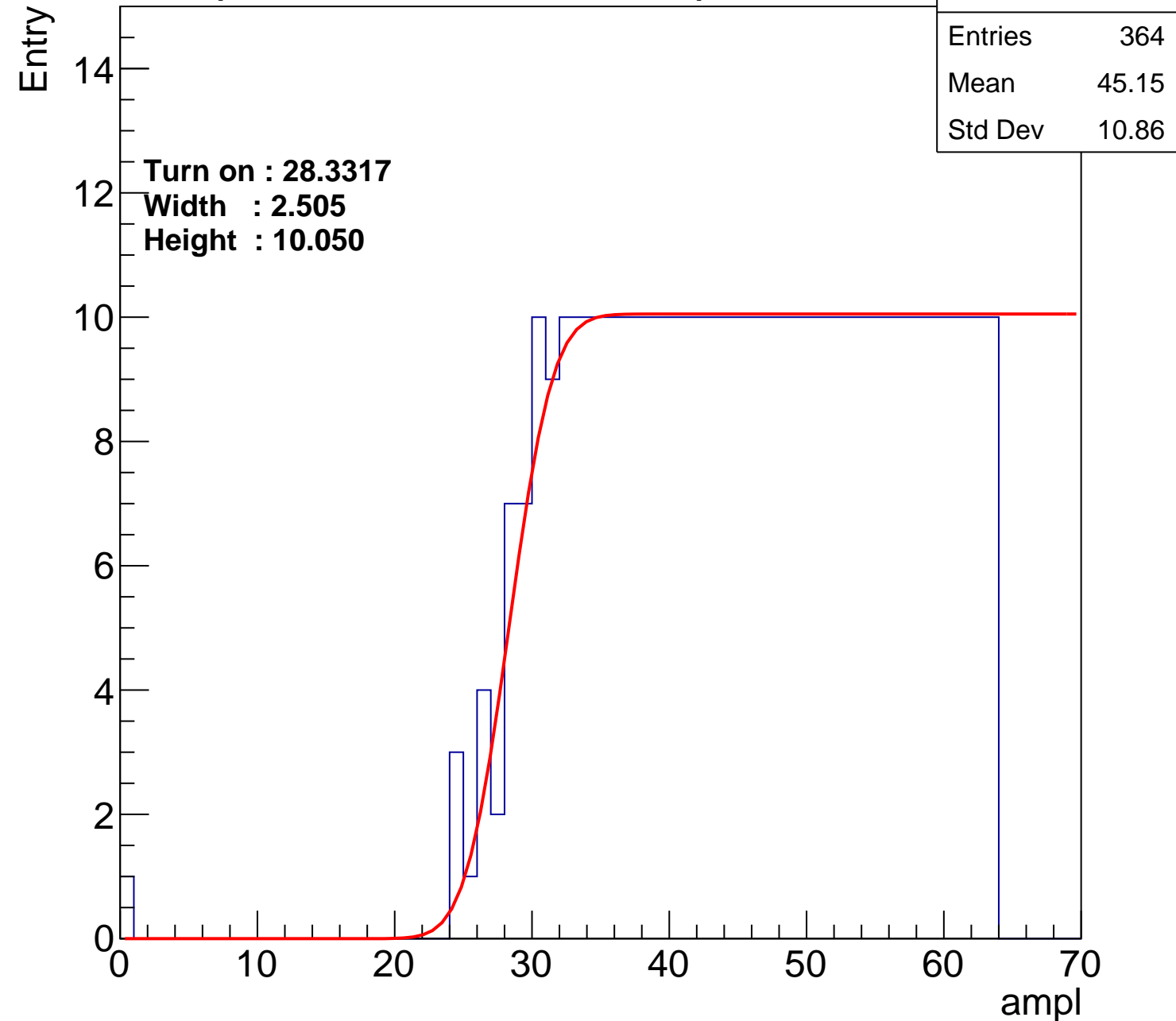
Width : 2.505

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch12

calib_packv5_042523_0143.root, FC#4, port A2

Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.8726

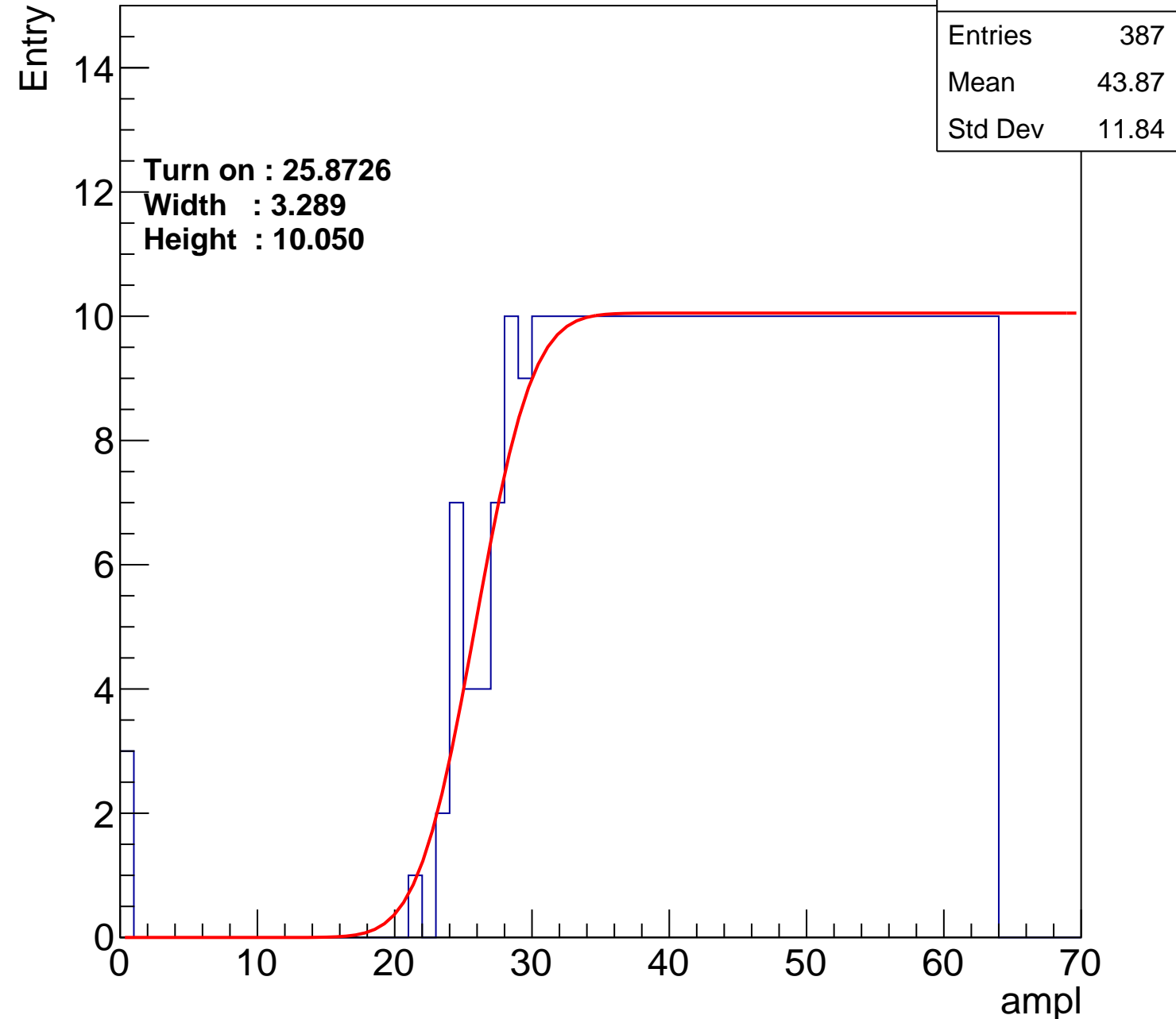
Width : 3.289

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch13

calib_packv5_042523_0143.root, FC#4, port A2

Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 28.0147

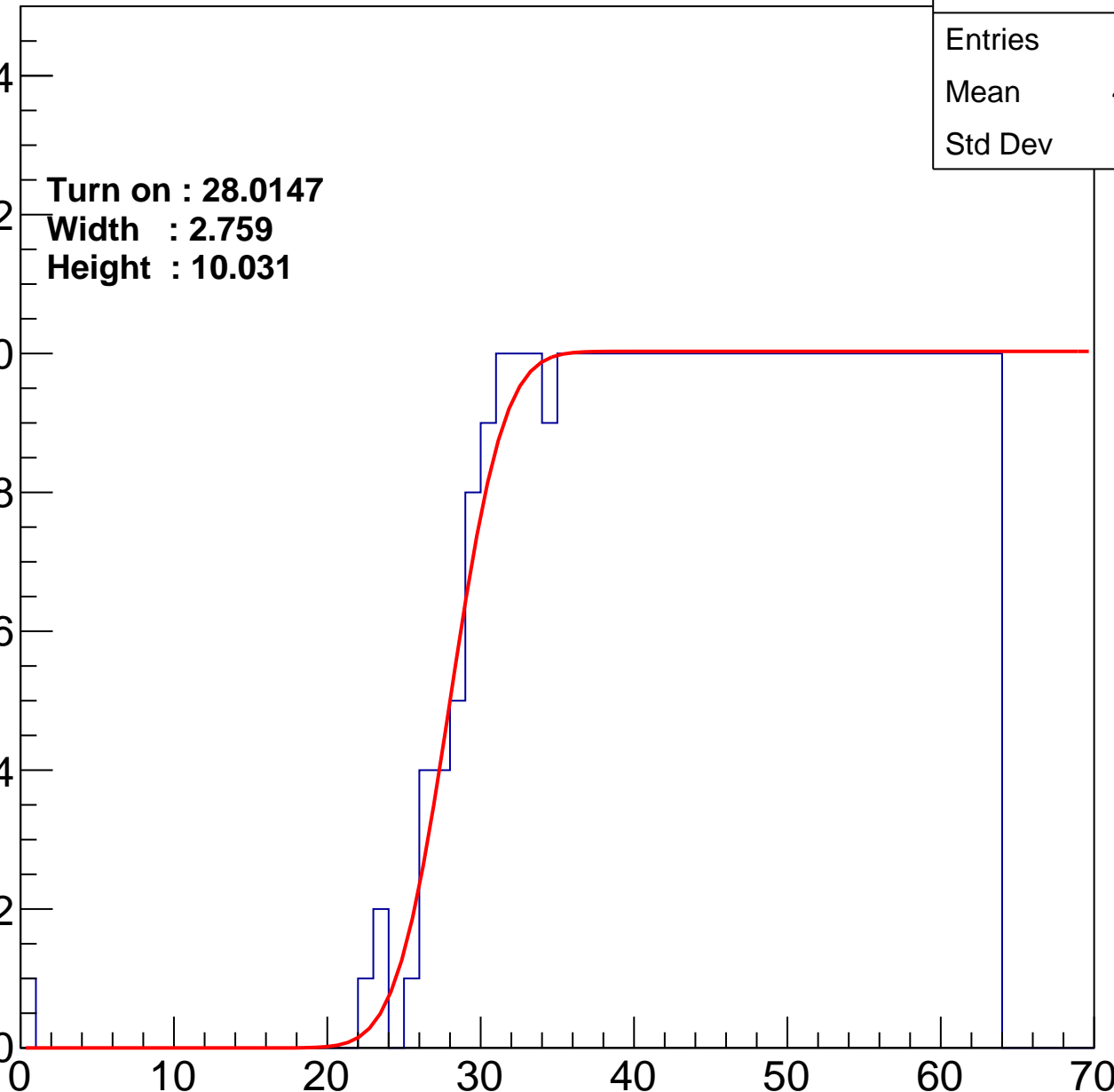
Width : 2.759

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch14

calib_packv5_042523_0143.root, FC#4, port A2

Entries	362
Mean	45.19
Std Dev	10.91

Turn on : 28.5239

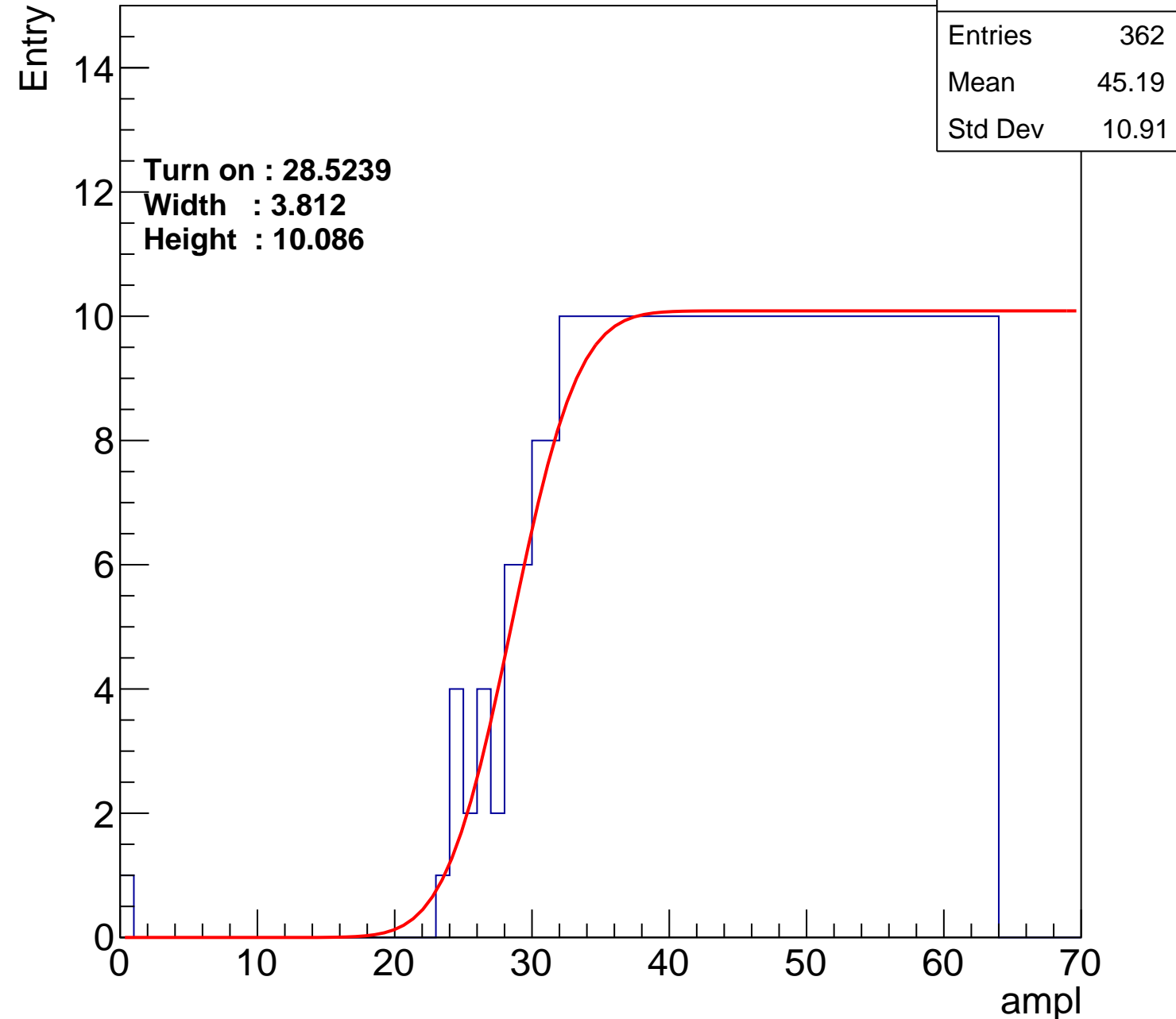
Width : 3.812

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch15

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.97
Std Dev	10.99

Turn on : 27.7038

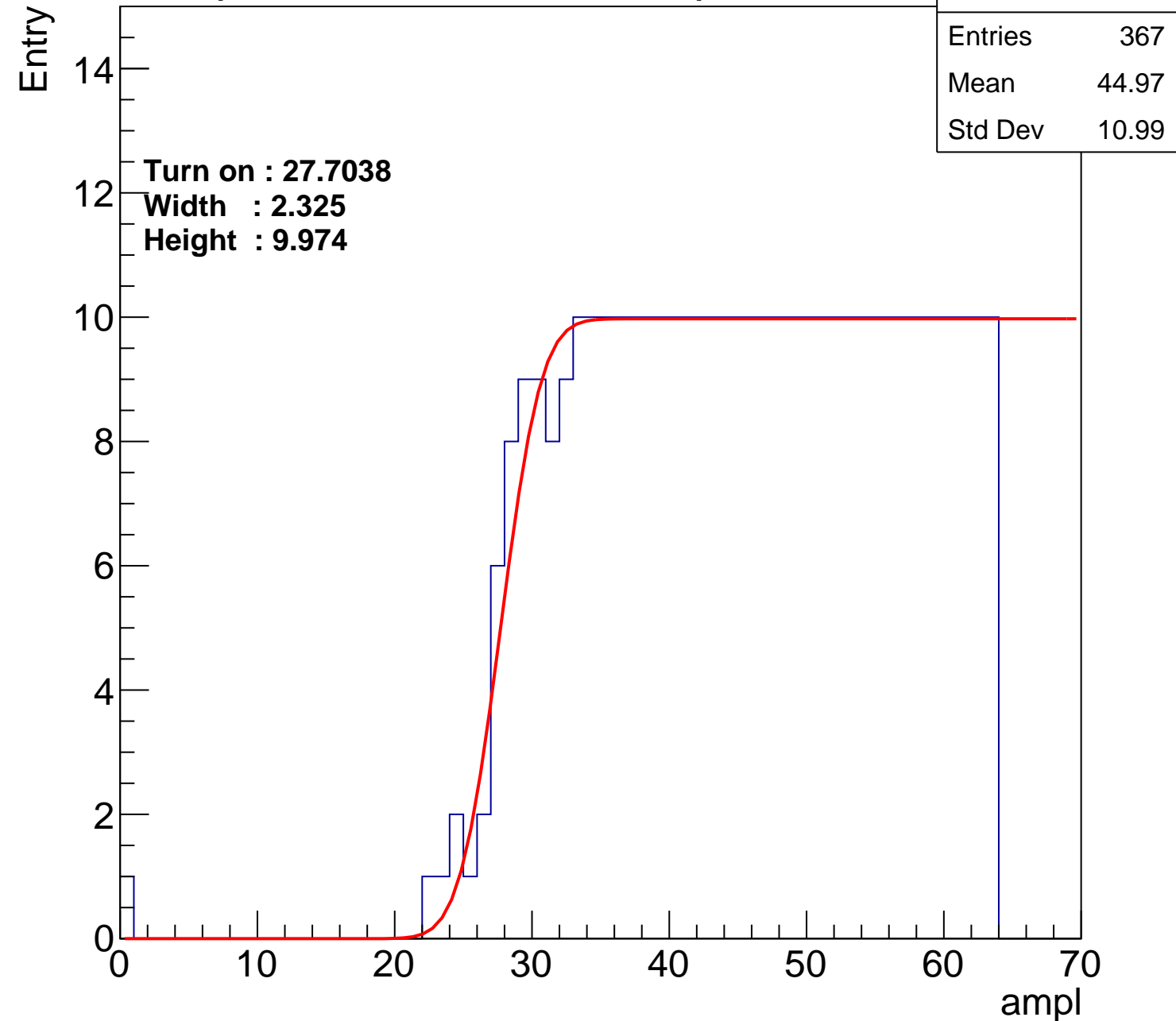
Width : 2.325

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch16

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.43
Std Dev	11.86

Turn on : 27.6365

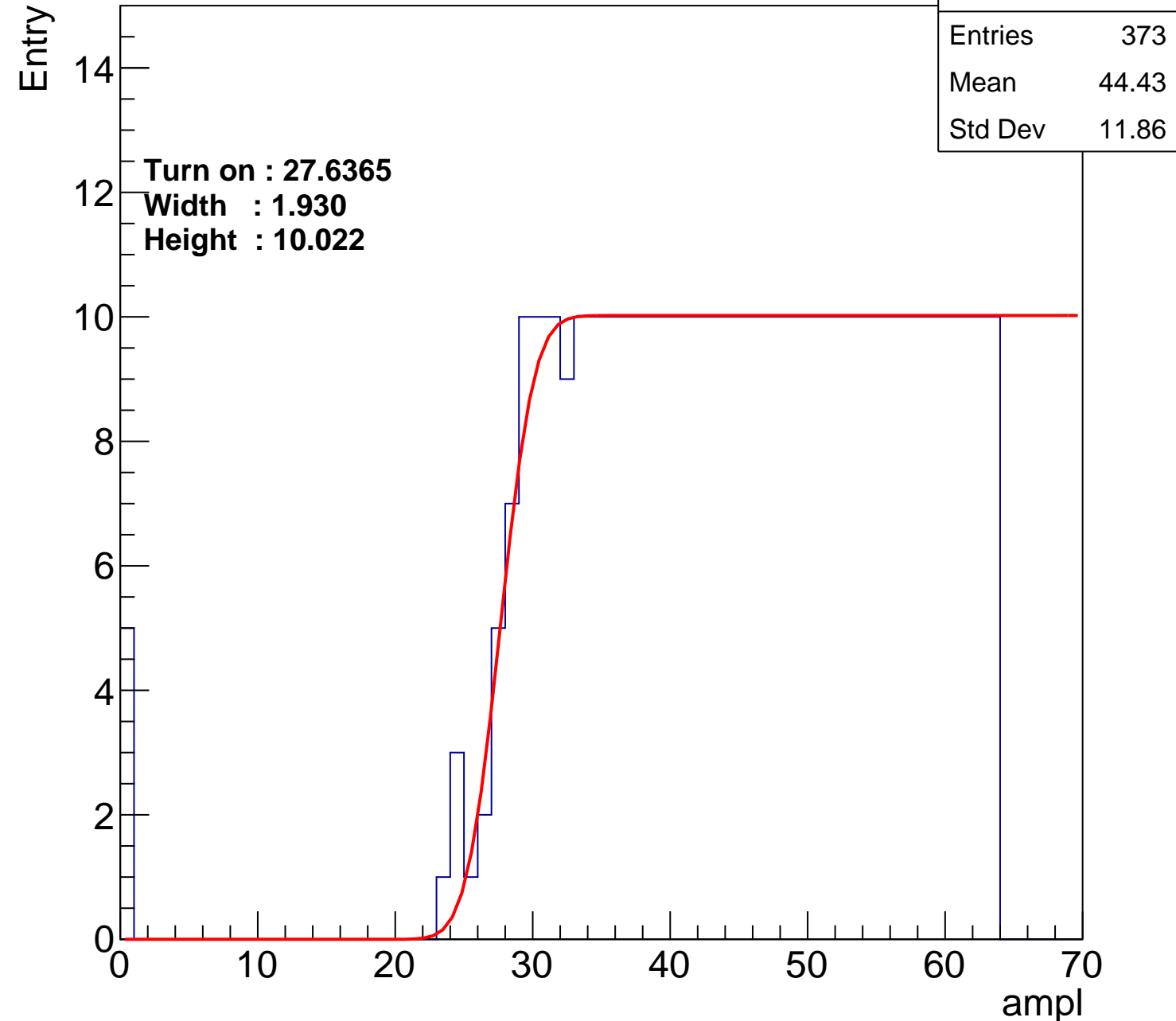
Width : 1.930

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch17

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	45.04
Std Dev	11.28

Turn on : 28.4161

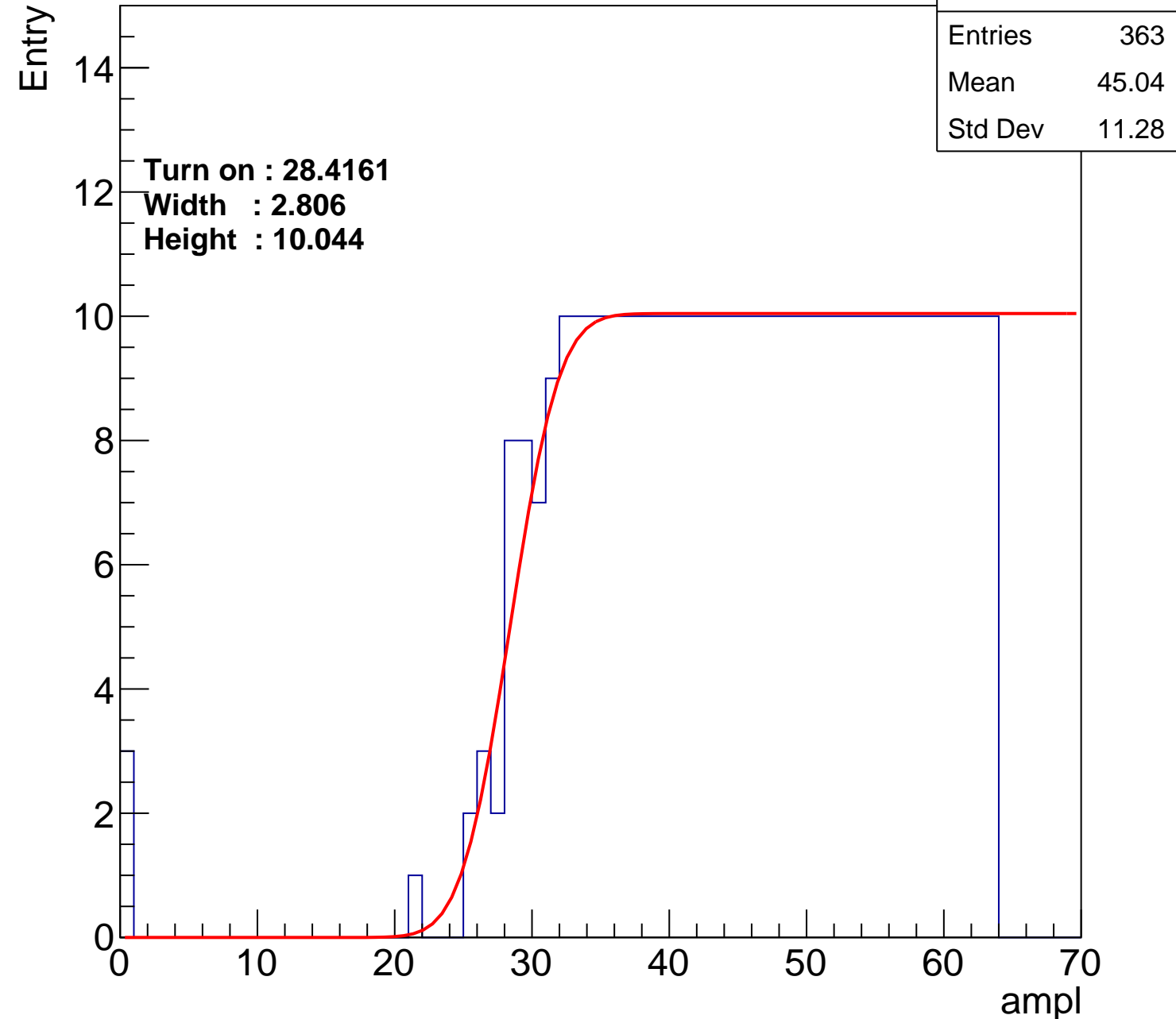
Width : 2.806

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch18

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.1272

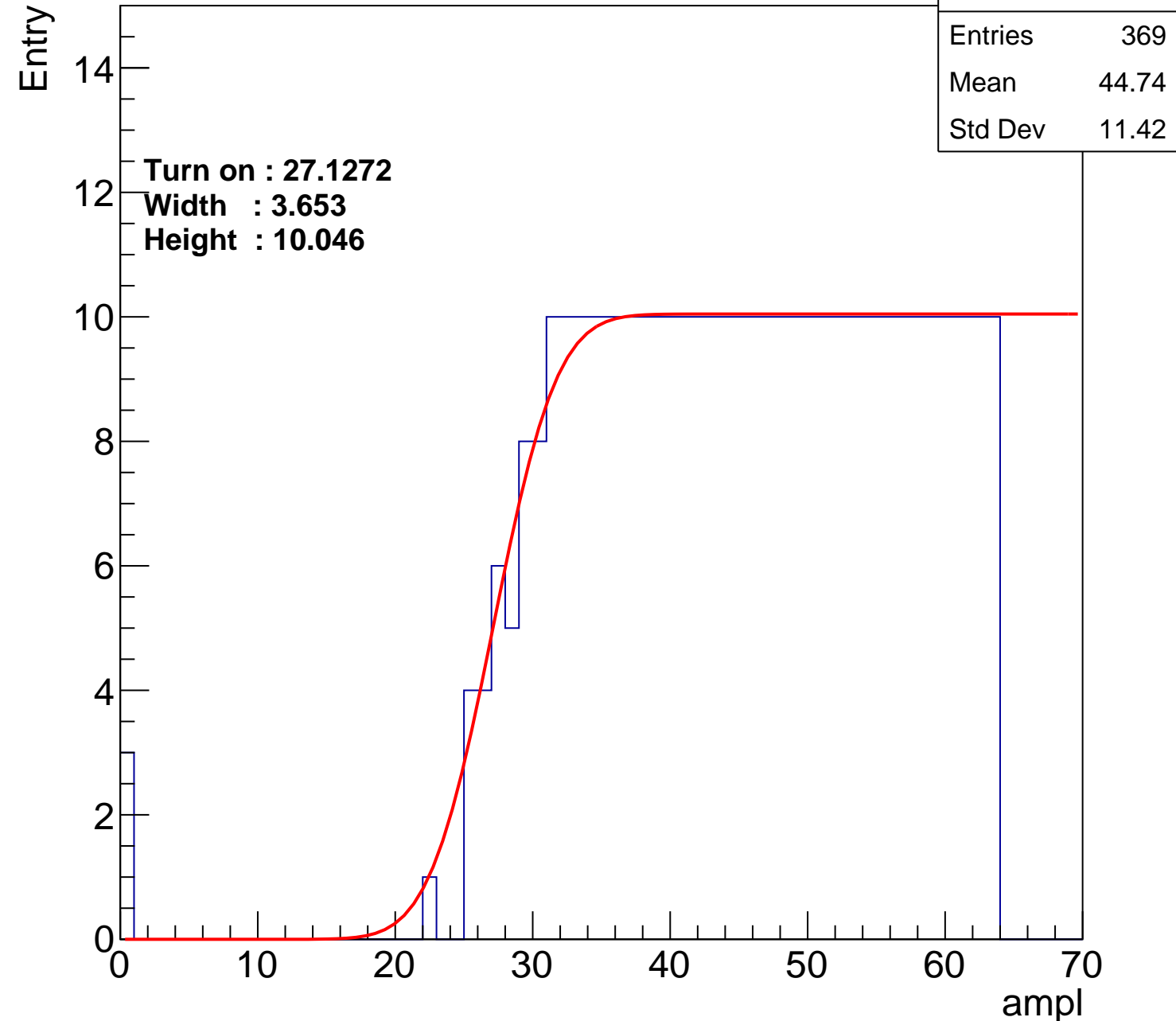
Width : 3.653

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch19

calib_packv5_042523_0143.root, FC#4, port A2

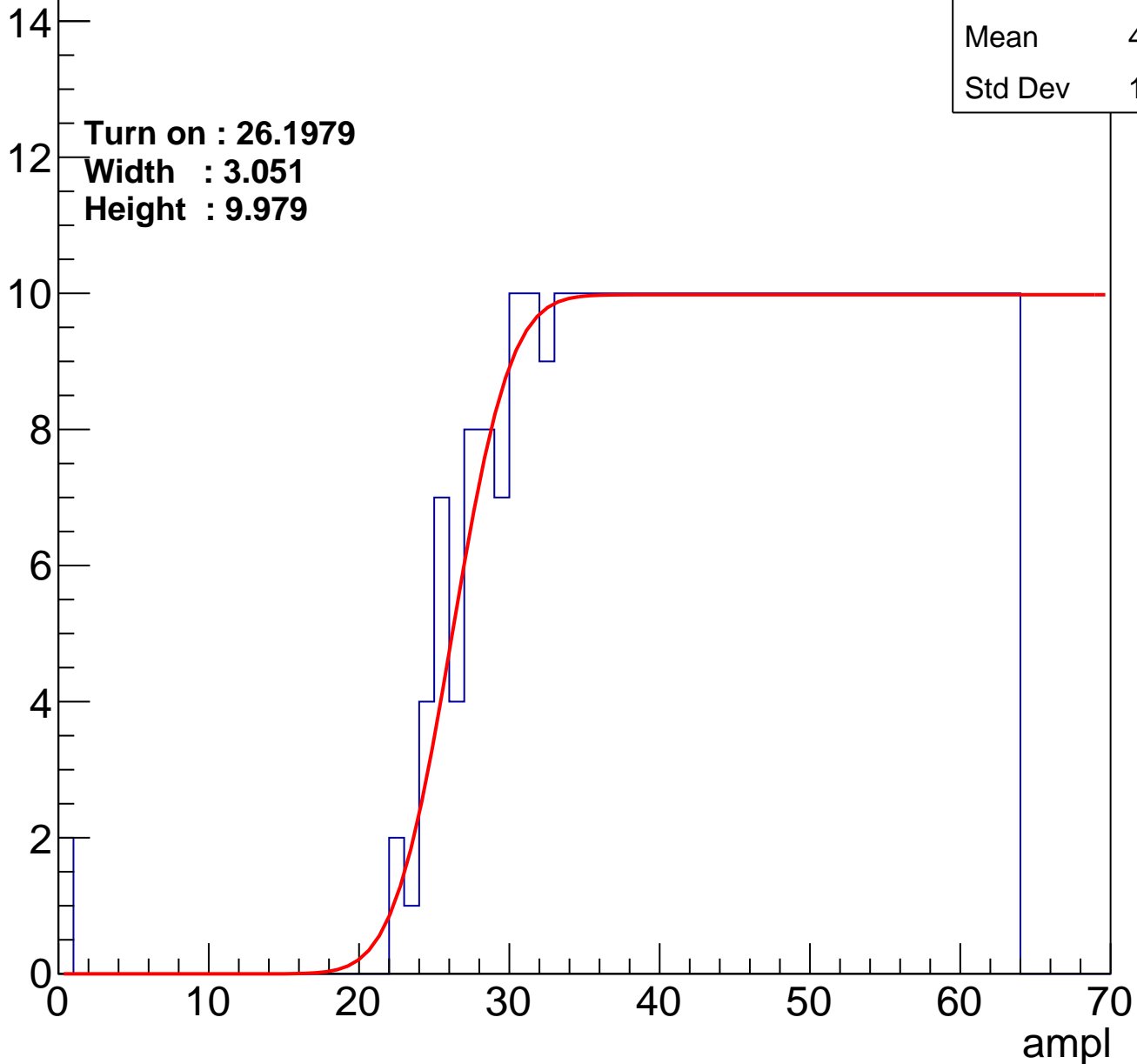
Entries	382
Mean	44.14
Std Dev	11.59

Turn on : 26.1979

Width : 3.051

Height : 9.979

Entry



B1L100S, U23-ch20

calib_packv5_042523_0143.root, FC#4, port A2

Entries	388
Mean	43.95
Std Dev	11.53

Turn on : 25.4525

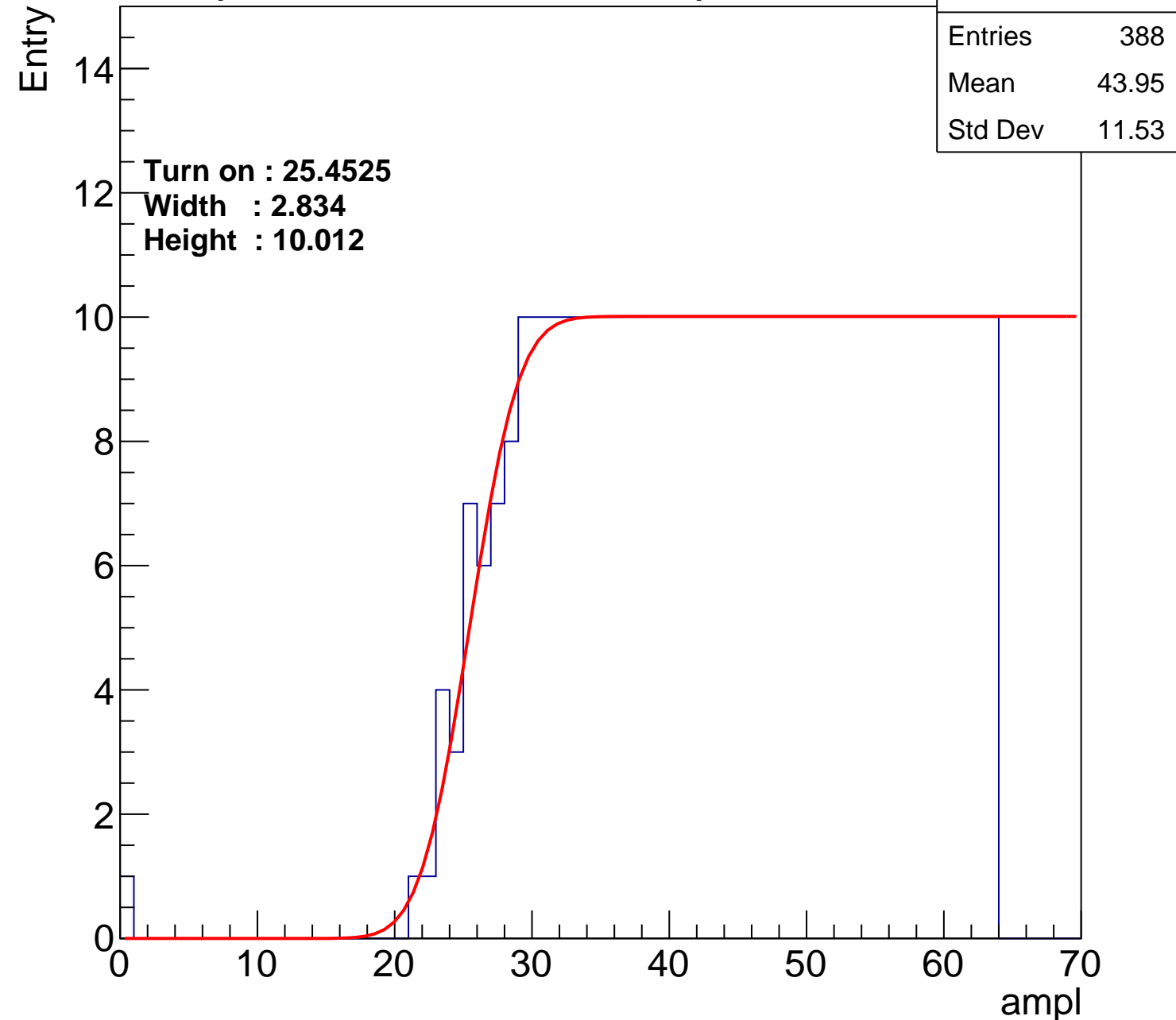
Width : 2.834

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch21

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.41
Std Dev	10.75

Turn on : 28.3581

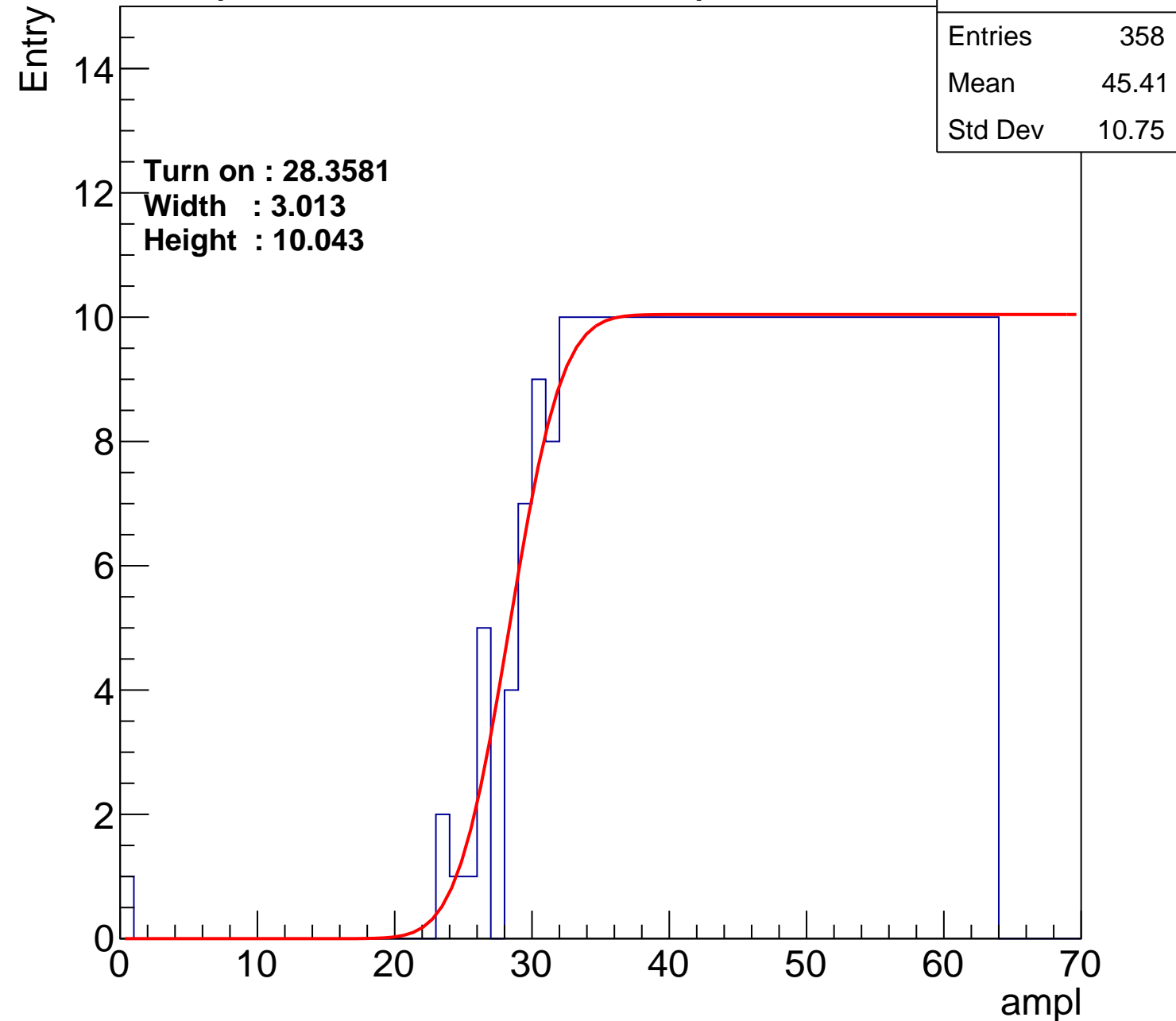
Width : 3.013

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch22

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.61
Std Dev	11.65

Turn on : 27.6493

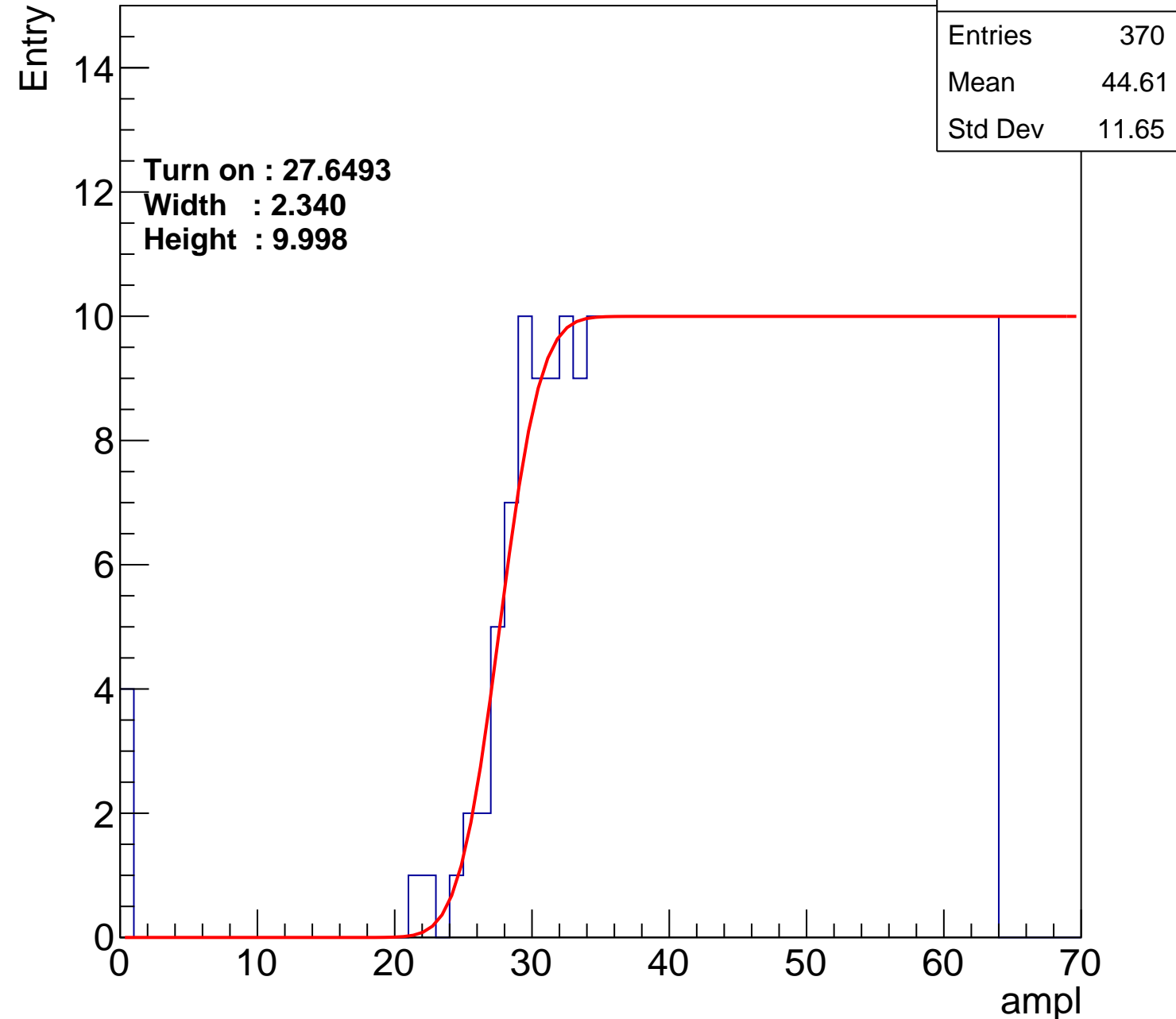
Width : 2.340

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch23

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.2415

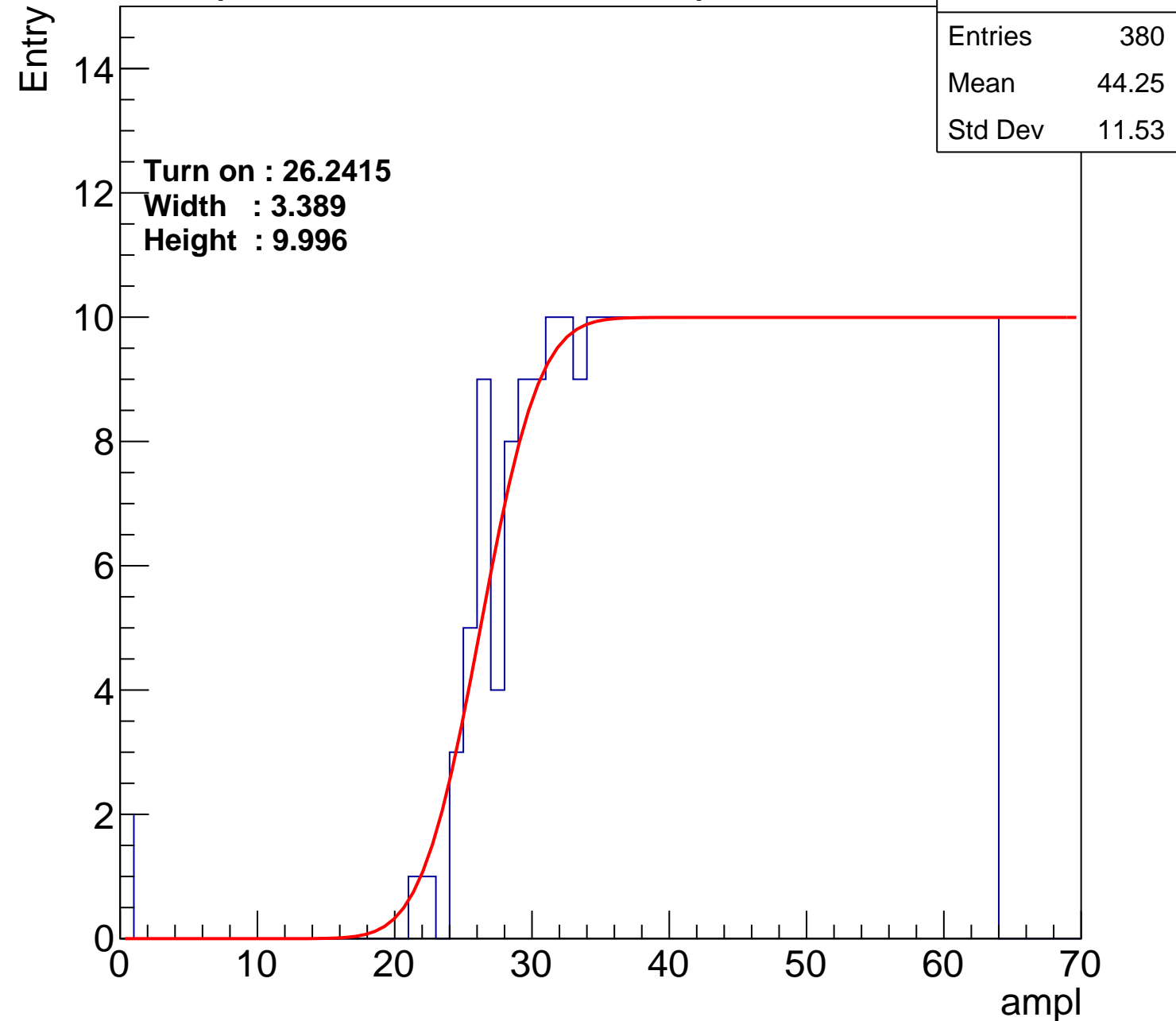
Width : 3.389

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch24

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 27.3548

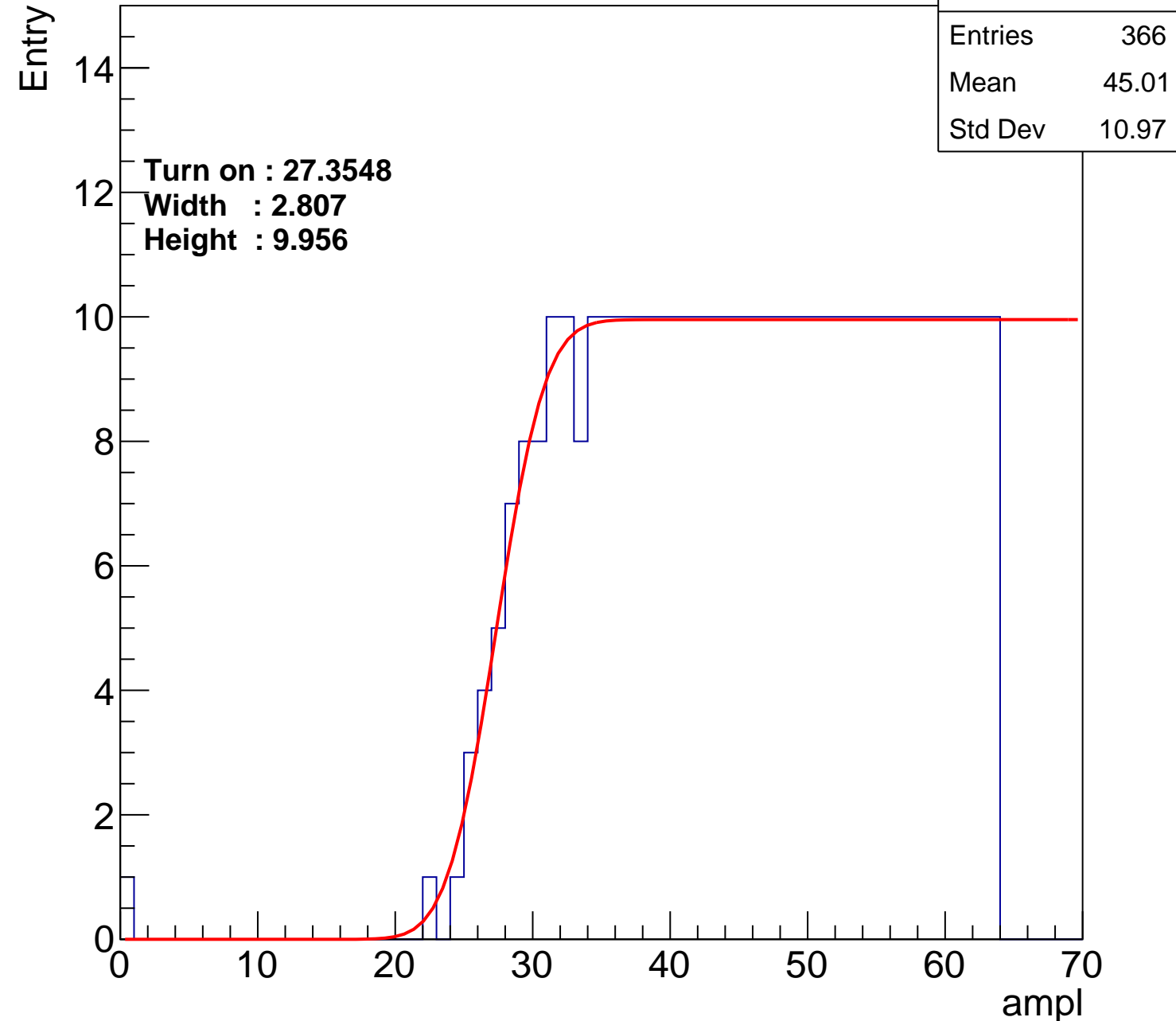
Width : 2.807

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch25

calib_packv5_042523_0143.root, FC#4, port A2

Entries	363
Mean	45.22
Std Dev	10.8

Turn on : 27.9309

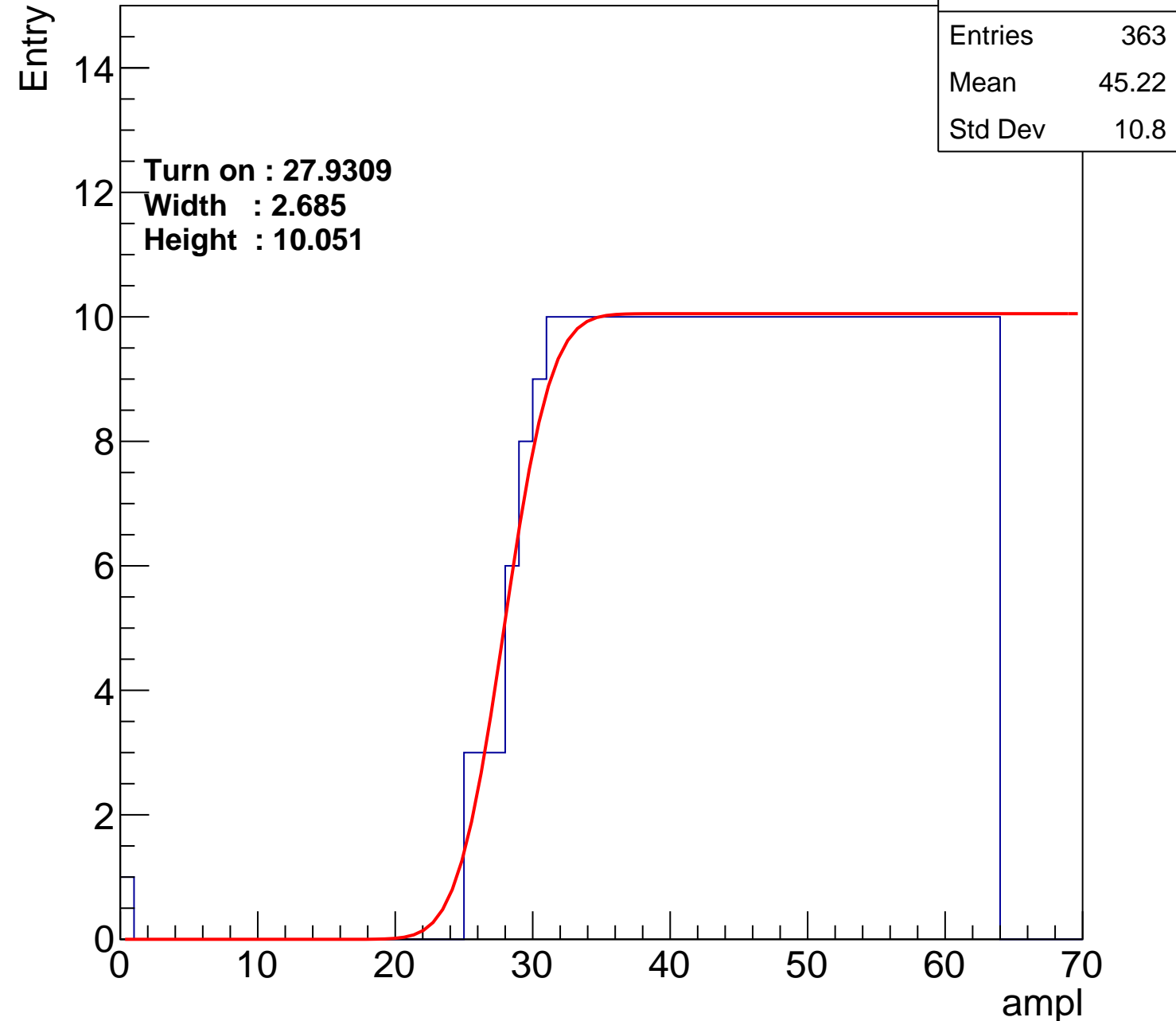
Width : 2.685

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch26

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 27.1849

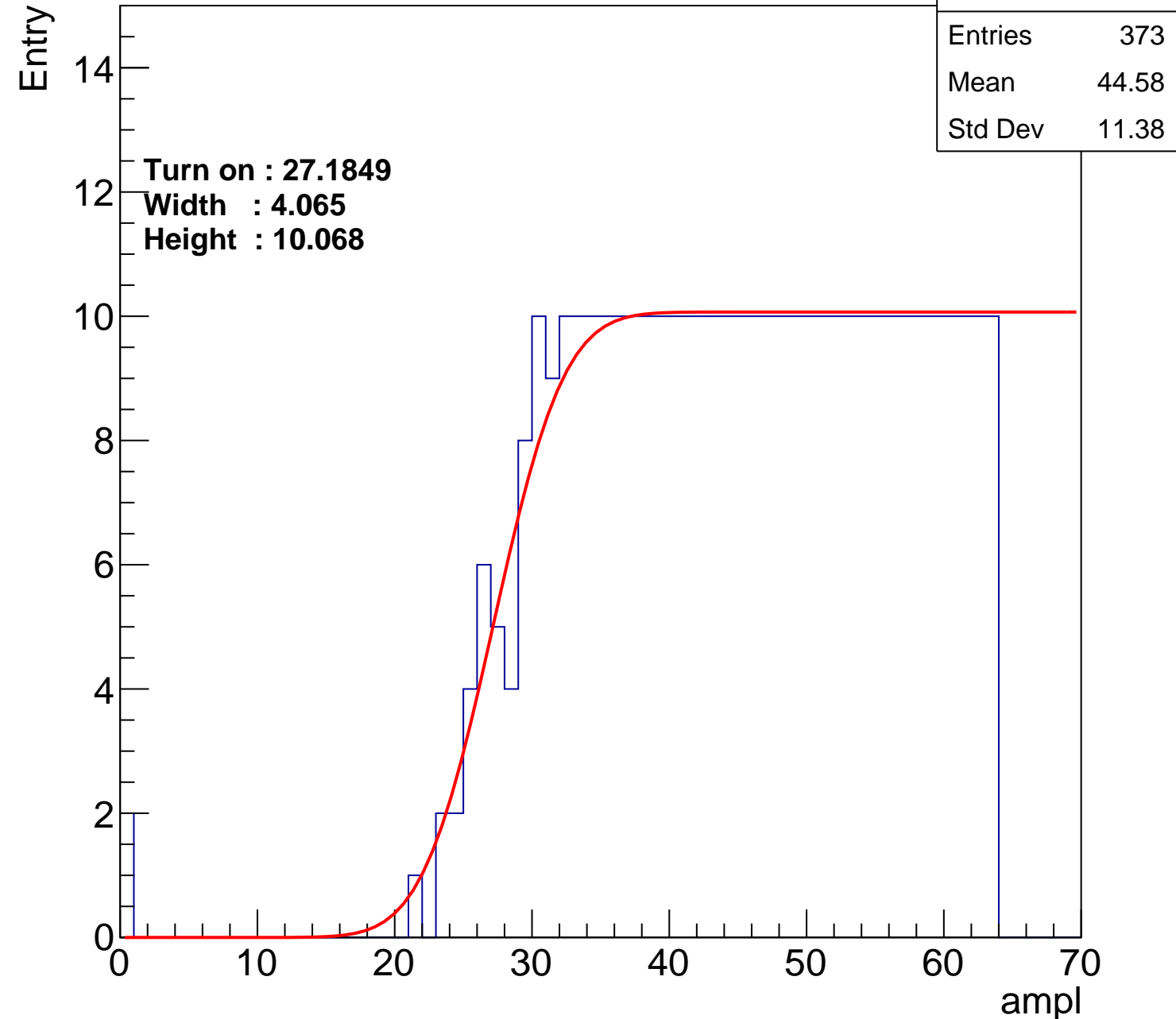
Width : 4.065

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch27

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.59
Std Dev	10.62

Turn on : 28.6038

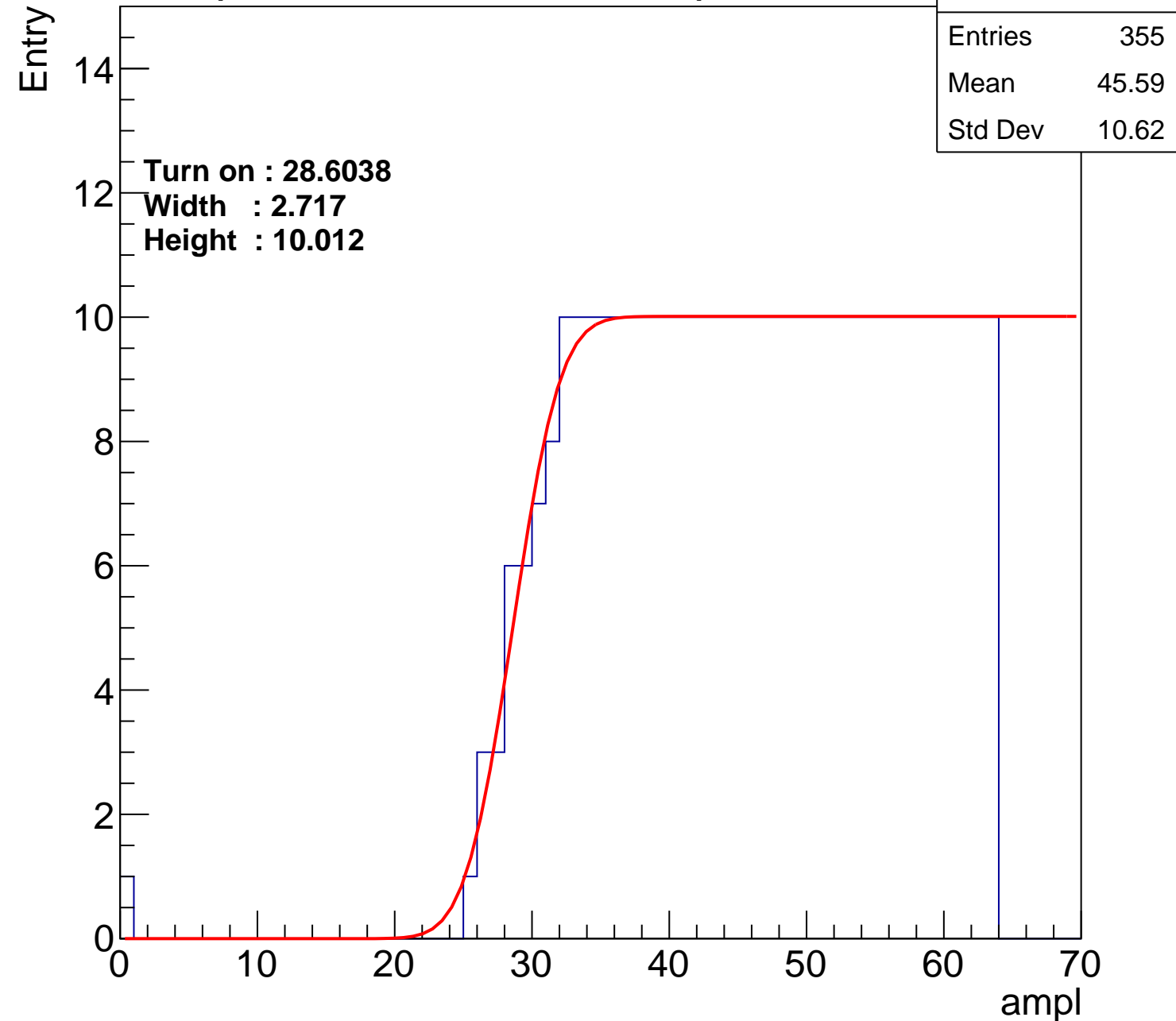
Width : 2.717

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch28

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 26.3680

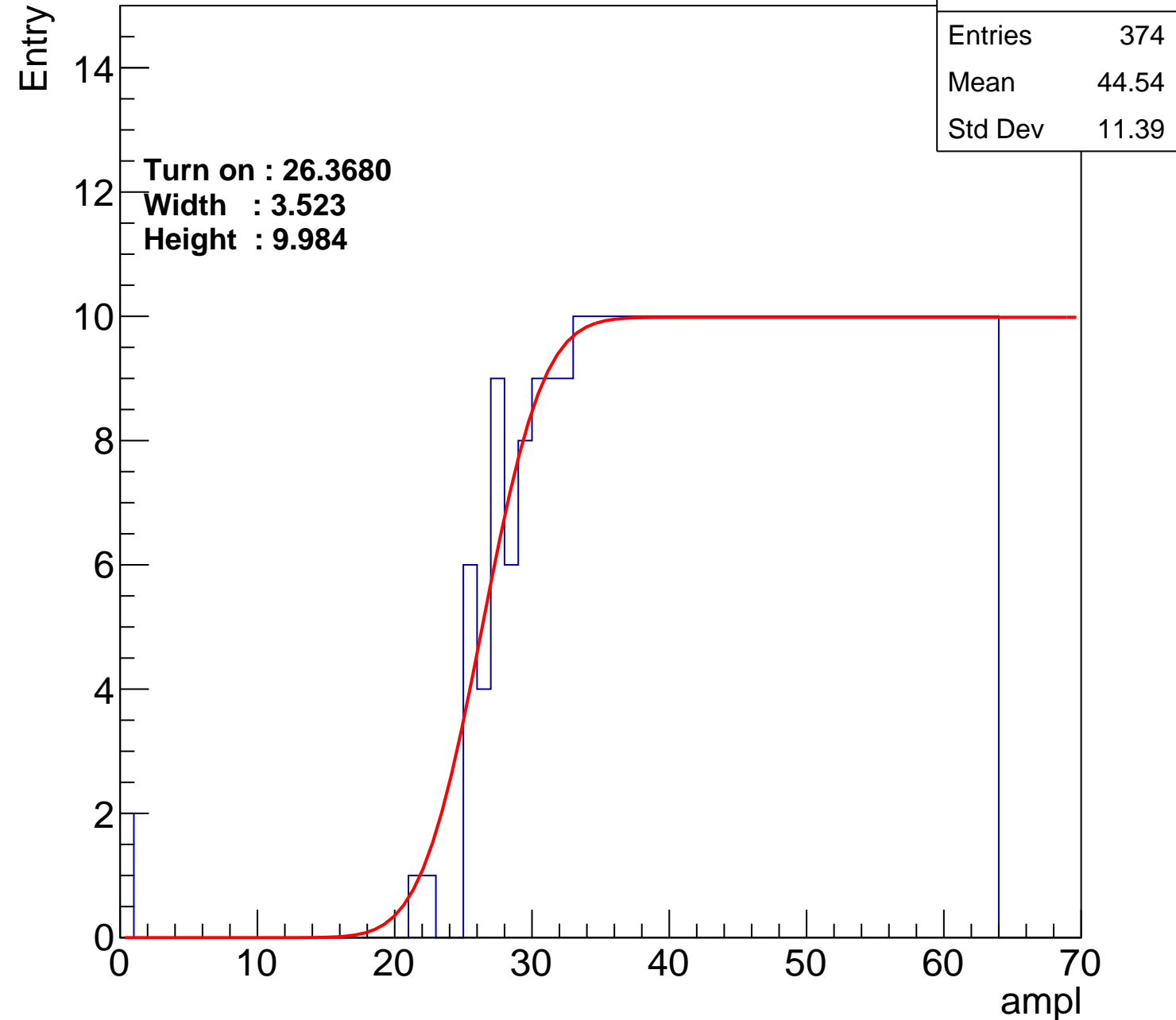
Width : 3.523

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch29

calib_packv5_042523_0143.root, FC#4, port A2

Entries	350
Mean	45.79
Std Dev	10.57

Turn on : 29.6153

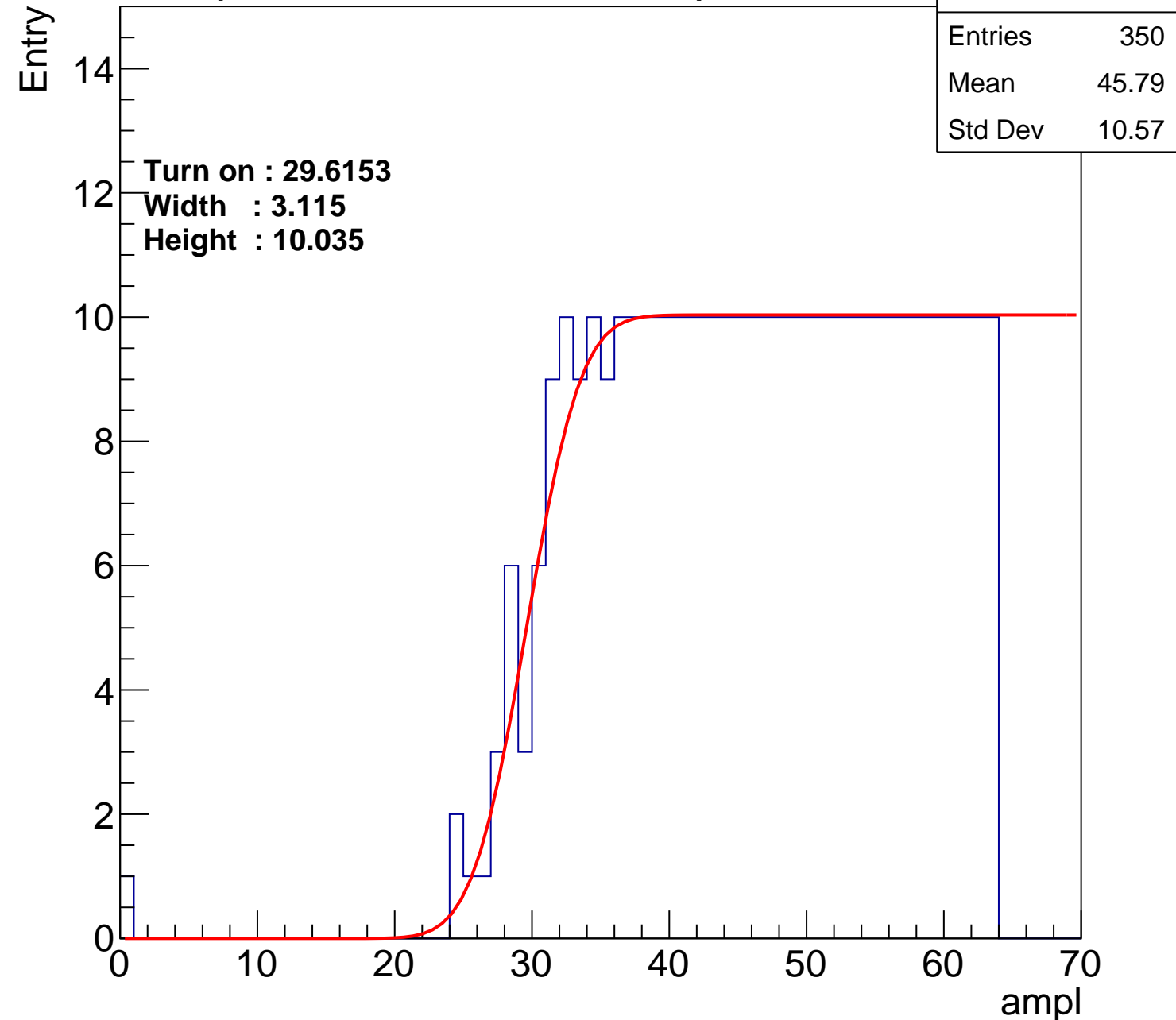
Width : 3.115

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch30

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.88
Std Dev	11.94

Turn on : 26.3989

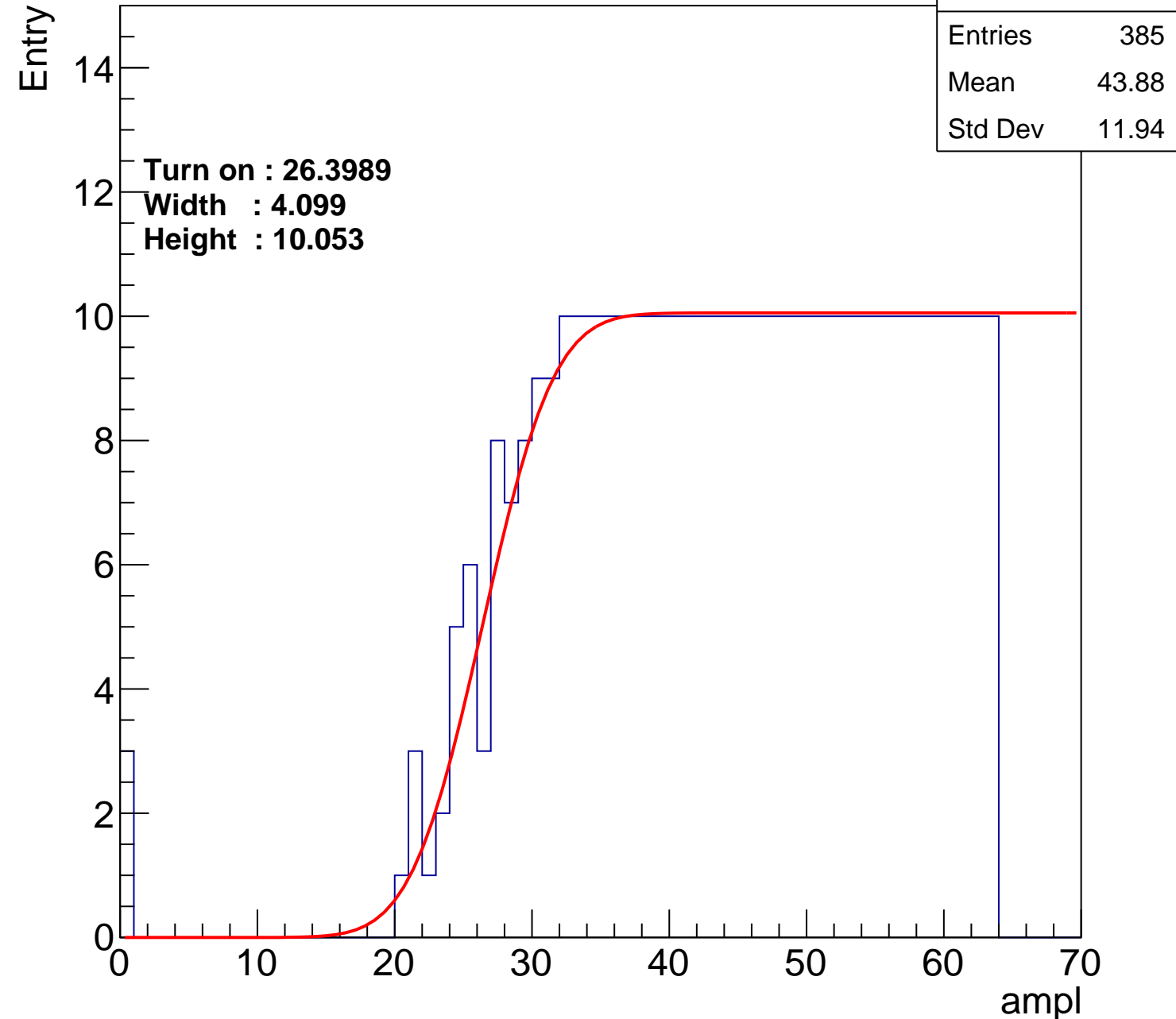
Width : 4.099

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch31

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.25
Std Dev	11.78

Turn on : 26.5400

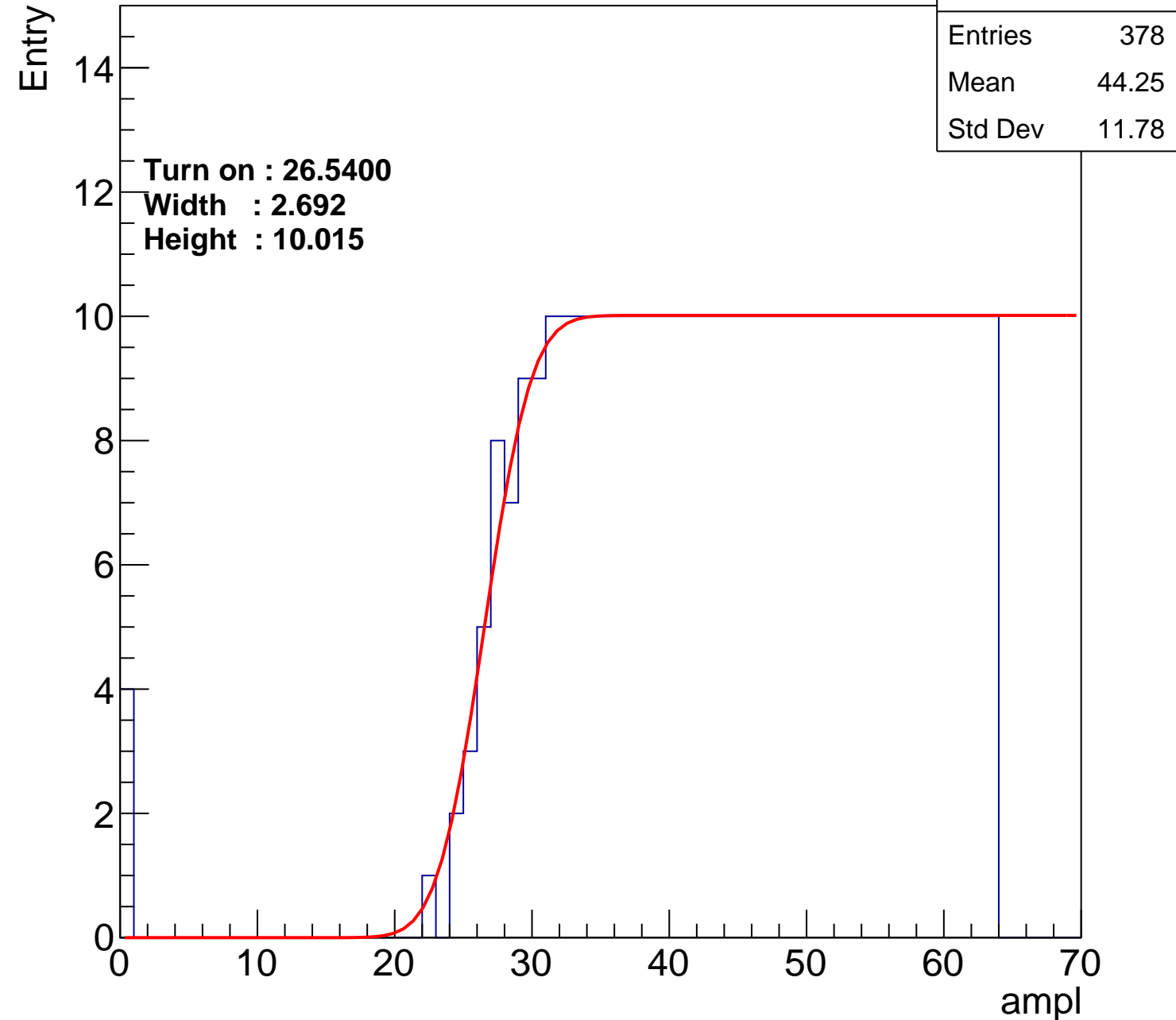
Width : 2.692

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch32

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	44.9
Std Dev	11.47

Turn on : 27.6607

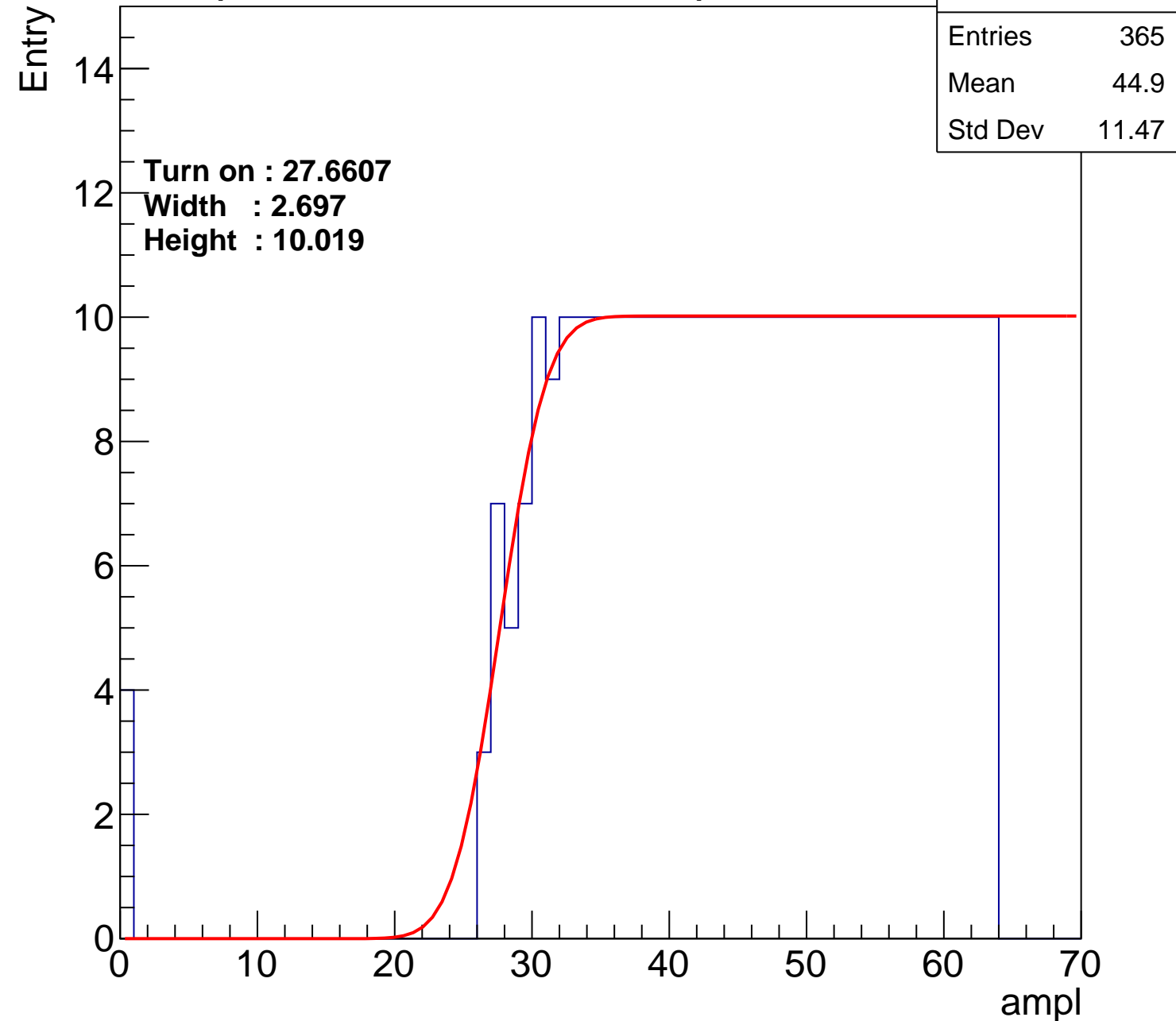
Width : 2.697

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch33

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.68
Std Dev	11.14

Turn on : 26.9701

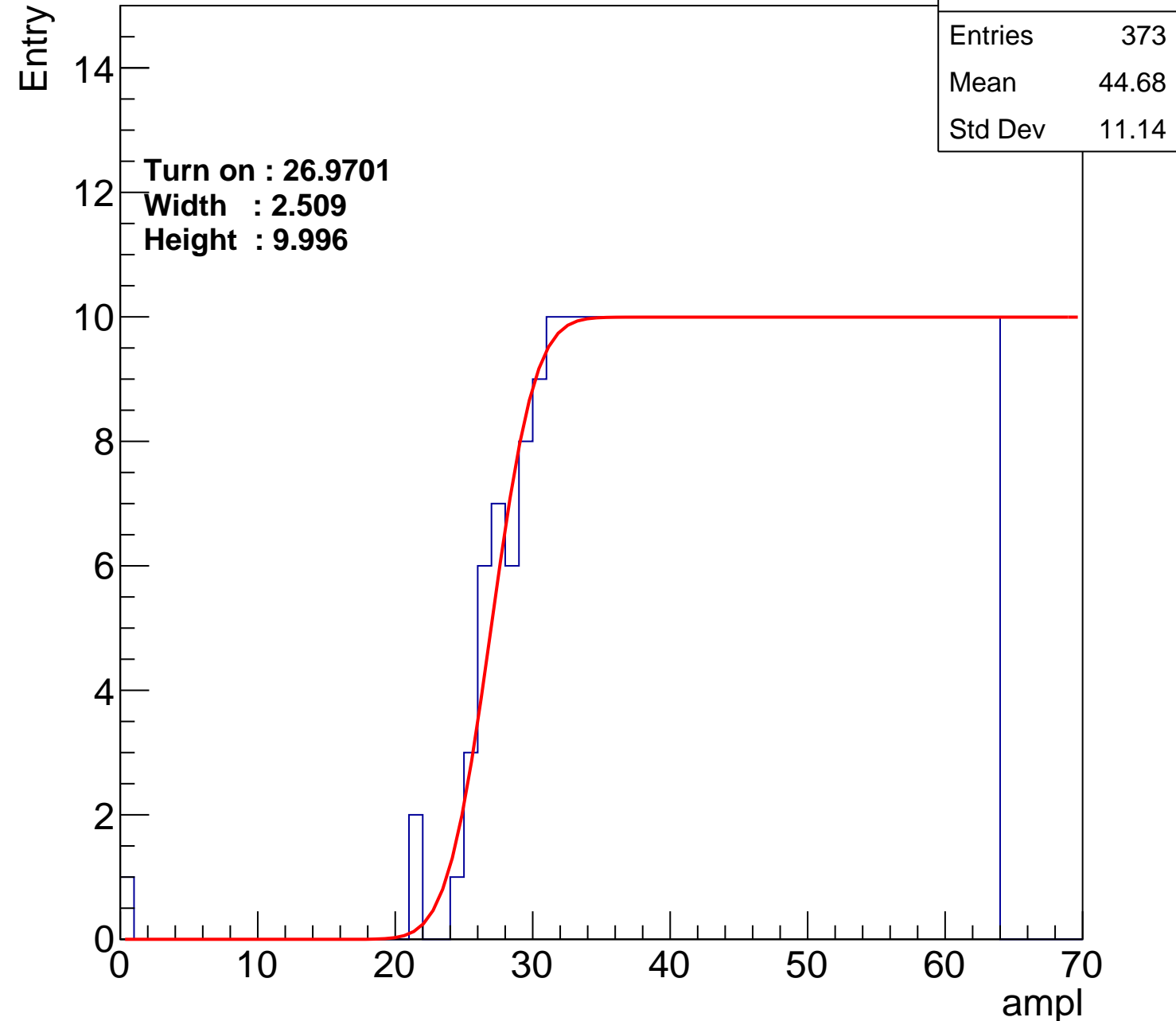
Width : 2.509

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch34

calib_packv5_042523_0143.root, FC#4, port A2

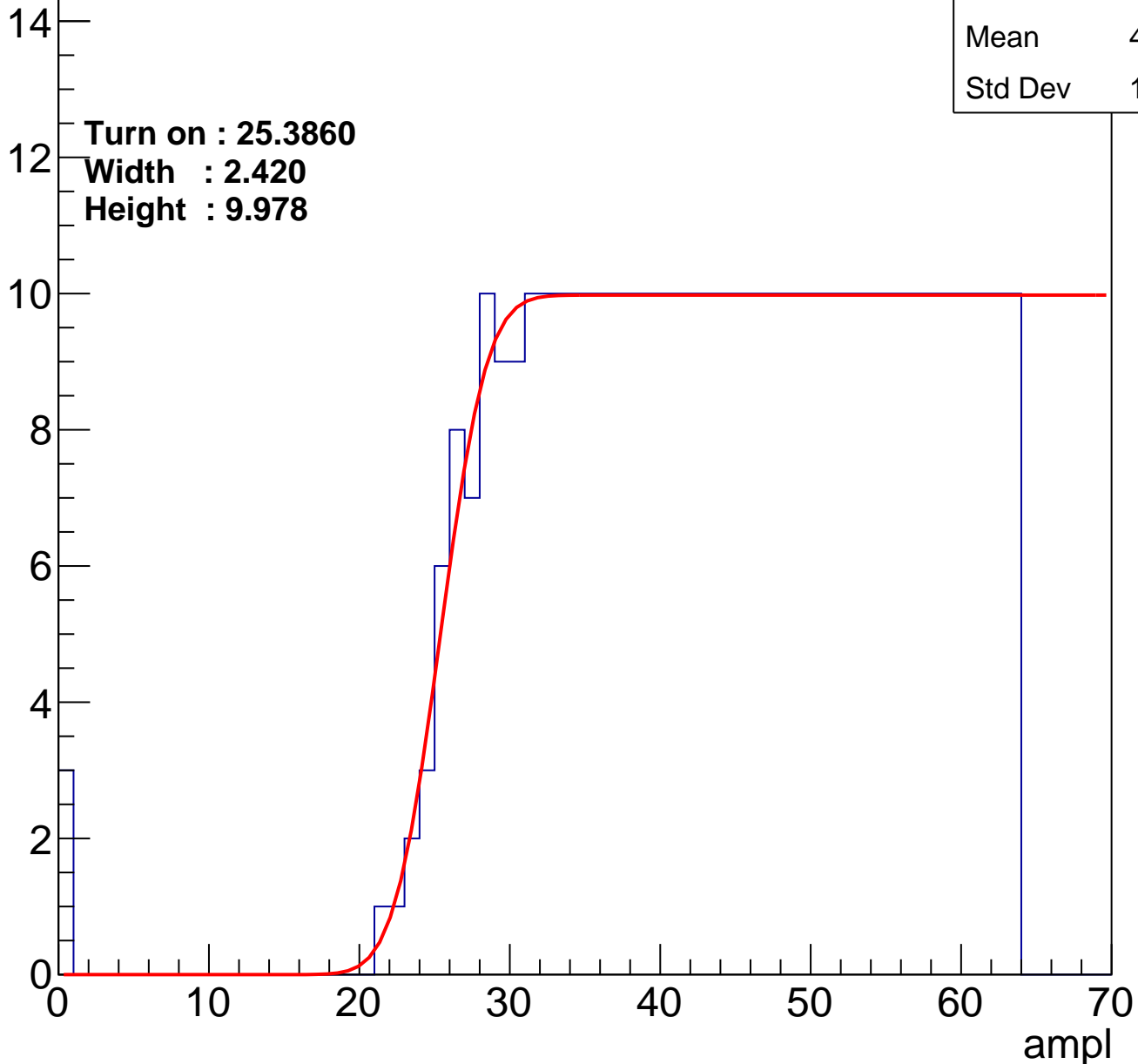
Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.3860

Width : 2.420

Height : 9.978

Entry



B1L100S, U23-ch35

calib_packv5_042523_0143.root, FC#4, port A2

Entries	358
Mean	45.32
Std Dev	11.1

Turn on : 28.6029

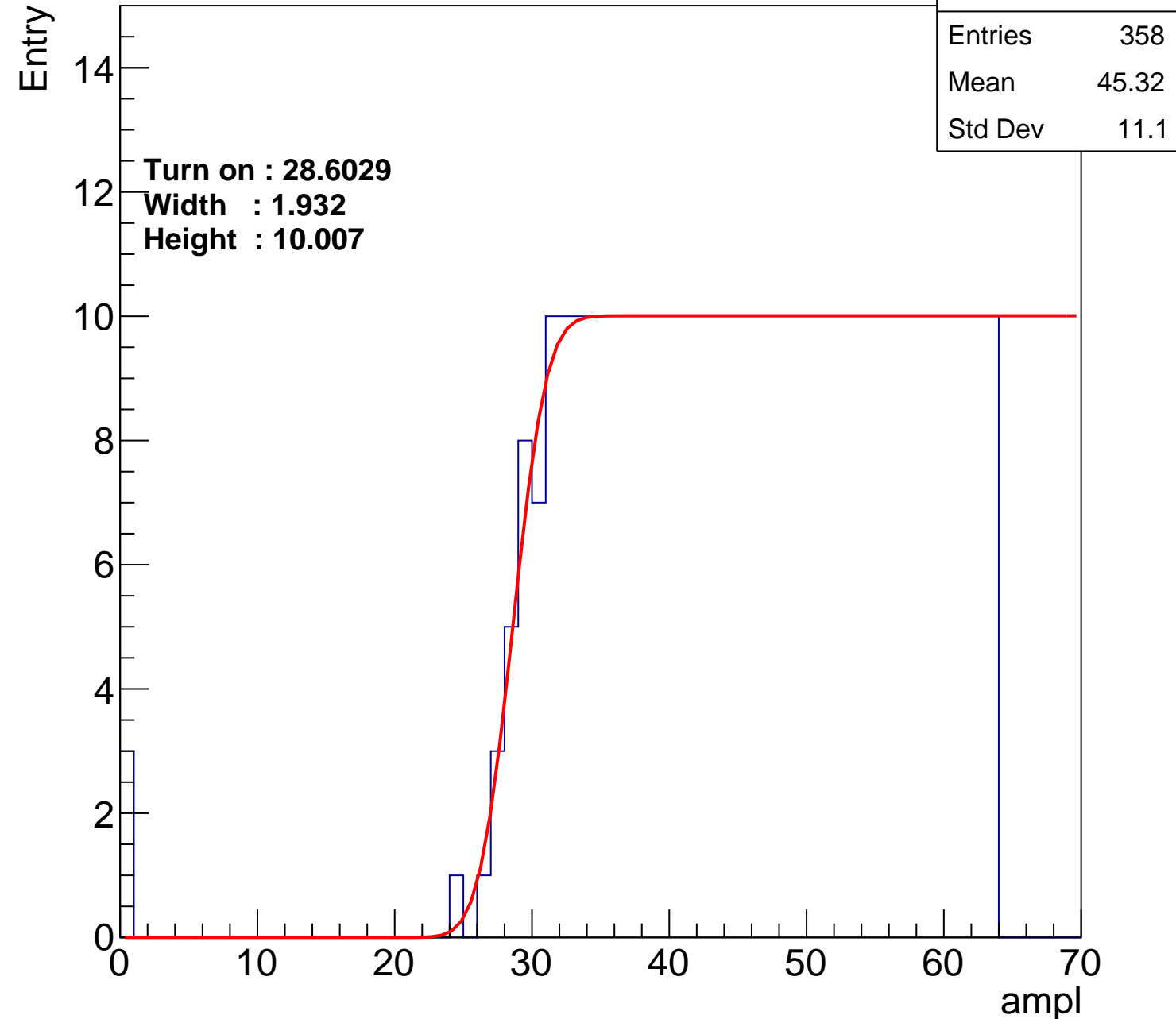
Width : 1.932

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch36

calib_packv5_042523_0143.root, FC#4, port A2

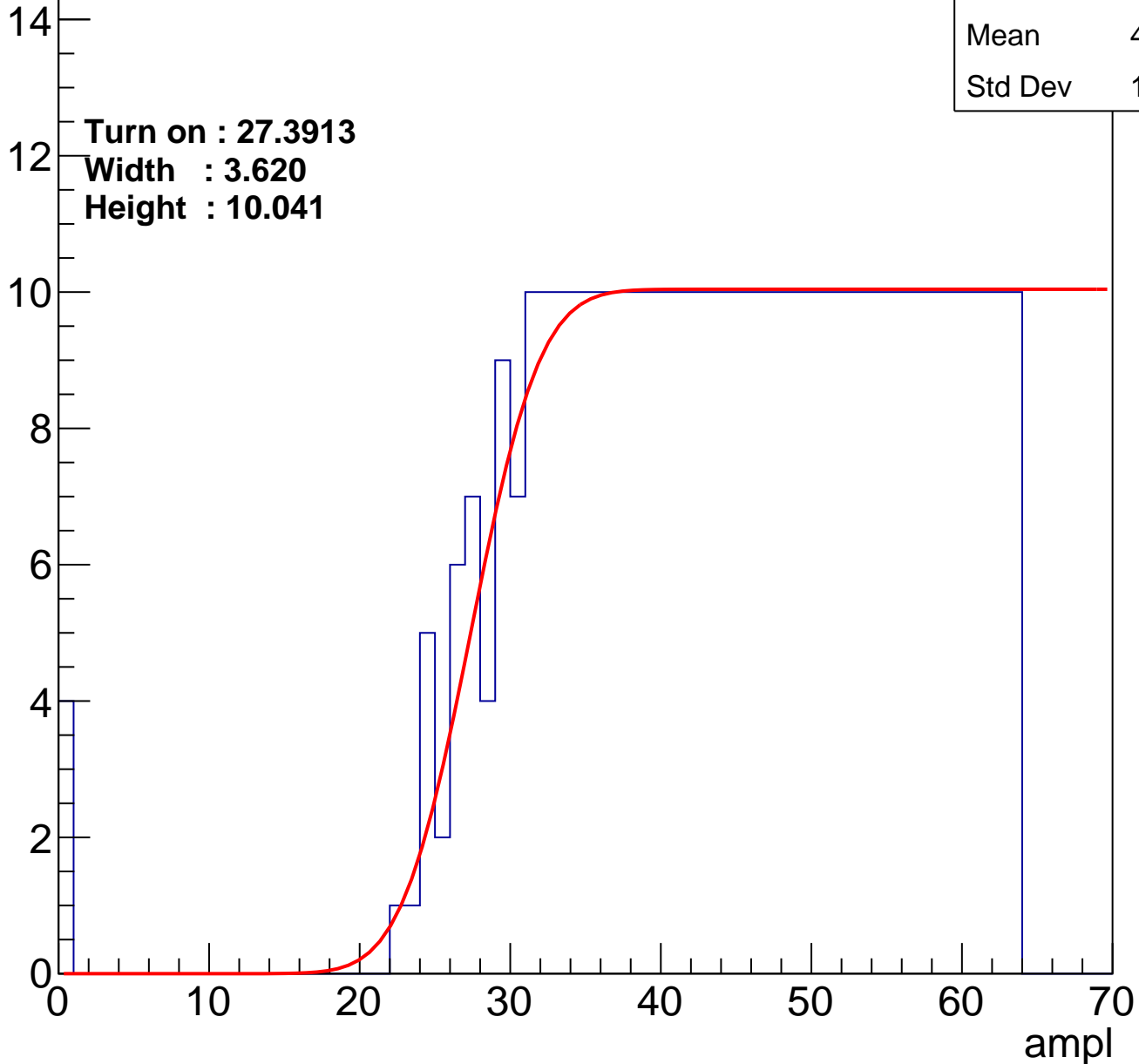
Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 27.3913

Width : 3.620

Height : 10.041

Entry



B1L100S, U23-ch37

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.53
Std Dev	11.4

Turn on : 26.7395

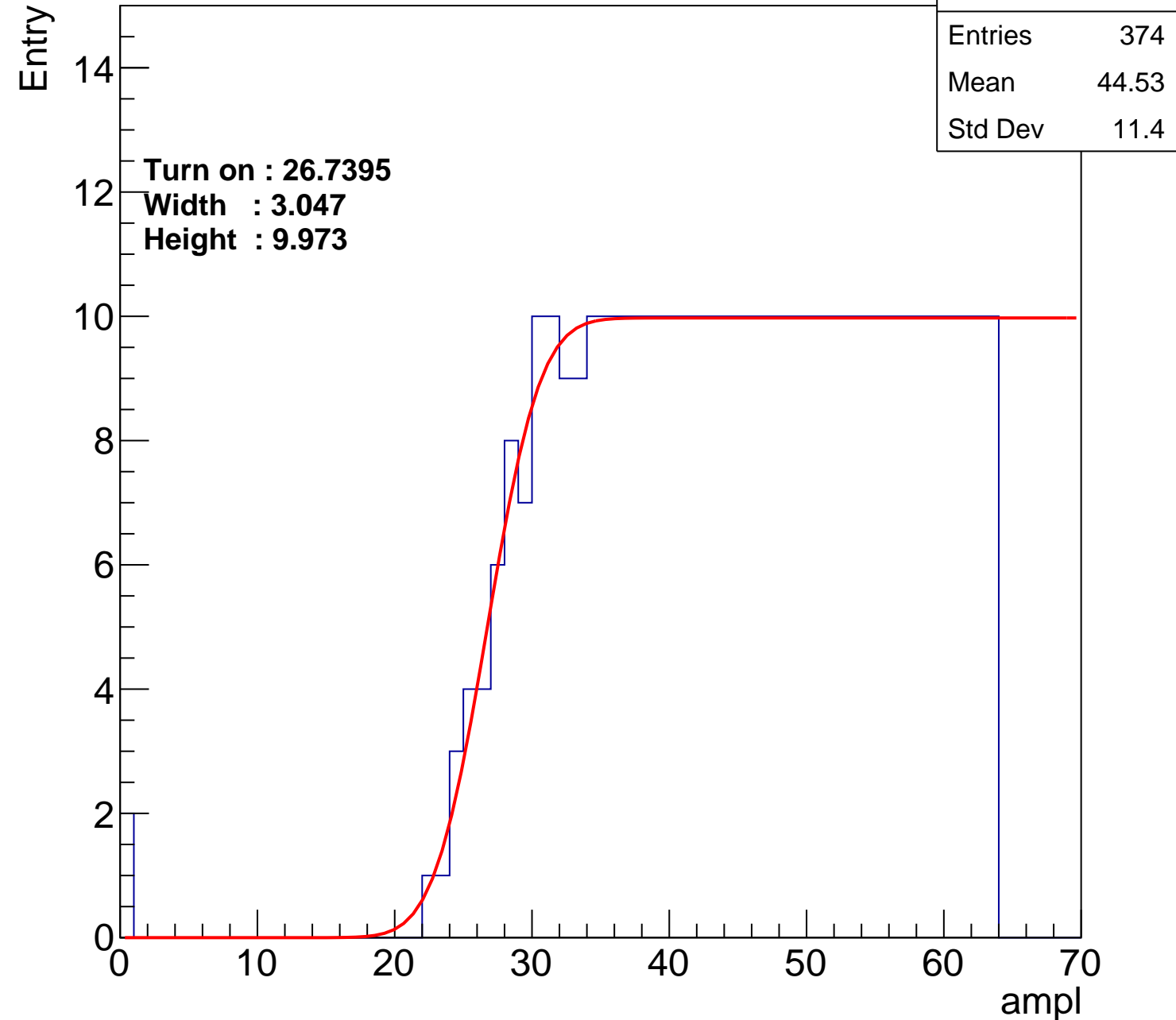
Width : 3.047

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch38

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.4408

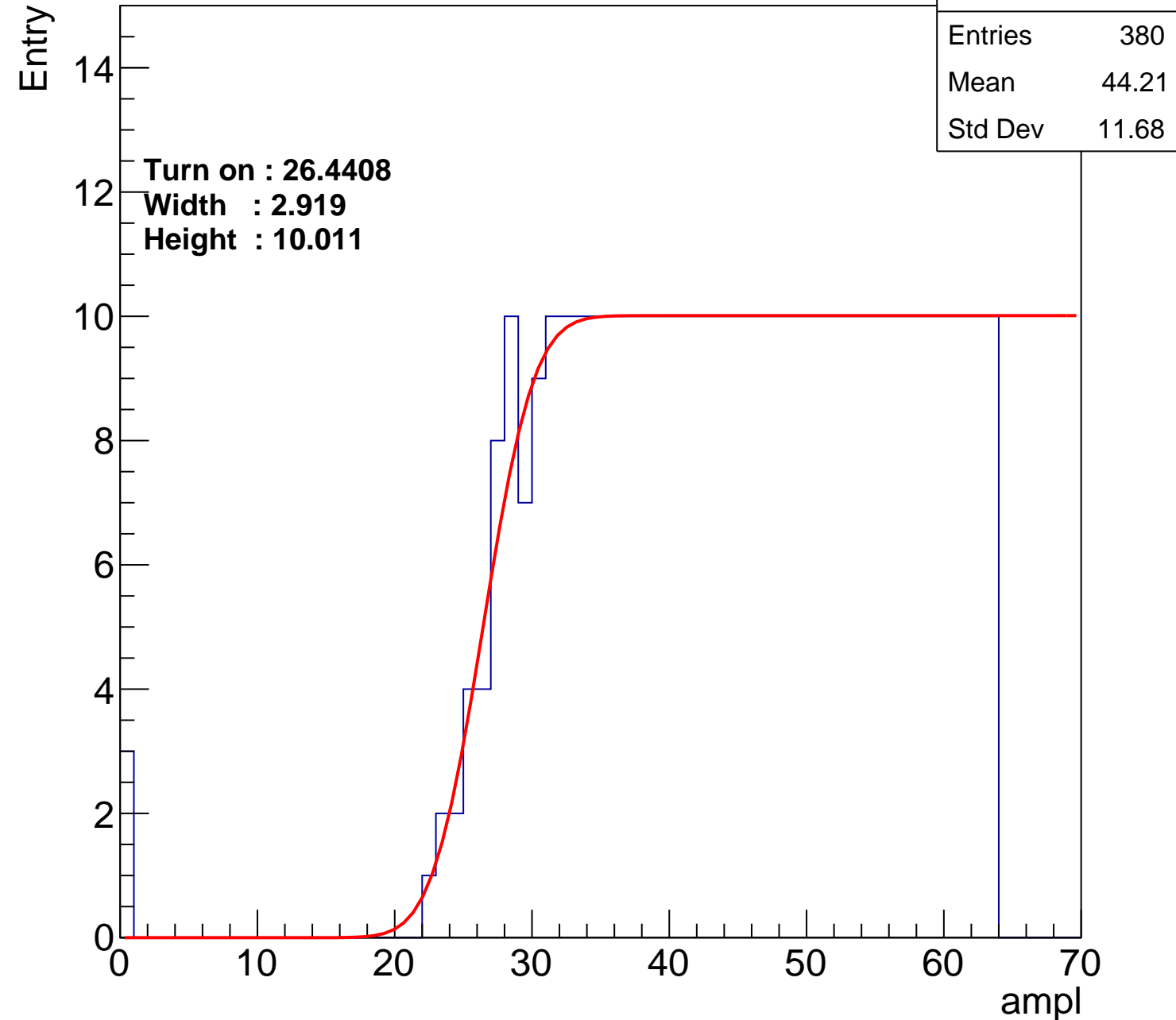
Width : 2.919

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch39

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.27
Std Dev	11.32

Turn on : 26.5281

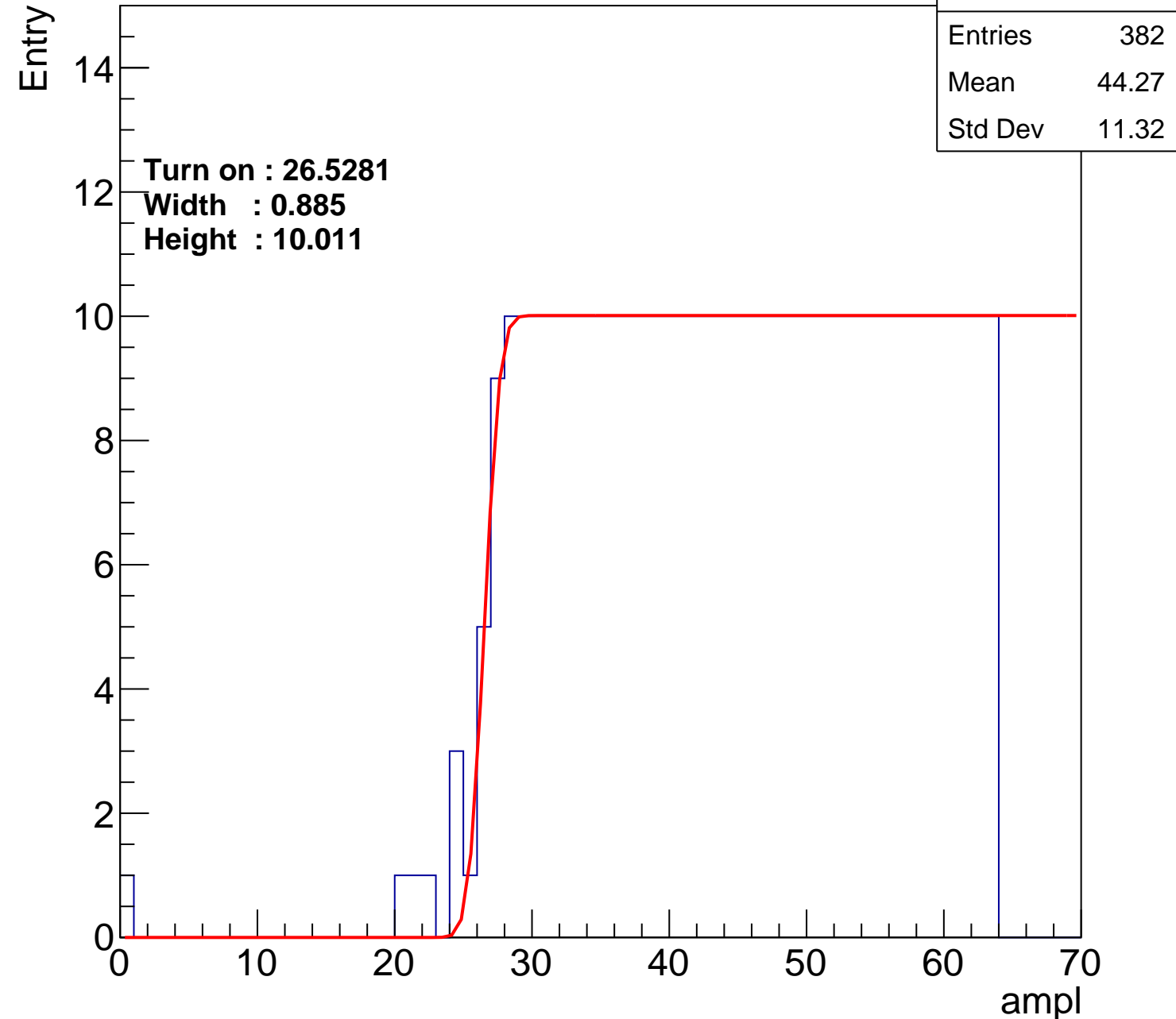
Width : 0.885

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch40

calib_packv5_042523_0143.root, FC#4, port A2

Entries	375
Mean	44.51
Std Dev	11.4

Turn on : 27.6908

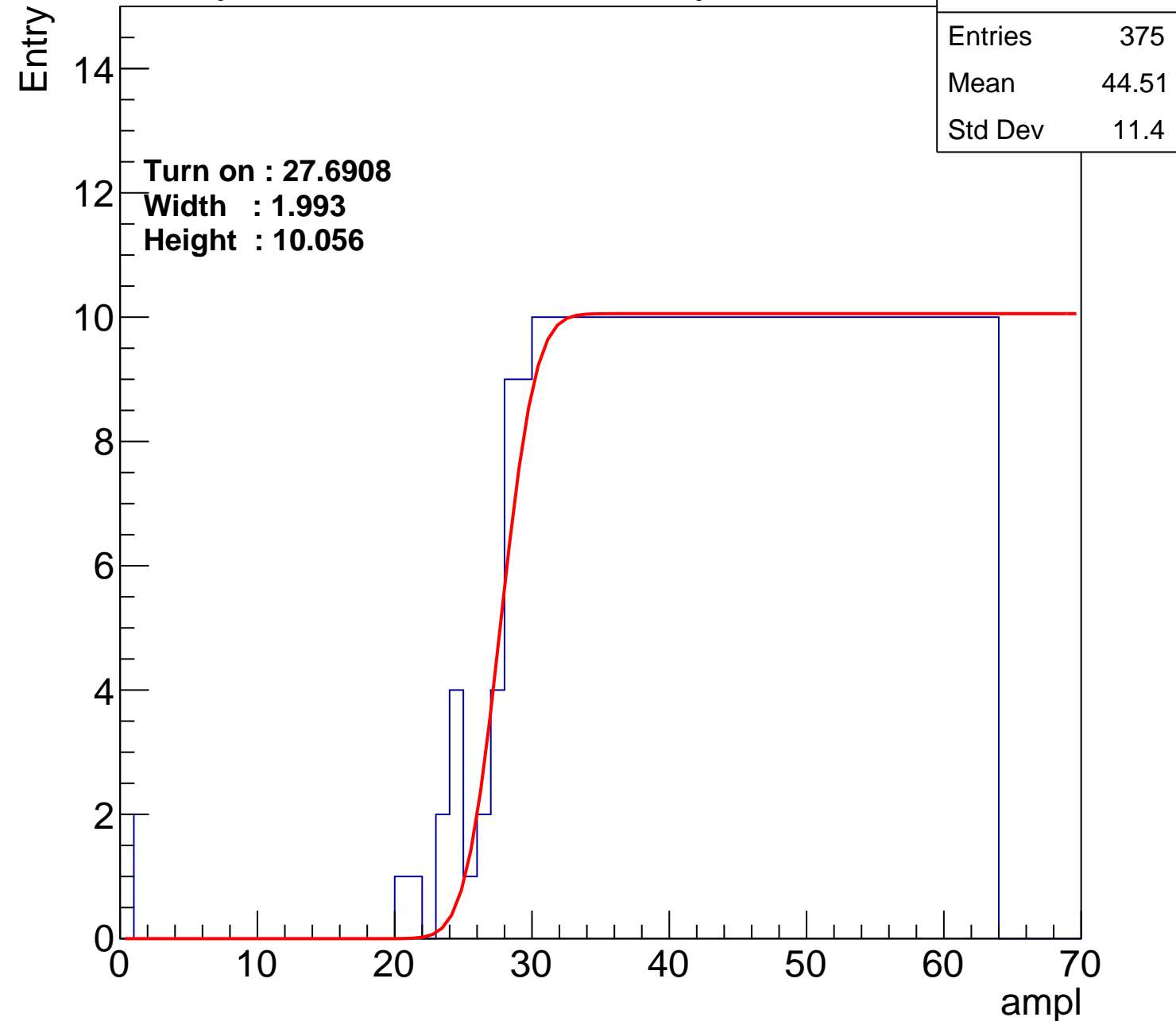
Width : 1.993

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch41

calib_packv5_042523_0143.root, FC#4, port A2

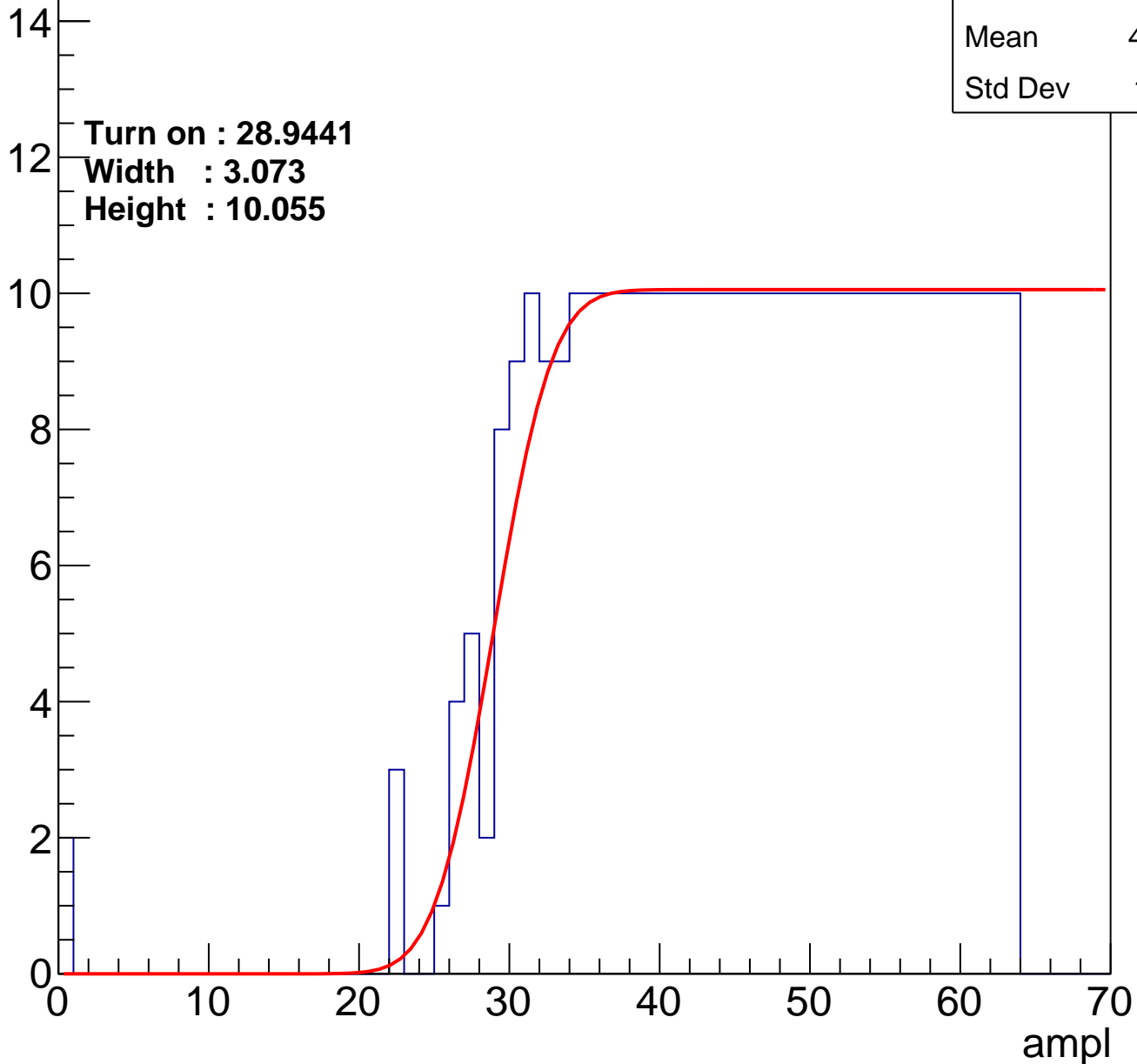
Entry

Entries	362
Mean	45.12
Std Dev	11.11

Turn on : 28.9441

Width : 3.073

Height : 10.055



B1L100S, U23-ch42

calib_packv5_042523_0143.root, FC#4, port A2

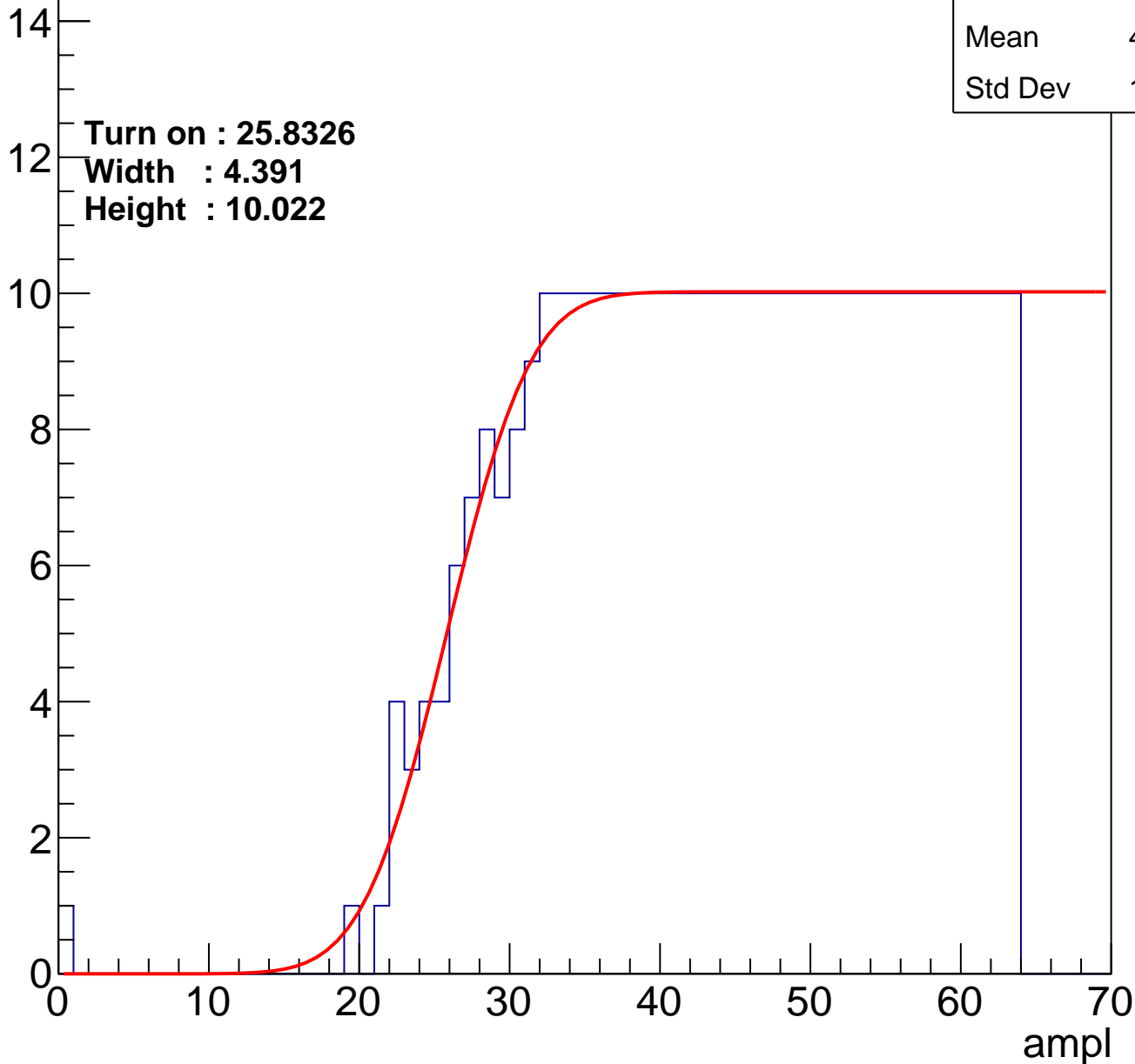
Entries	383
Mean	44.08
Std Dev	11.57

Turn on : 25.8326

Width : 4.391

Height : 10.022

Entry



B1L100S, U23-ch43

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.58
Std Dev	12.14

Turn on : 25.0342

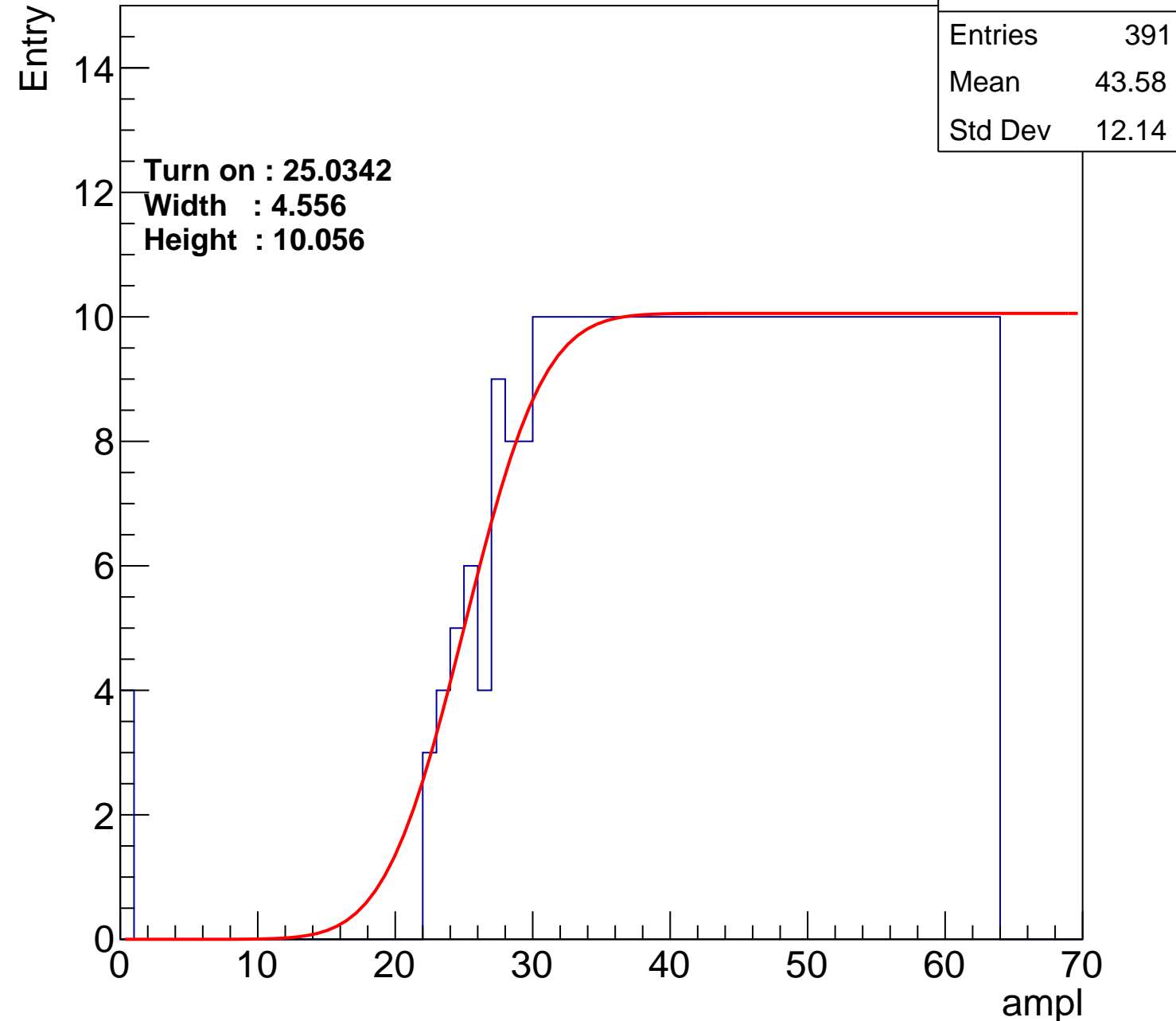
Width : 4.556

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch44

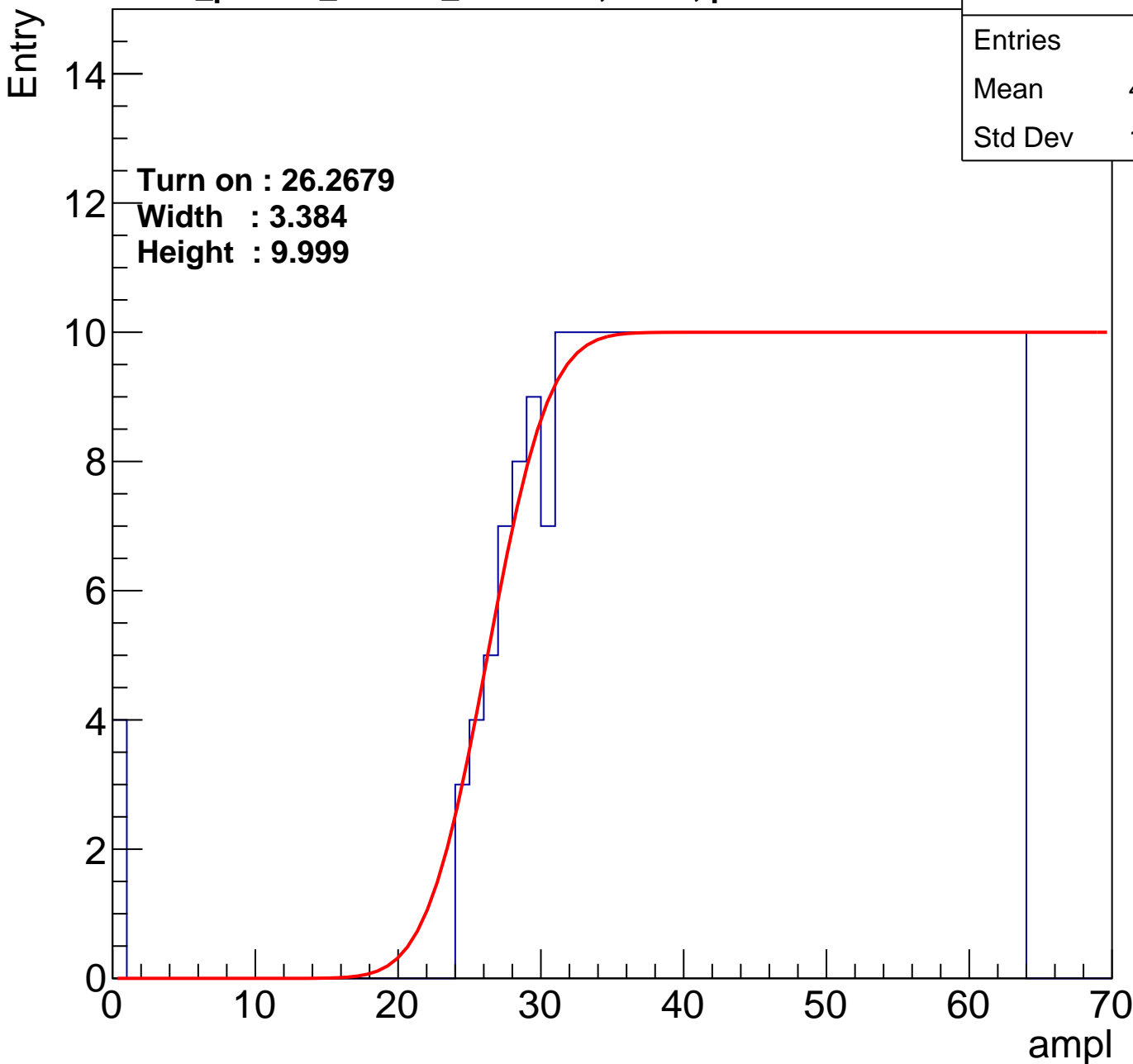
calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.2679

Width : 3.384

Height : 9.999



B1L100S, U23-ch45

calib_packv5_042523_0143.root, FC#4, port A2

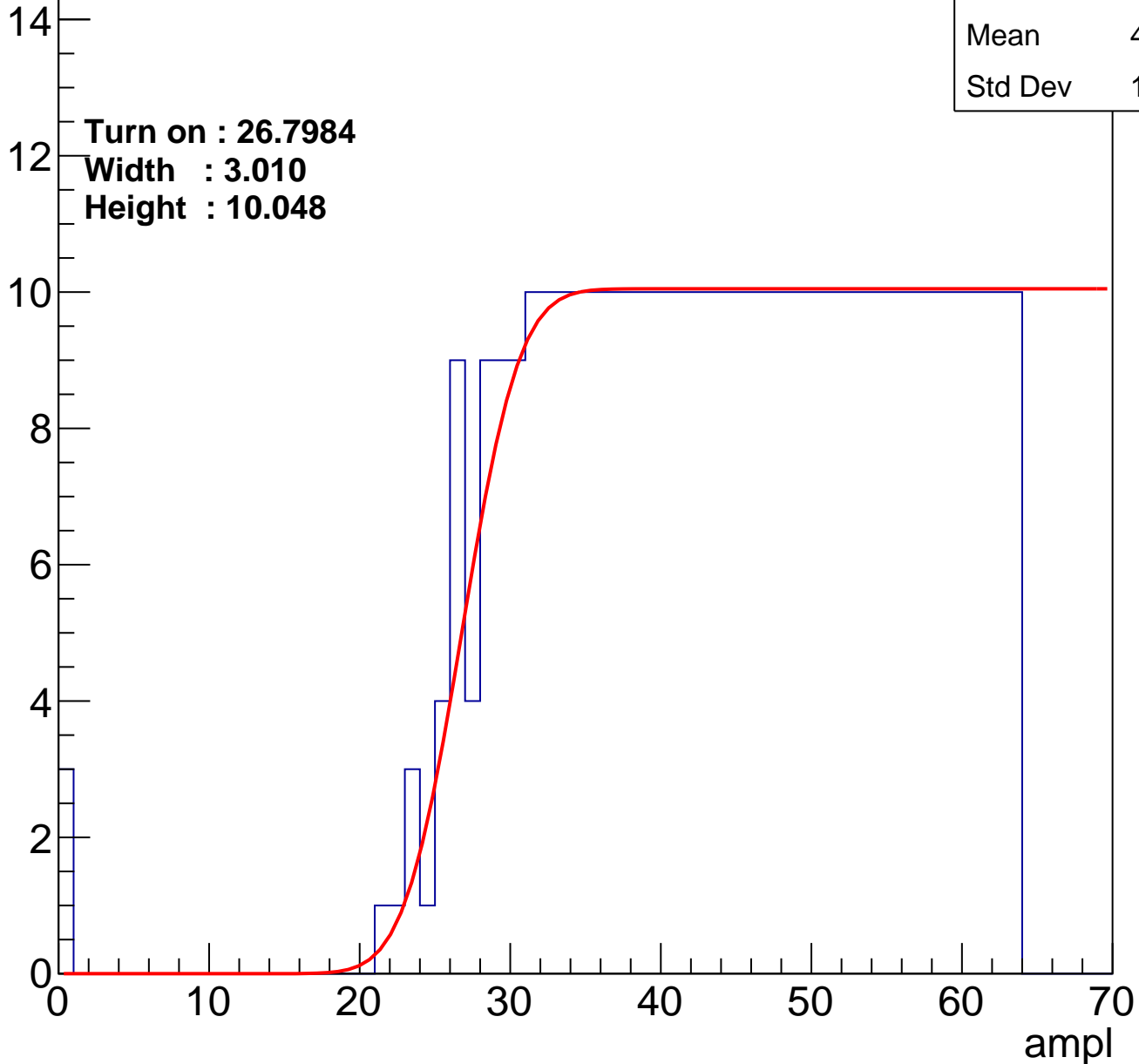
Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.7984

Width : 3.010

Height : 10.048

Entry



B1L100S, U23-ch46

calib_packv5_042523_0143.root, FC#4, port A2

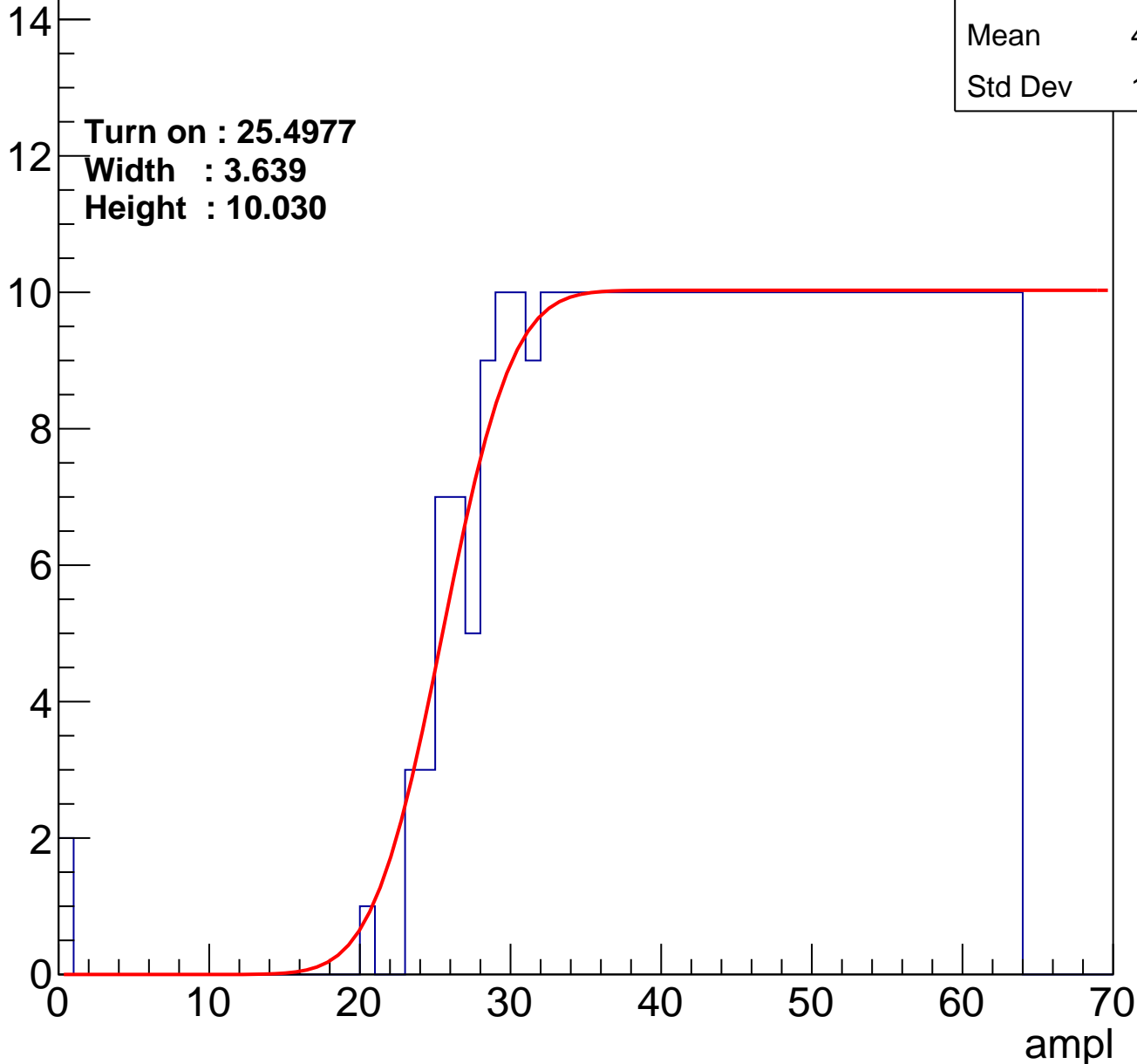
Entries	386
Mean	43.97
Std Dev	11.66

Turn on : 25.4977

Width : 3.639

Height : 10.030

Entry



B1L100S, U23-ch47

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.5853

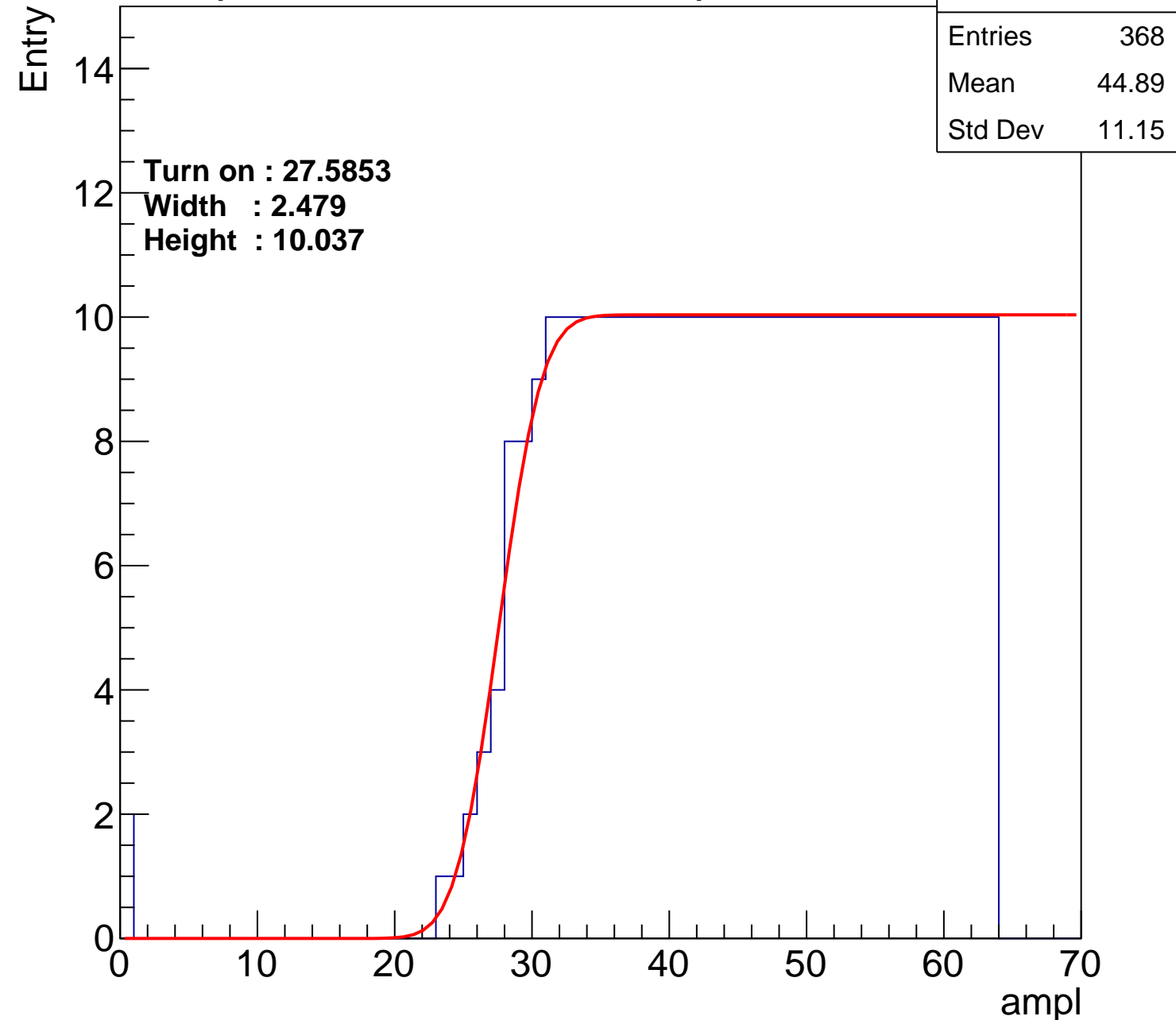
Width : 2.479

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch48

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.41
Std Dev	10.97

Turn on : 29.0476

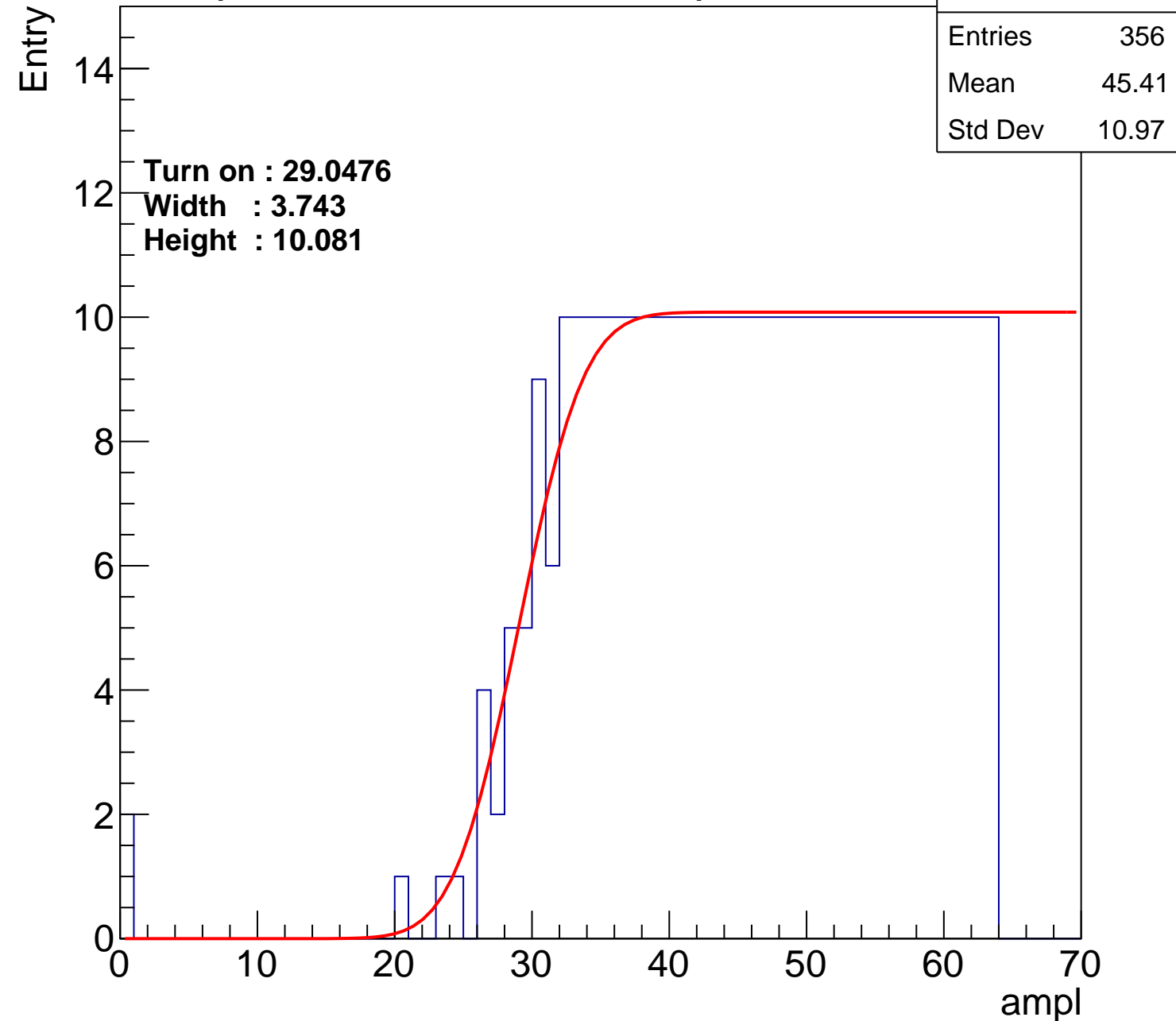
Width : 3.743

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch49

calib_packv5_042523_0143.root, FC#4, port A2

Entries	396
Mean	43.52
Std Dev	11.8

Turn on : 25.0266

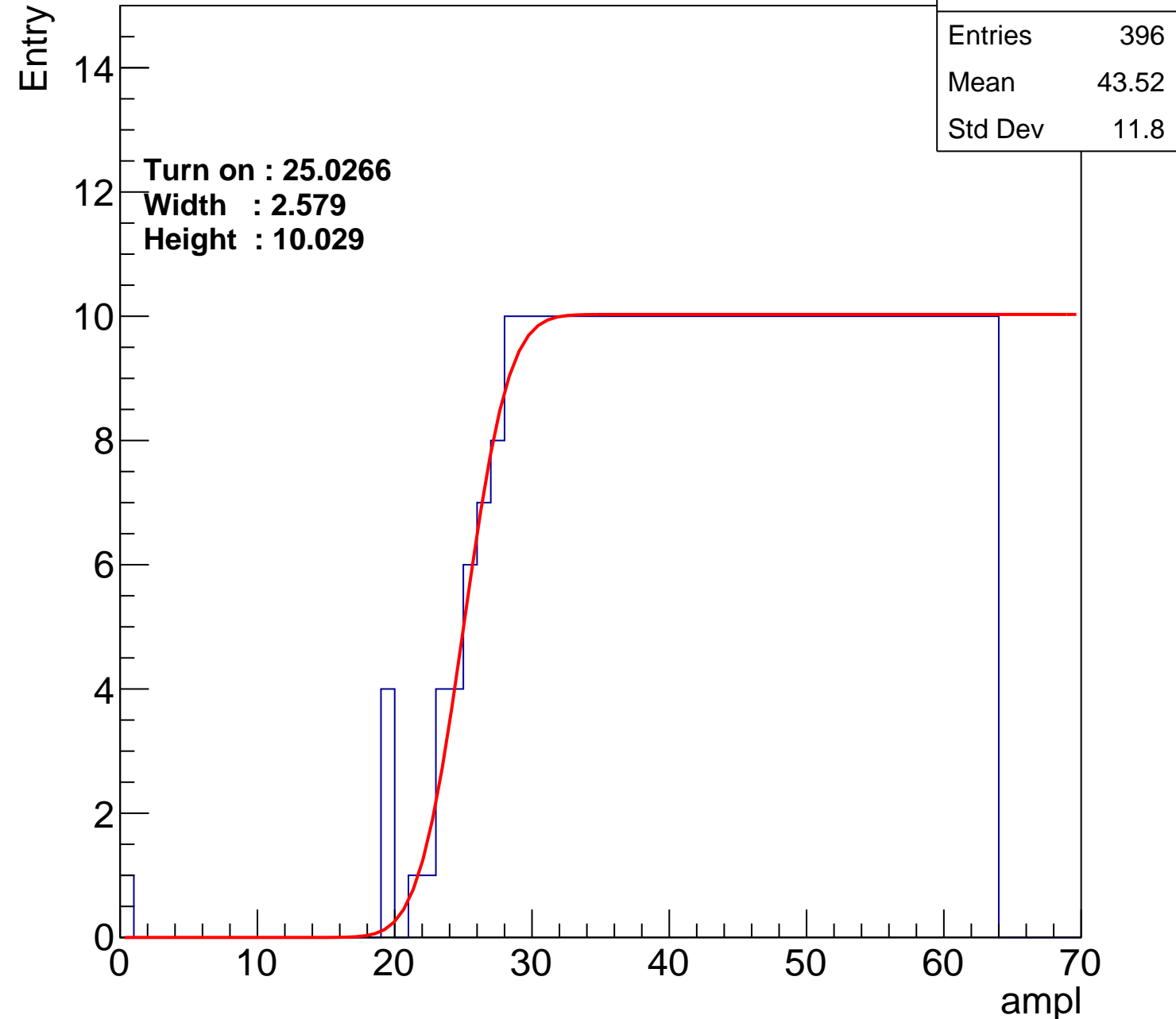
Width : 2.579

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch50

calib_packv5_042523_0143.root, FC#4, port A2

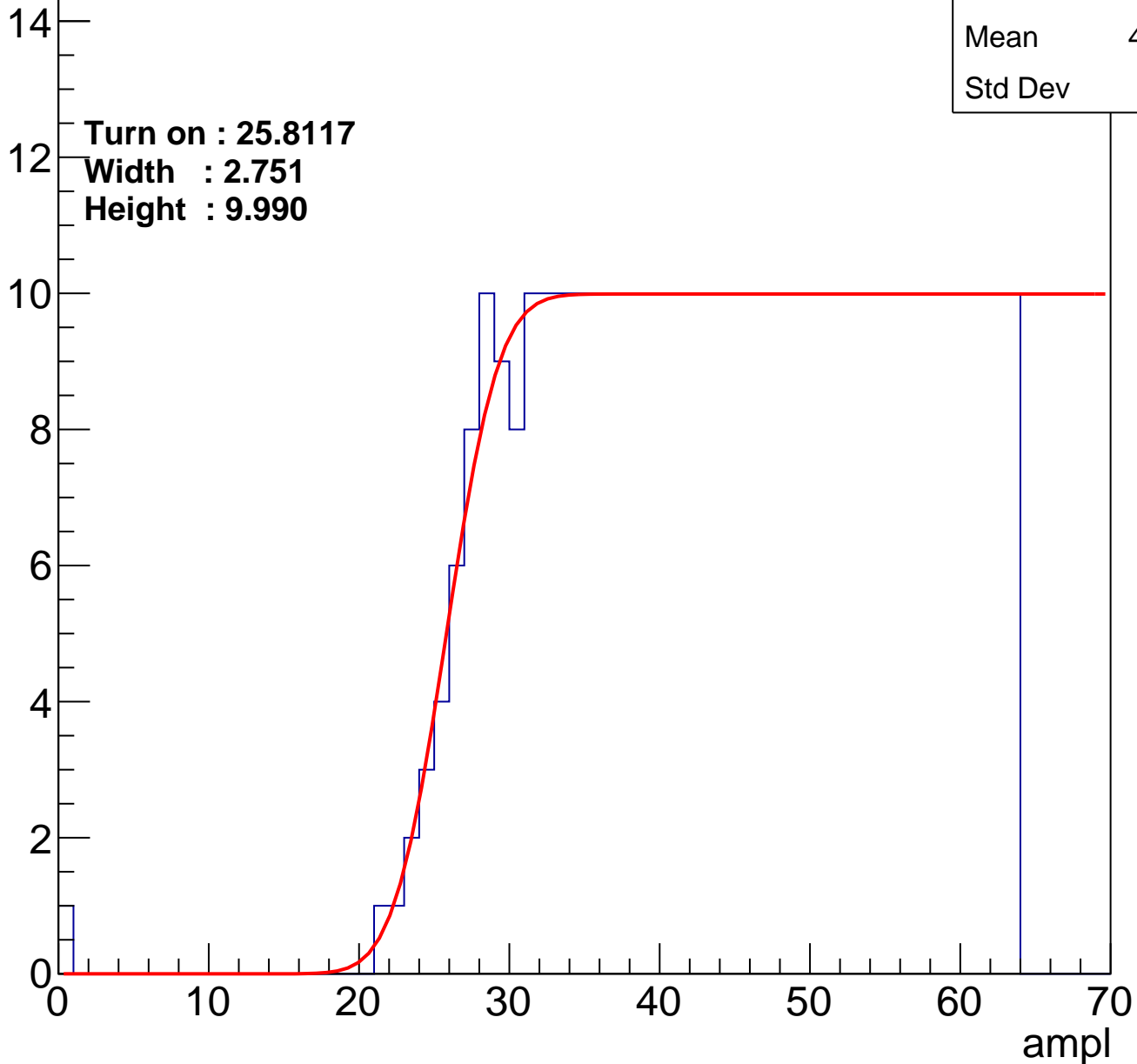
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.8117

Width : 2.751

Height : 9.990

Entry



B1L100S, U23-ch51

calib_packv5_042523_0143.root, FC#4, port A2

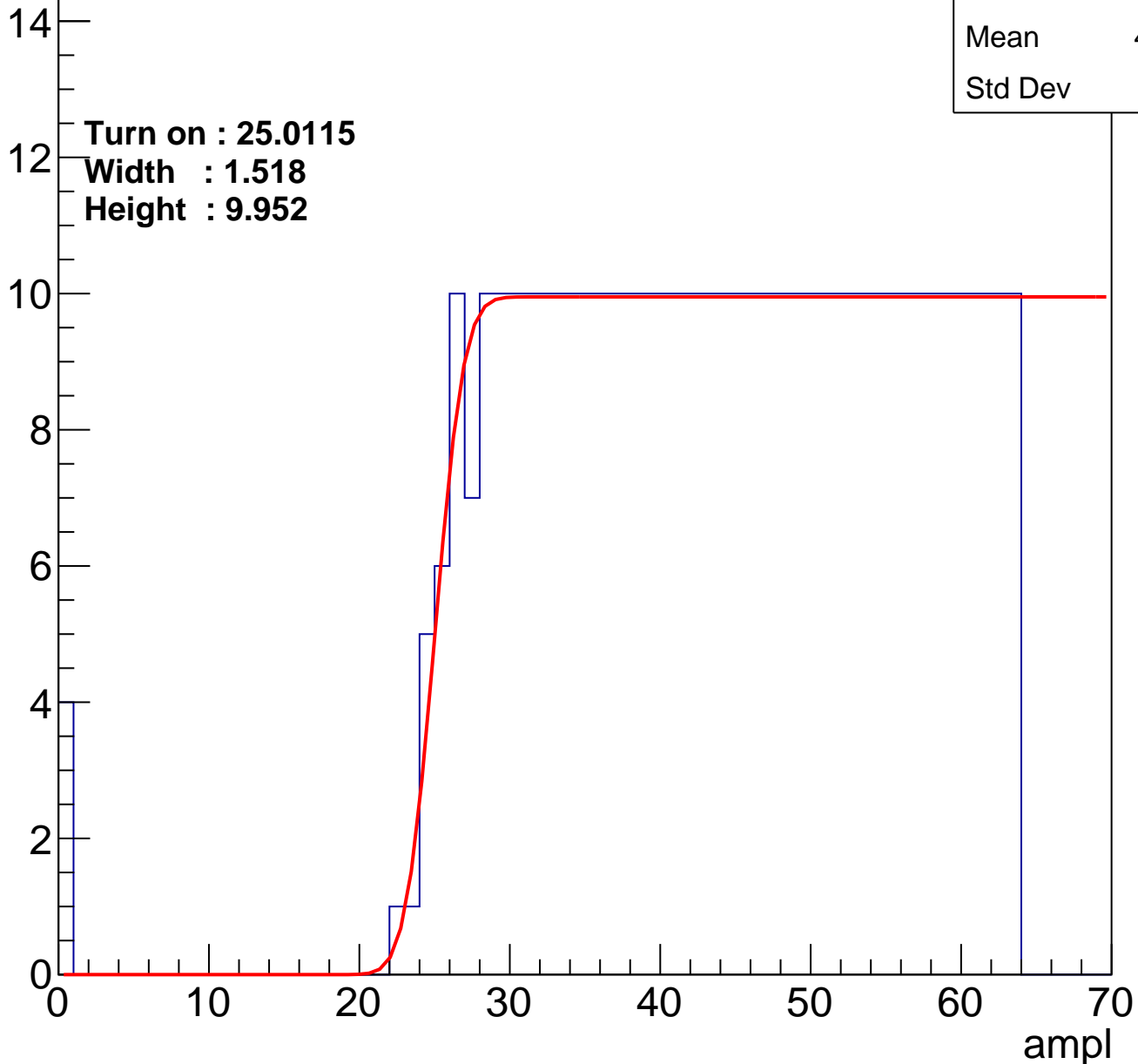
Entries	394
Mean	43.51
Std Dev	12.1

Turn on : 25.0115

Width : 1.518

Height : 9.952

Entry



B1L100S, U23-ch52

calib_packv5_042523_0143.root, FC#4, port A2

Entries	357
Mean	45.17
Std Dev	11.47

Turn on : 28.8536

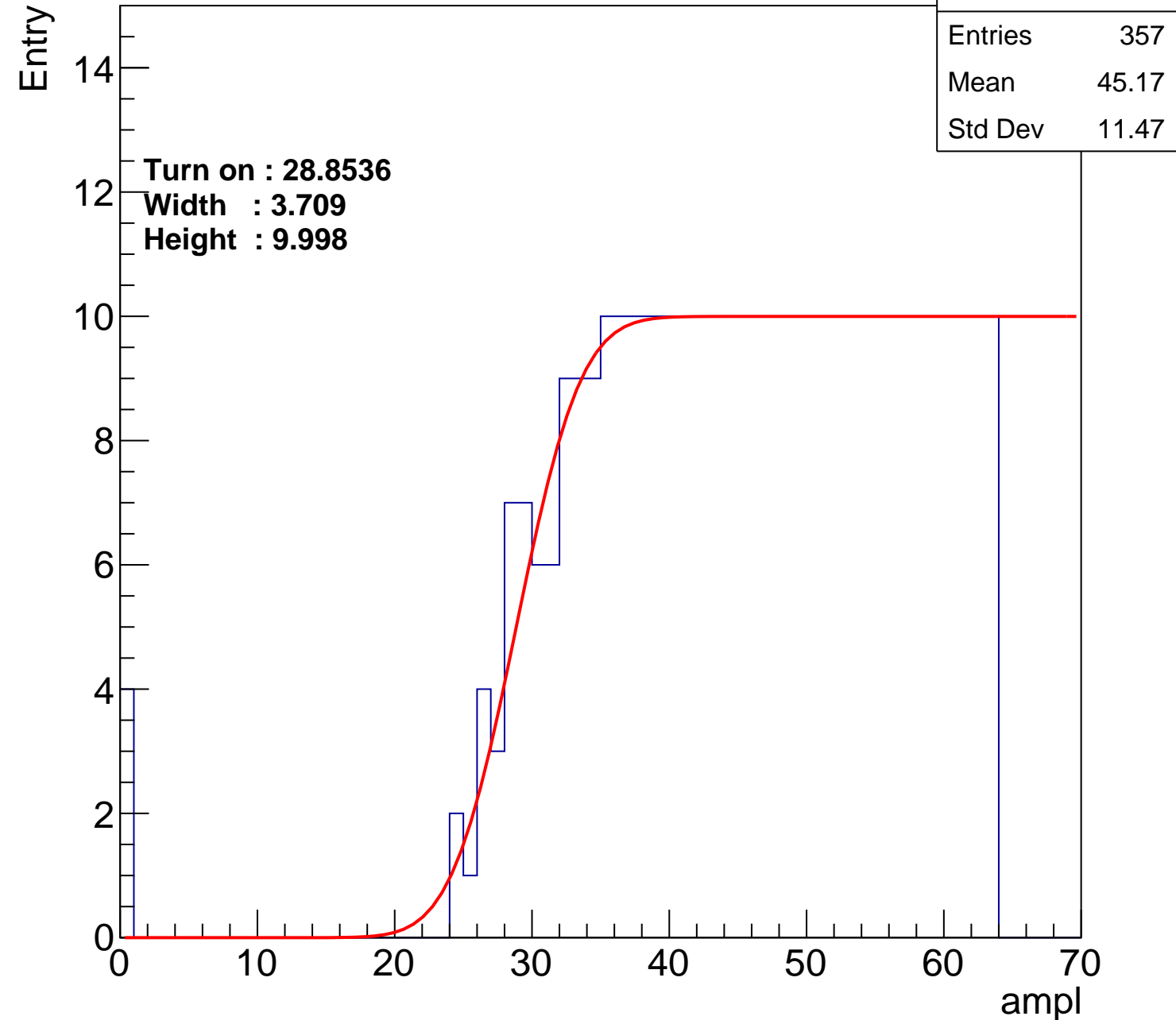
Width : 3.709

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch53

calib_packv5_042523_0143.root, FC#4, port A2

Entries	361
Mean	45.23
Std Dev	10.87

Turn on : 27.8971

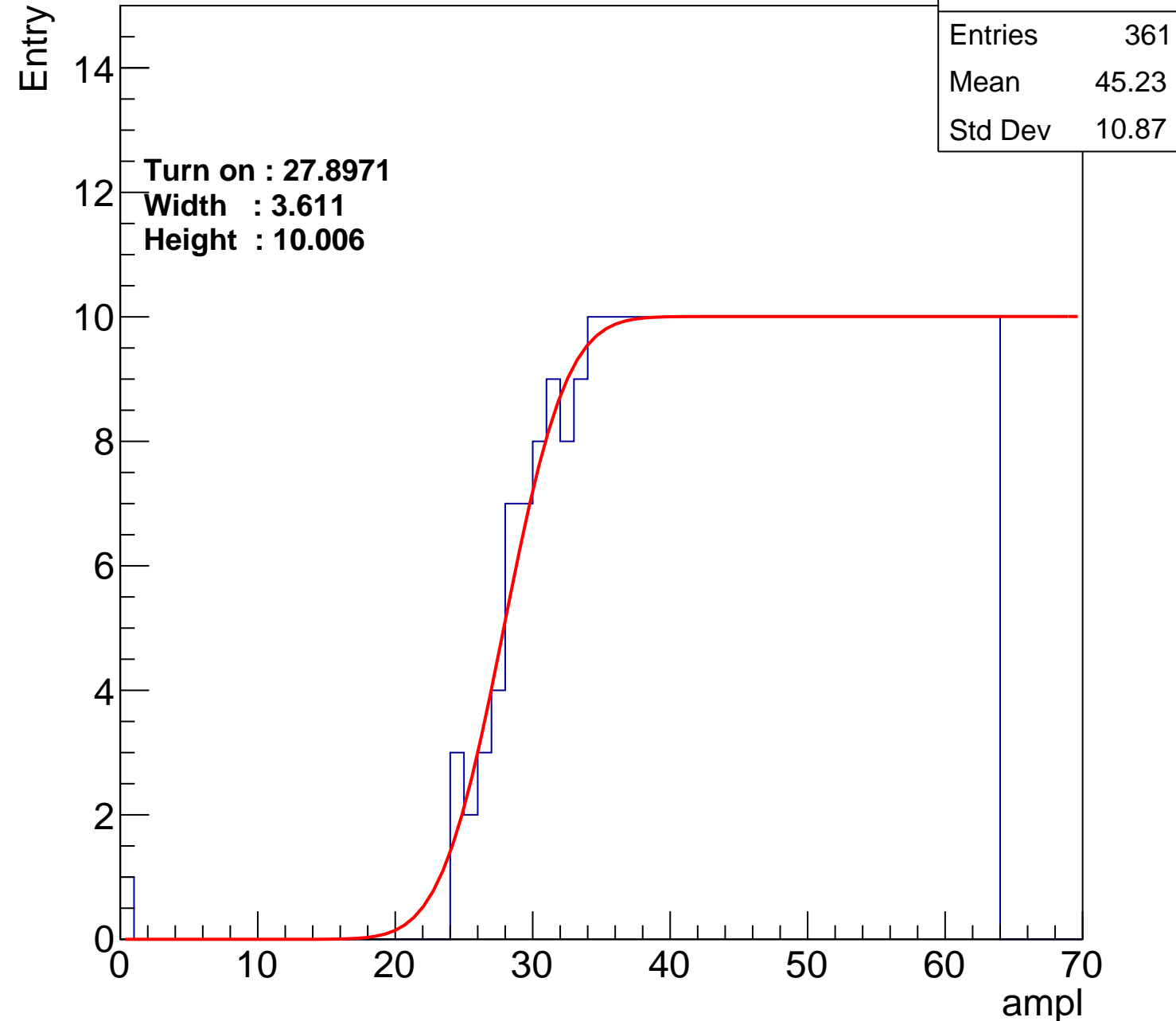
Width : 3.611

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch54

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.65
Std Dev	11.26

Turn on : 27.0663

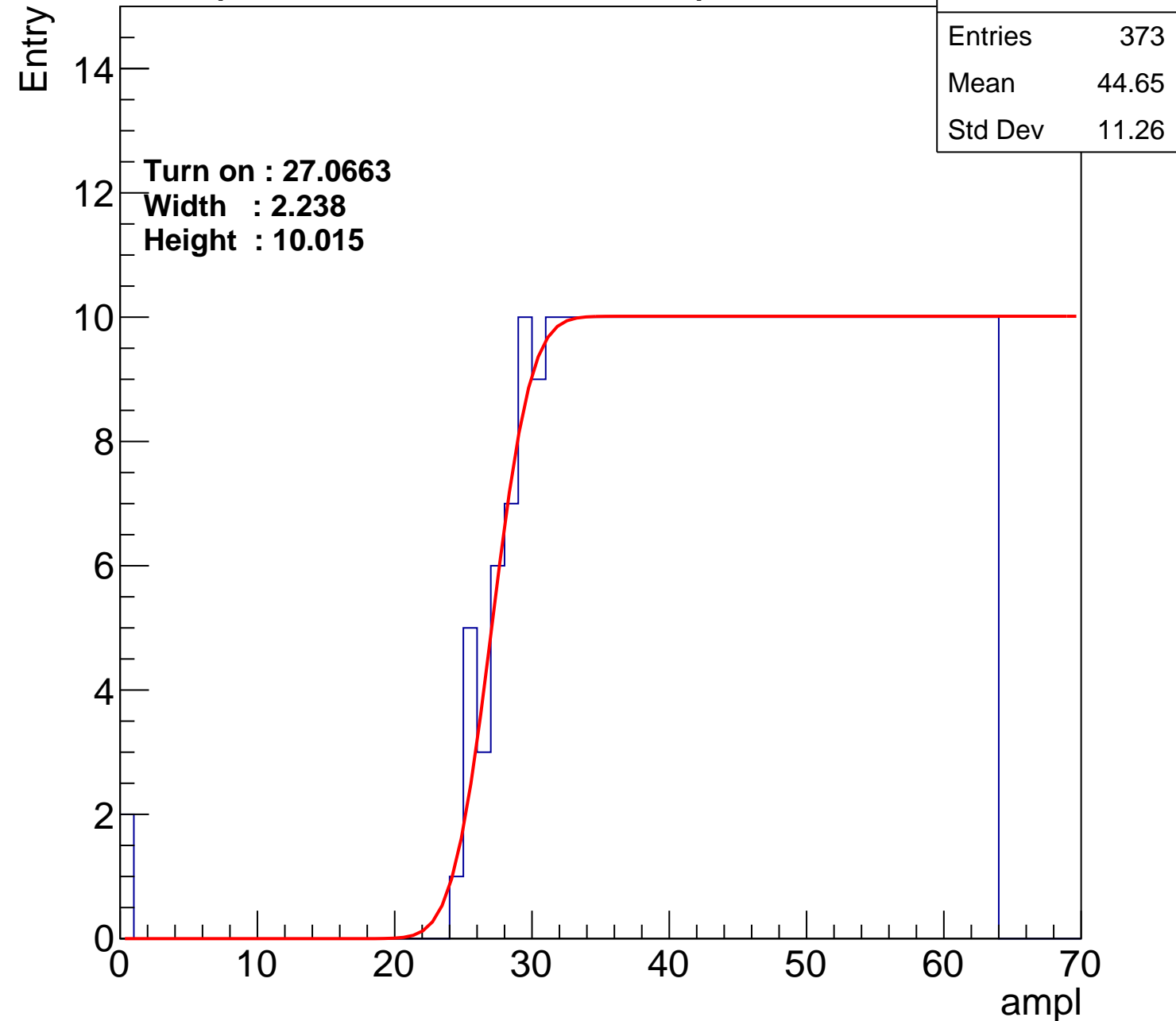
Width : 2.238

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch55

calib_packv5_042523_0143.root, FC#4, port A2

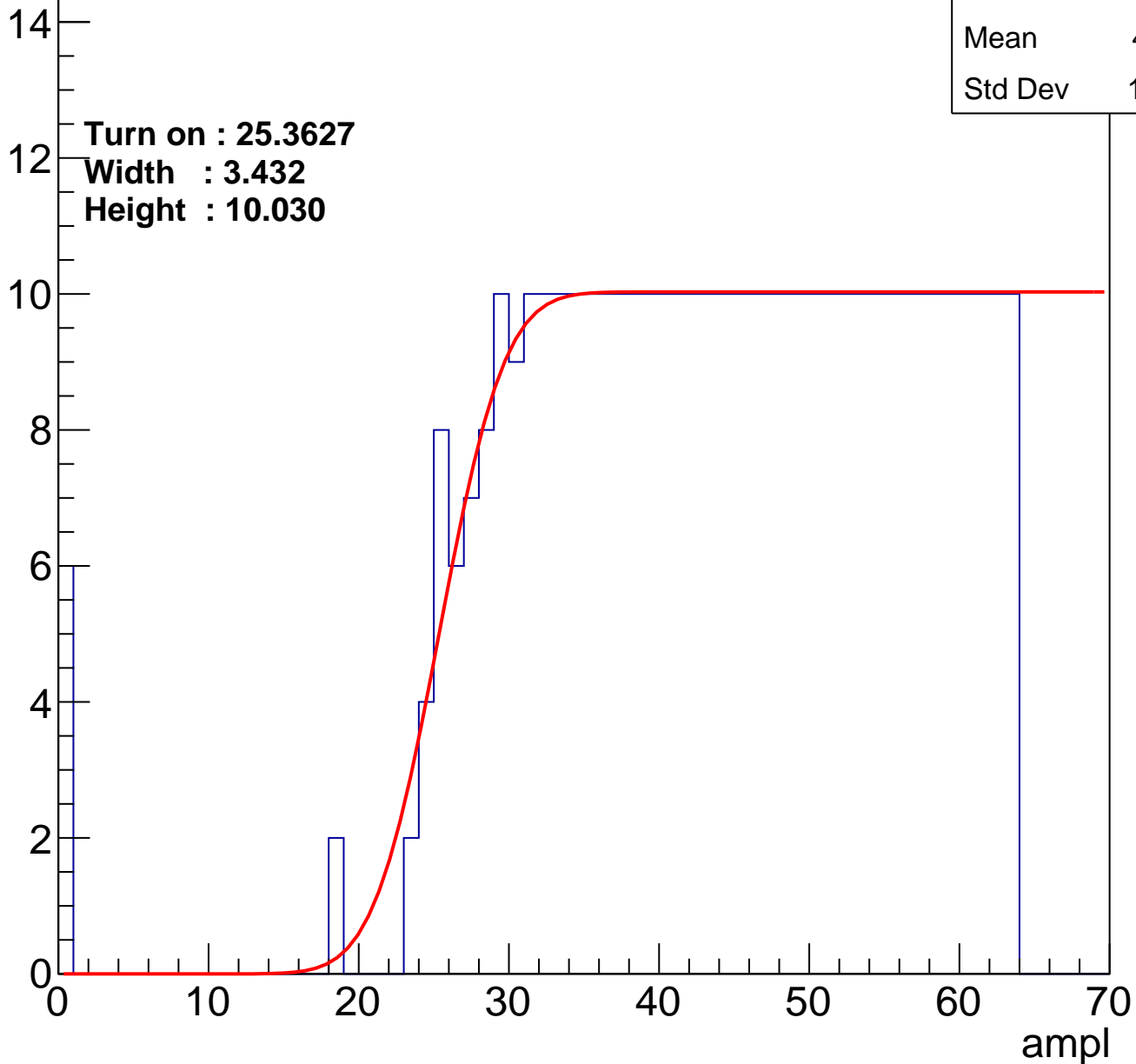
Entries	392
Mean	43.41
Std Dev	12.49

Turn on : 25.3627

Width : 3.432

Height : 10.030

Entry



B1L100S, U23-ch56

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.95
Std Dev	10.95

Turn on : 26.9307

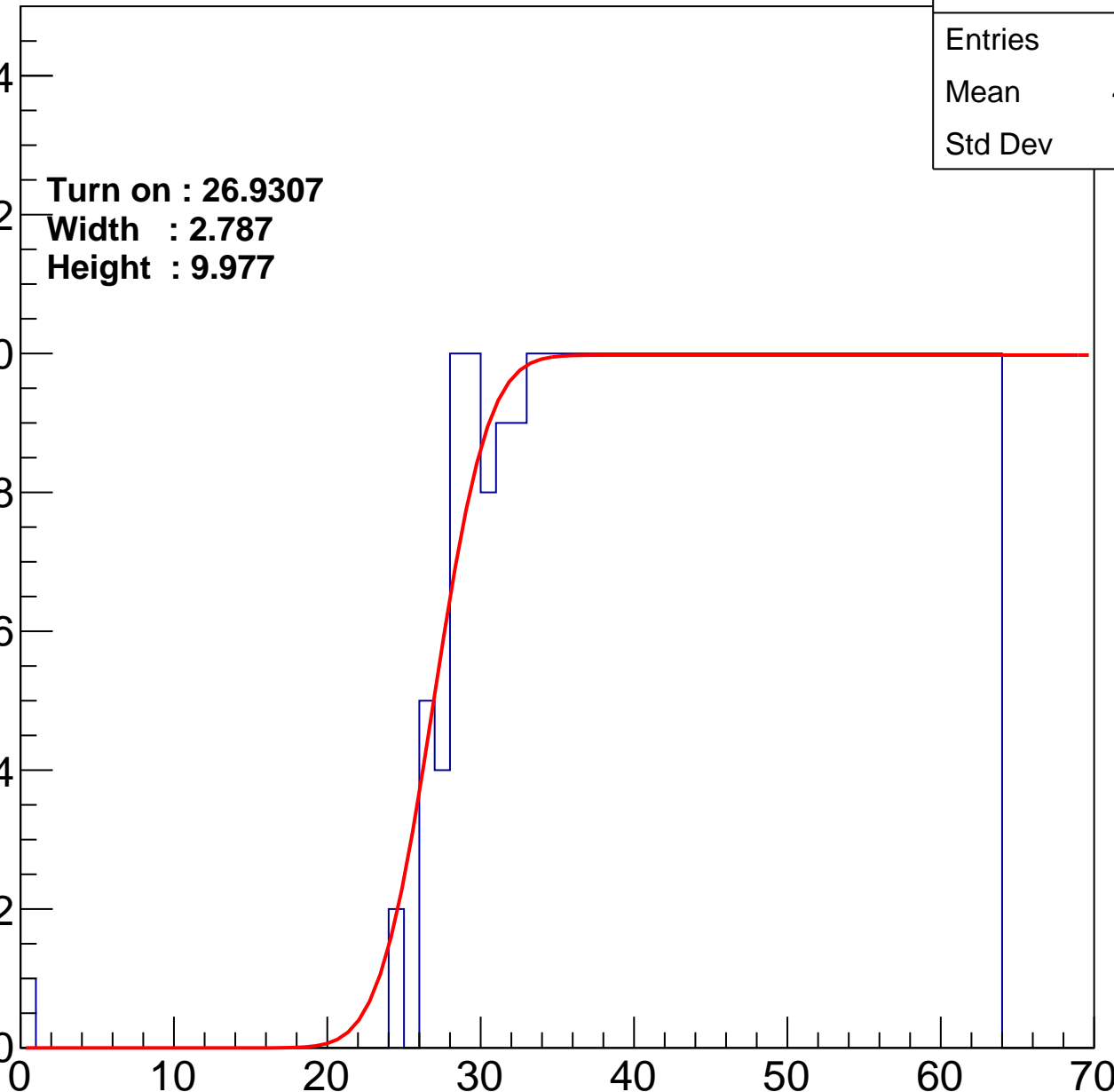
Width : 2.787

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch57

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.72
Std Dev	11.73

Turn on : 28.1256

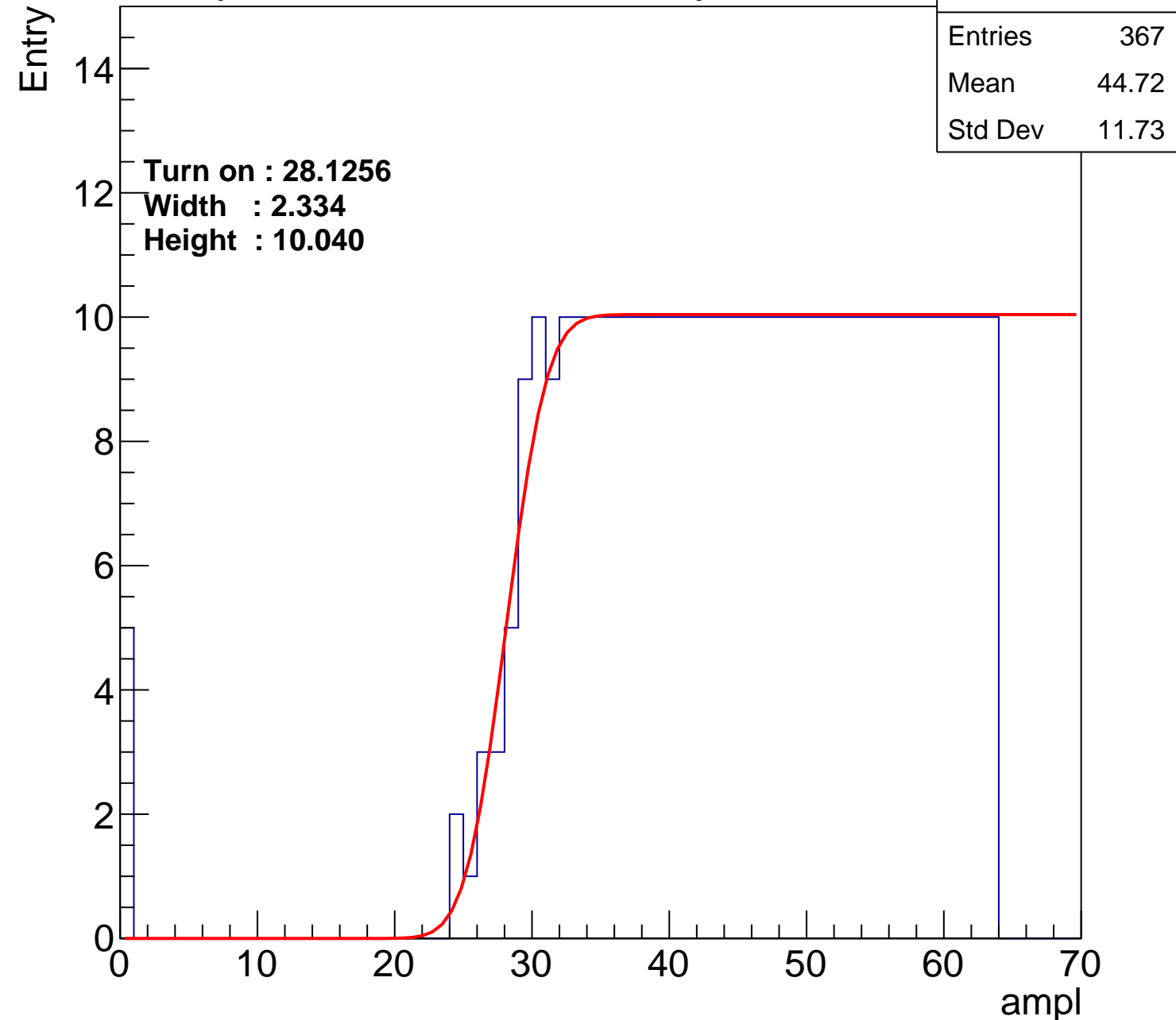
Width : 2.334

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch58

calib_packv5_042523_0143.root, FC#4, port A2

Entries	378
Mean	44.29
Std Dev	11.65

Turn on : 26.5540

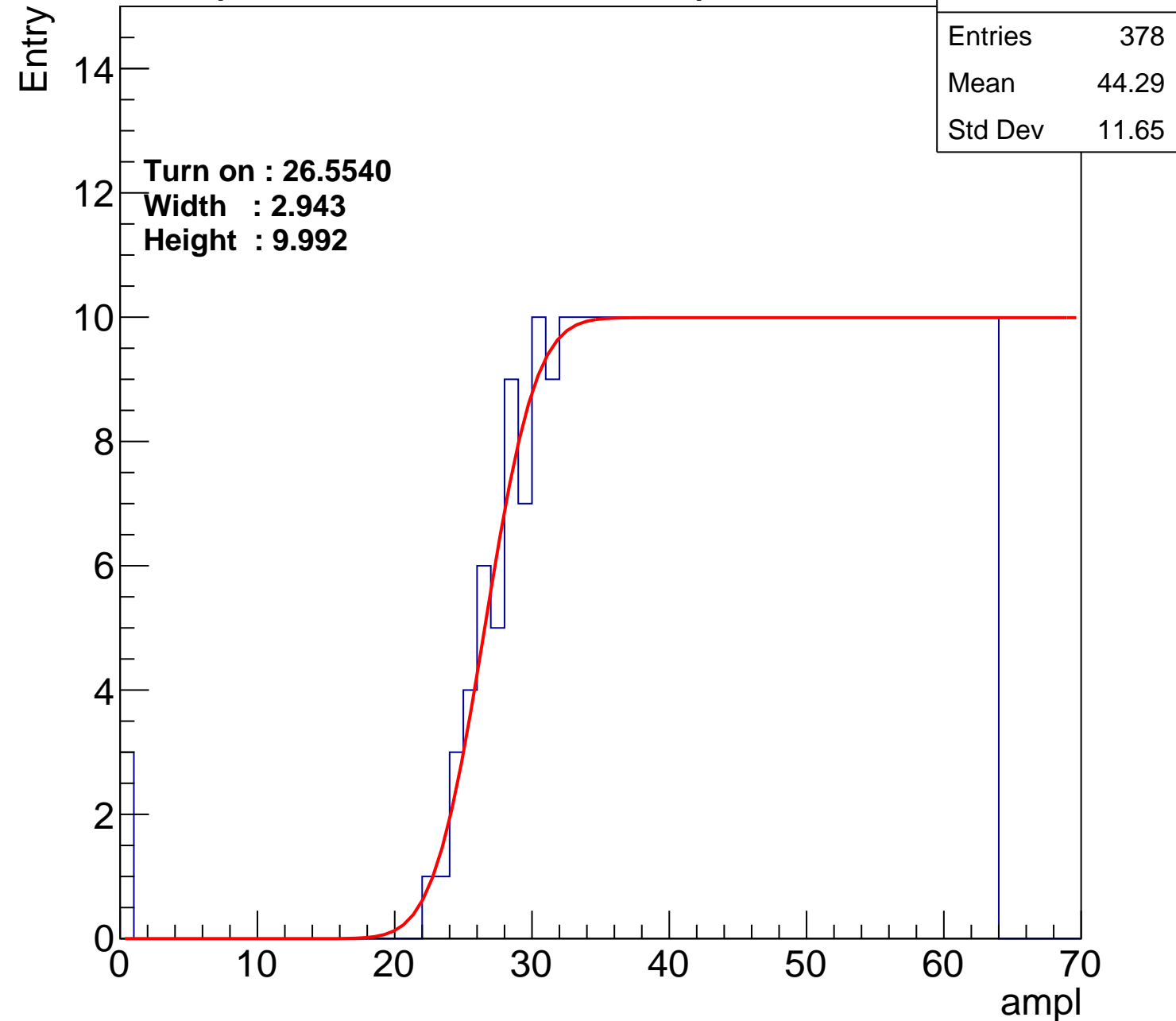
Width : 2.943

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch59

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.37
Std Dev	11.64

Turn on : 27.0732

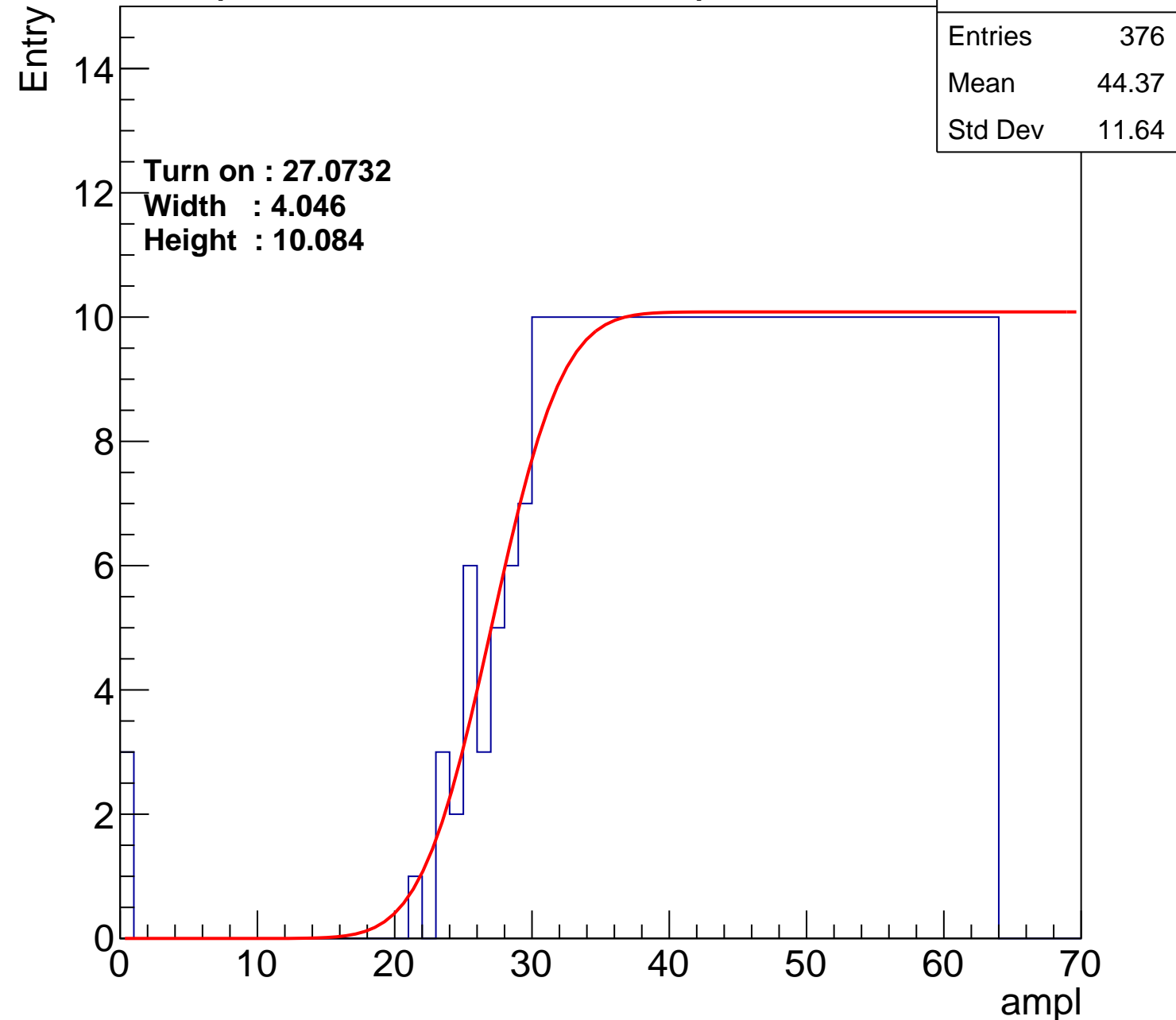
Width : 4.046

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch60

calib_packv5_042523_0143.root, FC#4, port A2

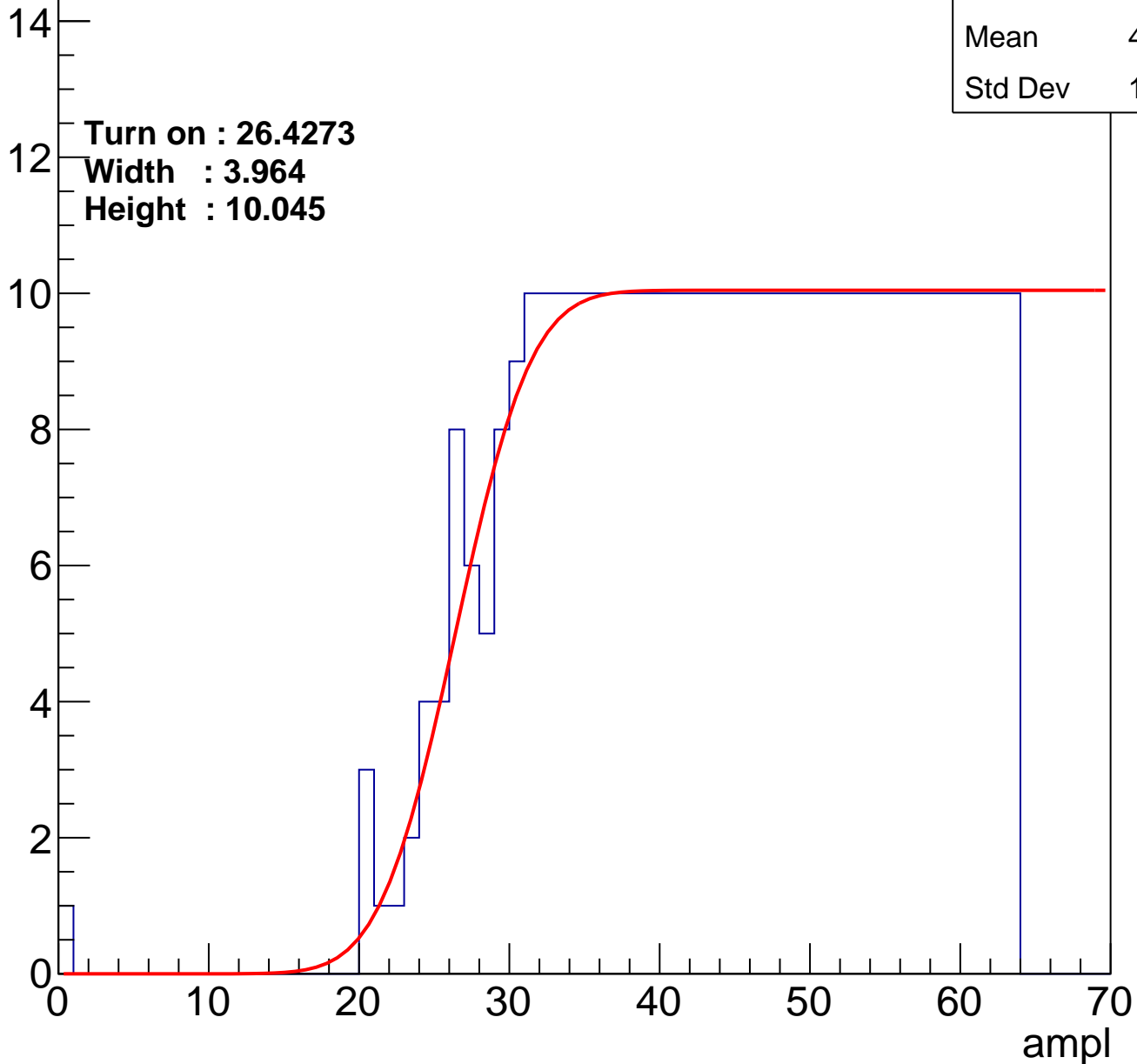
Entries	382
Mean	44.15
Std Dev	11.52

Turn on : 26.4273

Width : 3.964

Height : 10.045

Entry



B1L100S, U23-ch61

calib_packv5_042523_0143.root, FC#4, port A2

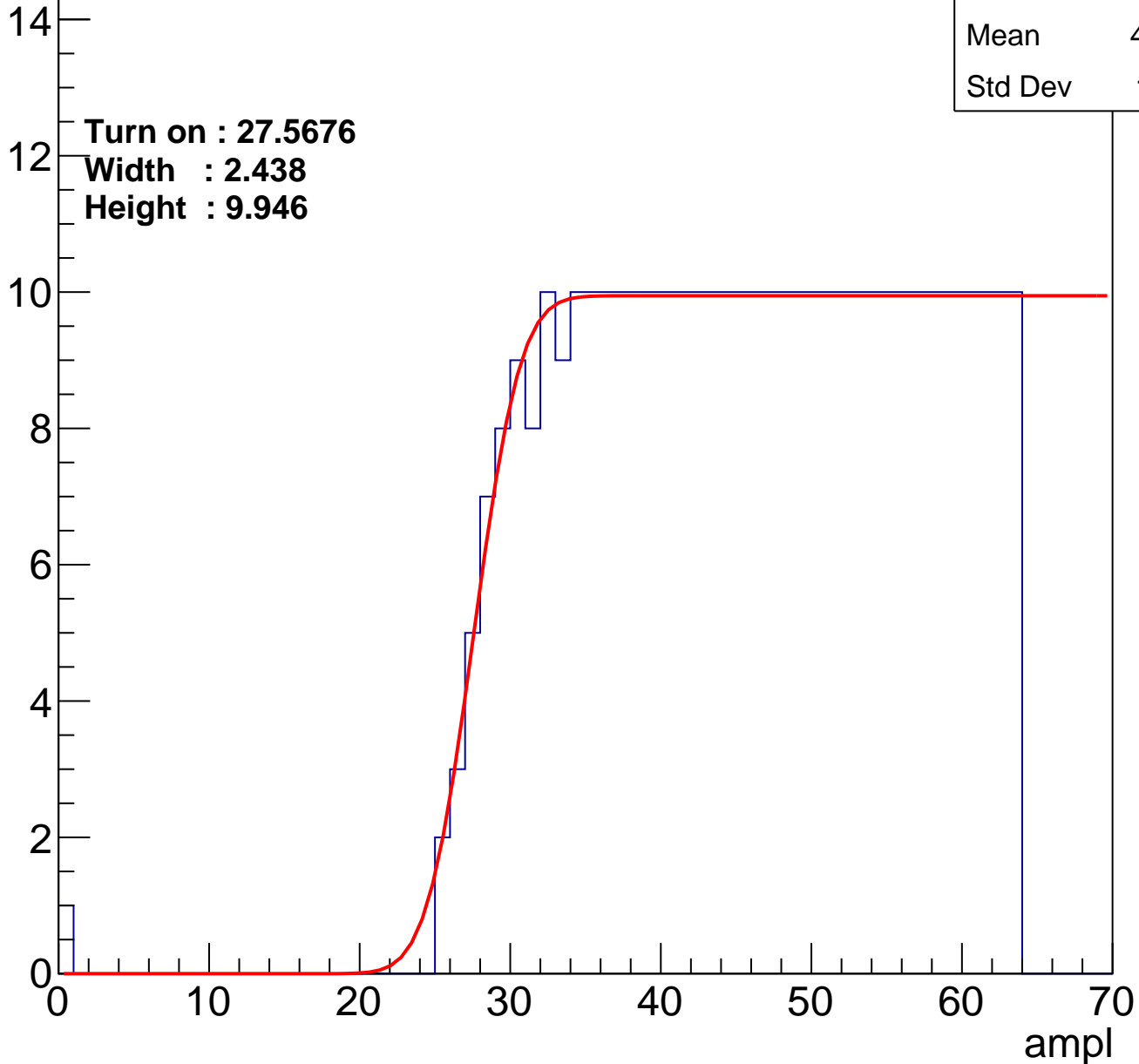
Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 27.5676

Width : 2.438

Height : 9.946

Entry



B1L100S, U23-ch62

calib_packv5_042523_0143.root, FC#4, port A2

Entries	379
Mean	44.44
Std Dev	11.21

Turn on : 25.9671

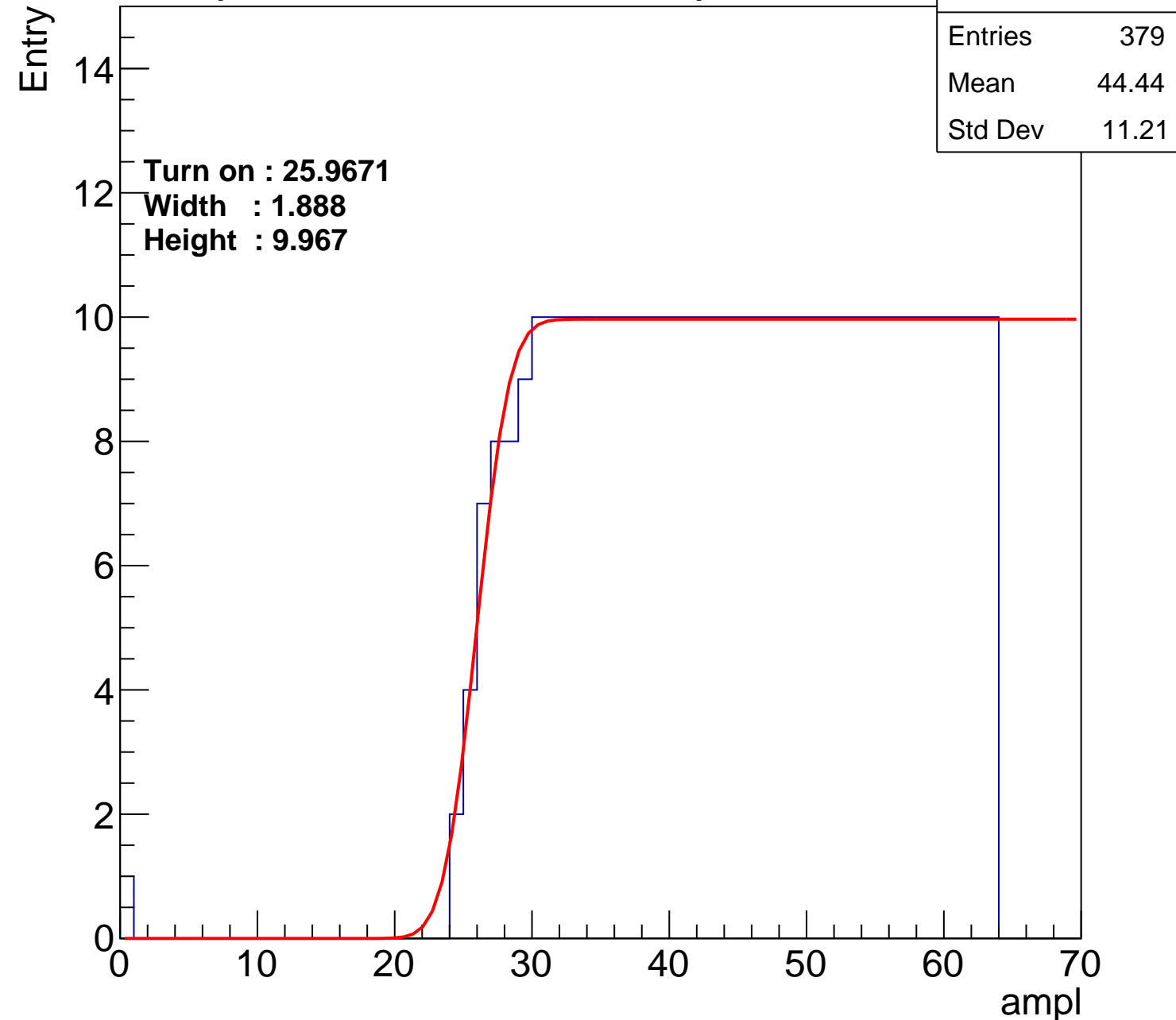
Width : 1.888

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch63

calib_packv5_042523_0143.root, FC#4, port A2

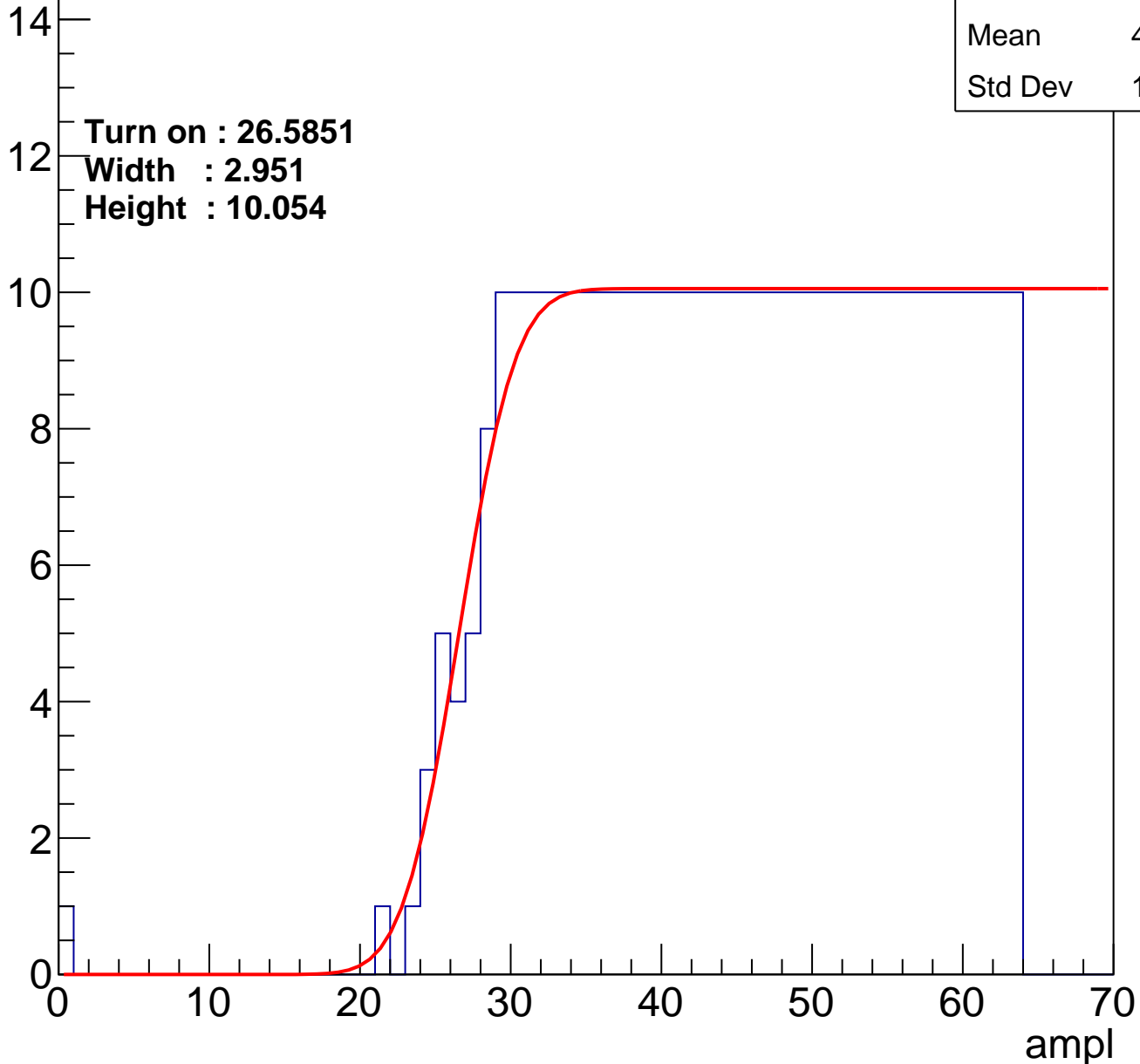
Entries	378
Mean	44.46
Std Dev	11.24

Turn on : 26.5851

Width : 2.951

Height : 10.054

Entry



B1L100S, U23-ch64

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.5742

Width : 1.698

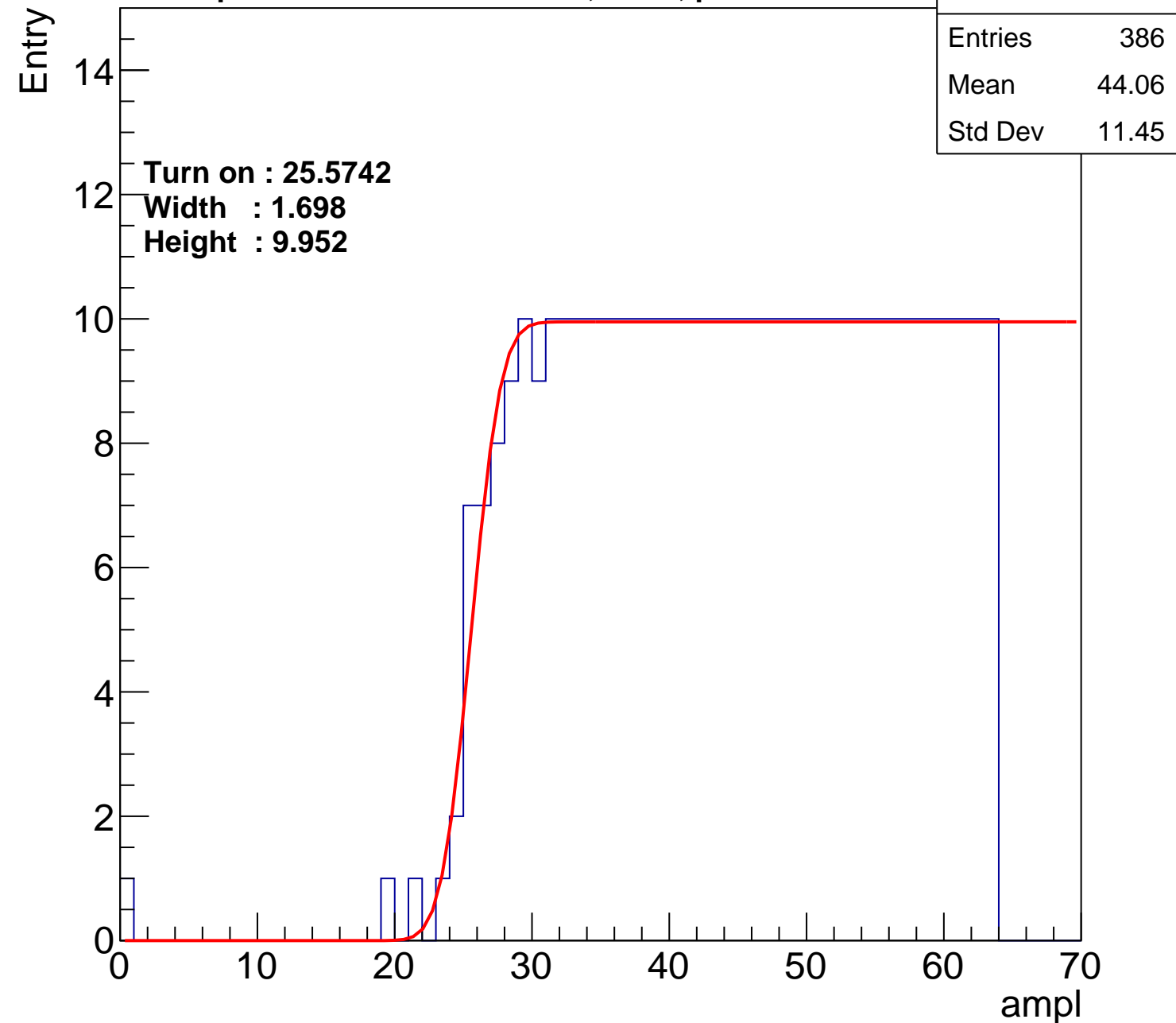
Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U23-ch65

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	45.01
Std Dev	11.12

Turn on : 27.9753

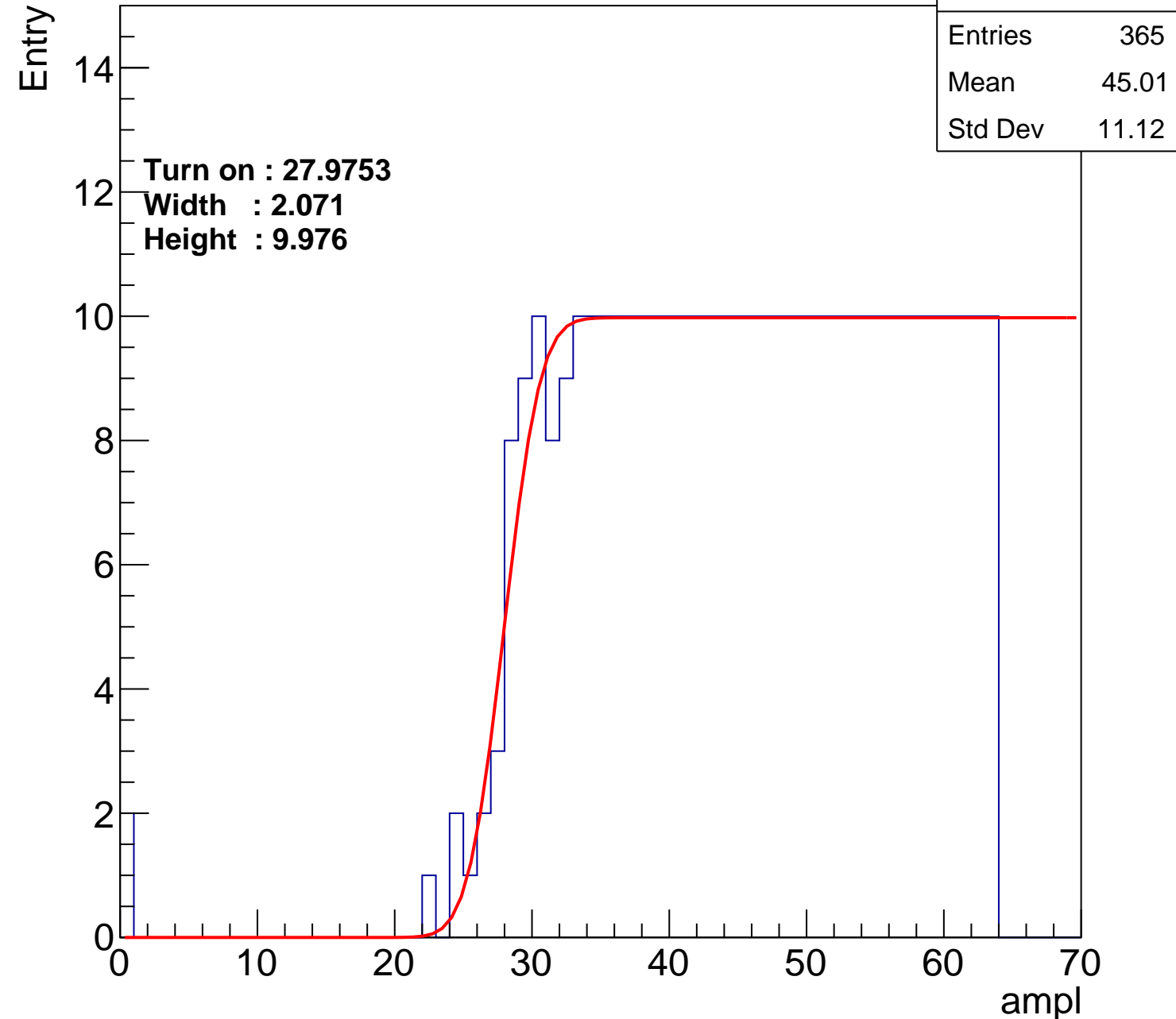
Width : 2.071

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch66

calib_packv5_042523_0143.root, FC#4, port A2

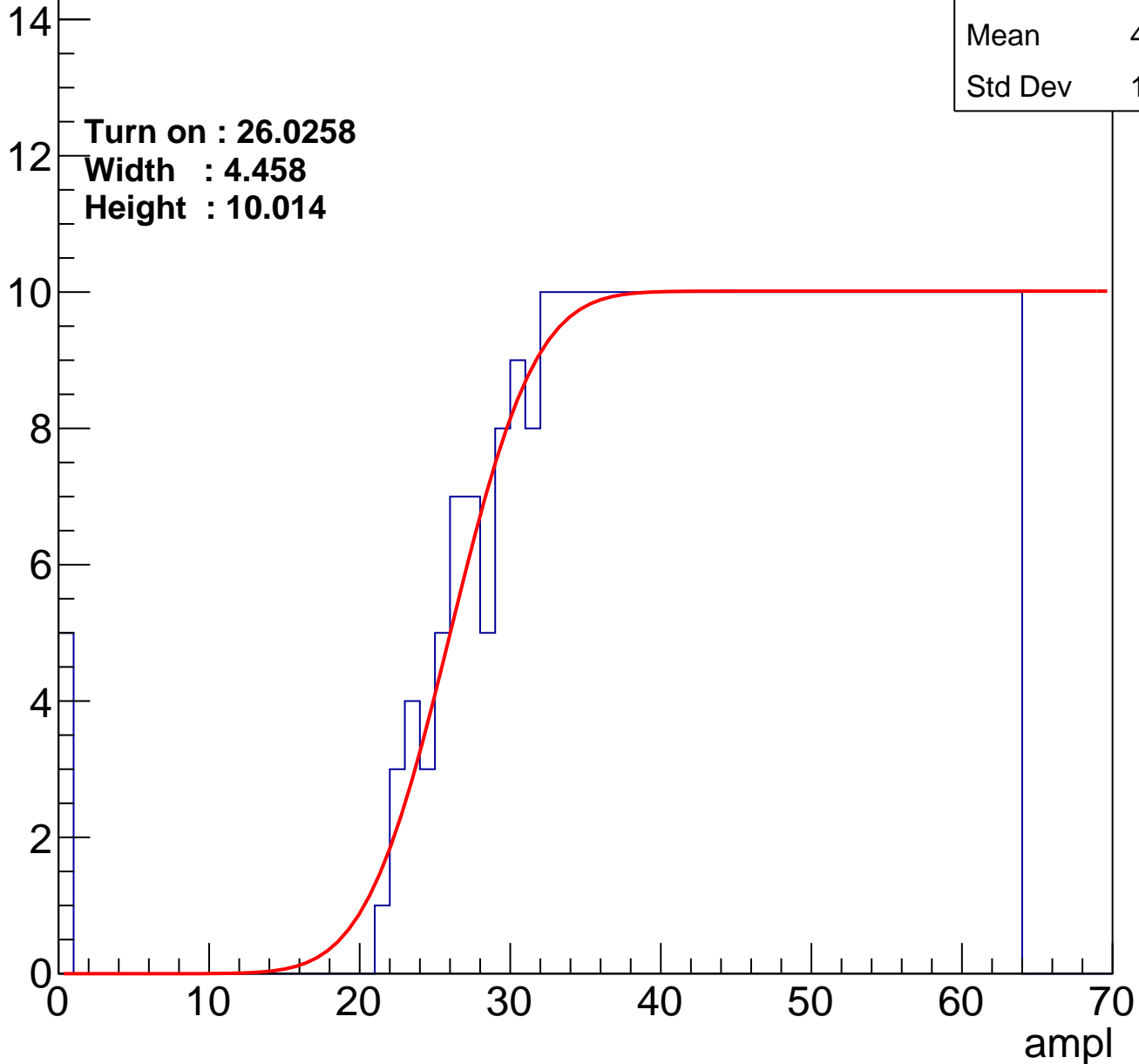
Entries	385
Mean	43.73
Std Dev	12.28

Turn on : 26.0258

Width : 4.458

Height : 10.014

Entry



B1L100S, U23-ch67

calib_packv5_042523_0143.root, FC#4, port A2

Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.7175

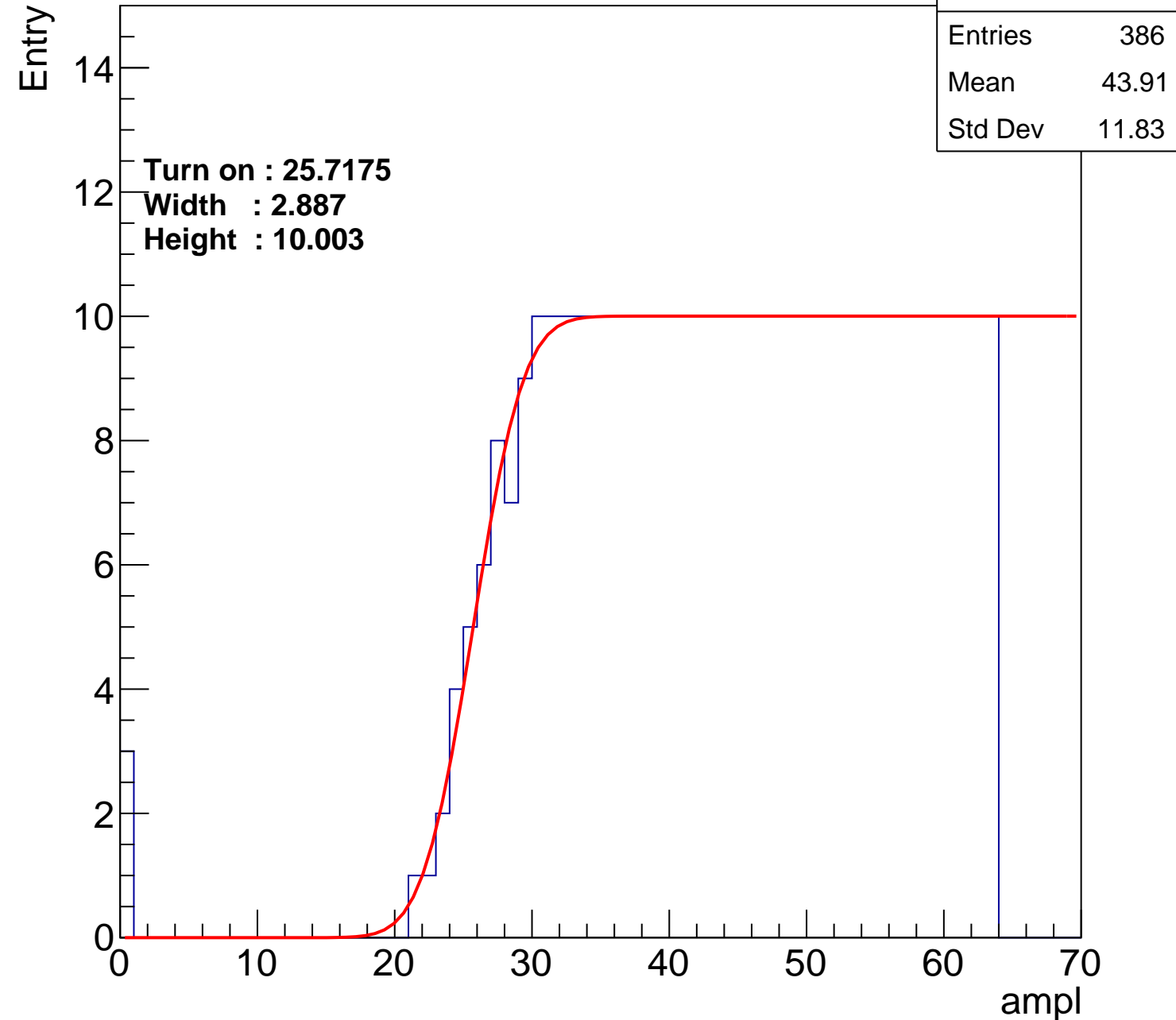
Width : 2.887

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch68

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.73
Std Dev	11.29

Turn on : 27.3161

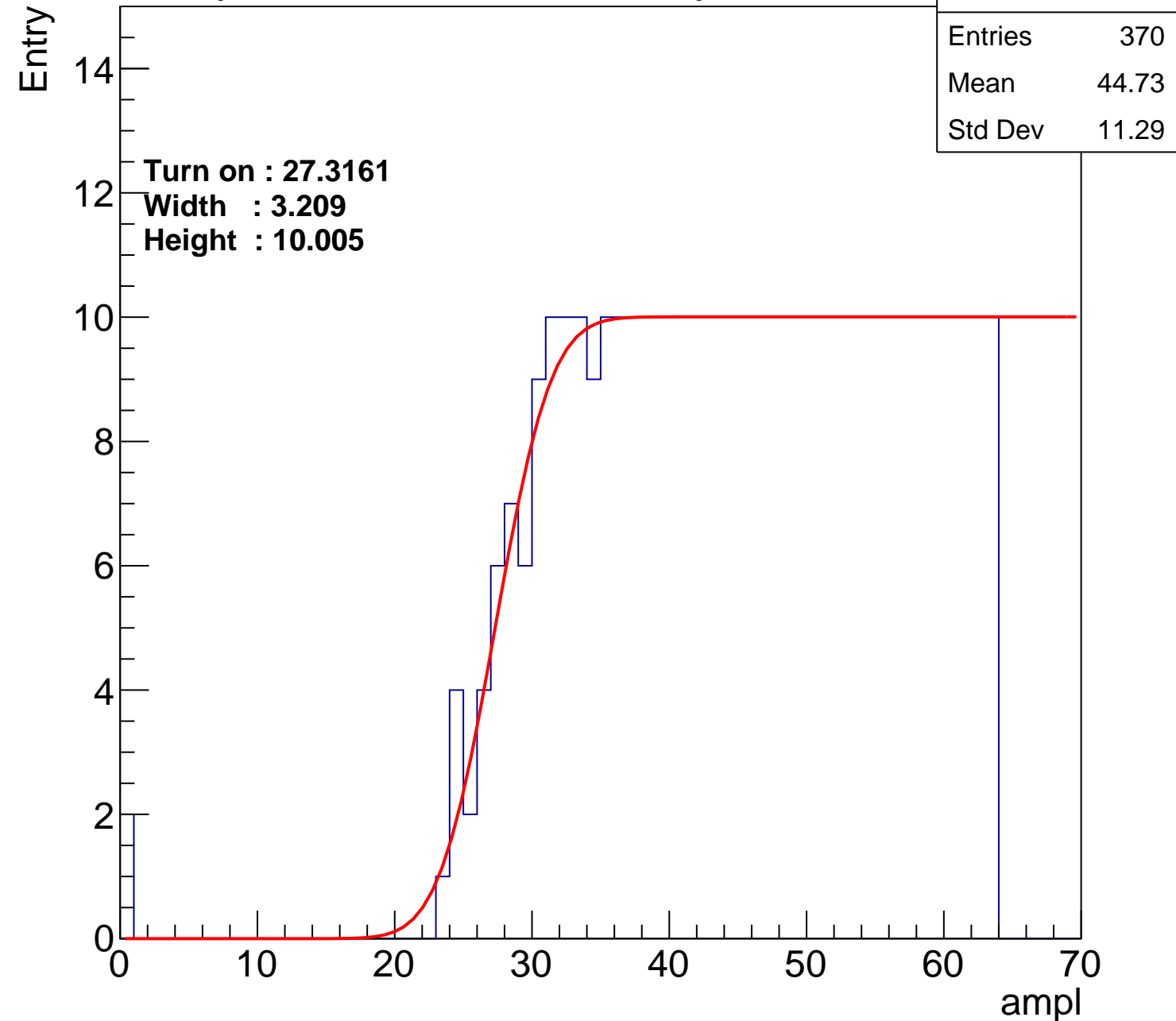
Width : 3.209

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch69

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 27.6798

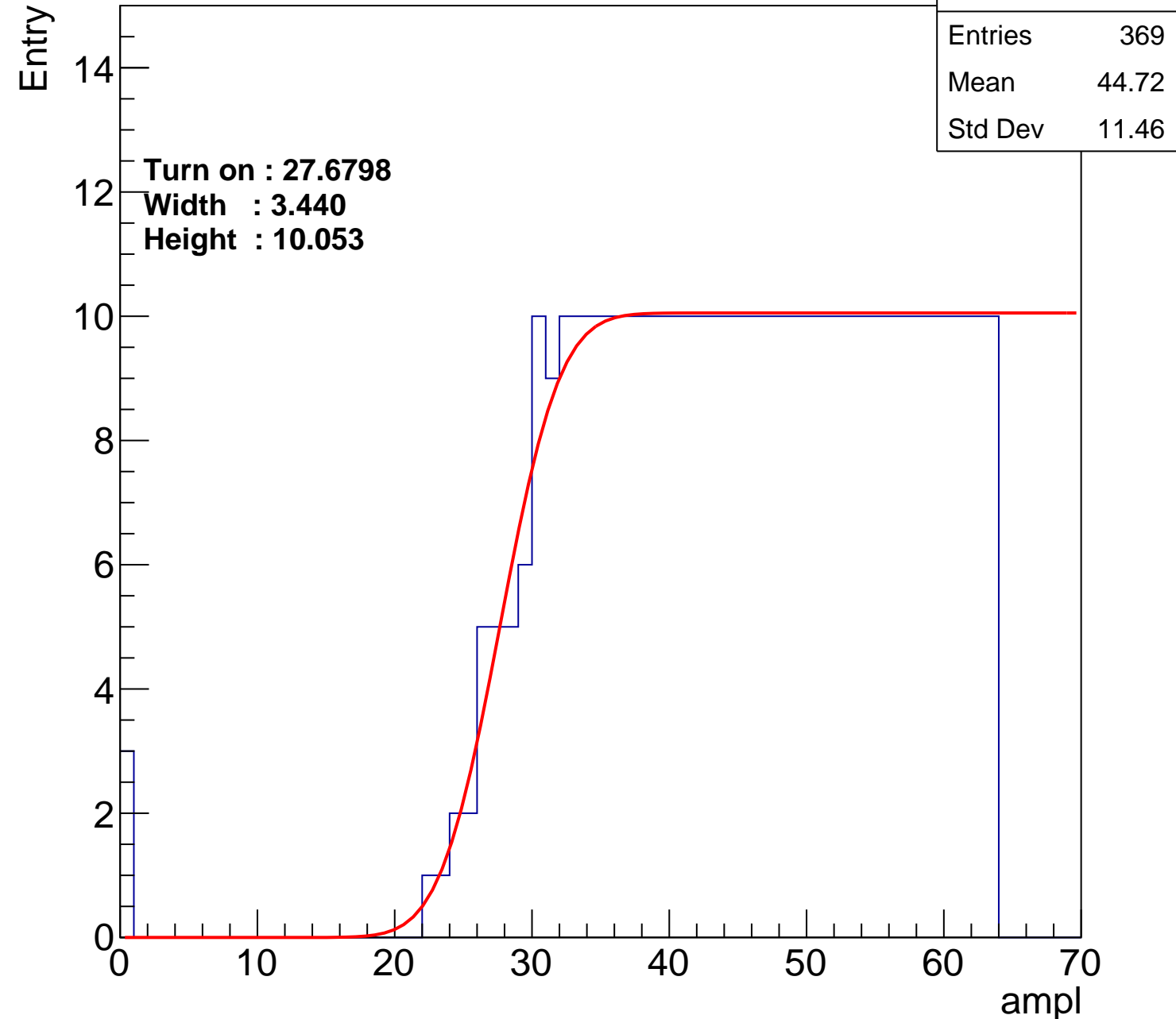
Width : 3.440

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch70

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.11
Std Dev	11.6

Turn on : 25.7422

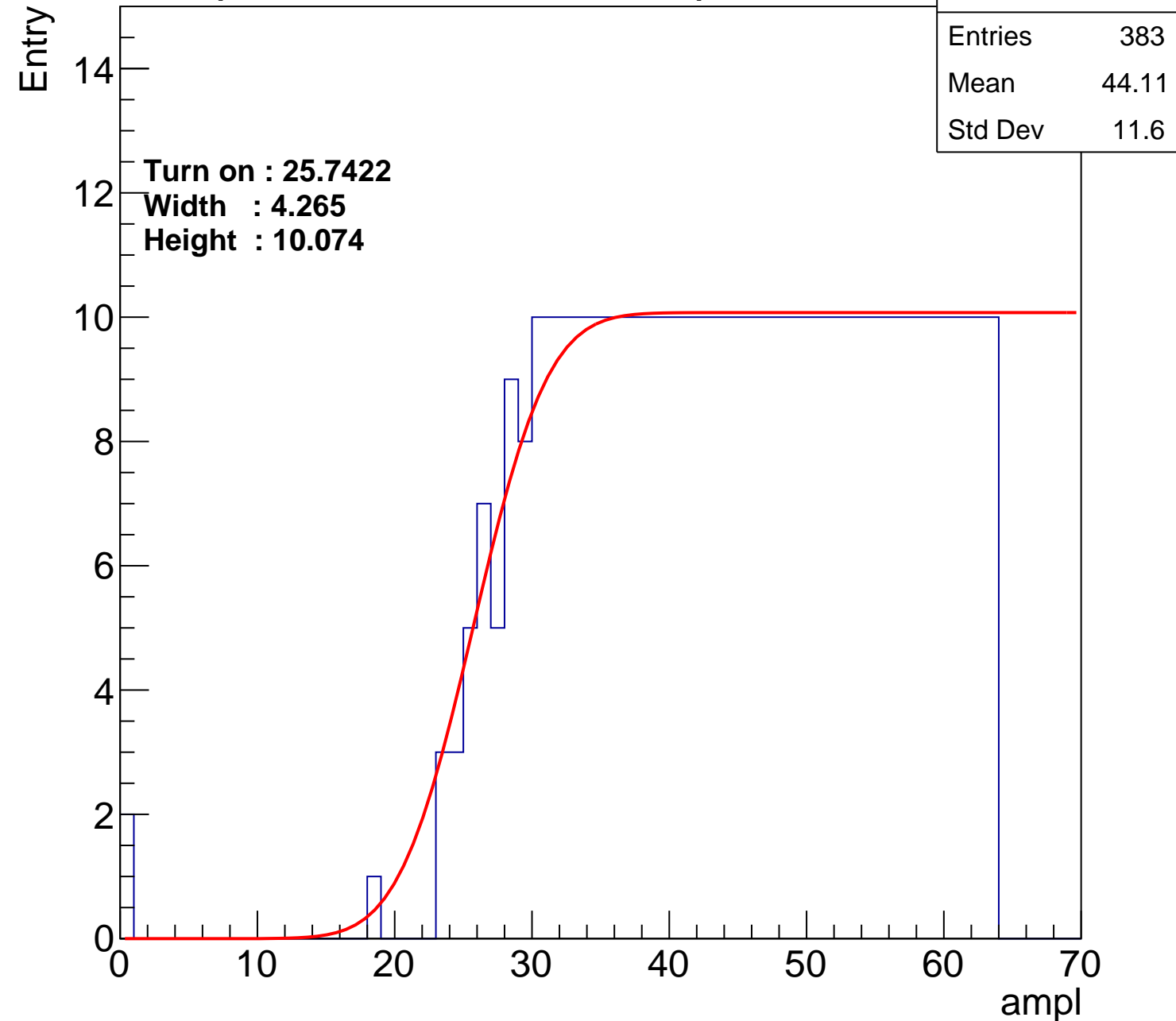
Width : 4.265

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch71

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.13
Std Dev	11.56

Turn on : 26.1362

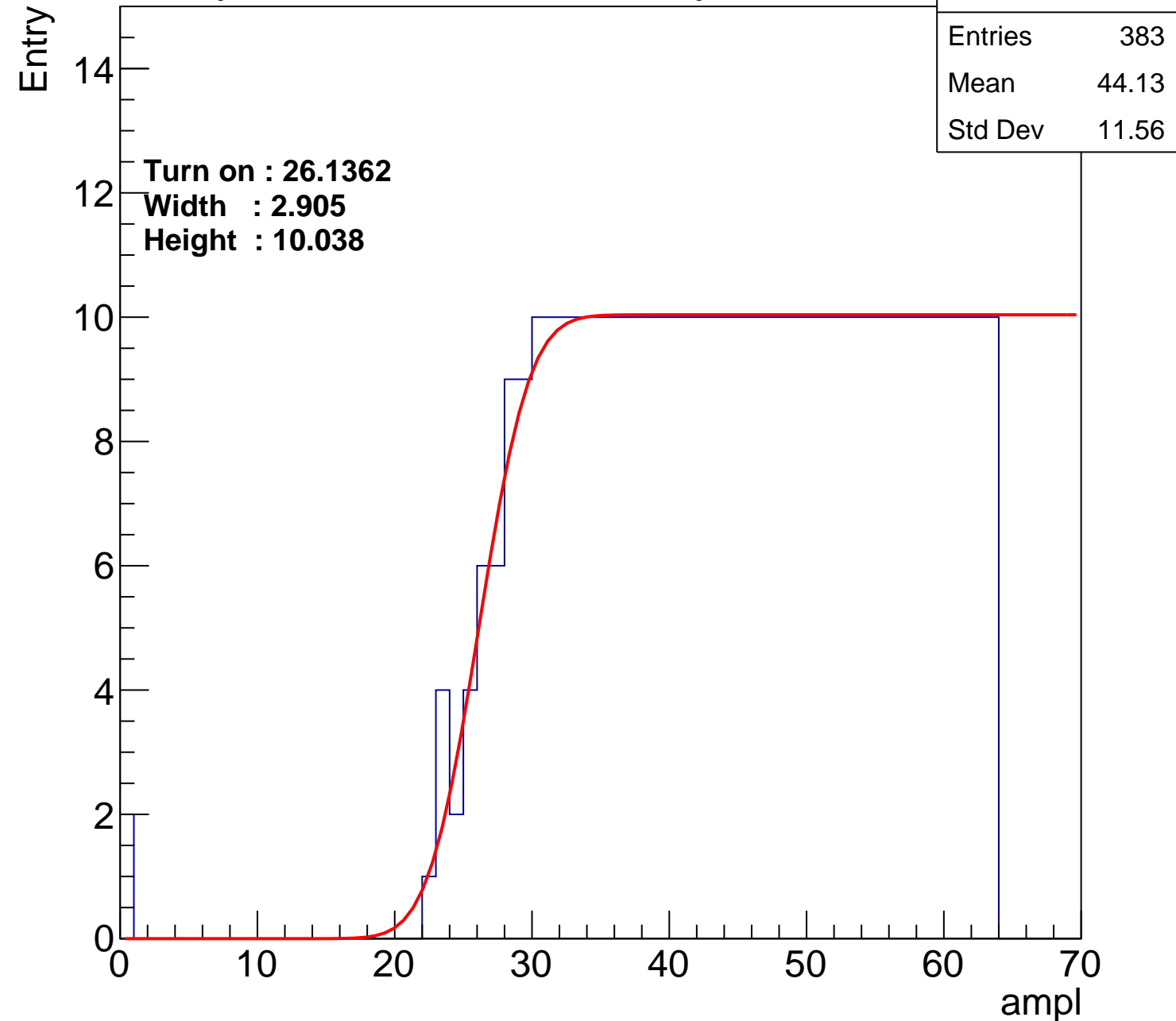
Width : 2.905

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch72

calib_packv5_042523_0143.root, FC#4, port A2

Entries	360
Mean	45.34
Std Dev	10.77

Turn on : 28.5538

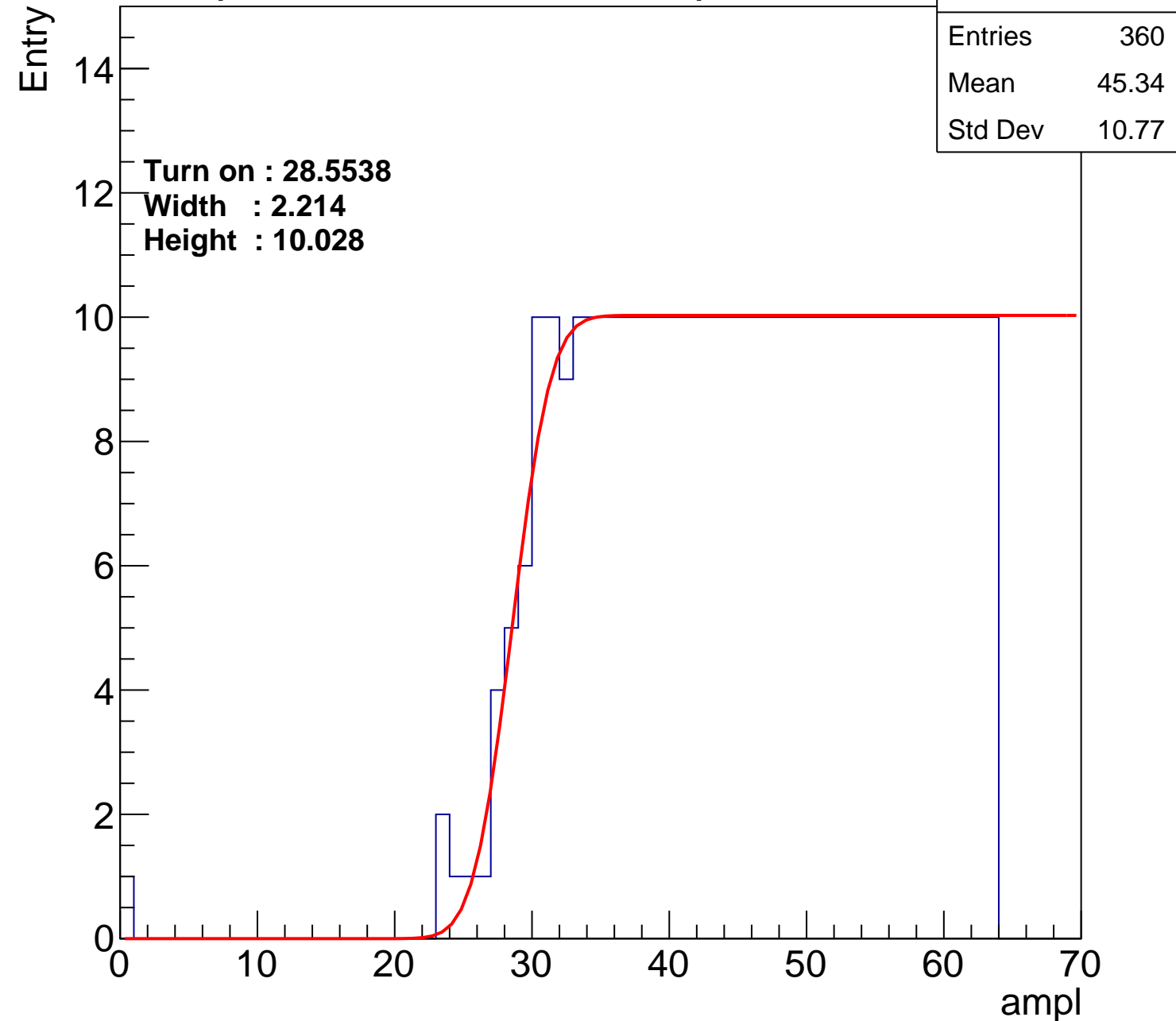
Width : 2.214

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch73

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.77
Std Dev	11.72

Turn on : 25.2526

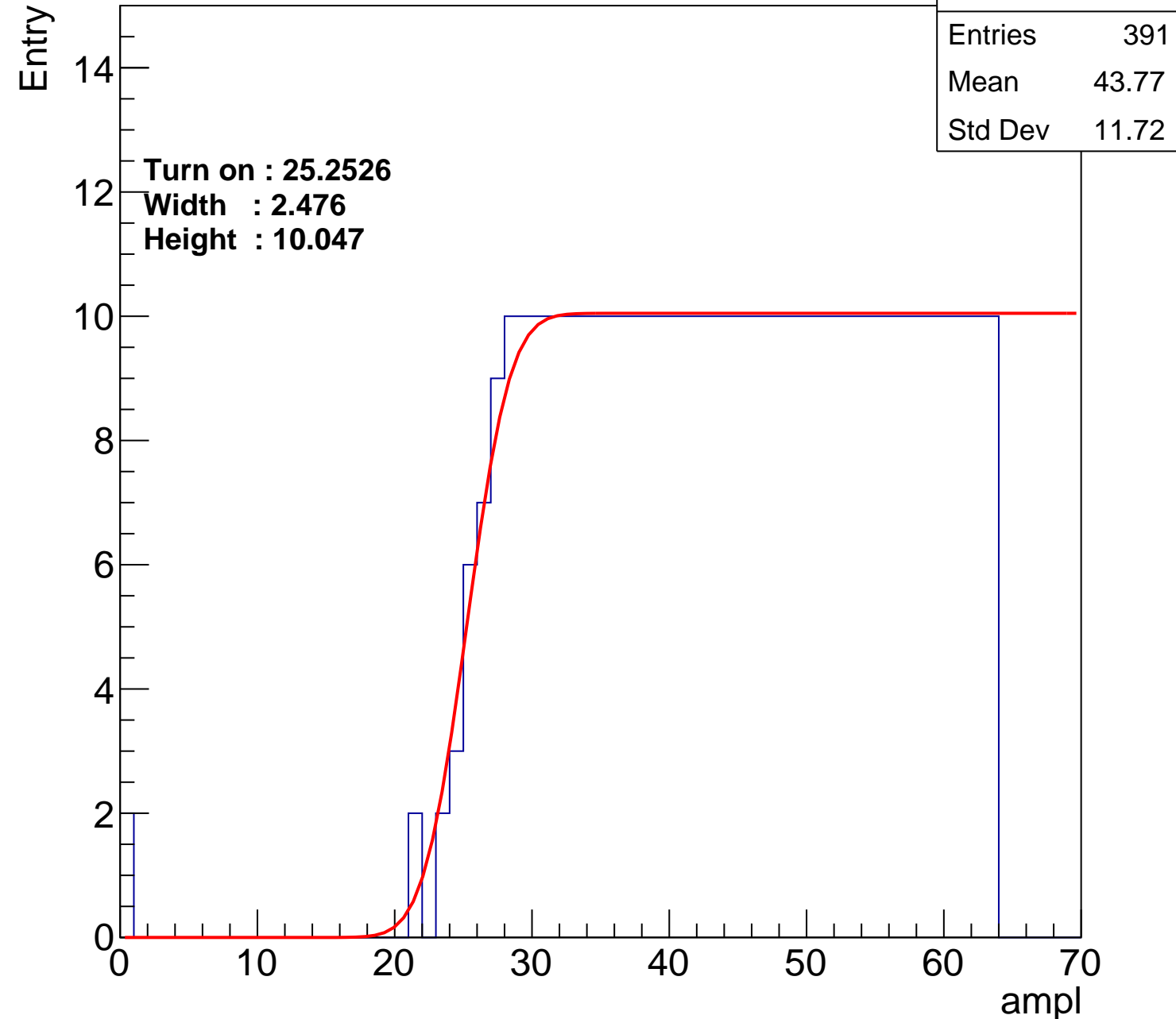
Width : 2.476

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch74

calib_packv5_042523_0143.root, FC#4, port A2

Entries	377
Mean	44.35
Std Dev	11.53

Turn on : 26.3254

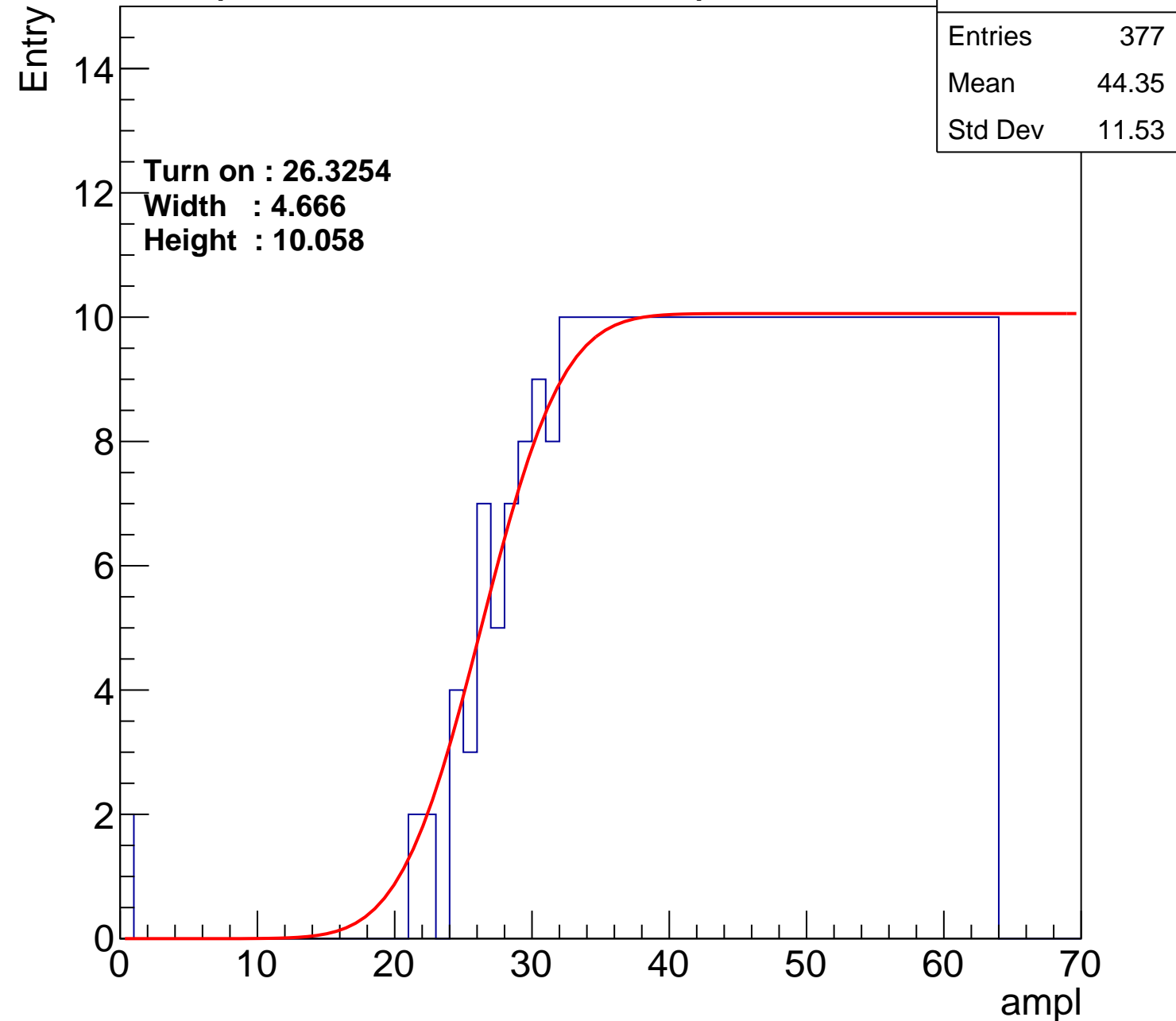
Width : 4.666

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch75

calib_packv5_042523_0143.root, FC#4, port A2

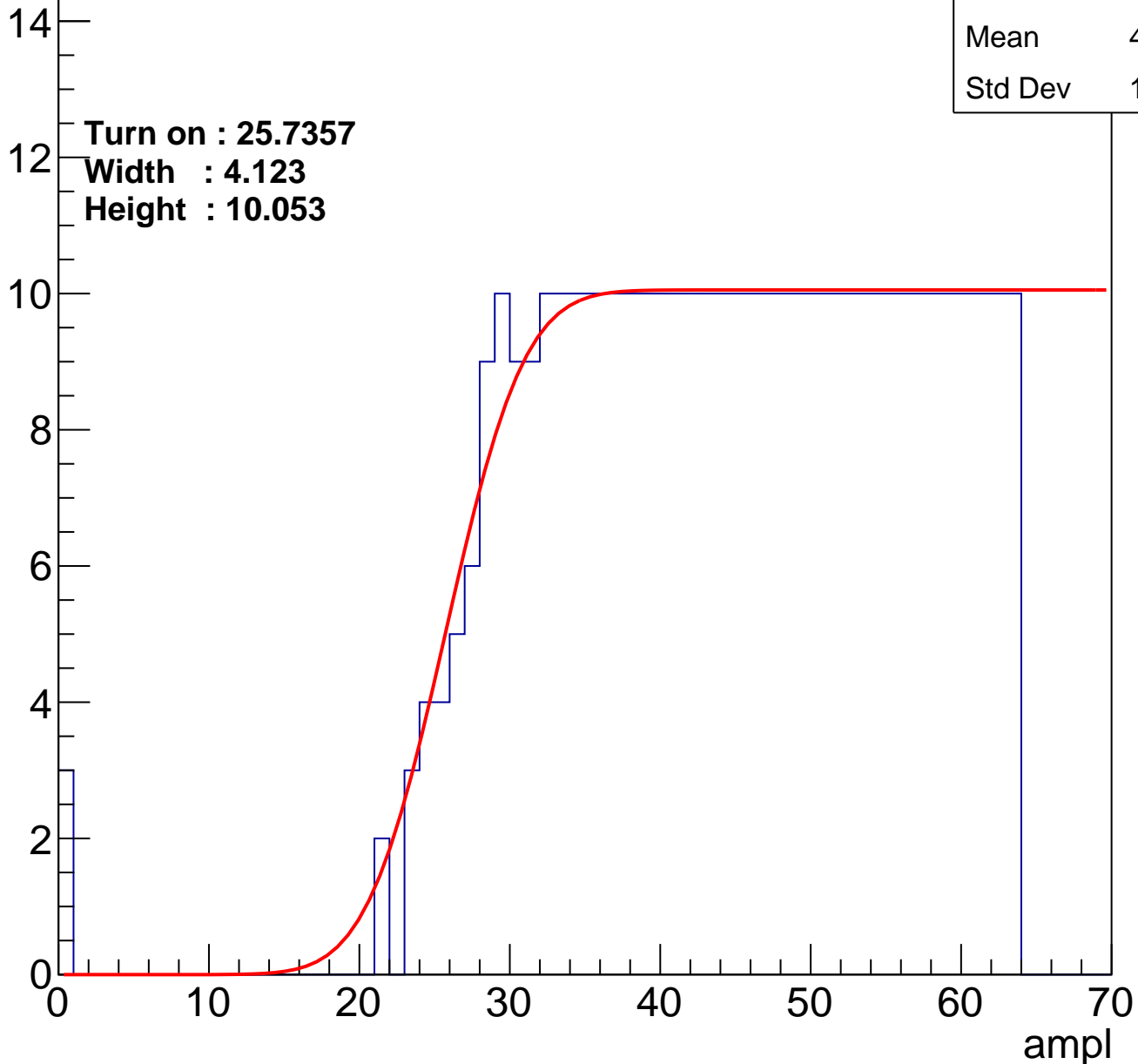
Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 25.7357

Width : 4.123

Height : 10.053

Entry



B1L100S, U23-ch76

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.1801

Width : 2.762

Height : 9.978

Entry

14

12

10

8

6

4

2

0

0

10

20

30

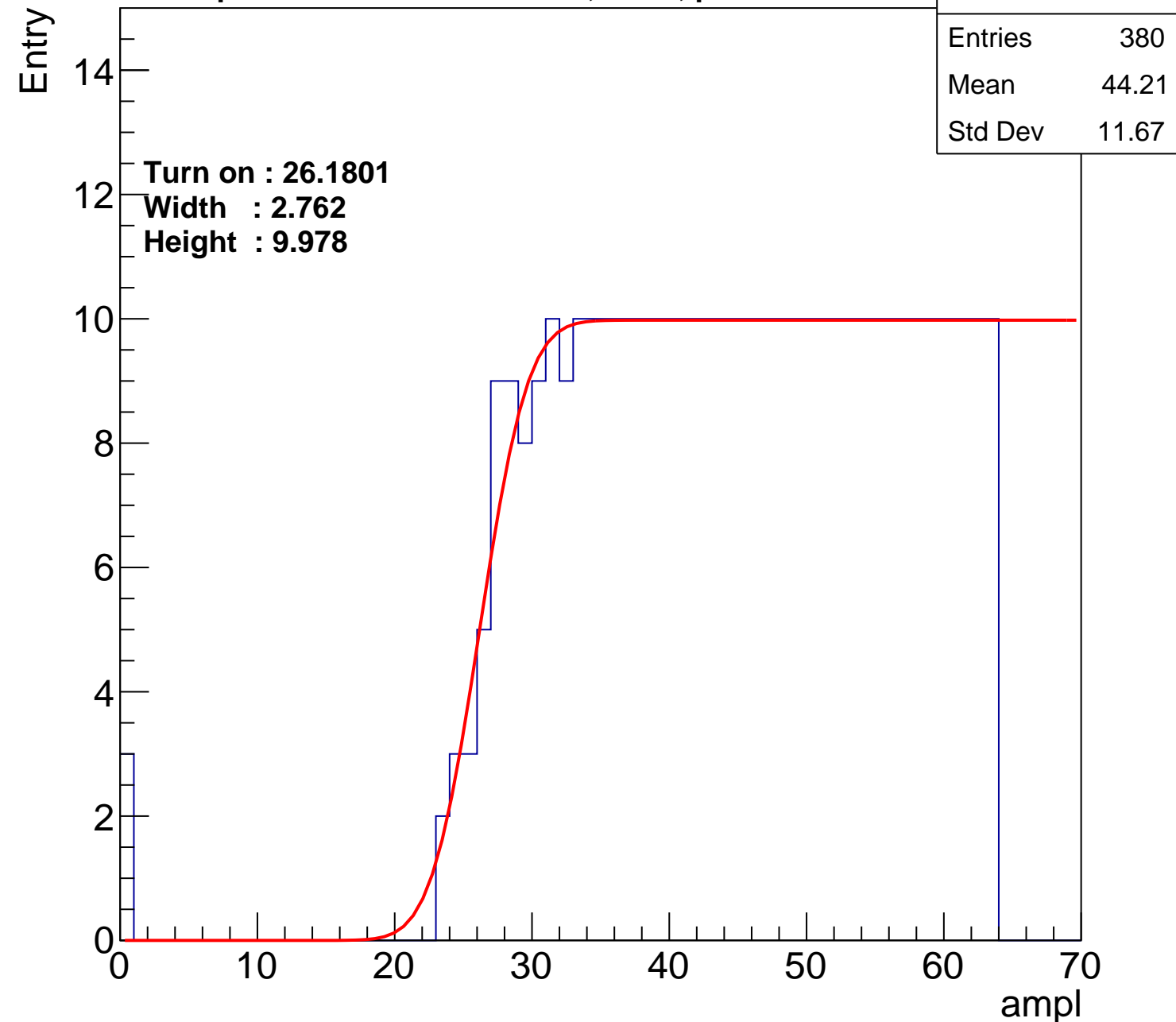
40

50

60

70

ampl



B1L100S, U23-ch77

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.8431

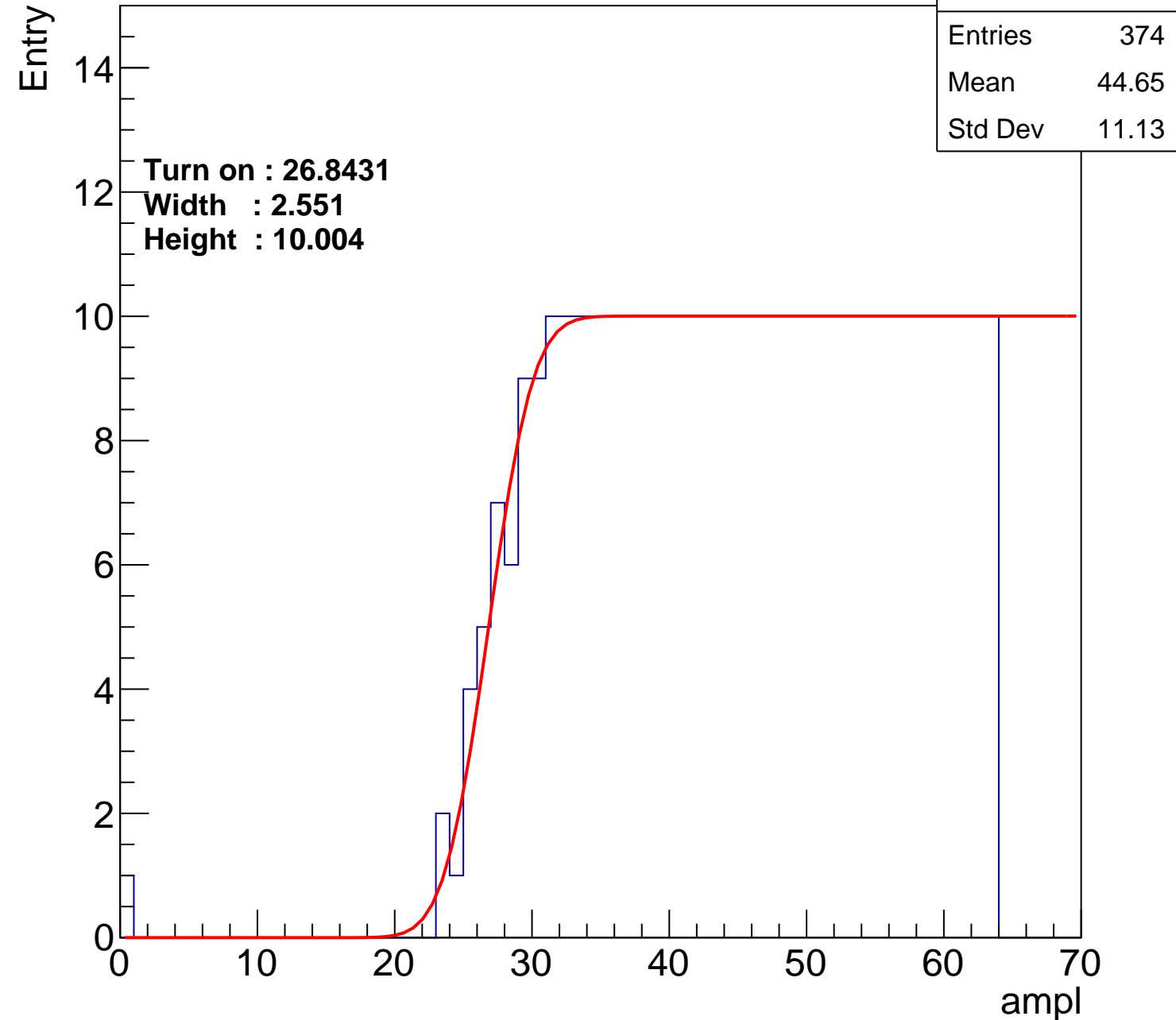
Width : 2.551

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch78

calib_packv5_042523_0143.root, FC#4, port A2

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 25.7631

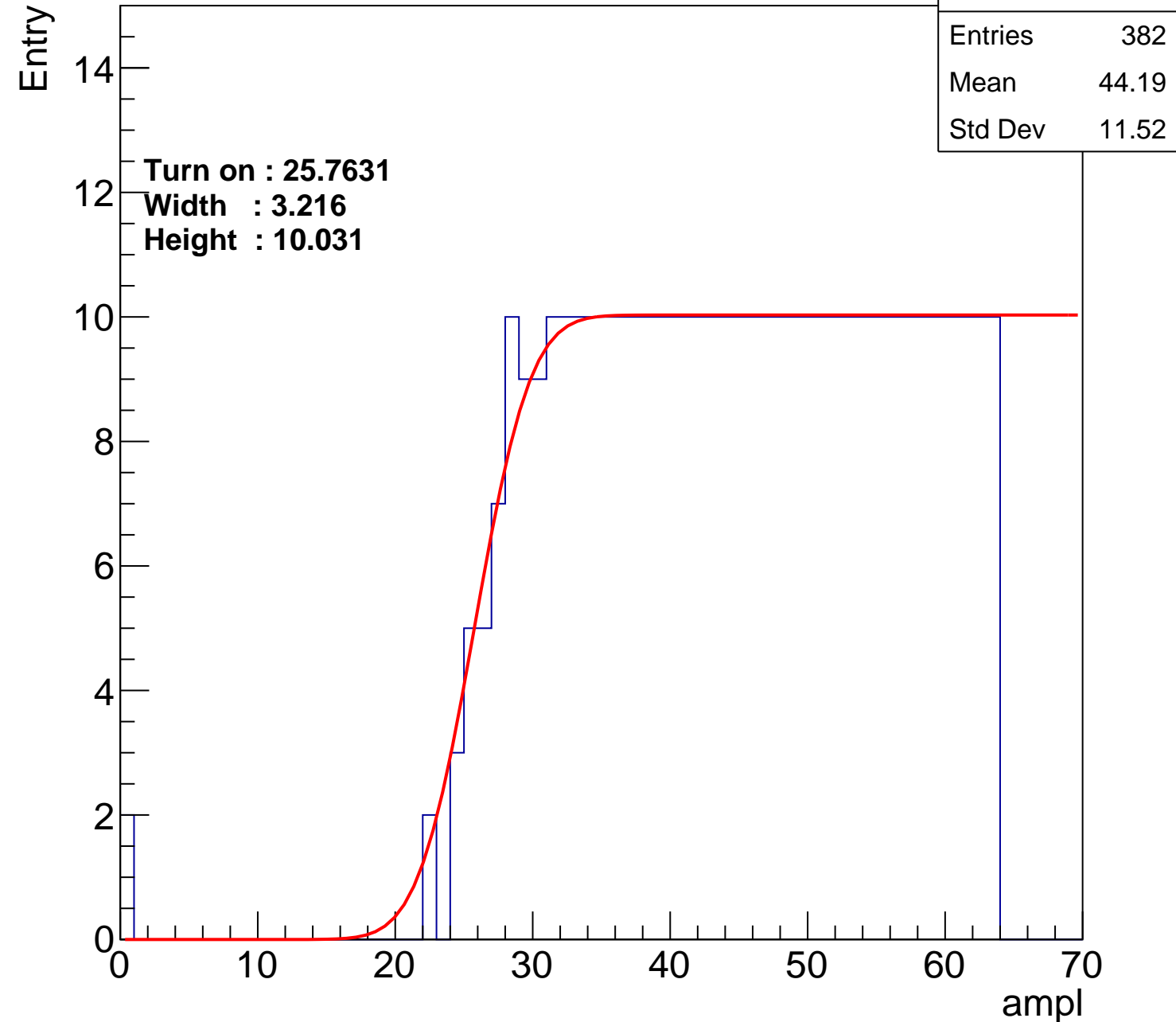
Width : 3.216

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch79

calib_packv5_042523_0143.root, FC#4, port A2

Entries	367
Mean	44.77
Std Dev	11.46

Turn on : 27.4801

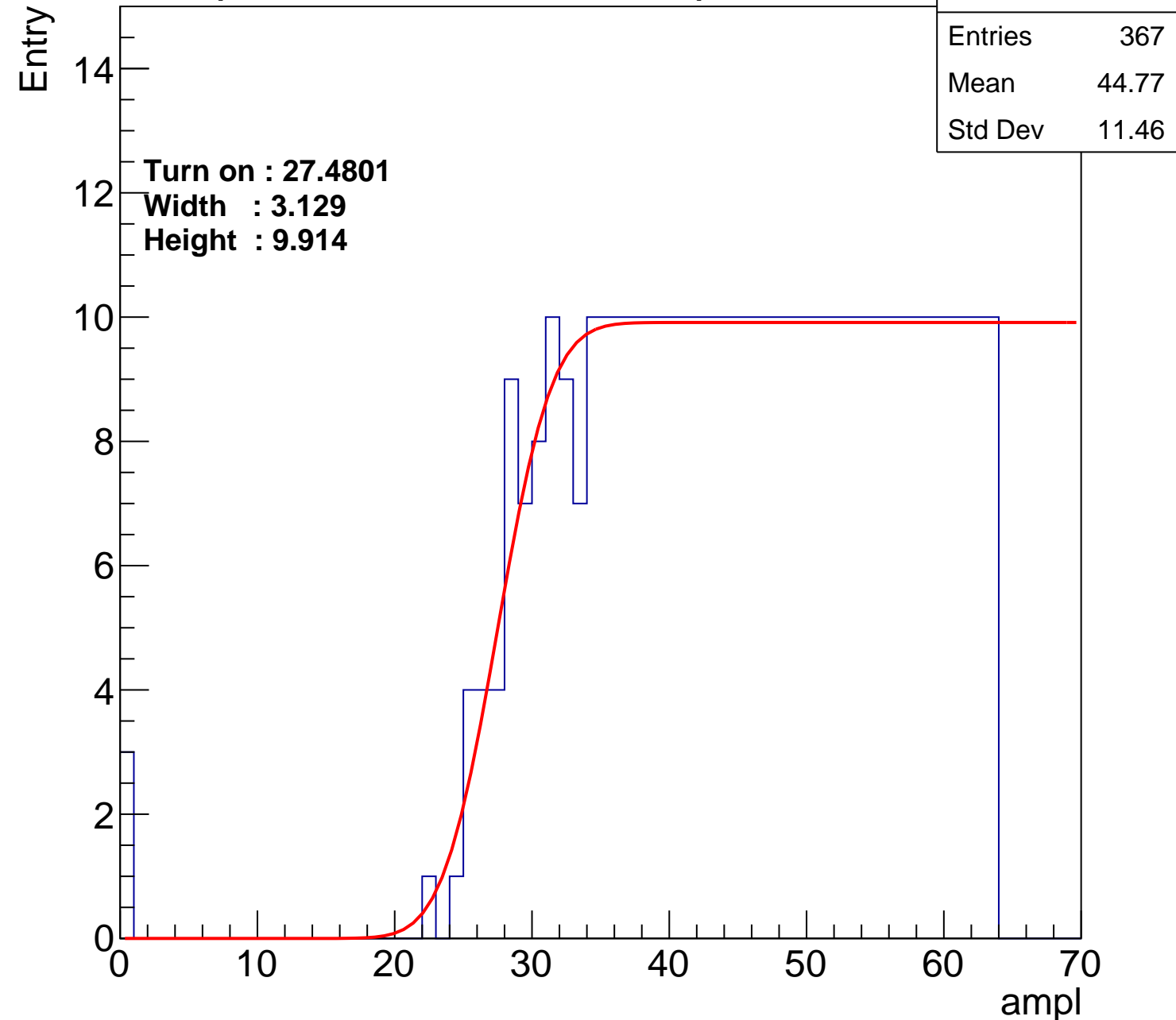
Width : 3.129

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch80

calib_packv5_042523_0143.root, FC#4, port A2

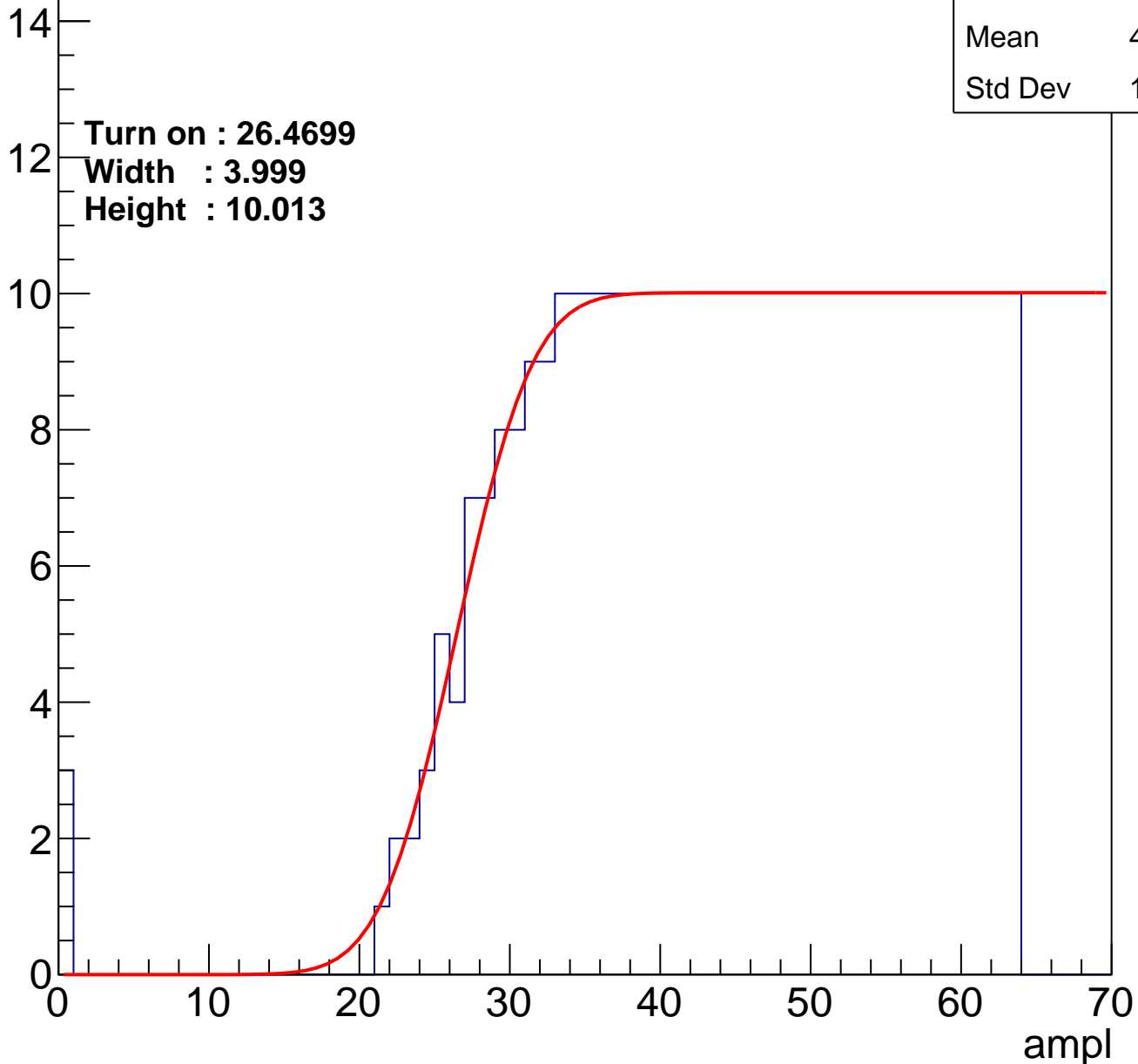
Entries	378
Mean	44.22
Std Dev	11.75

Turn on : 26.4699

Width : 3.999

Height : 10.013

Entry



B1L100S, U23-ch81

calib_packv5_042523_0143.root, FC#4, port A2

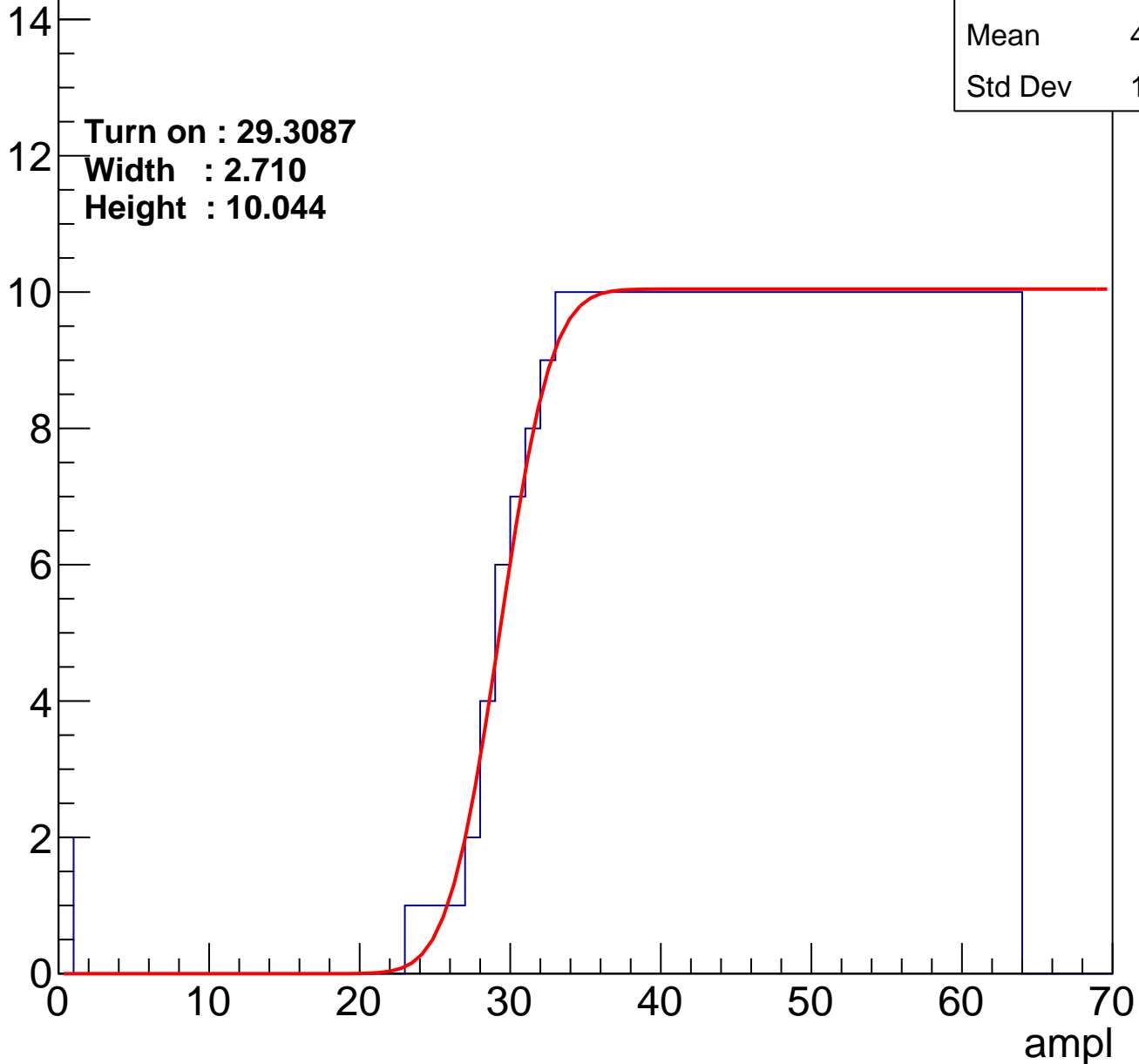
Entries	352
Mean	45.64
Std Dev	10.82

Turn on : 29.3087

Width : 2.710

Height : 10.044

Entry



B1L100S, U23-ch82

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.55
Std Dev	11.19

Turn on : 25.9510

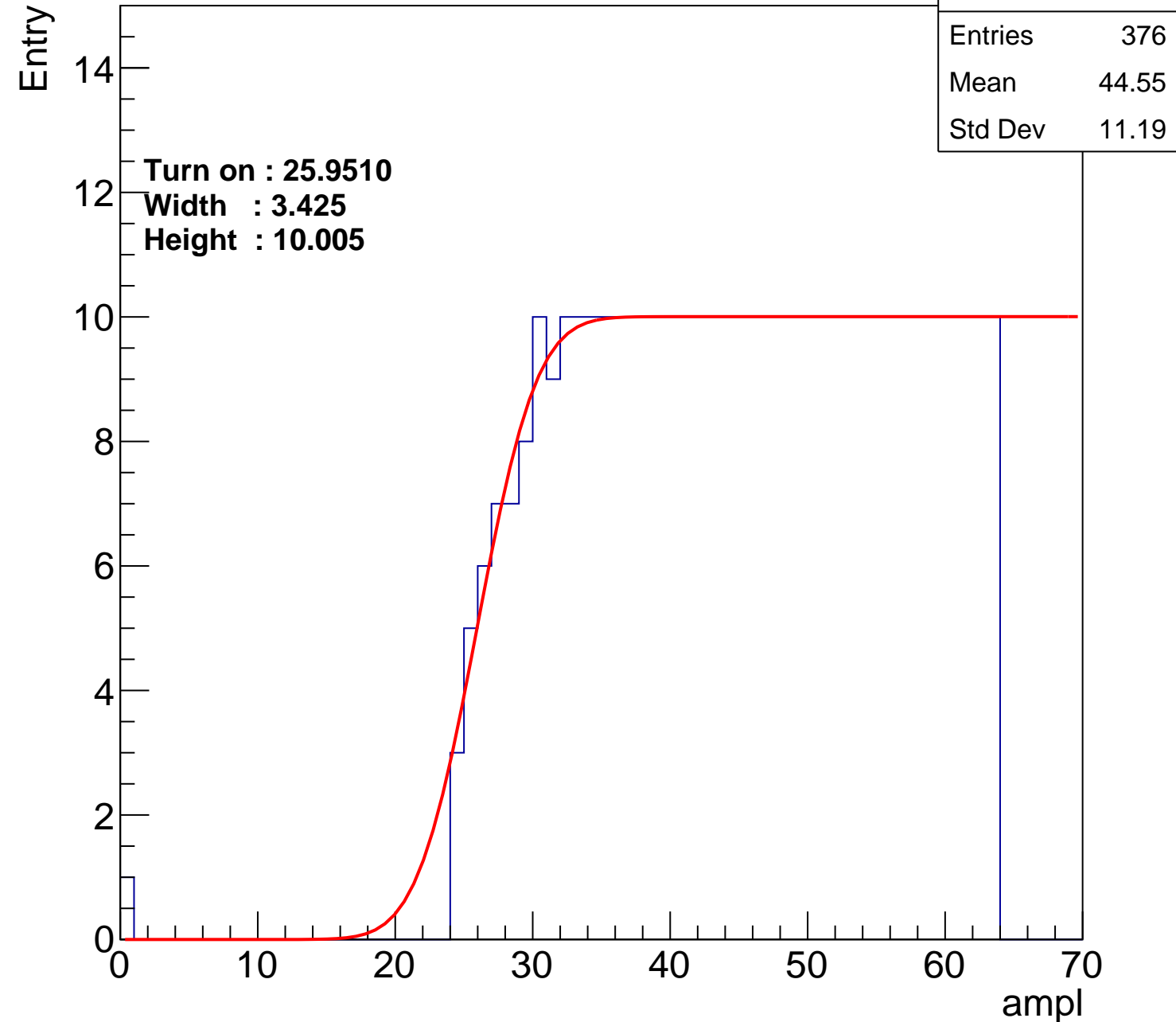
Width : 3.425

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch83

calib_packv5_042523_0143.root, FC#4, port A2

Entries	368
Mean	44.85
Std Dev	11.21

Turn on : 27.3454

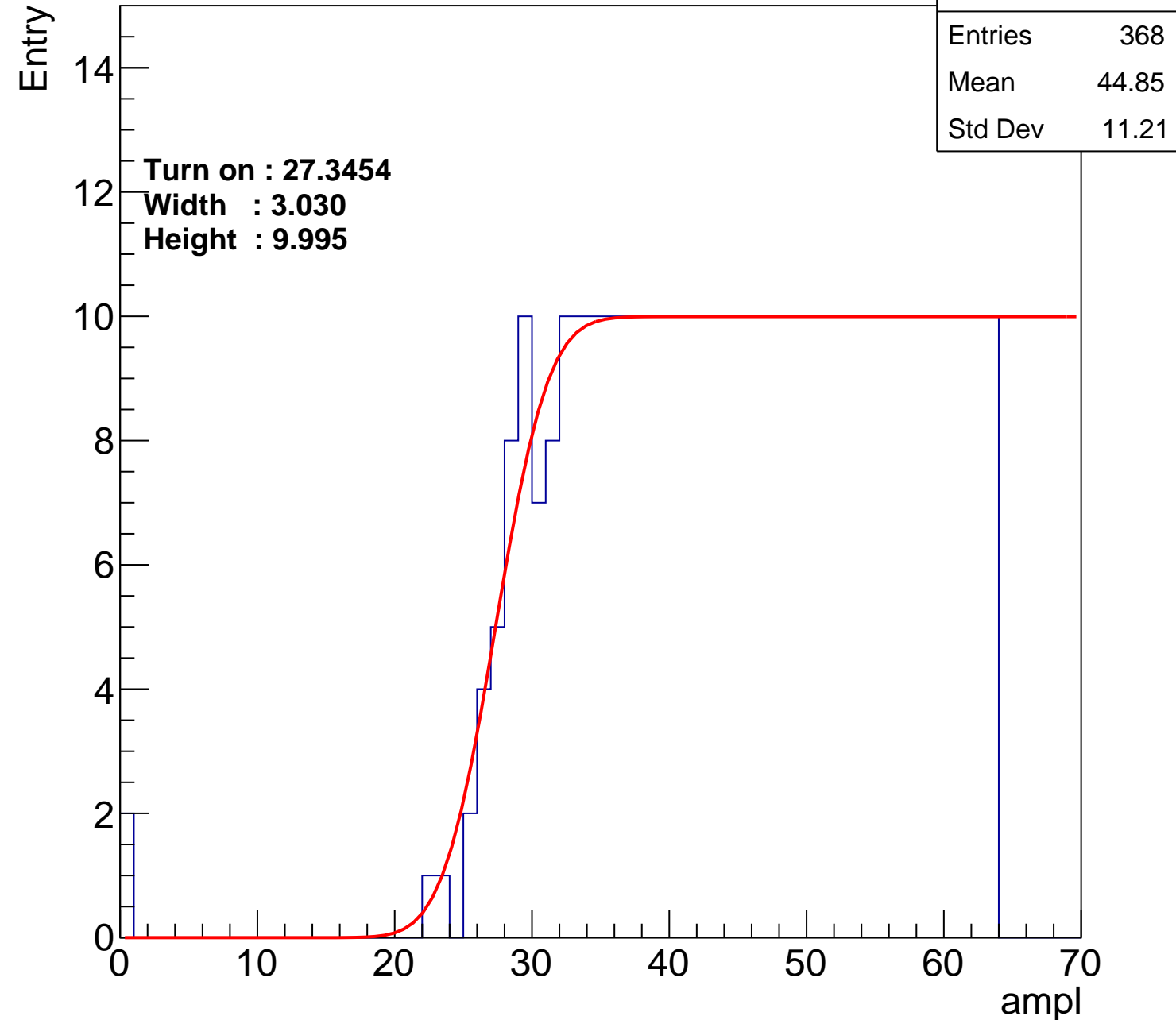
Width : 3.030

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch84

calib_packv5_042523_0143.root, FC#4, port A2

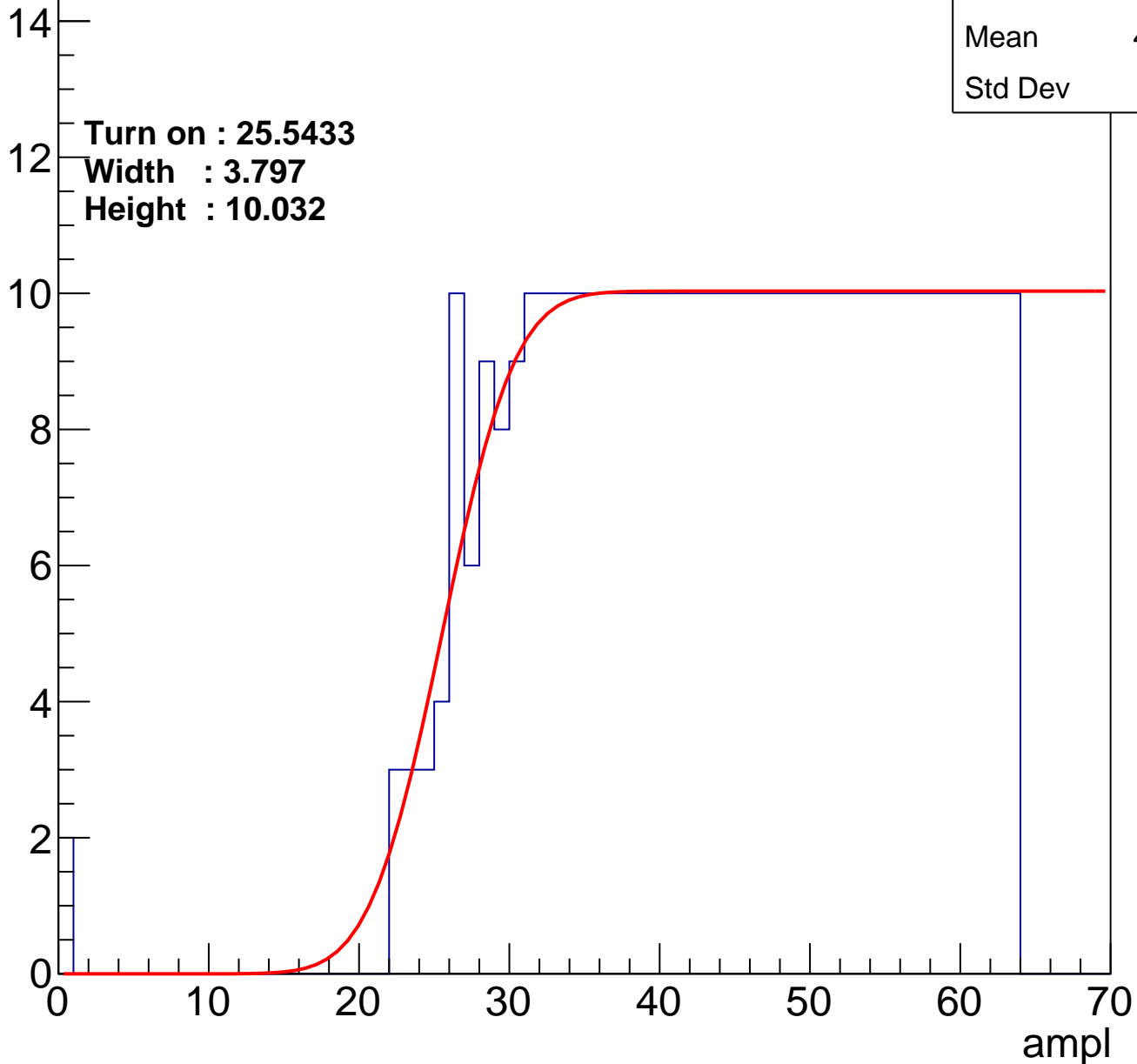
Entries	387
Mean	43.91
Std Dev	11.7

Turn on : 25.5433

Width : 3.797

Height : 10.032

Entry



B1L100S, U23-ch85

calib_packv5_042523_0143.root, FC#4, port A2

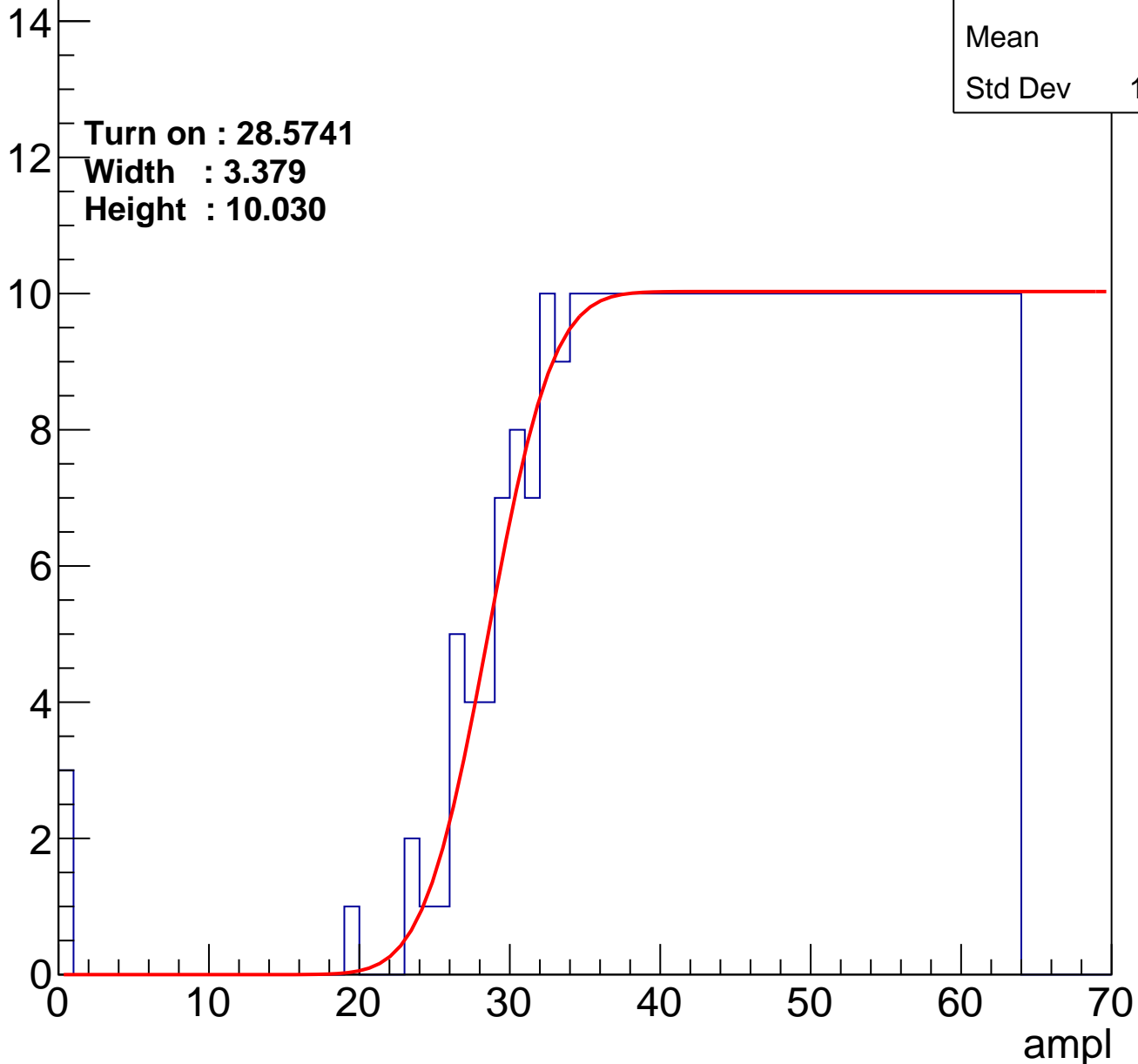
Entries	362
Mean	45
Std Dev	11.38

Turn on : 28.5741

Width : 3.379

Height : 10.030

Entry

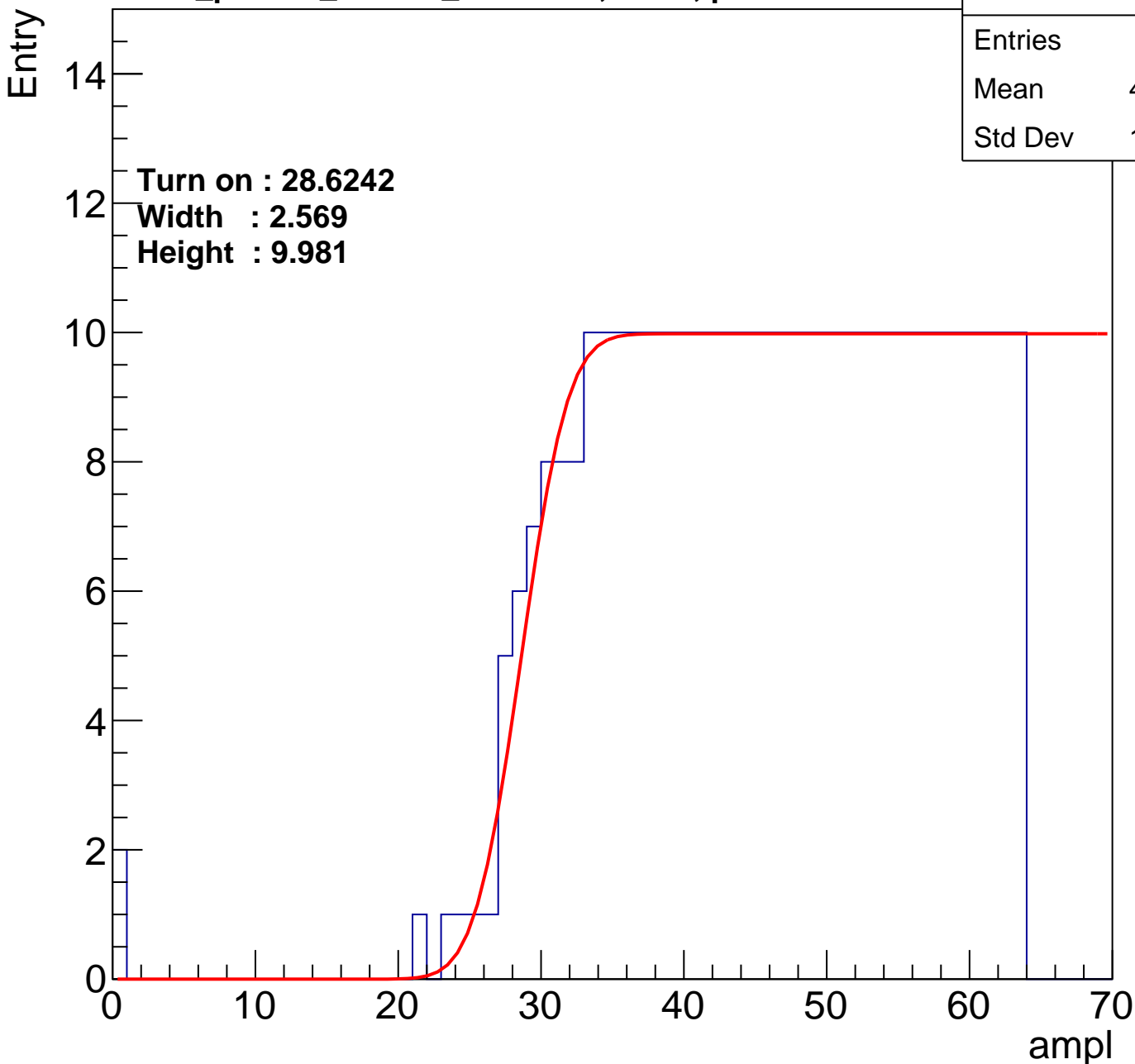


calib_packv5_042523_0143.root, FC#4, port A2

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.26
Std Dev	11.04

Height : 9.981



B1L100S, U23-ch87

calib_packv5_042523_0143.root, FC#4, port A2

Entries	355
Mean	45.55
Std Dev	10.67

Turn on : 28.6177

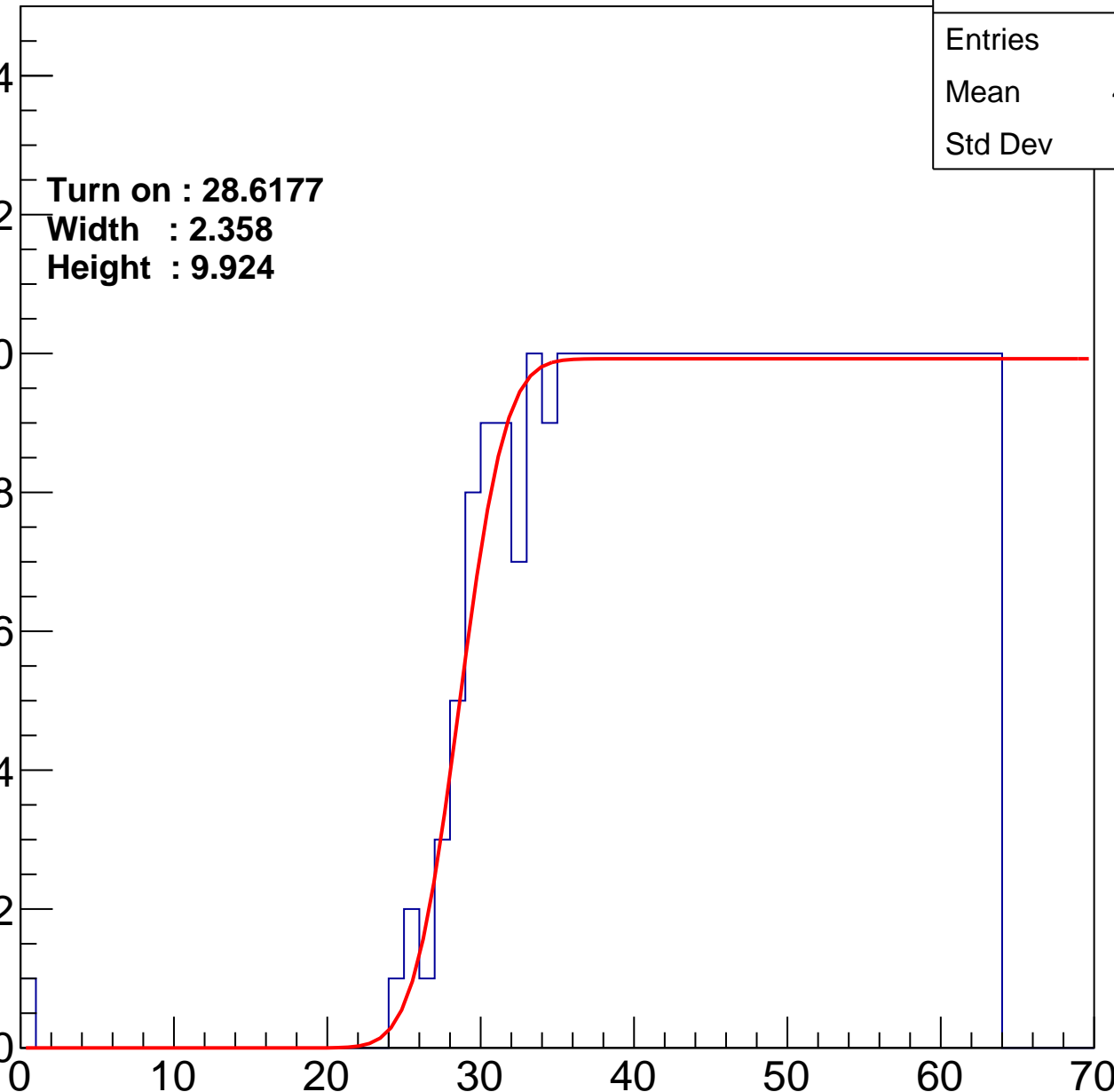
Width : 2.358

Height : 9.924

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch88

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.12
Std Dev	11.89

Turn on : 26.1588

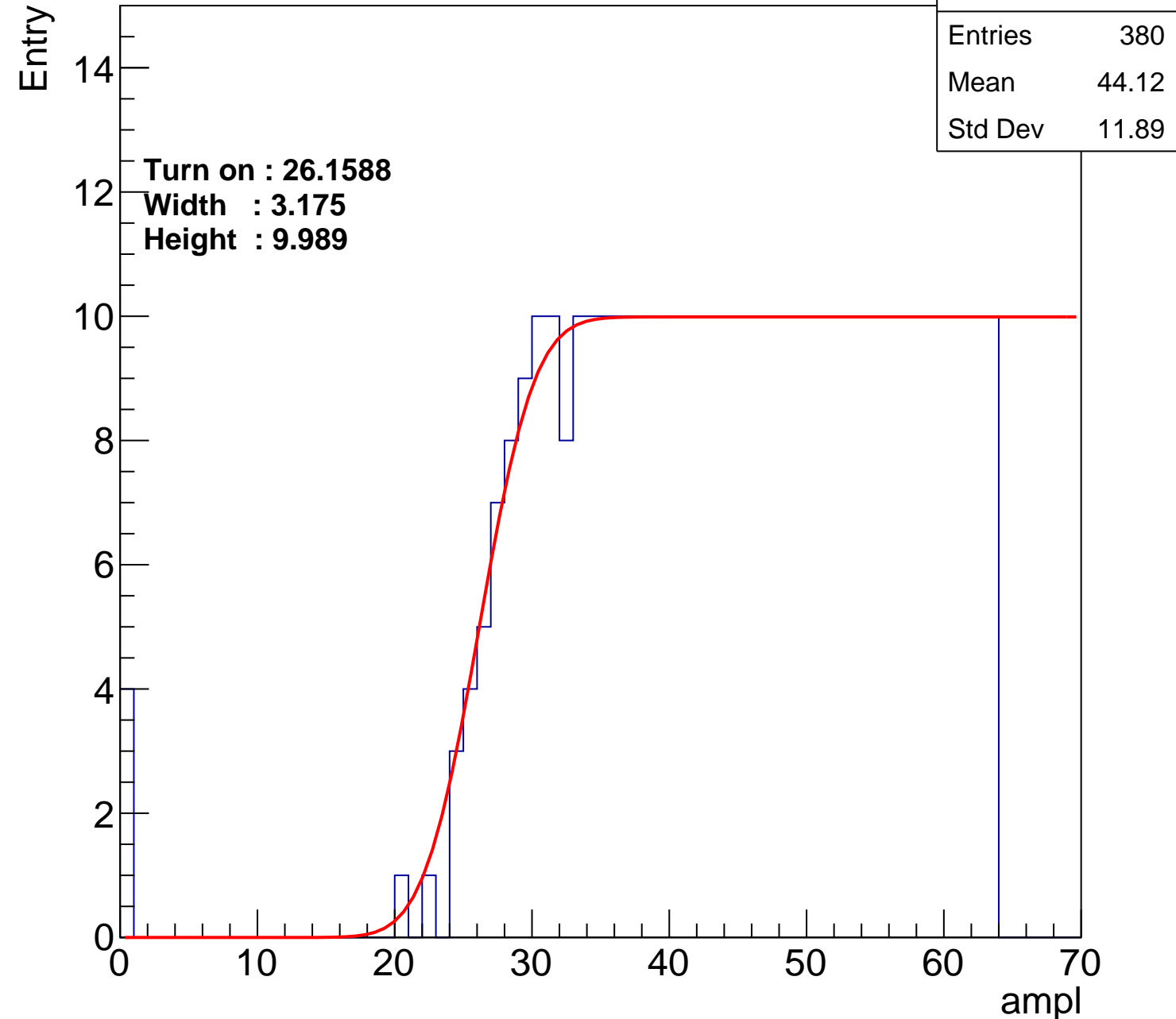
Width : 3.175

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch89

calib_packv5_042523_0143.root, FC#4, port A2

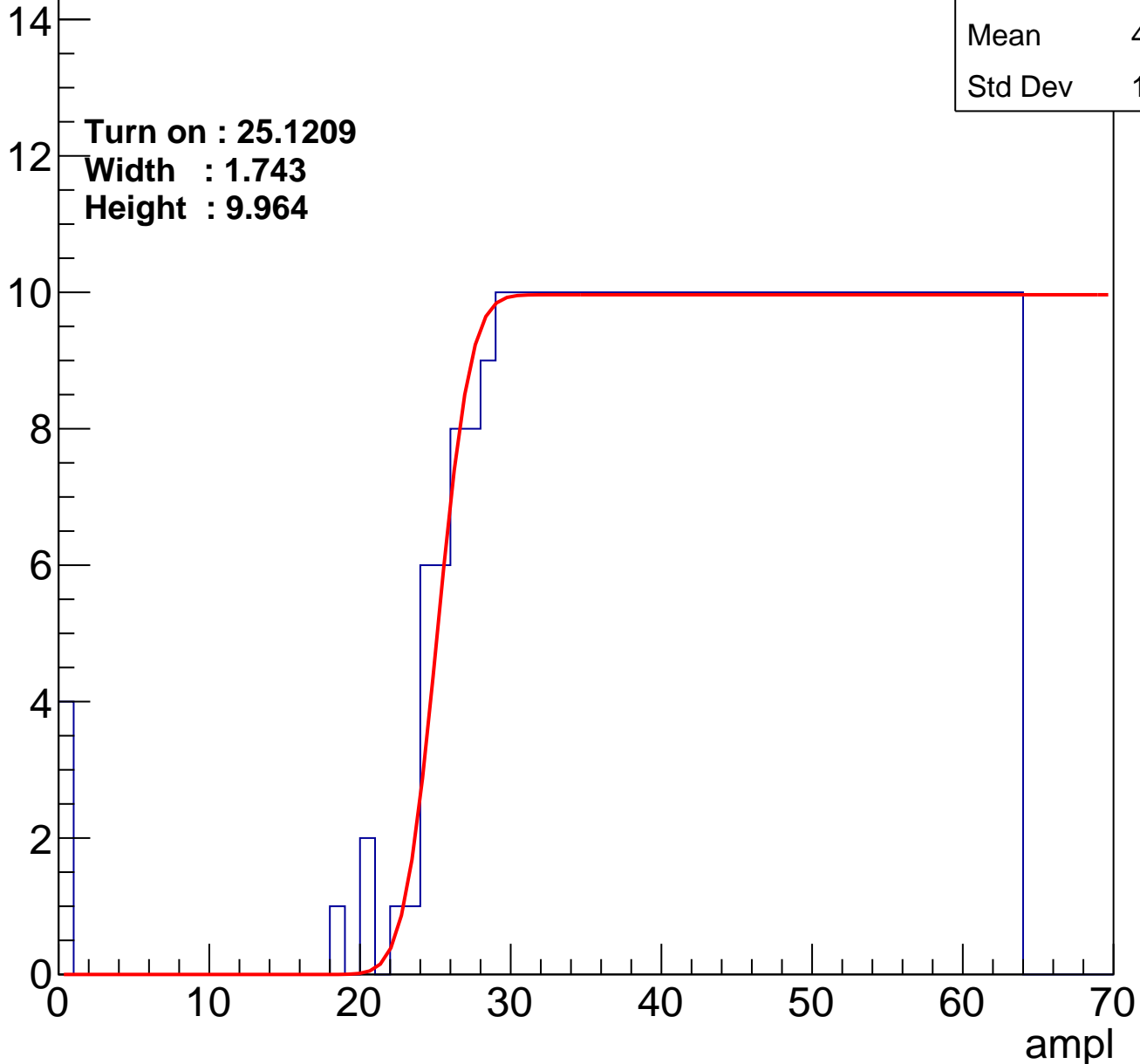
Entries	396
Mean	43.37
Std Dev	12.23

Turn on : 25.1209

Width : 1.743

Height : 9.964

Entry



B1L100S, U23-ch90

calib_packv5_042523_0143.root, FC#4, port A2

Entries	380
Mean	44.2
Std Dev	11.69

Turn on : 26.4563

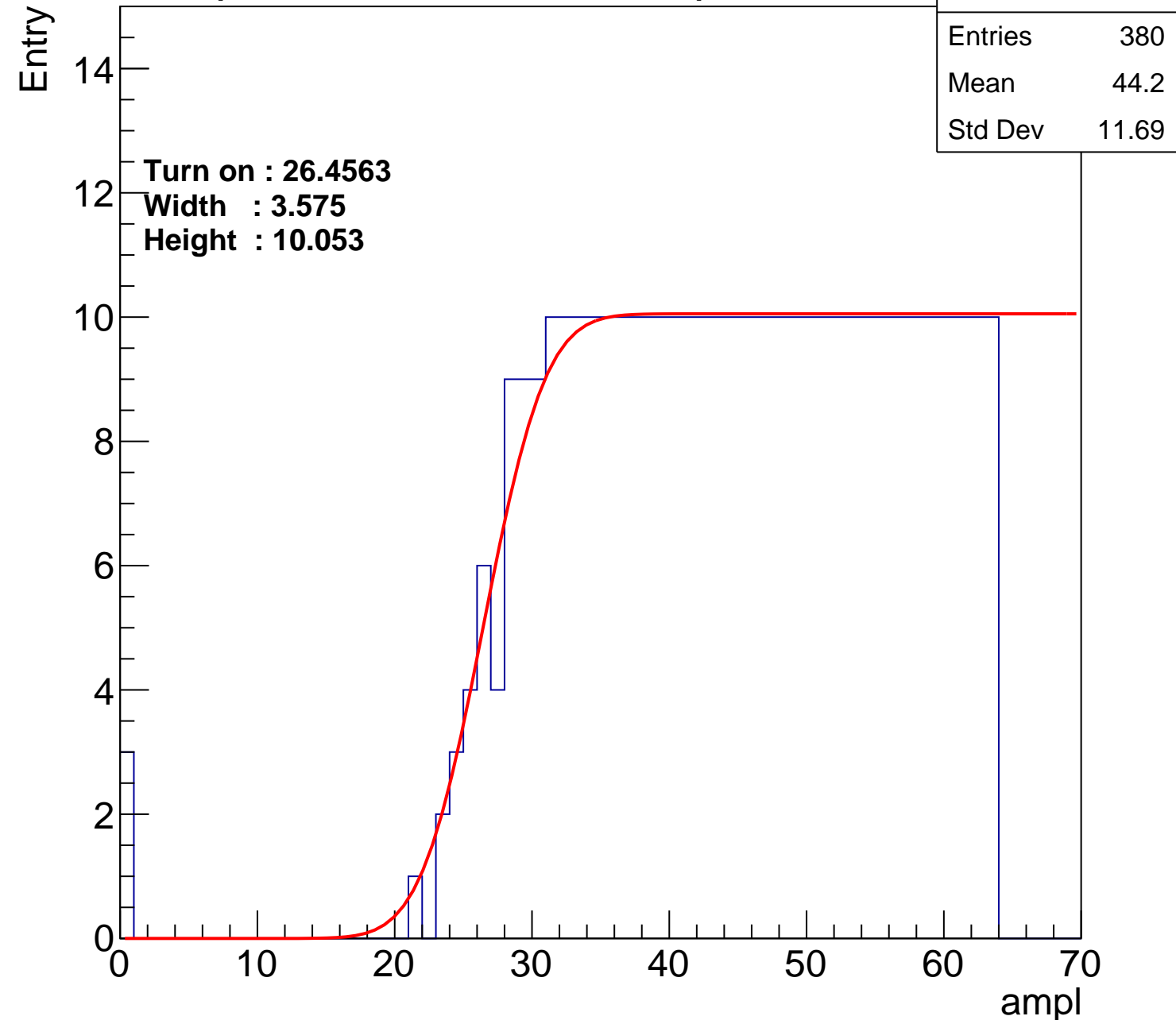
Width : 3.575

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch91

calib_packv5_042523_0143.root, FC#4, port A2

Entries	359
Mean	45.25
Std Dev	11.06

Turn on : 28.5929

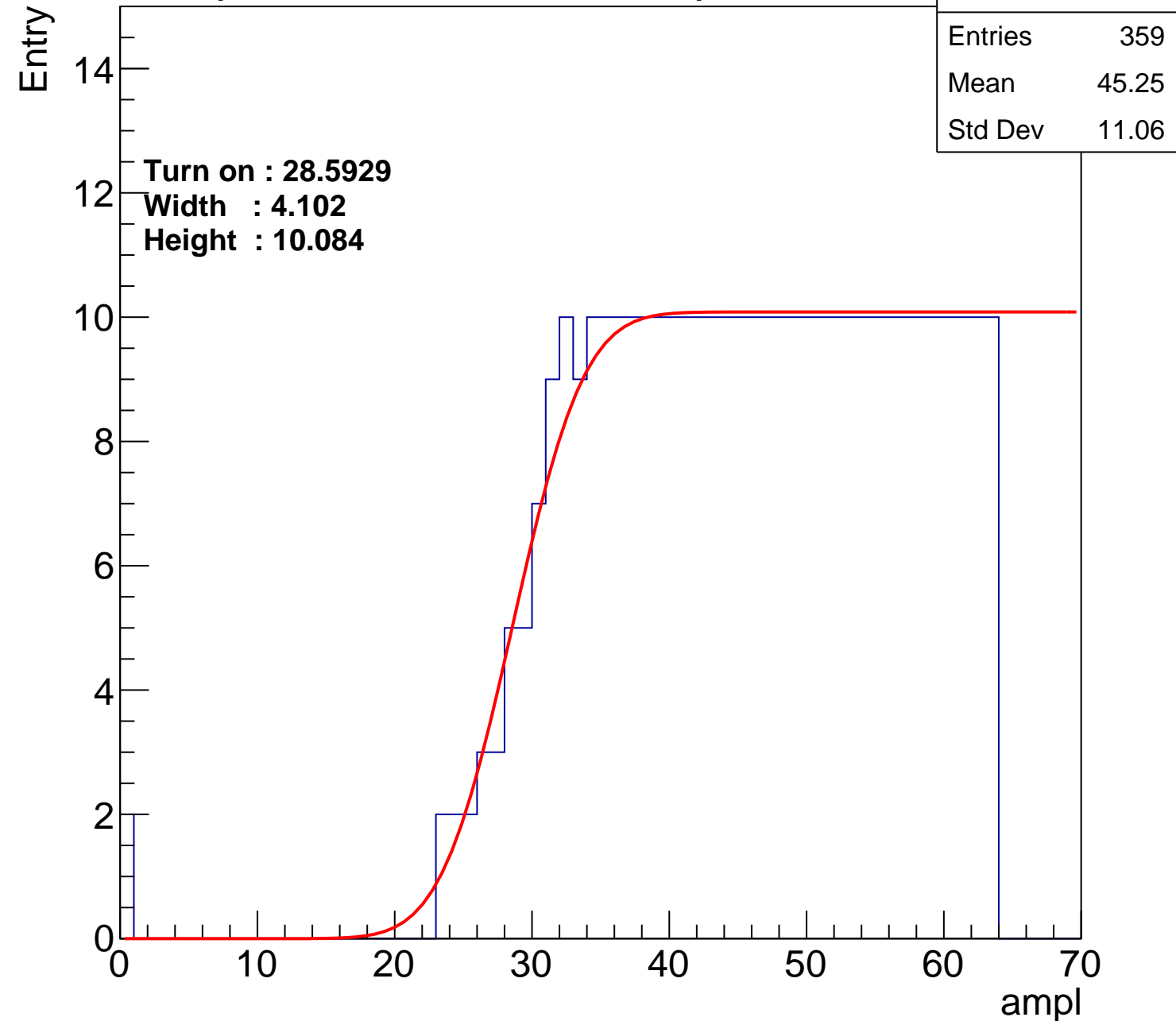
Width : 4.102

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch92

calib_packv5_042523_0143.root, FC#4, port A2

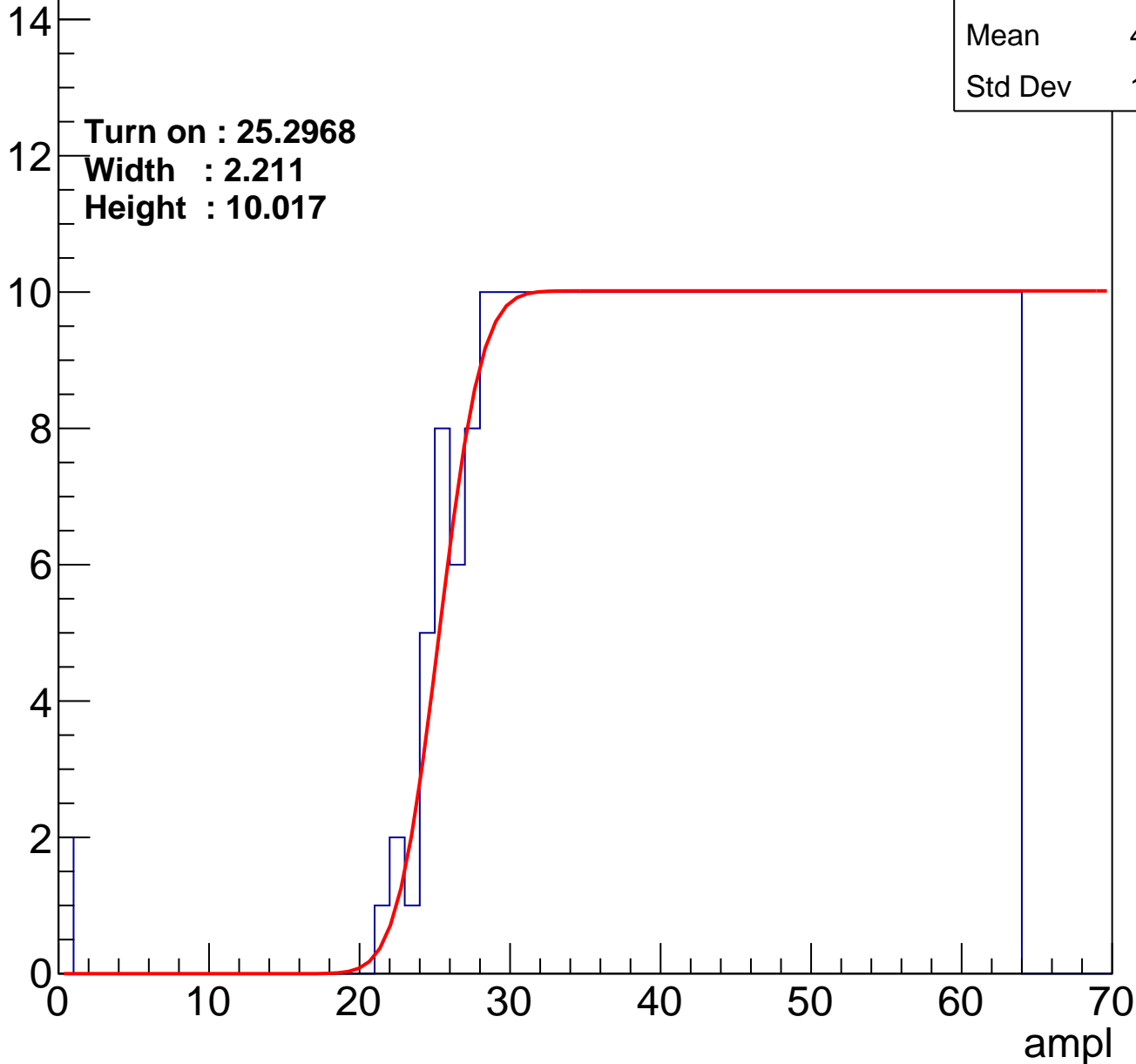
Entries	393
Mean	43.66
Std Dev	11.78

Turn on : 25.2968

Width : 2.211

Height : 10.017

Entry



B1L100S, U23-ch93

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.99
Std Dev	11.09

Turn on : 28.1032

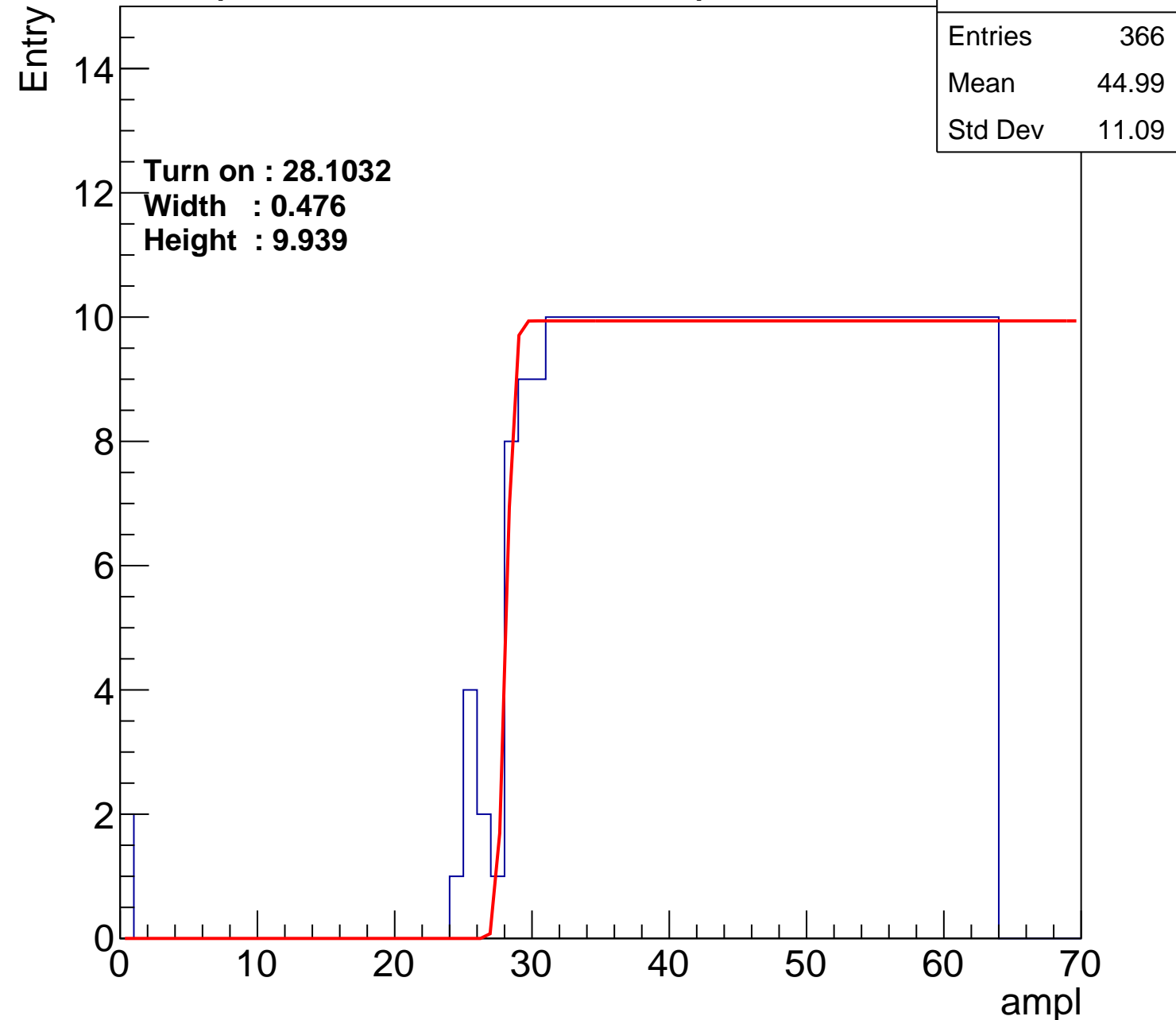
Width : 0.476

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch94

calib_packv5_042523_0143.root, FC#4, port A2

Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.4895

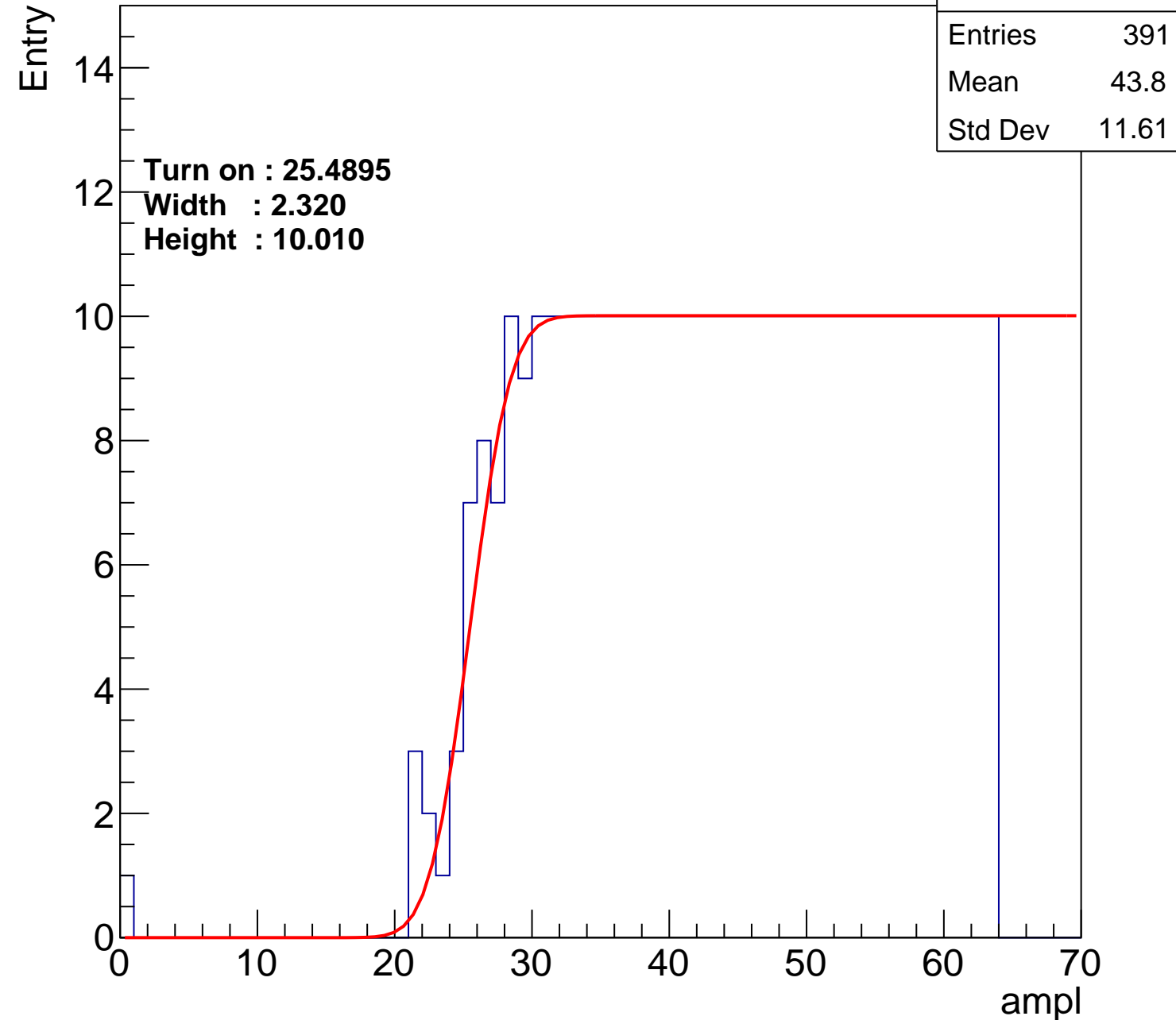
Width : 2.320

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch95

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.68
Std Dev	11.31

Turn on : 26.9038

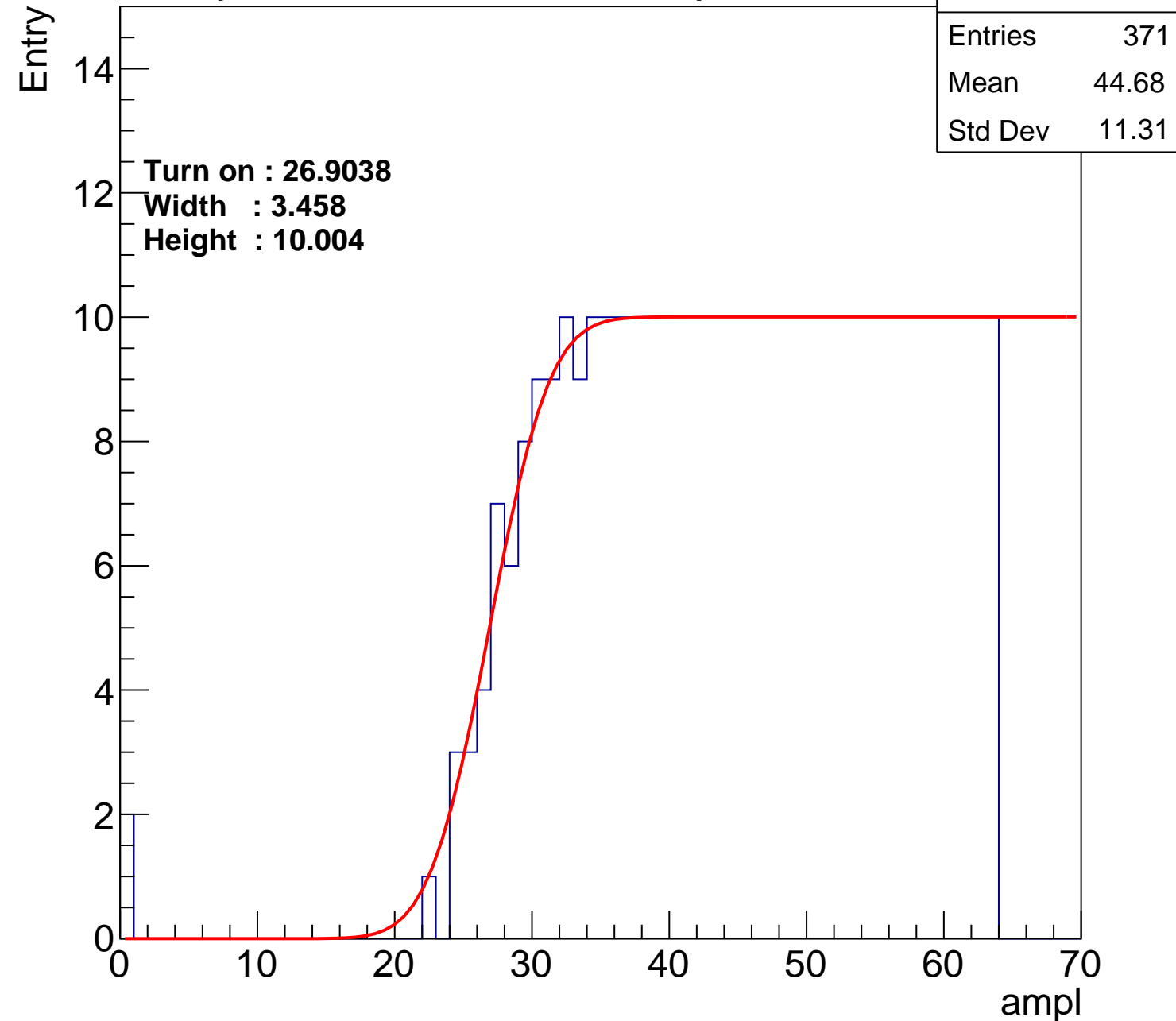
Width : 3.458

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch96

calib_packv5_042523_0143.root, FC#4, port A2

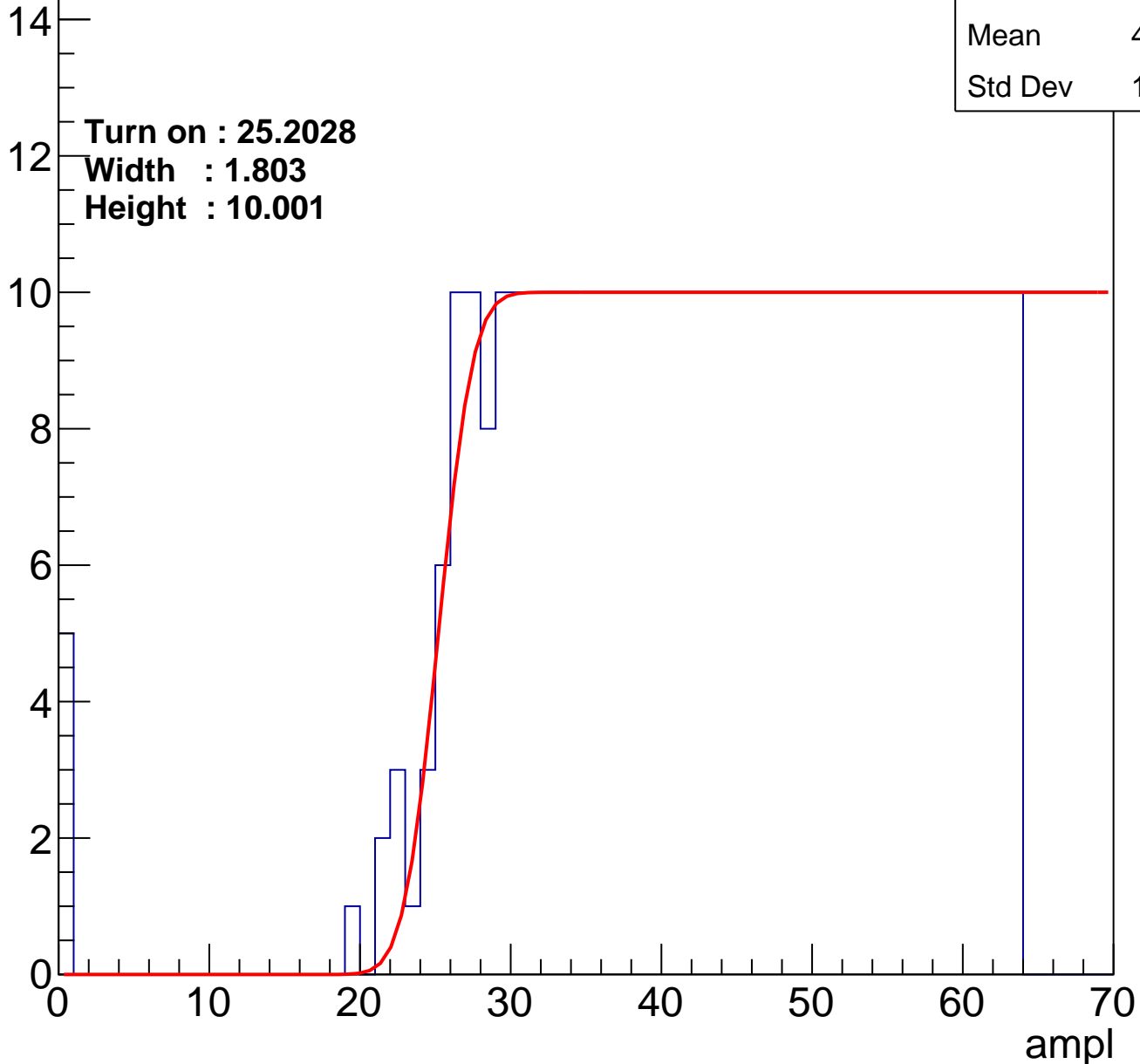
Entries	399
Mean	43.17
Std Dev	12.43

Turn on : 25.2028

Width : 1.803

Height : 10.001

Entry



B1L100S, U23-ch97

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.66
Std Dev	12.08

Turn on : 25.8001

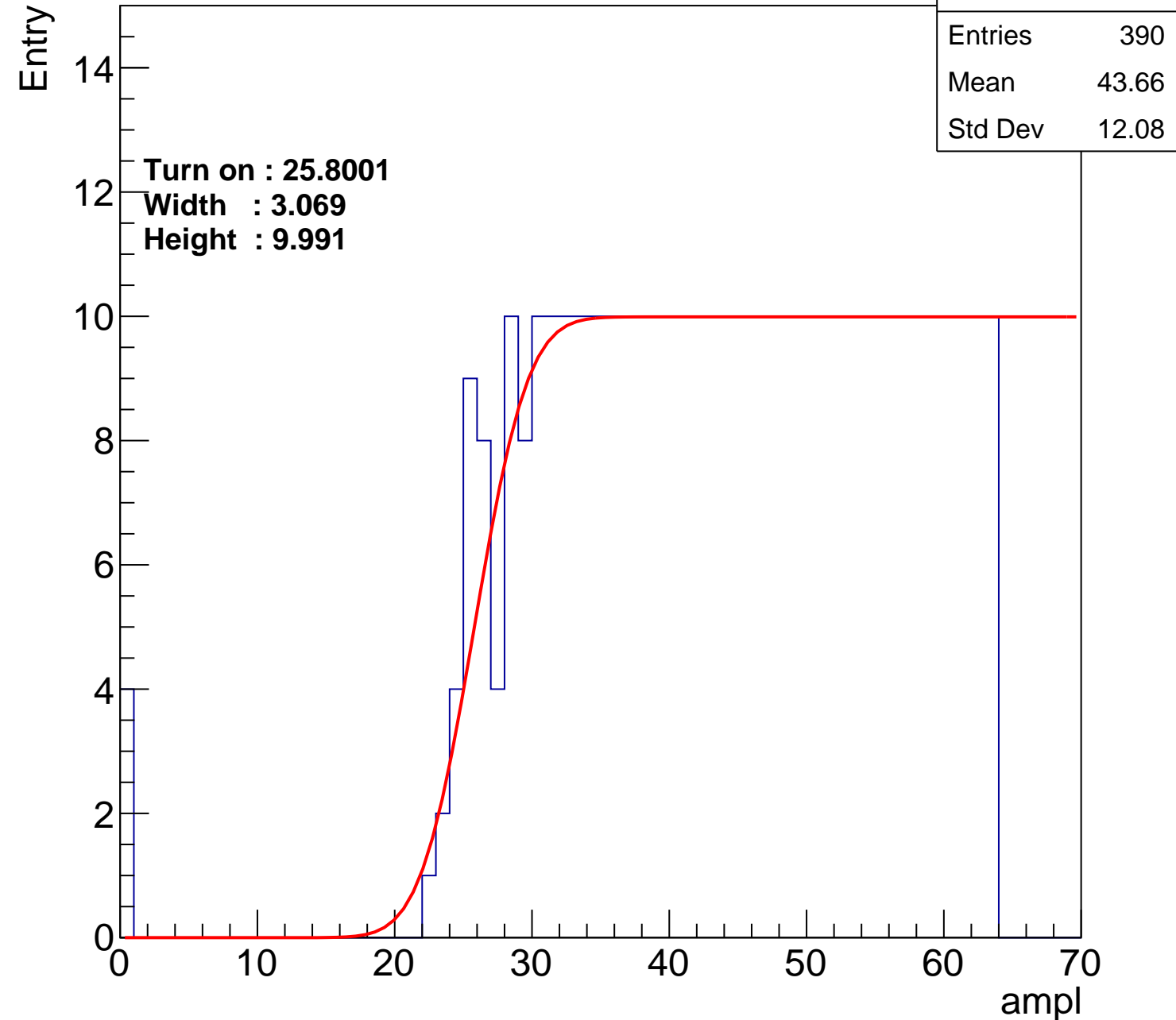
Width : 3.069

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch98

calib_packv5_042523_0143.root, FC#4, port A2

Entries	383
Mean	44.18
Std Dev	11.56

Turn on : 26.4930

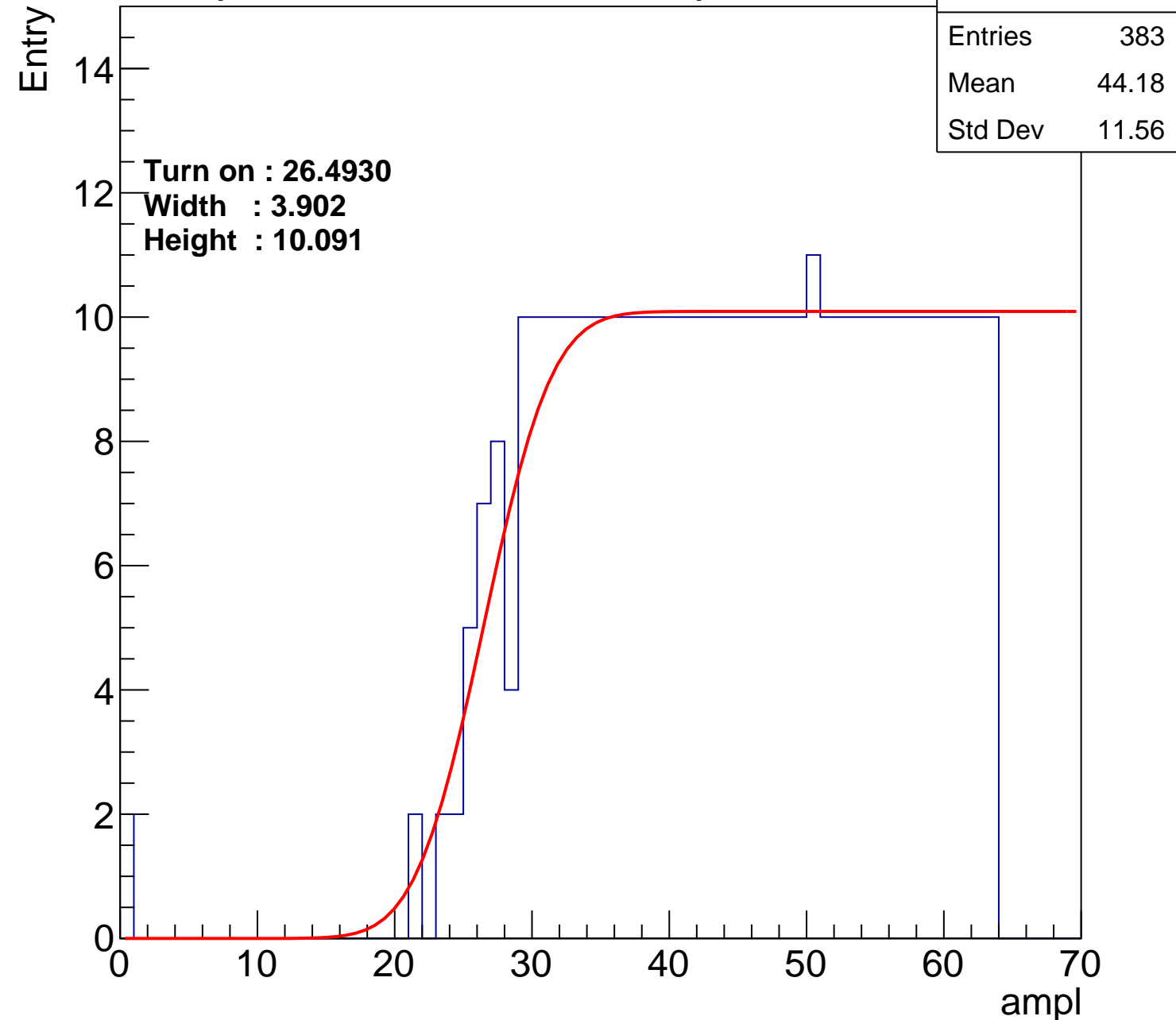
Width : 3.902

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch99

calib_packv5_042523_0143.root, FC#4, port A2

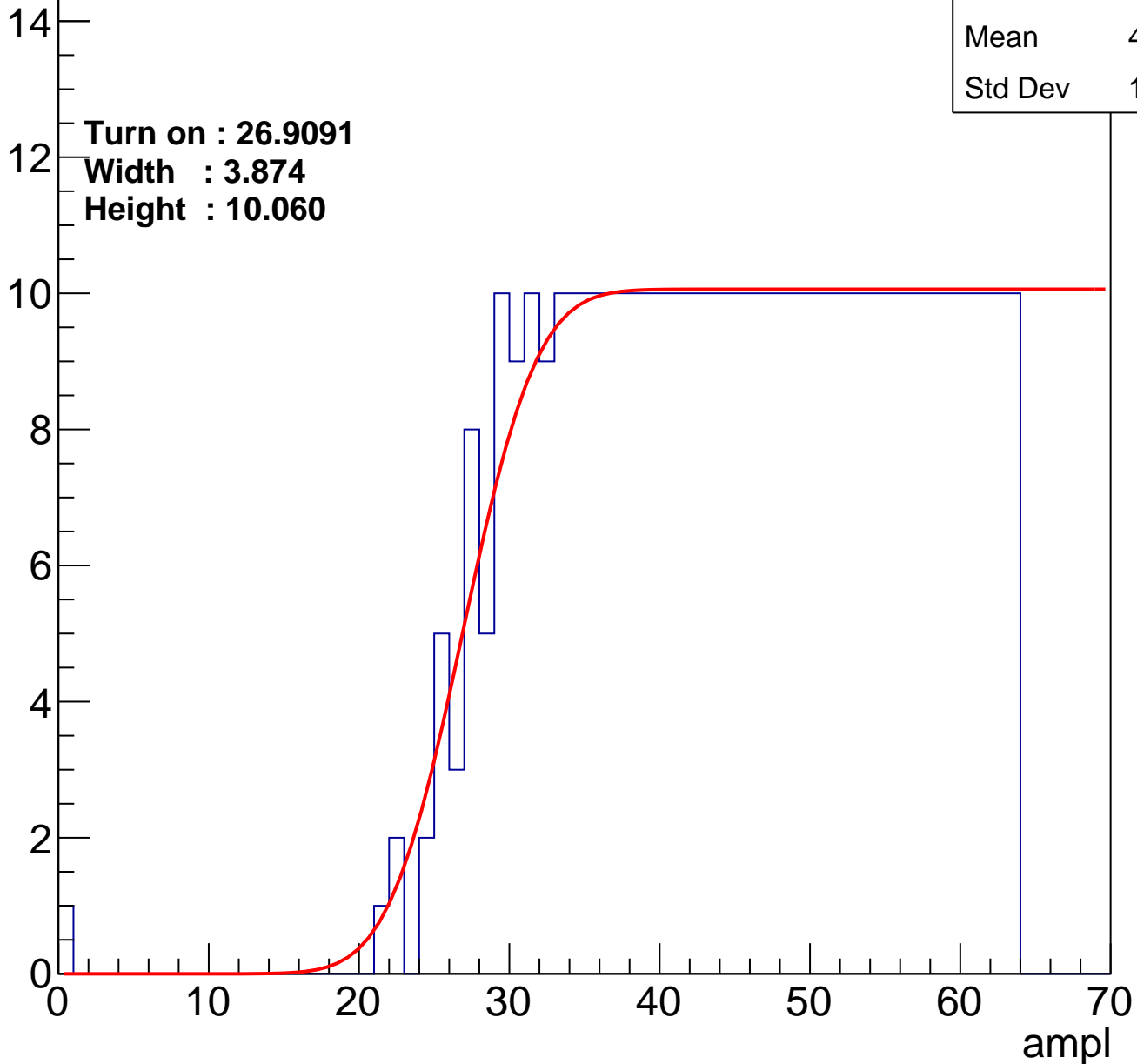
Entries	375
Mean	44.56
Std Dev	11.22

Turn on : 26.9091

Width : 3.874

Height : 10.060

Entry



B1L100S, U23-ch100

calib_packv5_042523_0143.root, FC#4, port A2

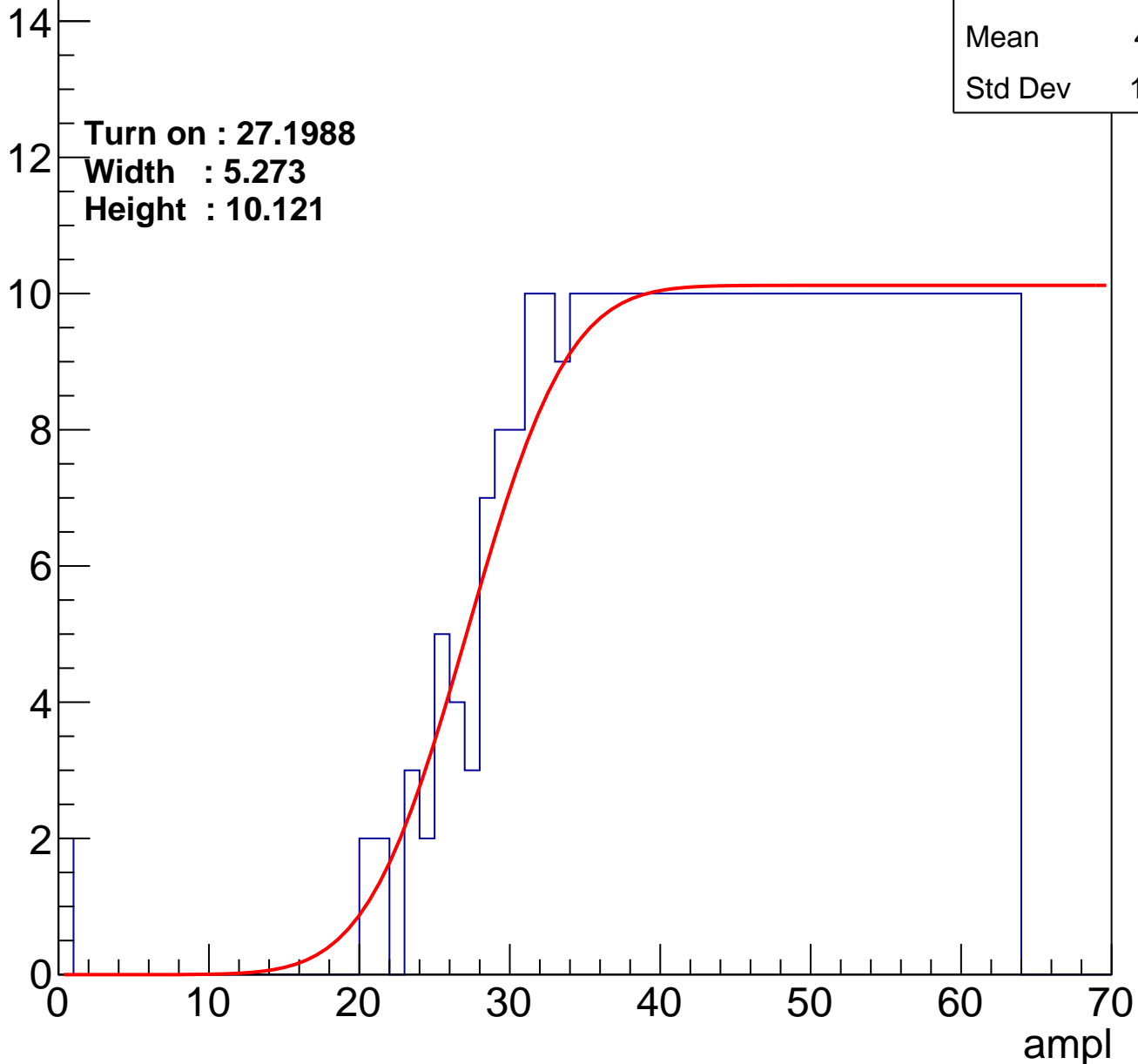
Entries	375
Mean	44.41
Std Dev	11.55

Turn on : 27.1988

Width : 5.273

Height : 10.121

Entry



B1L100S, U23-ch101

calib_packv5_042523_0143.root, FC#4, port A2

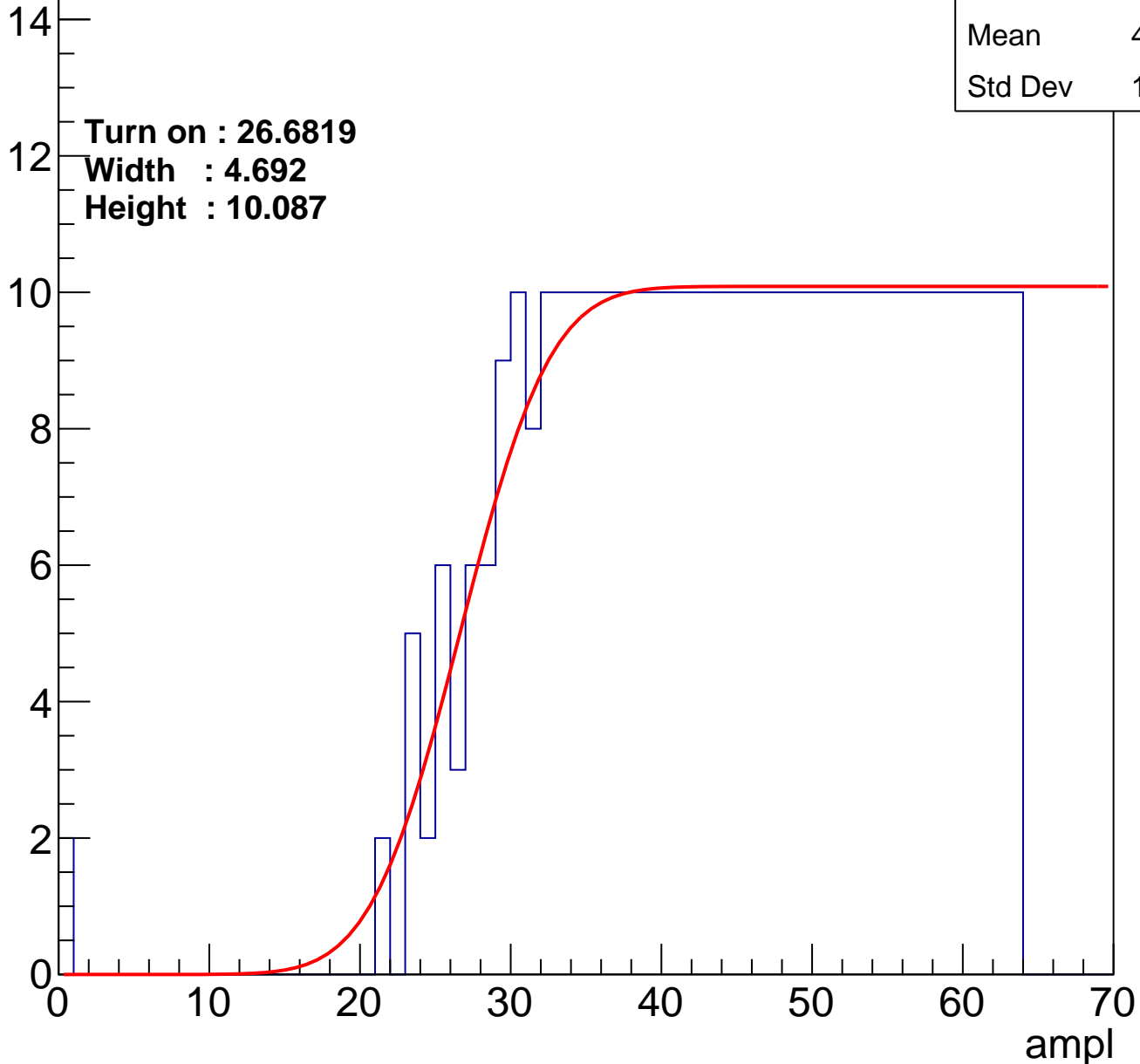
Entries	379
Mean	44.25
Std Dev	11.58

Turn on : 26.6819

Width : 4.692

Height : 10.087

Entry



B1L100S, U23-ch102

calib_packv5_042523_0143.root, FC#4, port A2

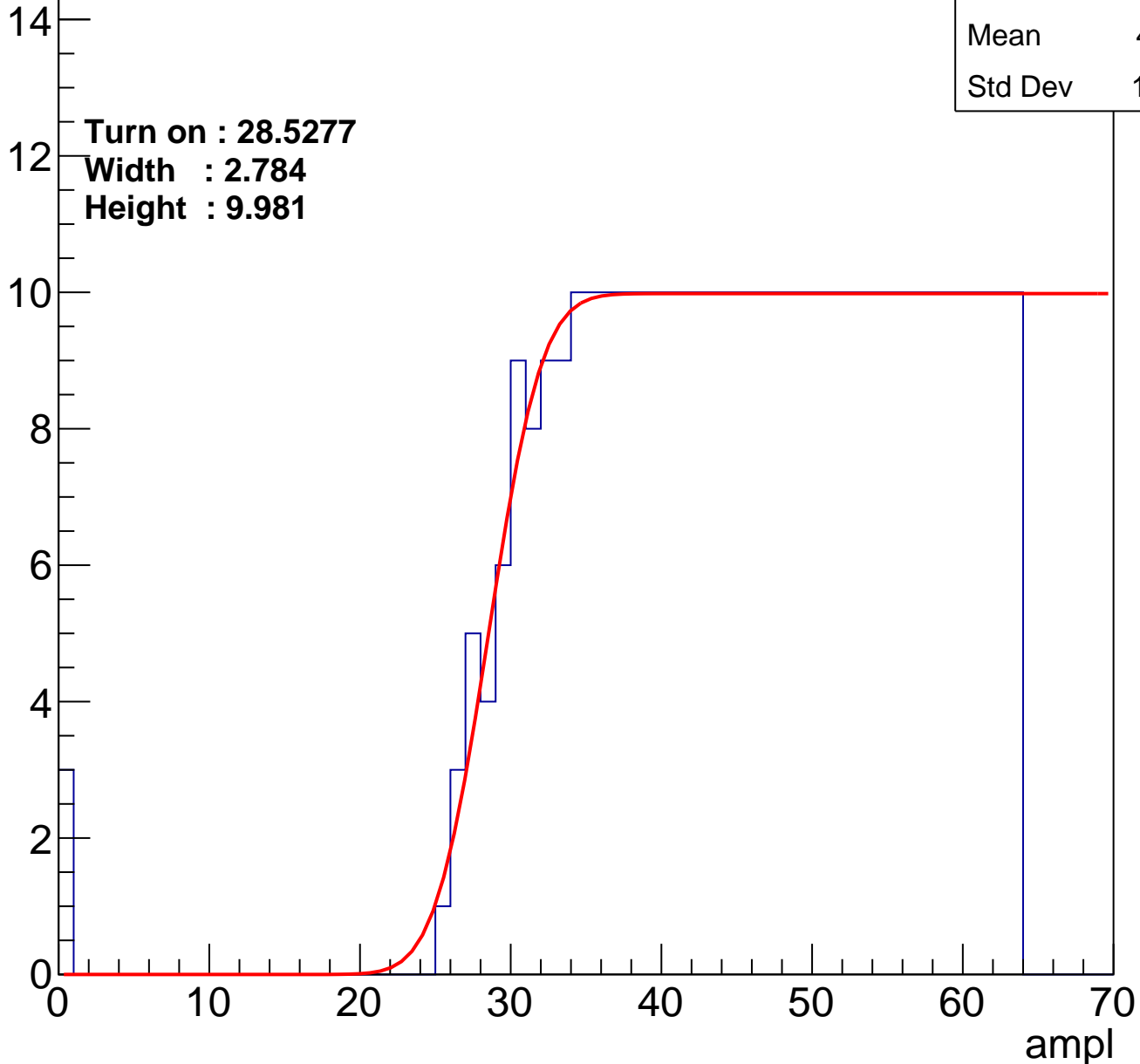
Entries	357
Mean	45.31
Std Dev	11.15

Turn on : 28.5277

Width : 2.784

Height : 9.981

Entry



B1L100S, U23-ch103

calib_packv5_042523_0143.root, FC#4, port A2

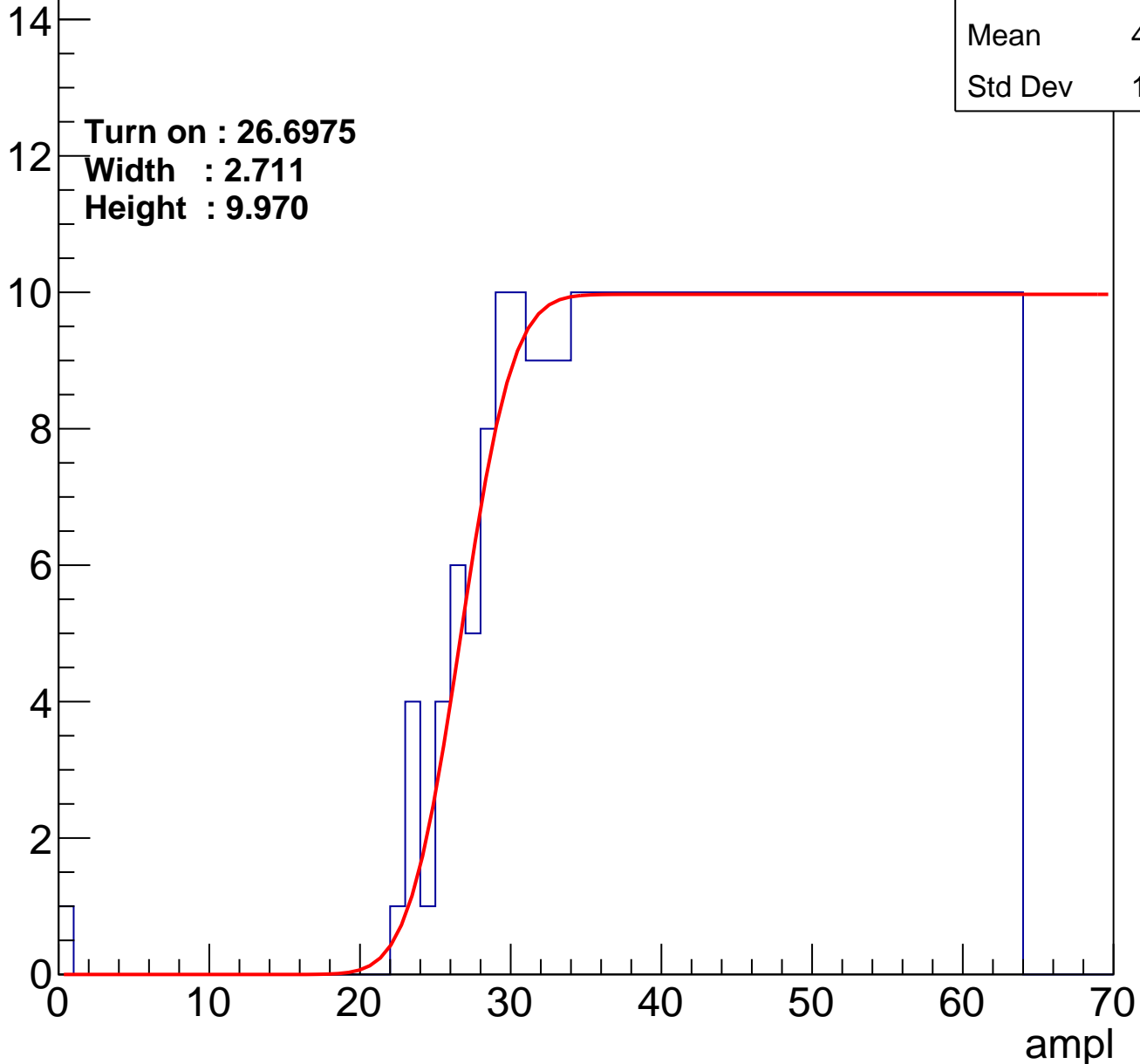
Entries	377
Mean	44.45
Std Dev	11.29

Turn on : 26.6975

Width : 2.711

Height : 9.970

Entry



B1L100S, U23-ch104

calib_packv5_042523_0143.root, FC#4, port A2

Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 26.8216

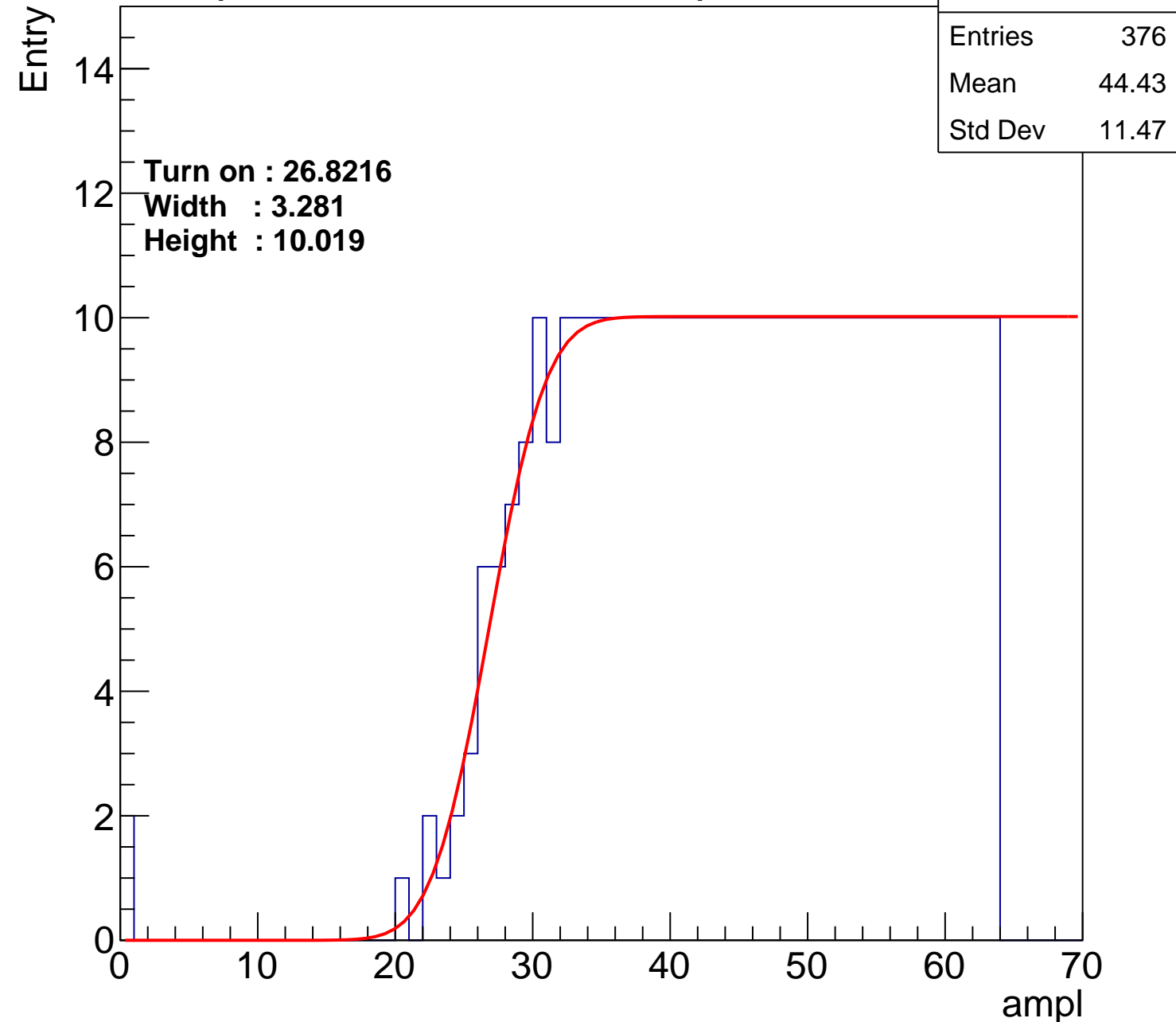
Width : 3.281

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch105

calib_packv5_042523_0143.root, FC#4, port A2

Entries	369
Mean	44.74
Std Dev	11.42

Turn on : 27.4226

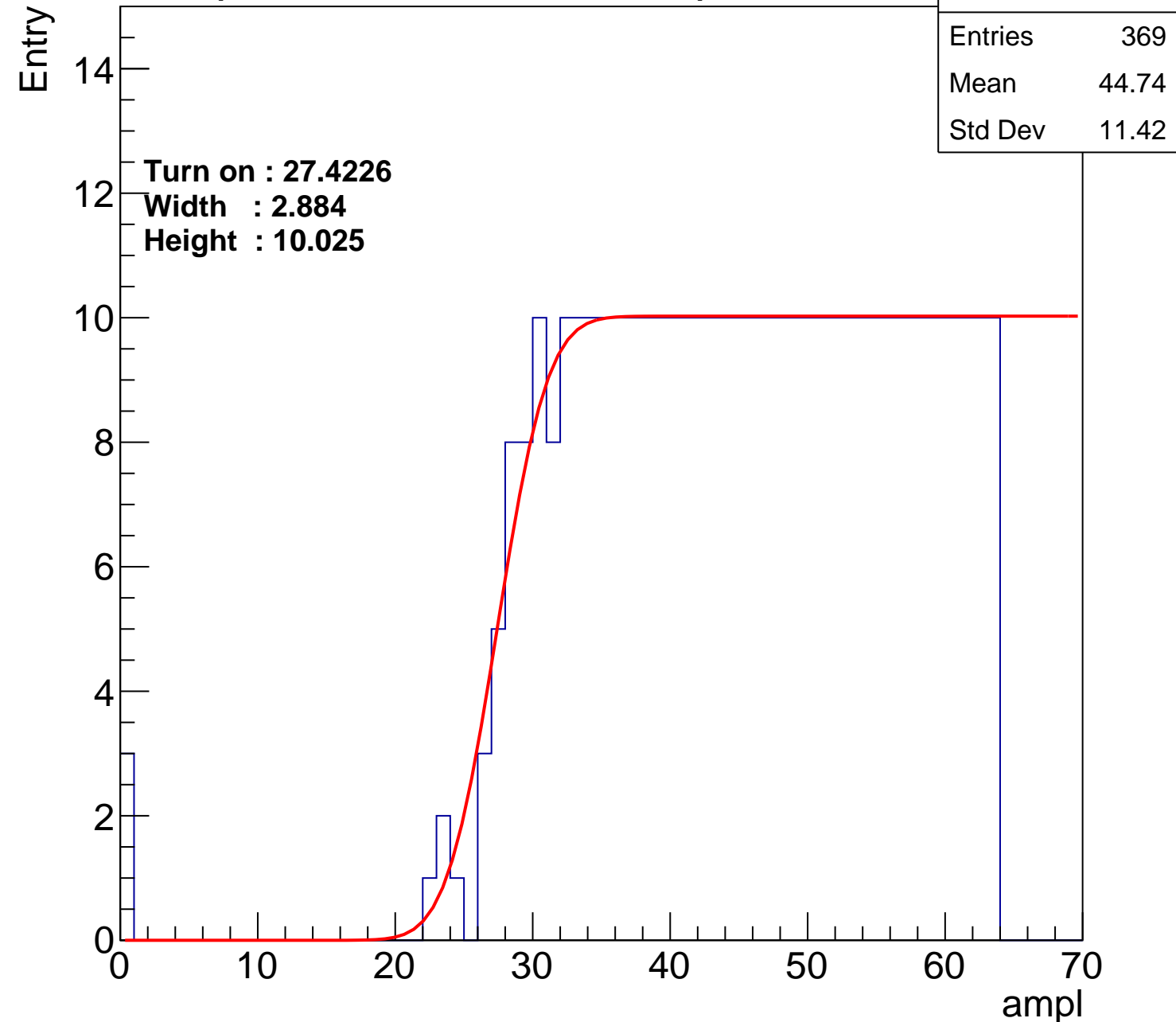
Width : 2.884

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch106

calib_packv5_042523_0143.root, FC#4, port A2

Entries	404
Mean	43.12
Std Dev	12.07

Turn on : 23.9896

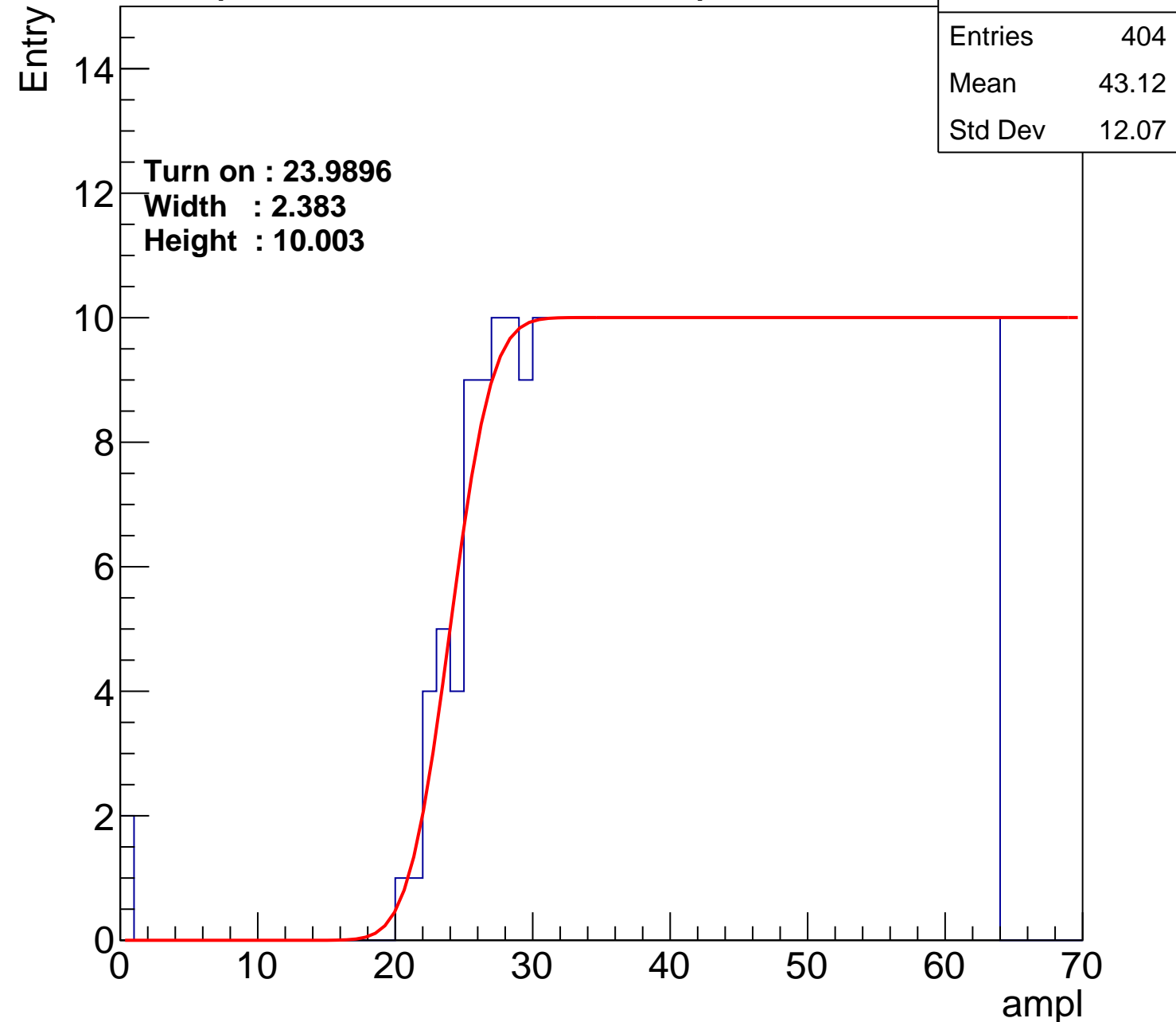
Width : 2.383

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch107

calib_packv5_042523_0143.root, FC#4, port A2

Entries	366
Mean	44.79
Std Dev	11.59

Turn on : 28.5775

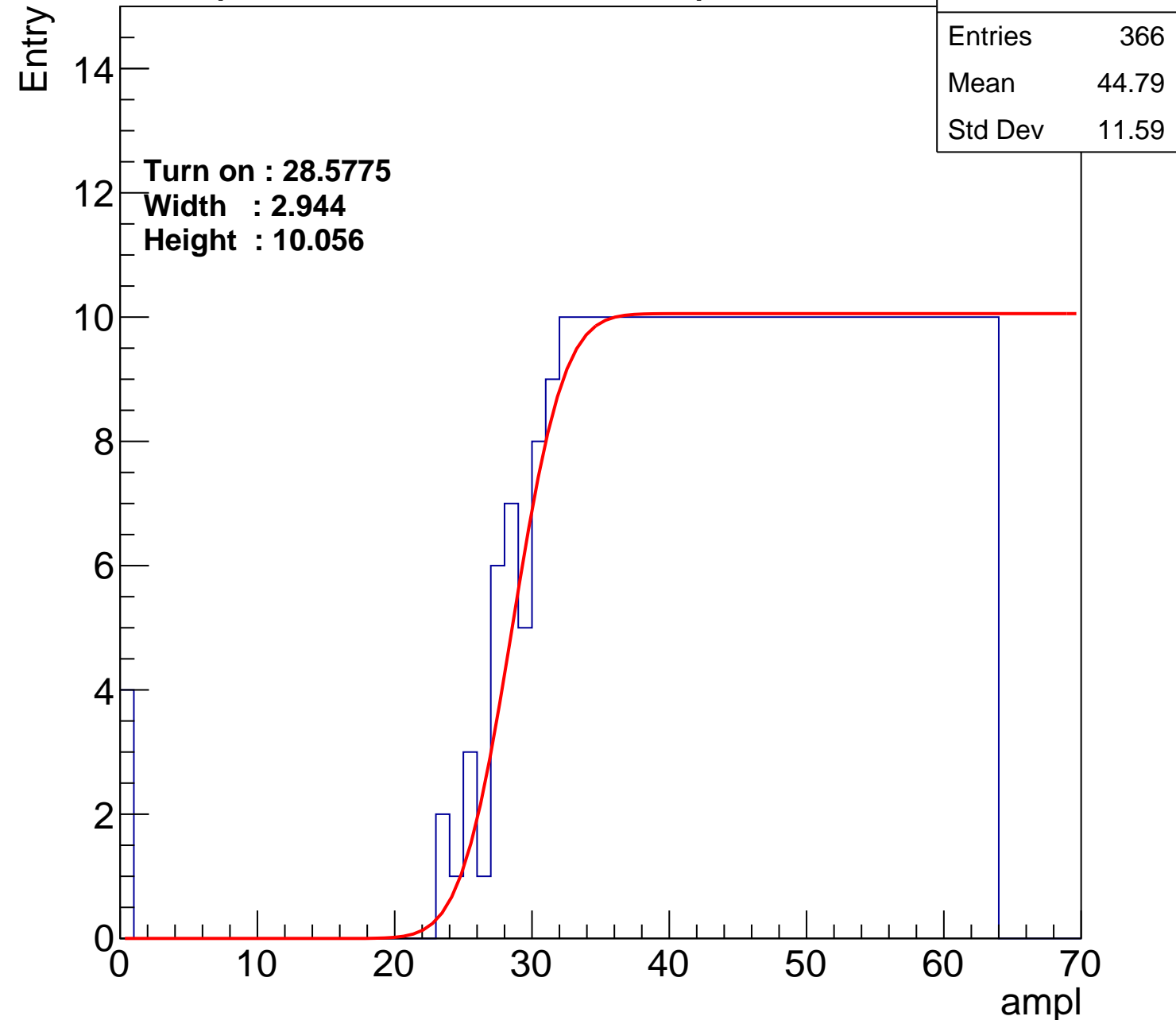
Width : 2.944

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch108

calib_packv5_042523_0143.root, FC#4, port A2

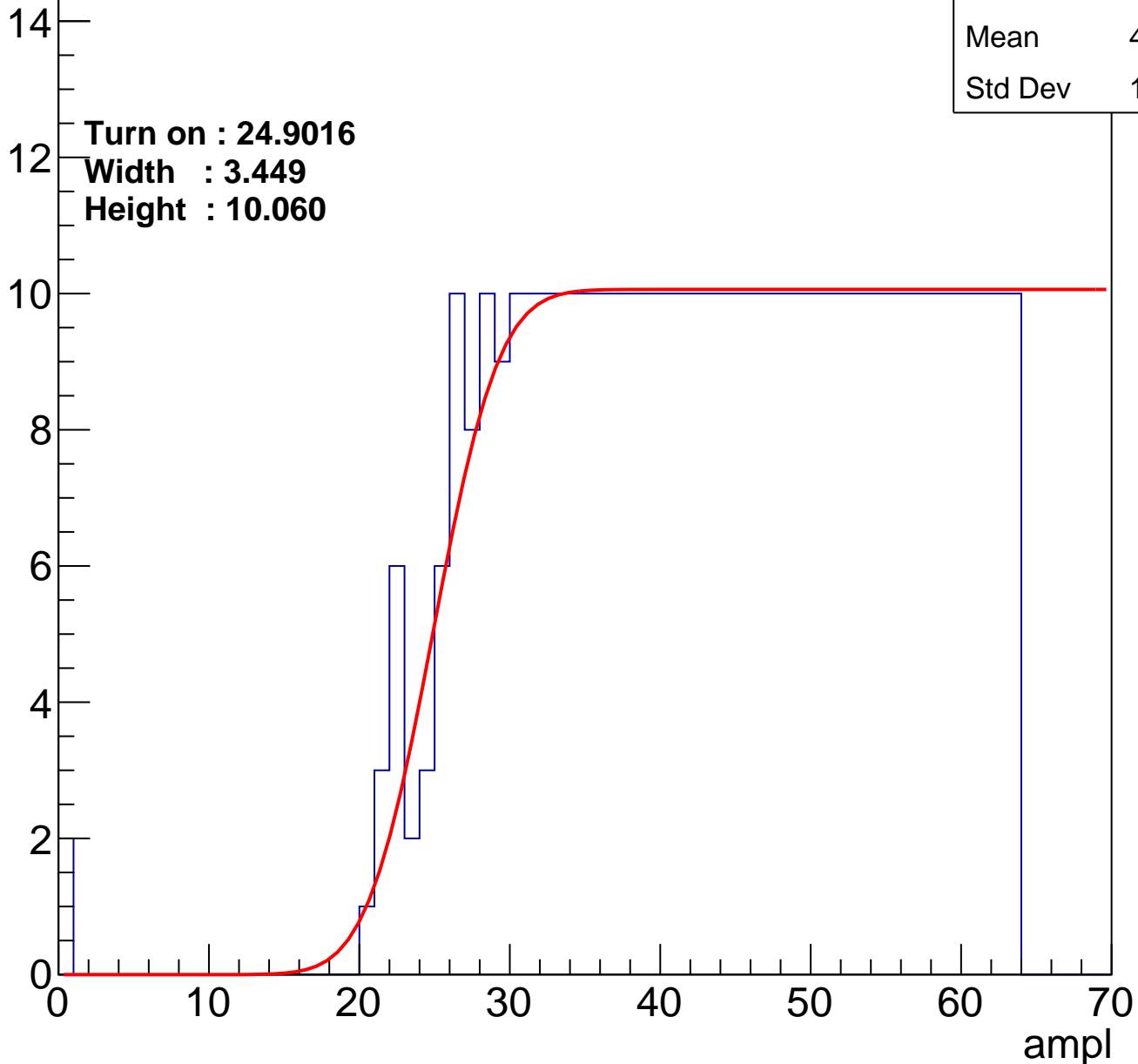
Entries	400
Mean	43.27
Std Dev	12.04

Turn on : 24.9016

Width : 3.449

Height : 10.060

Entry



B1L100S, U23-ch109

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.61
Std Dev	12.16

Turn on : 25.4590

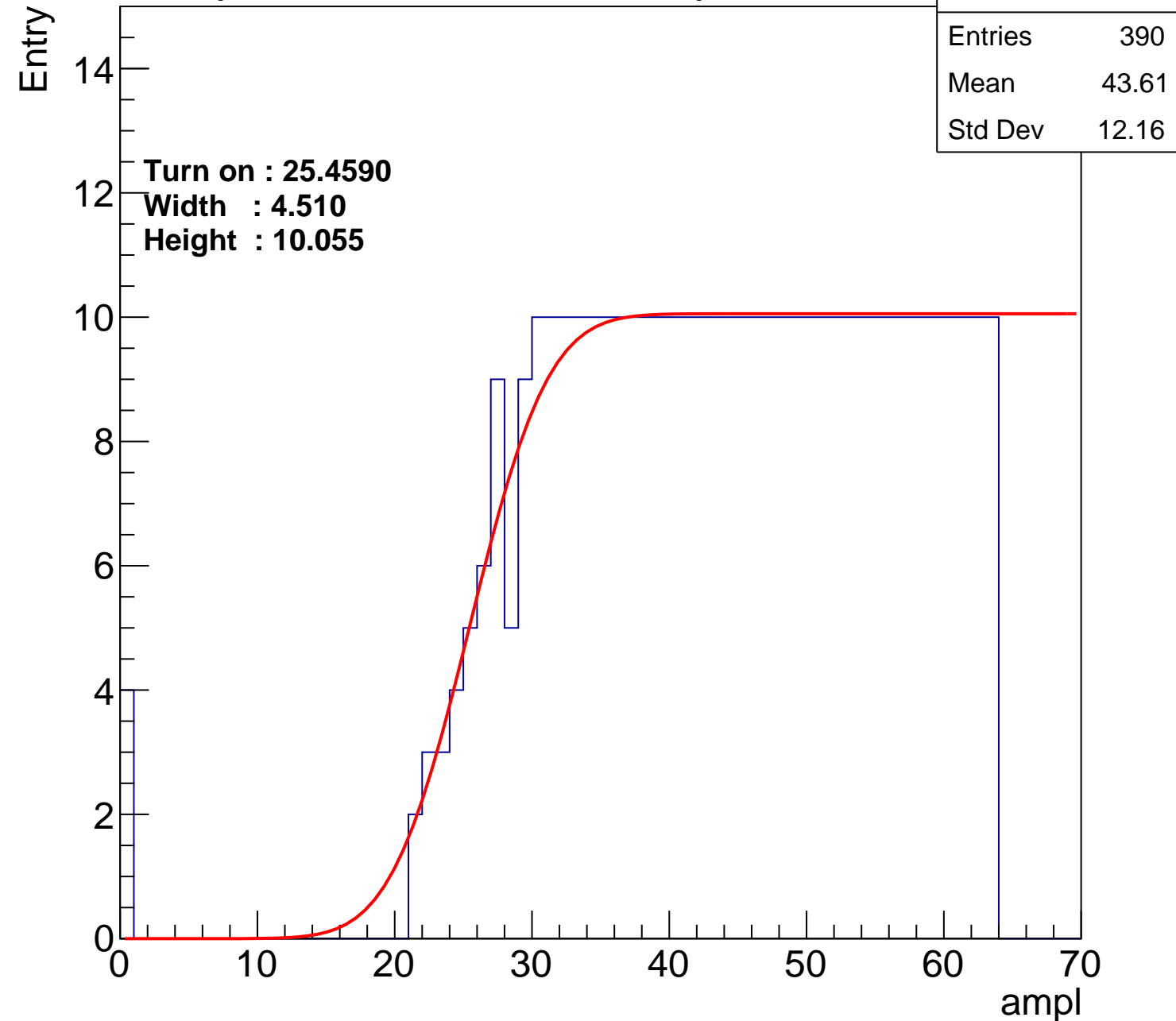
Width : 4.510

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch110

calib_packv5_042523_0143.root, FC#4, port A2

Entries	373
Mean	44.57
Std Dev	11.4

Turn on : 27.4659

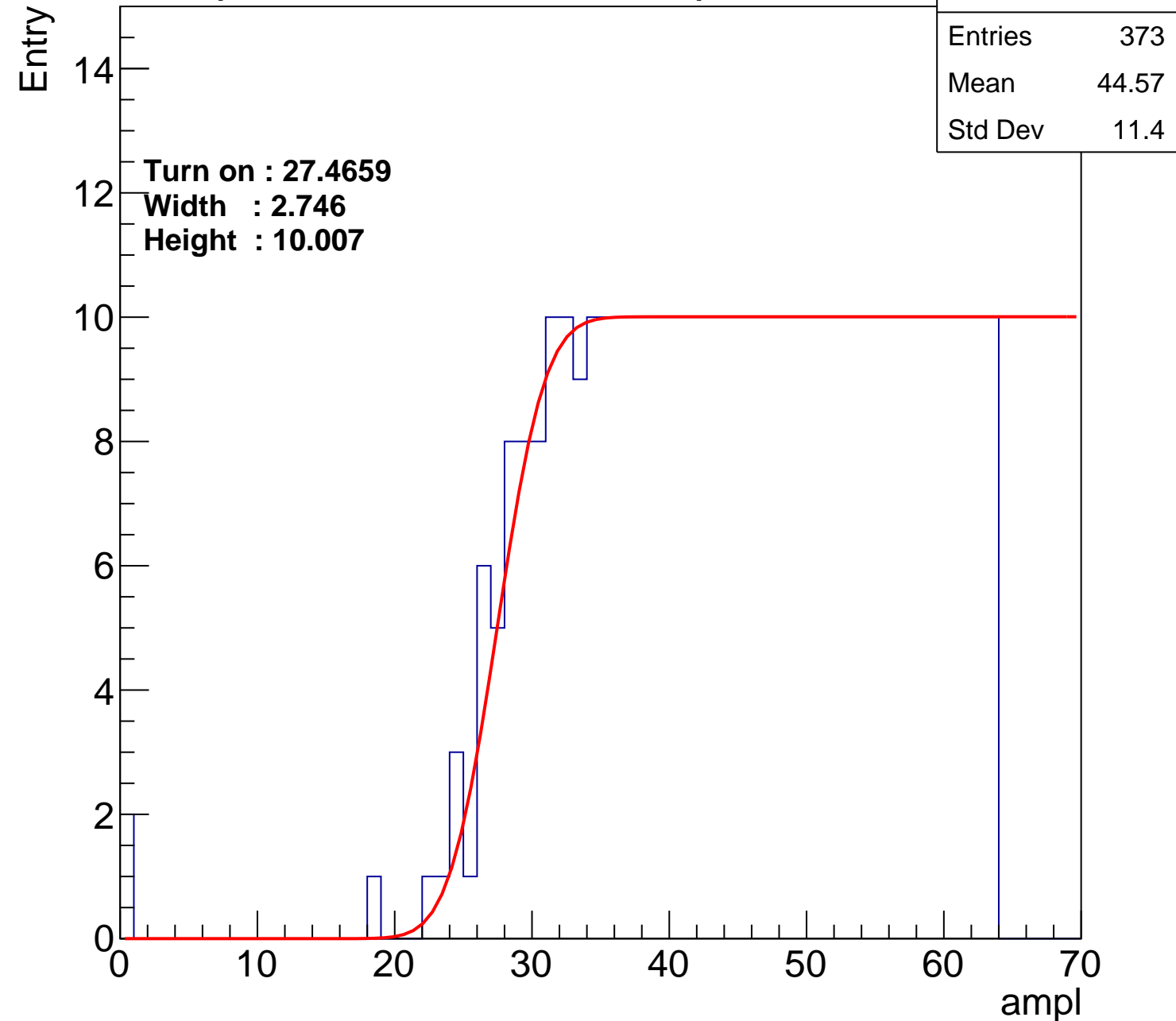
Width : 2.746

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch111

calib_packv5_042523_0143.root, FC#4, port A2

Entries	348
Mean	45.85
Std Dev	10.68

Turn on : 29.8442

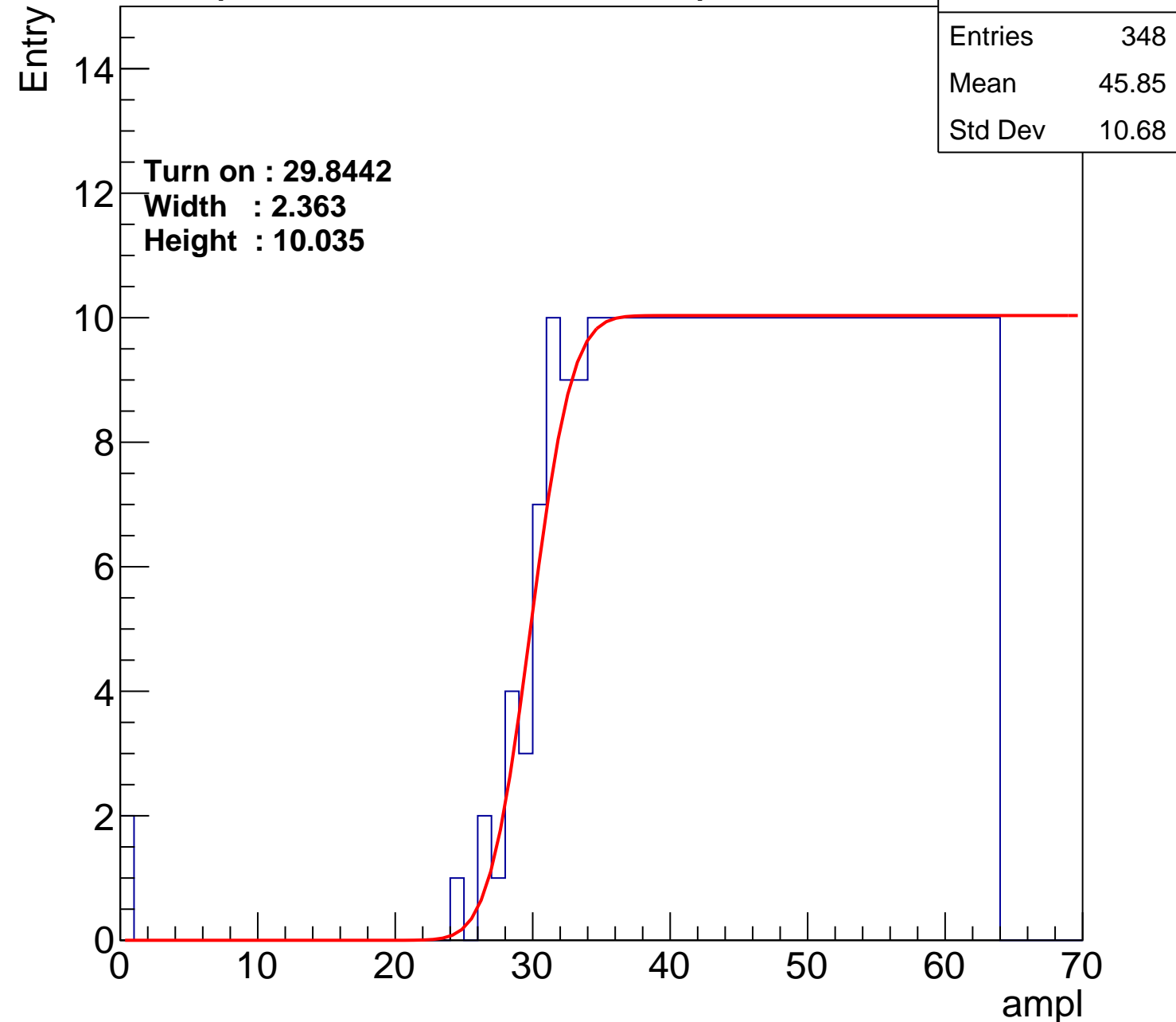
Width : 2.363

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch112

calib_packv5_042523_0143.root, FC#4, port A2

Entries	389
Mean	43.77
Std Dev	11.82

Turn on : 25.1724

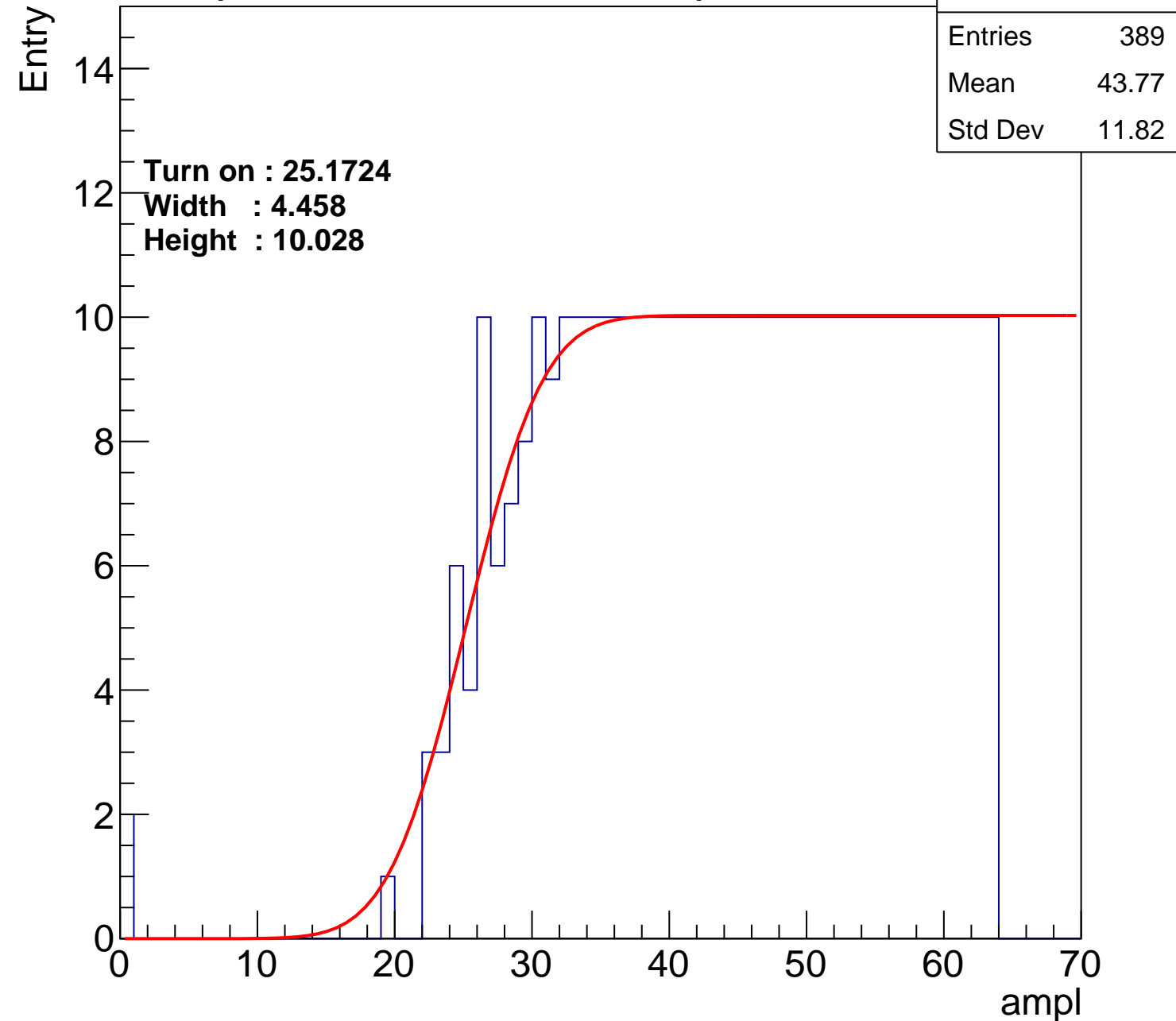
Width : 4.458

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch113

calib_packv5_042523_0143.root, FC#4, port A2

Entries	390
Mean	43.73
Std Dev	11.84

Turn on : 25.9542

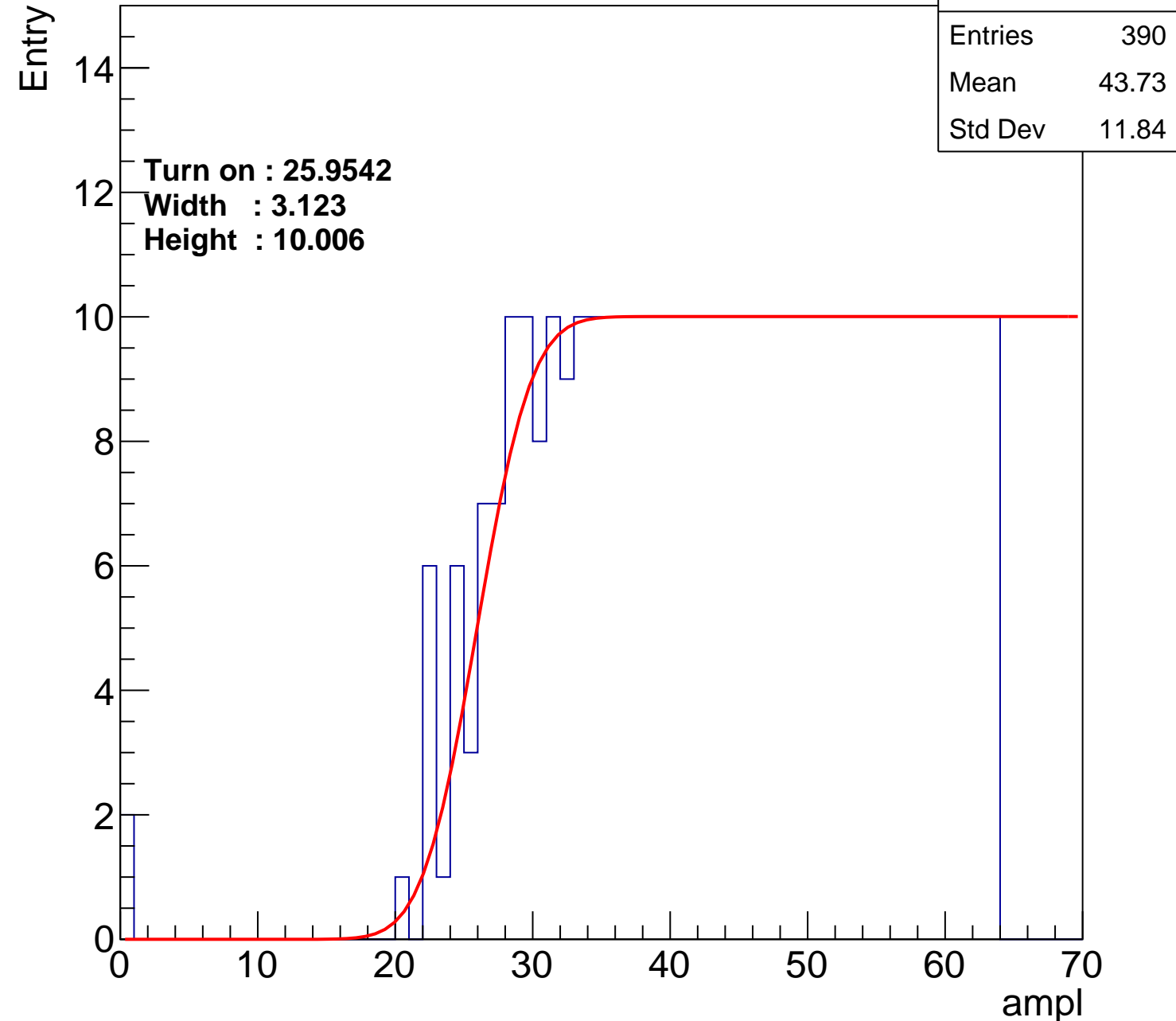
Width : 3.123

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch114

calib_packv5_042523_0143.root, FC#4, port A2

Entries	384
Mean	43.88
Std Dev	12.05

Turn on : 26.3745

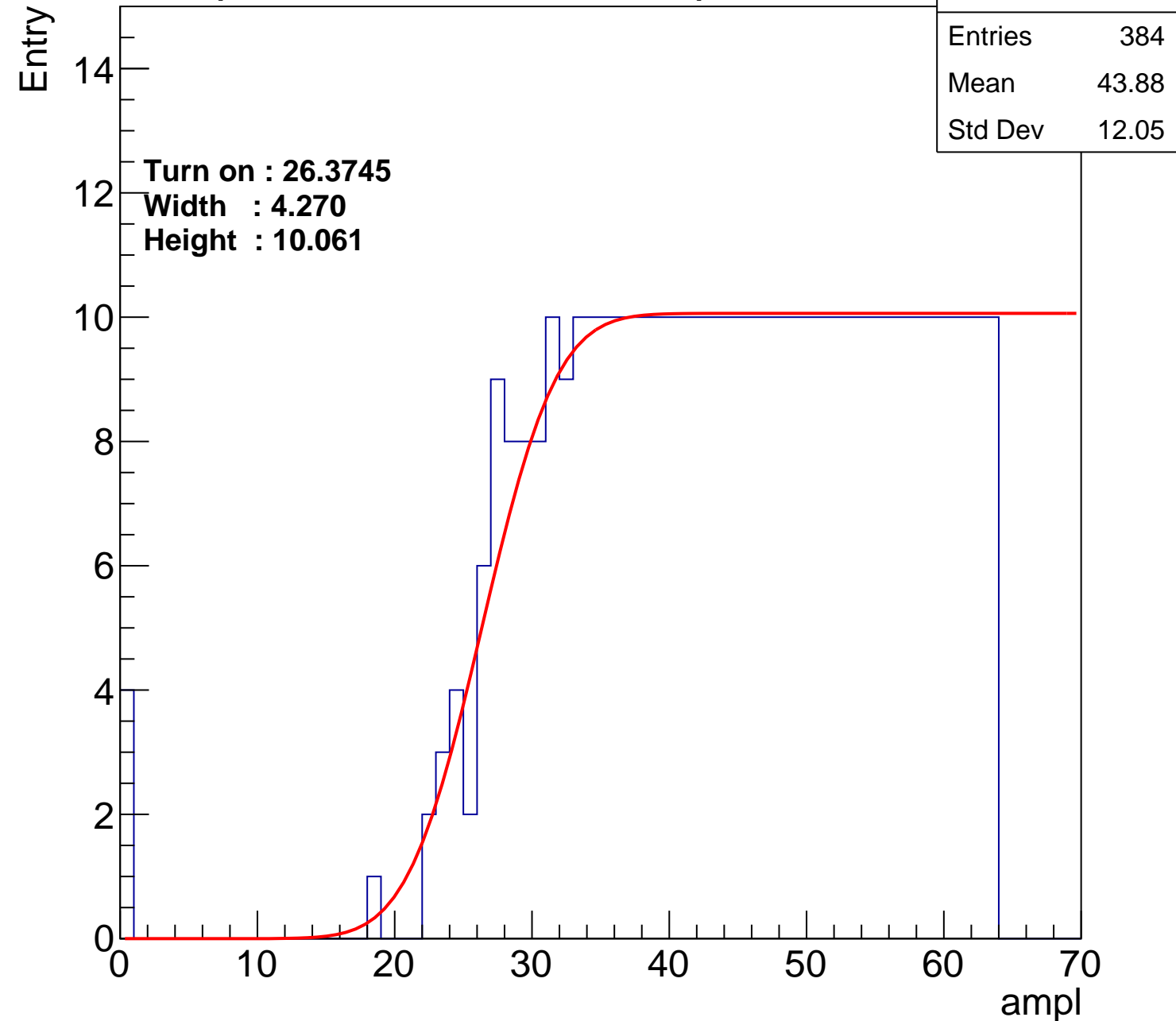
Width : 4.270

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch115

calib_packv5_042523_0143.root, FC#4, port A2

Entries	385
Mean	43.98
Std Dev	11.69

Turn on : 25.9337

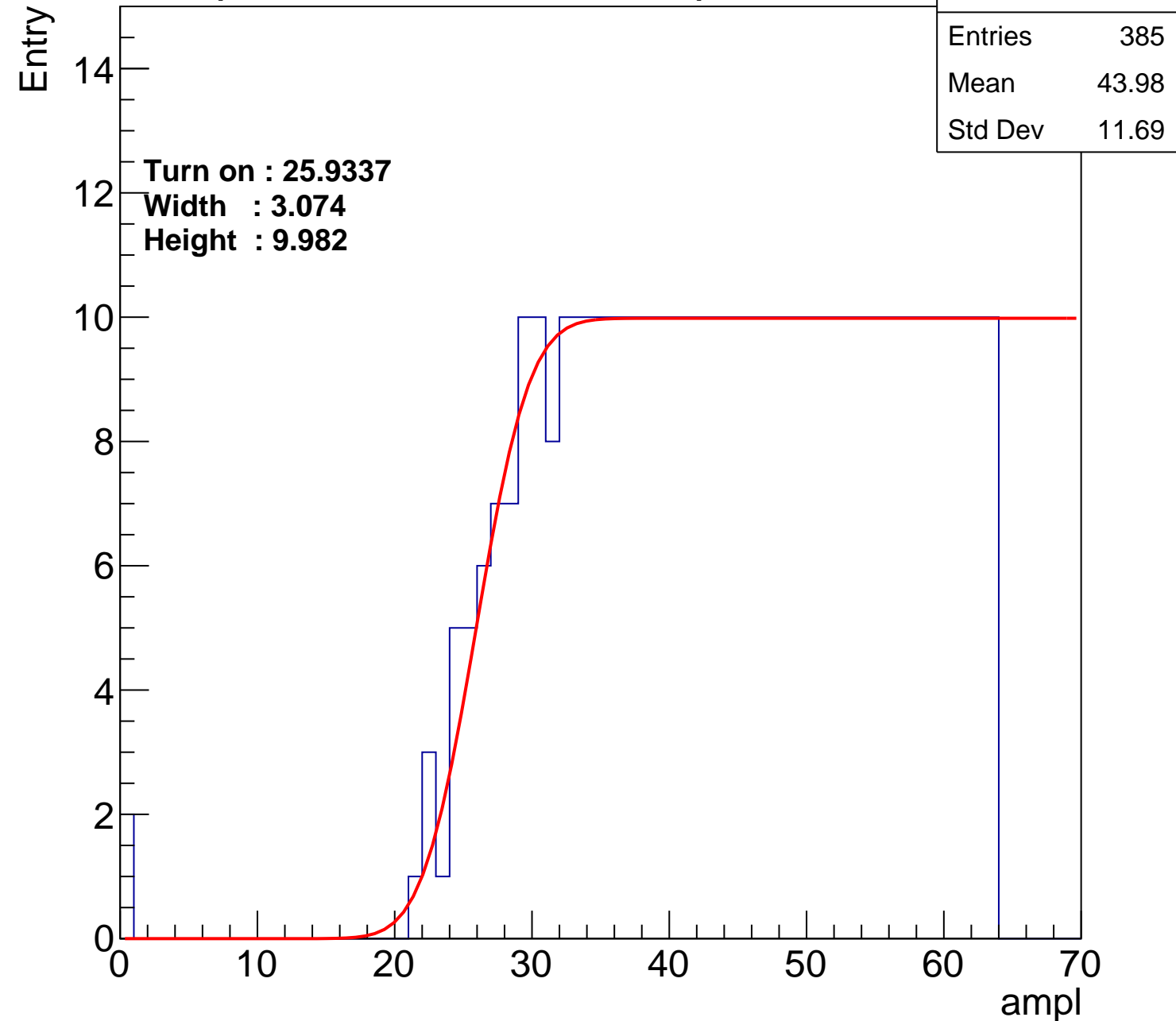
Width : 3.074

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch116

calib_packv5_042523_0143.root, FC#4, port A2

Entries	370
Mean	44.82
Std Dev	11.08

Turn on : 27.7618

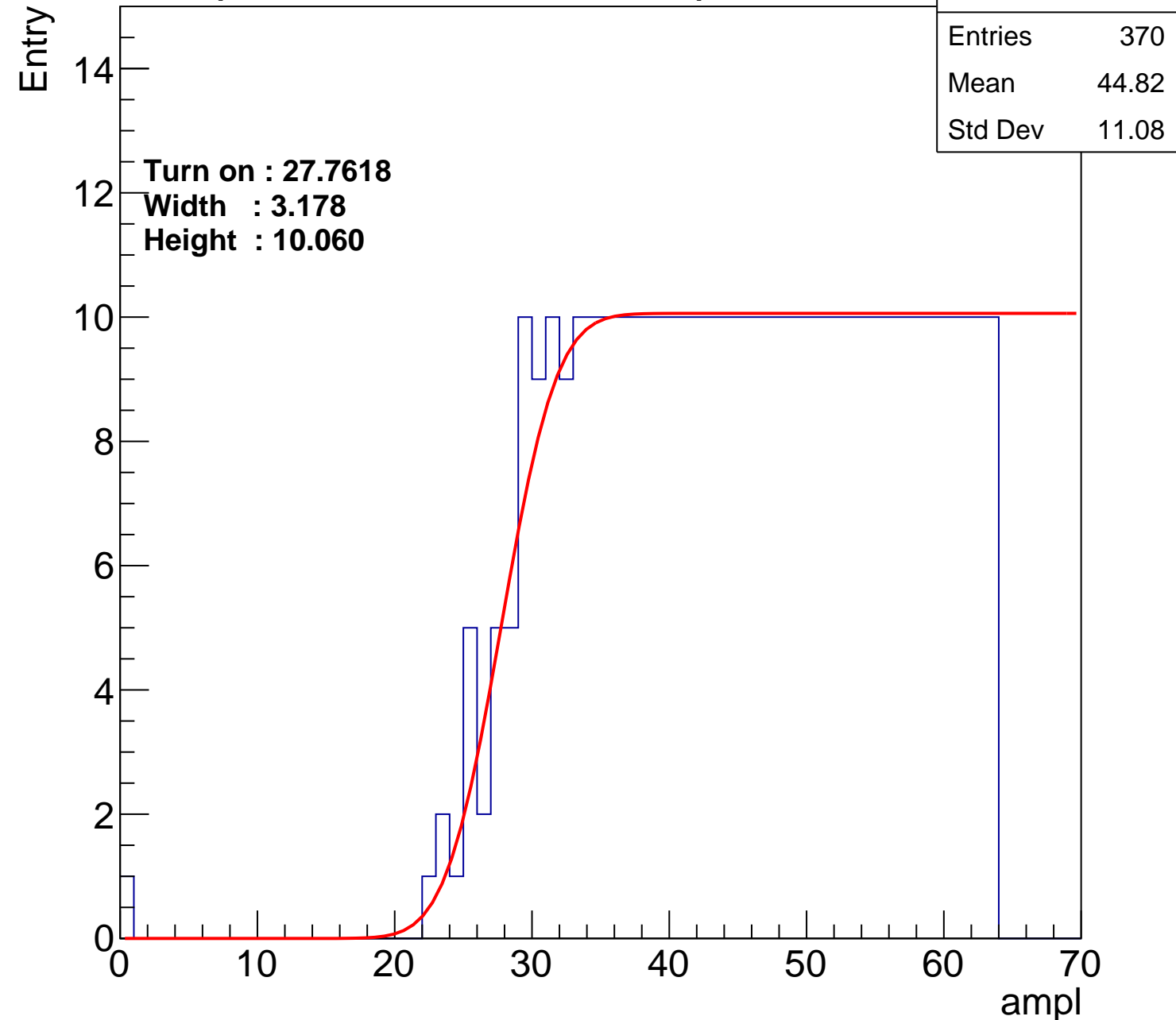
Width : 3.178

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch117

calib_packv5_042523_0143.root, FC#4, port A2

Entries	374
Mean	44.56
Std Dev	11.37

Turn on : 26.9809

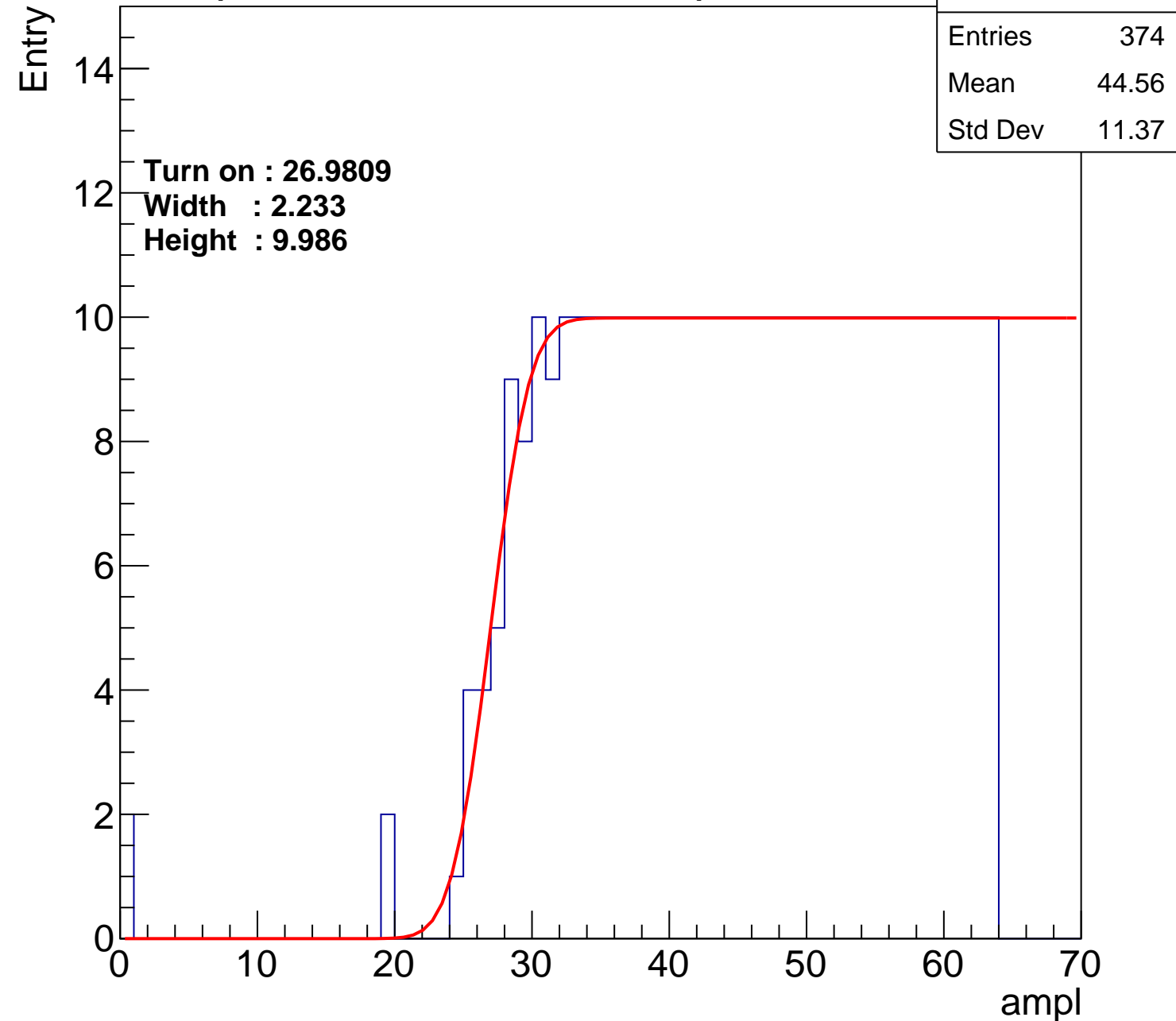
Width : 2.233

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch118

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.65
Std Dev	11.46

Turn on : 27.5030

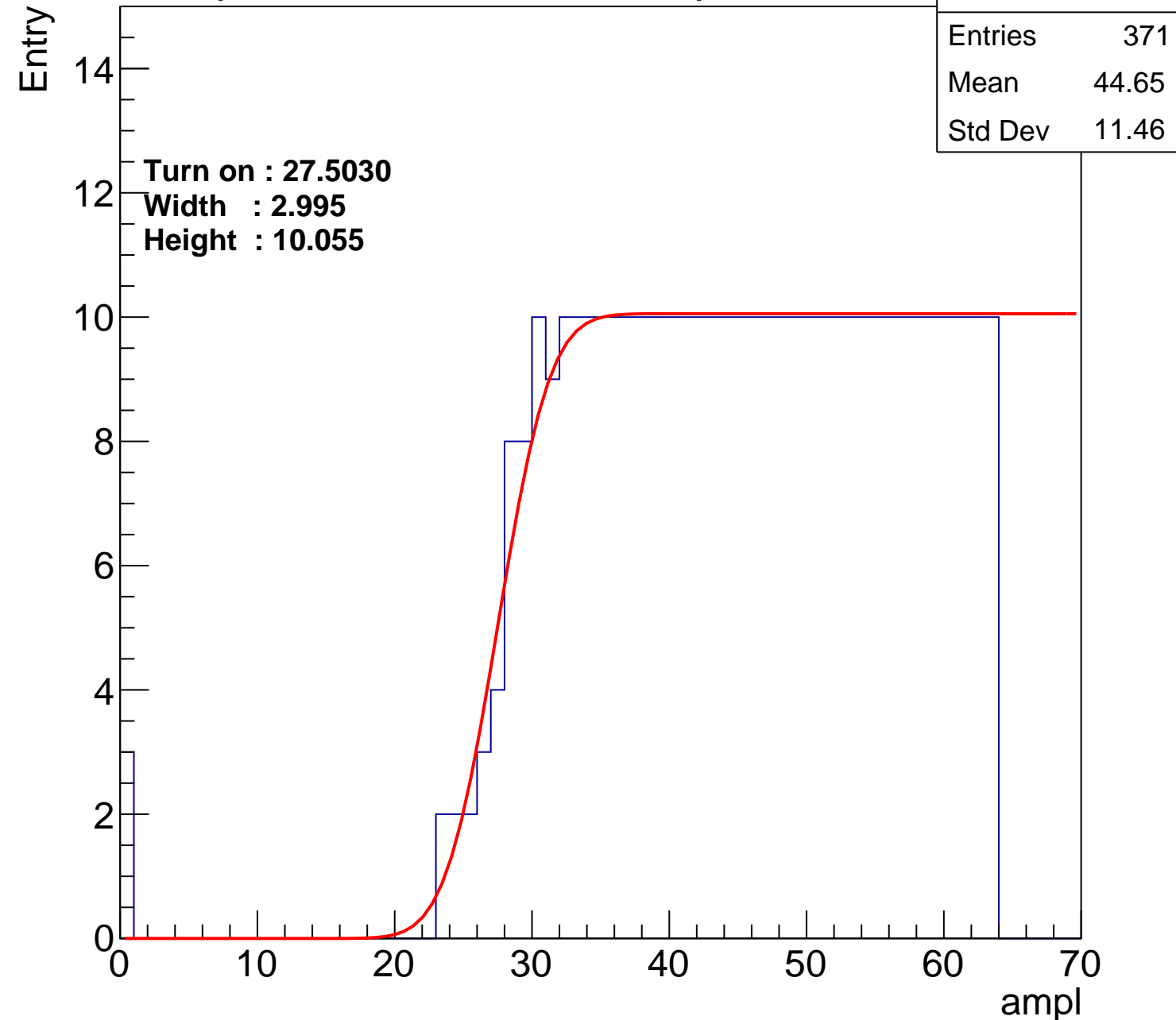
Width : 2.995

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch119

calib_packv5_042523_0143.root, FC#4, port A2

Entries	356
Mean	45.32
Std Dev	11.2

Turn on : 29.0645

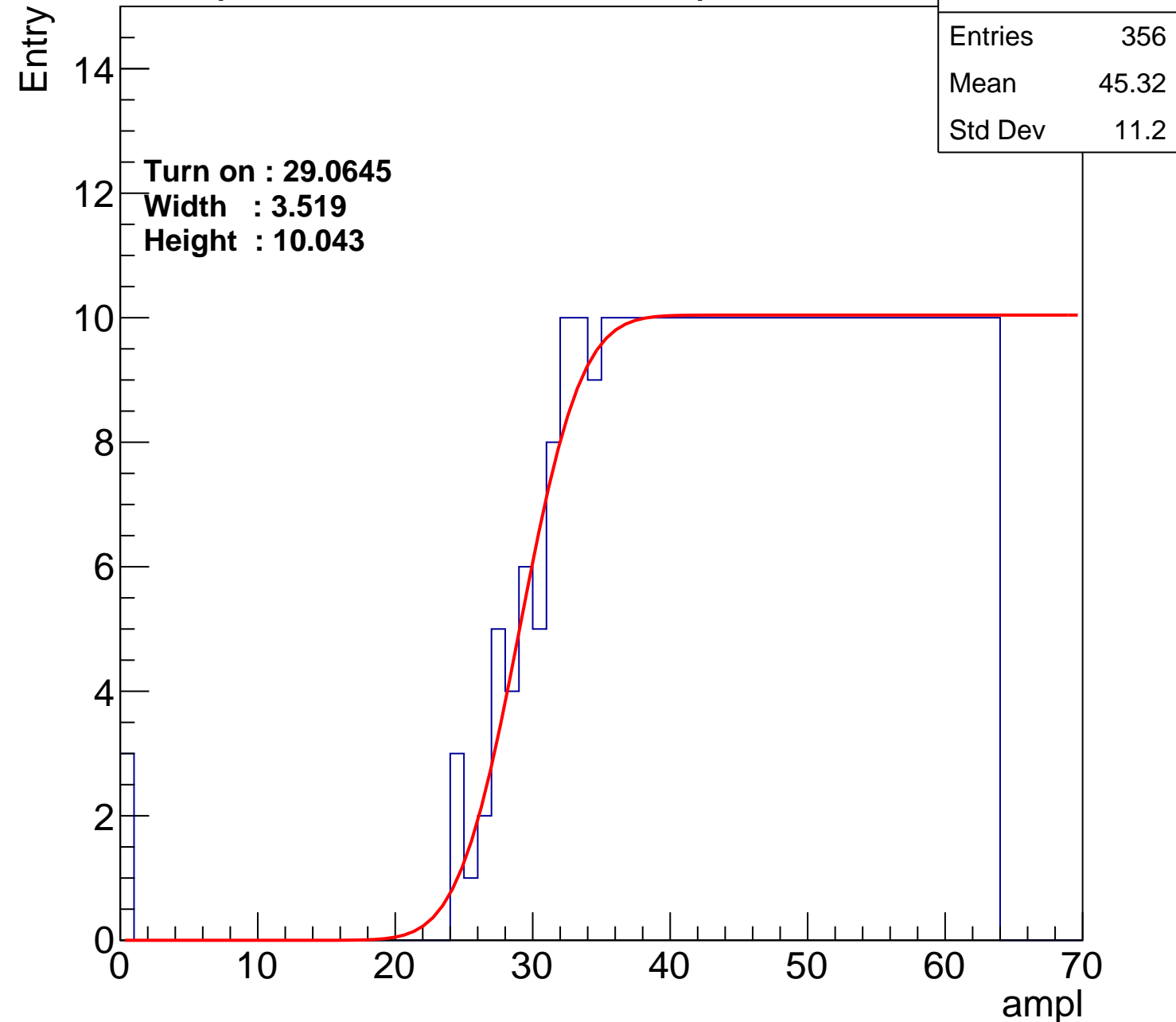
Width : 3.519

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch120

calib_packv5_042523_0143.root, FC#4, port A2

Entries	381
Mean	44.04
Std Dev	11.95

Turn on : 26.9589

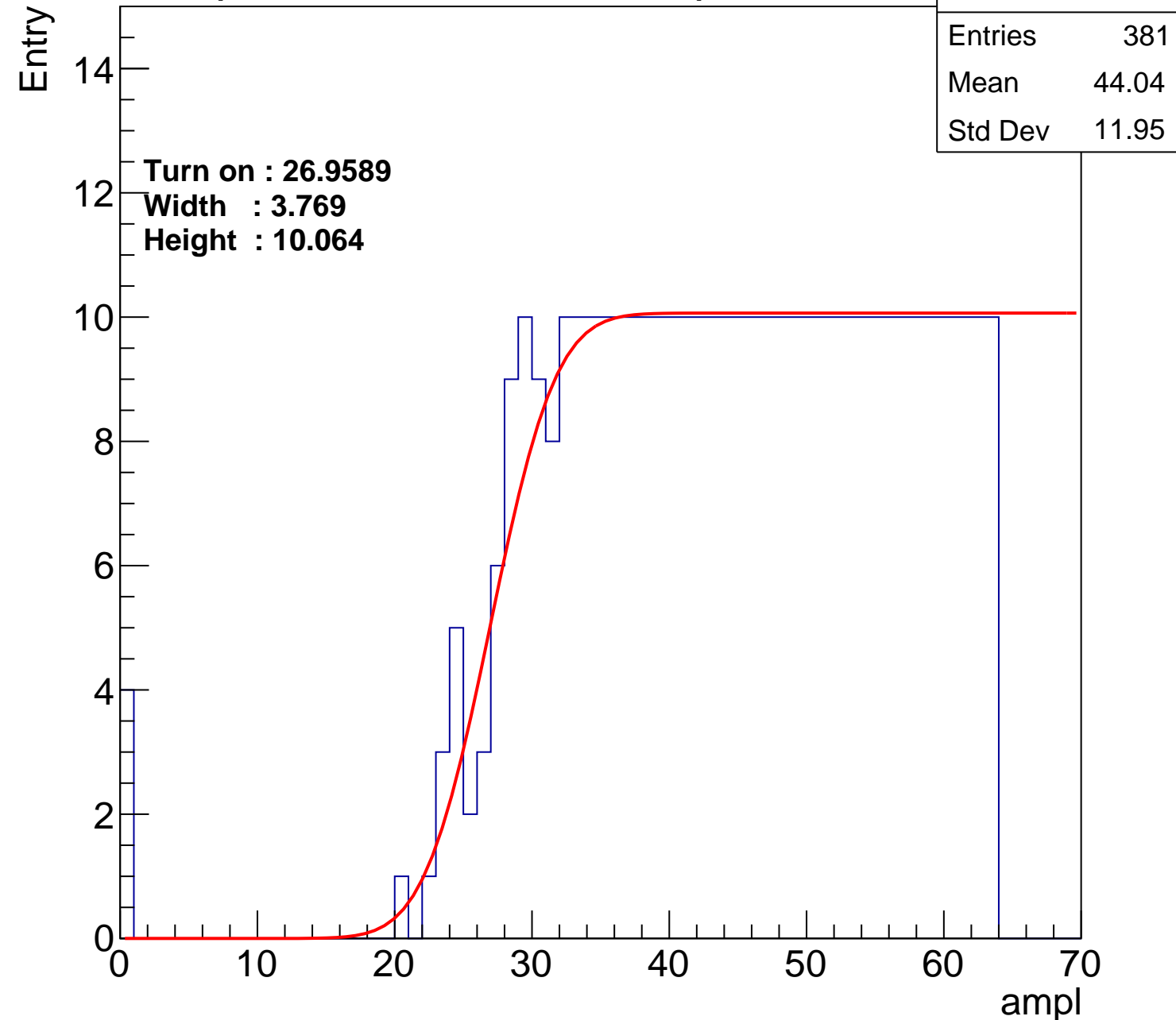
Width : 3.769

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch121

calib_packv5_042523_0143.root, FC#4, port A2

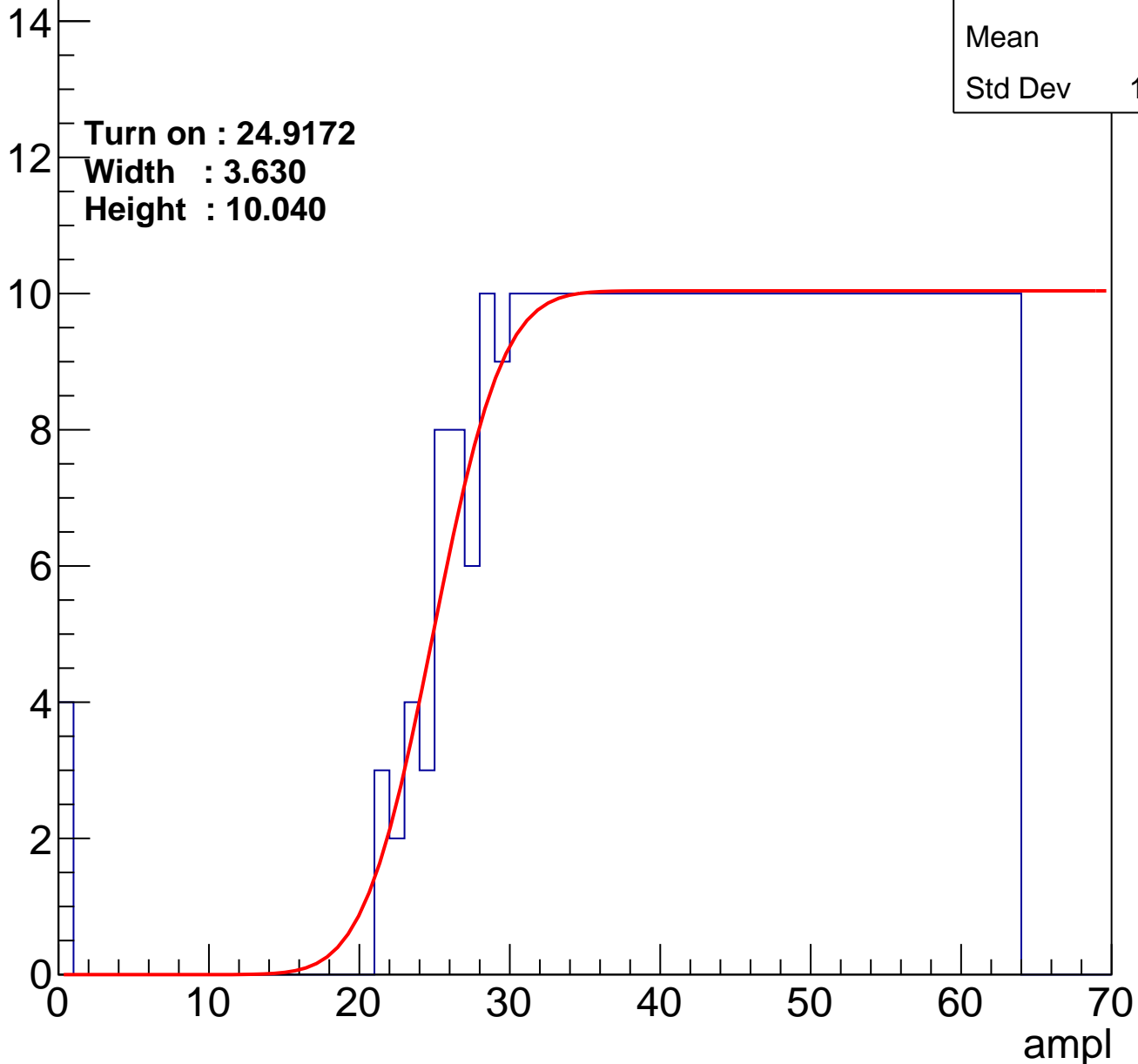
Entries	397
Mean	43.3
Std Dev	12.26

Turn on : 24.9172

Width : 3.630

Height : 10.040

Entry



B1L100S, U23-ch122

calib_packv5_042523_0143.root, FC#4, port A2

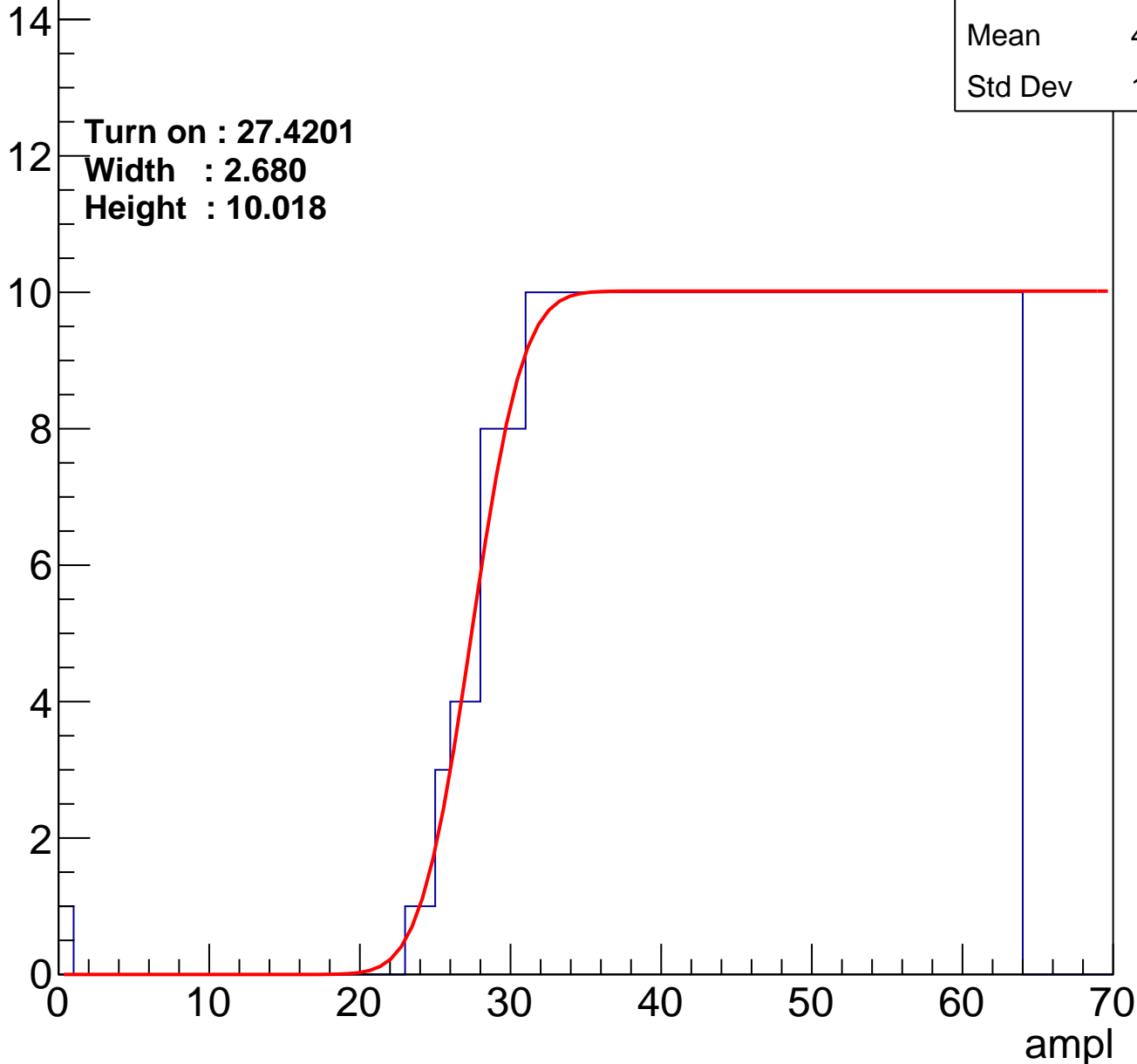
Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.4201

Width : 2.680

Height : 10.018

Entry



B1L100S, U23-ch123

calib_packv5_042523_0143.root, FC#4, port A2

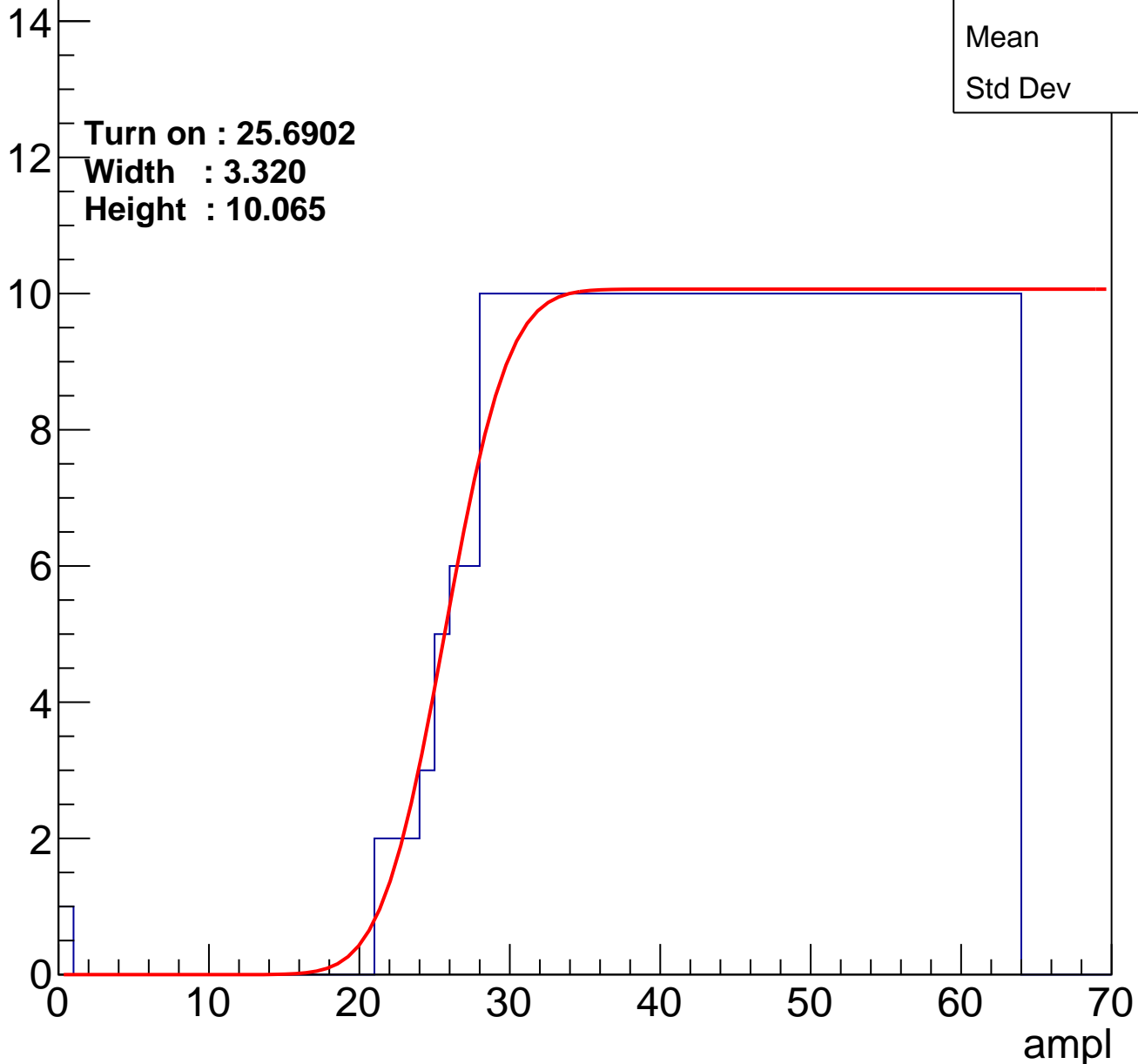
Entries	387
Mean	44
Std Dev	11.5

Turn on : 25.6902

Width : 3.320

Height : 10.065

Entry



B1L100S, U23-ch124

calib_packv5_042523_0143.root, FC#4, port A2

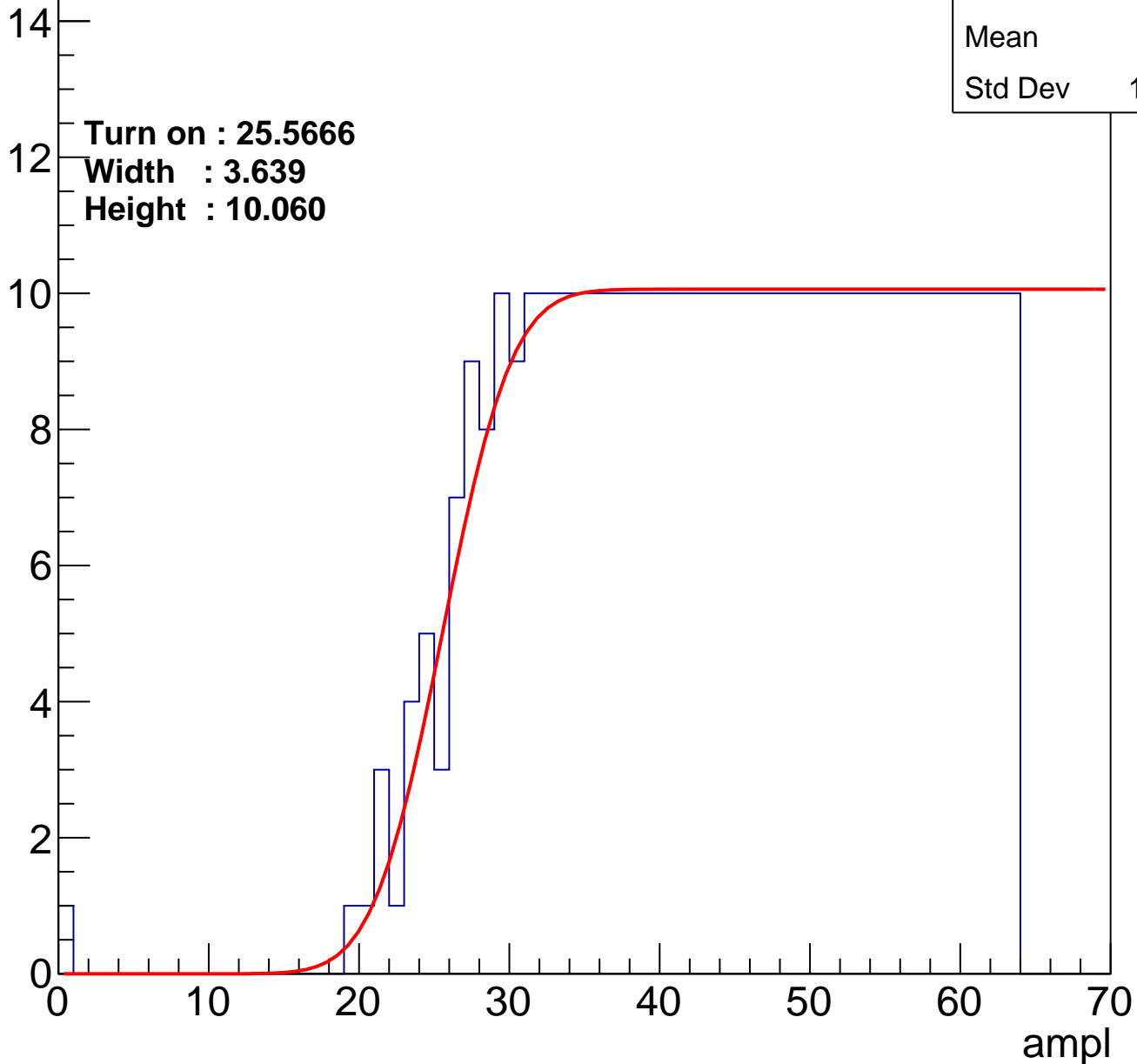
Entries	392
Mean	43.7
Std Dev	11.72

Turn on : 25.5666

Width : 3.639

Height : 10.060

Entry



B1L100S, U23-ch125

calib_packv5_042523_0143.root, FC#4, port A2

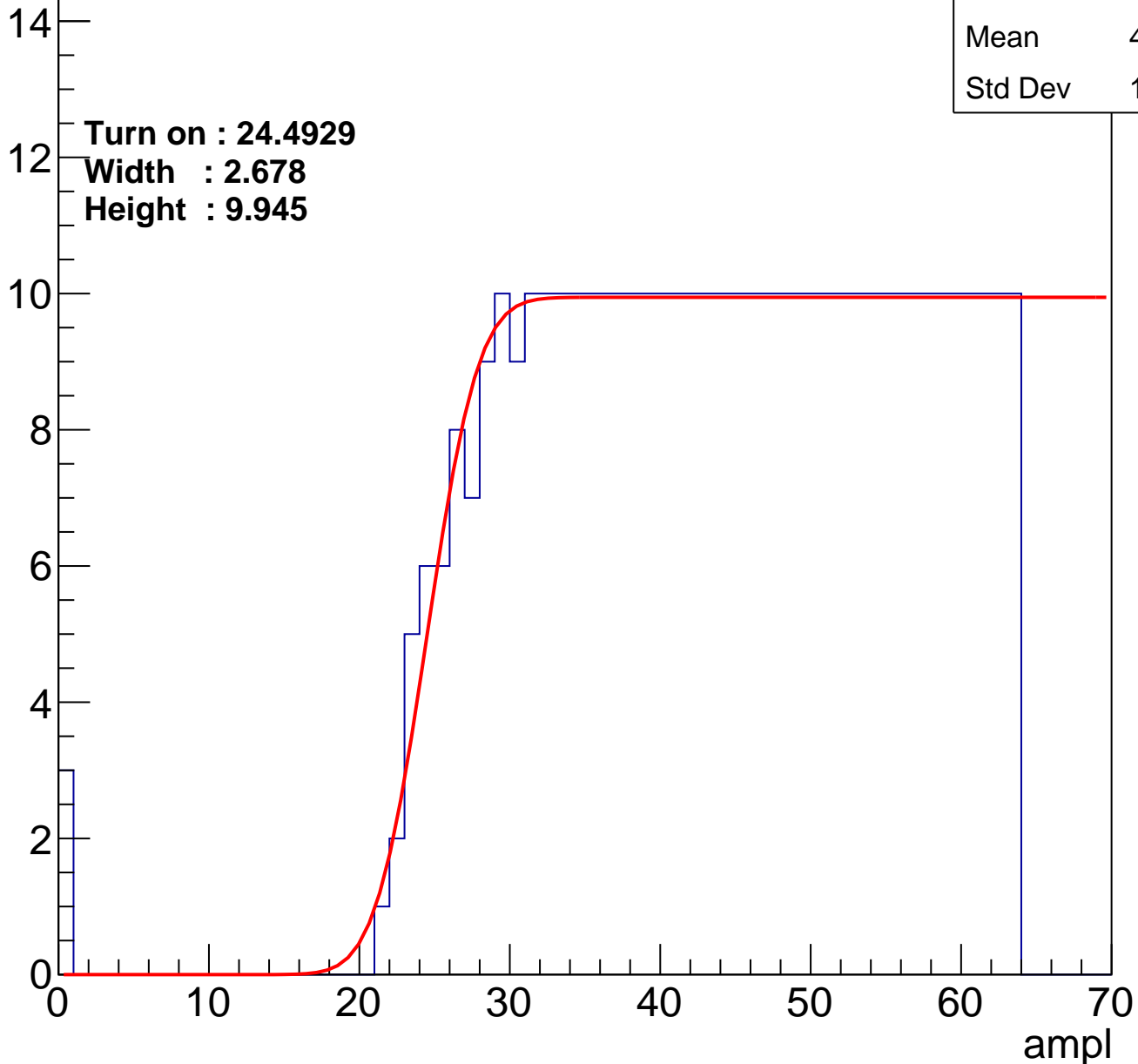
Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 24.4929

Width : 2.678

Height : 9.945

Entry



B1L100S, U23-ch126

calib_packv5_042523_0143.root, FC#4, port A2

Entries	365
Mean	44.84
Std Dev	11.49

Turn on : 28.3755

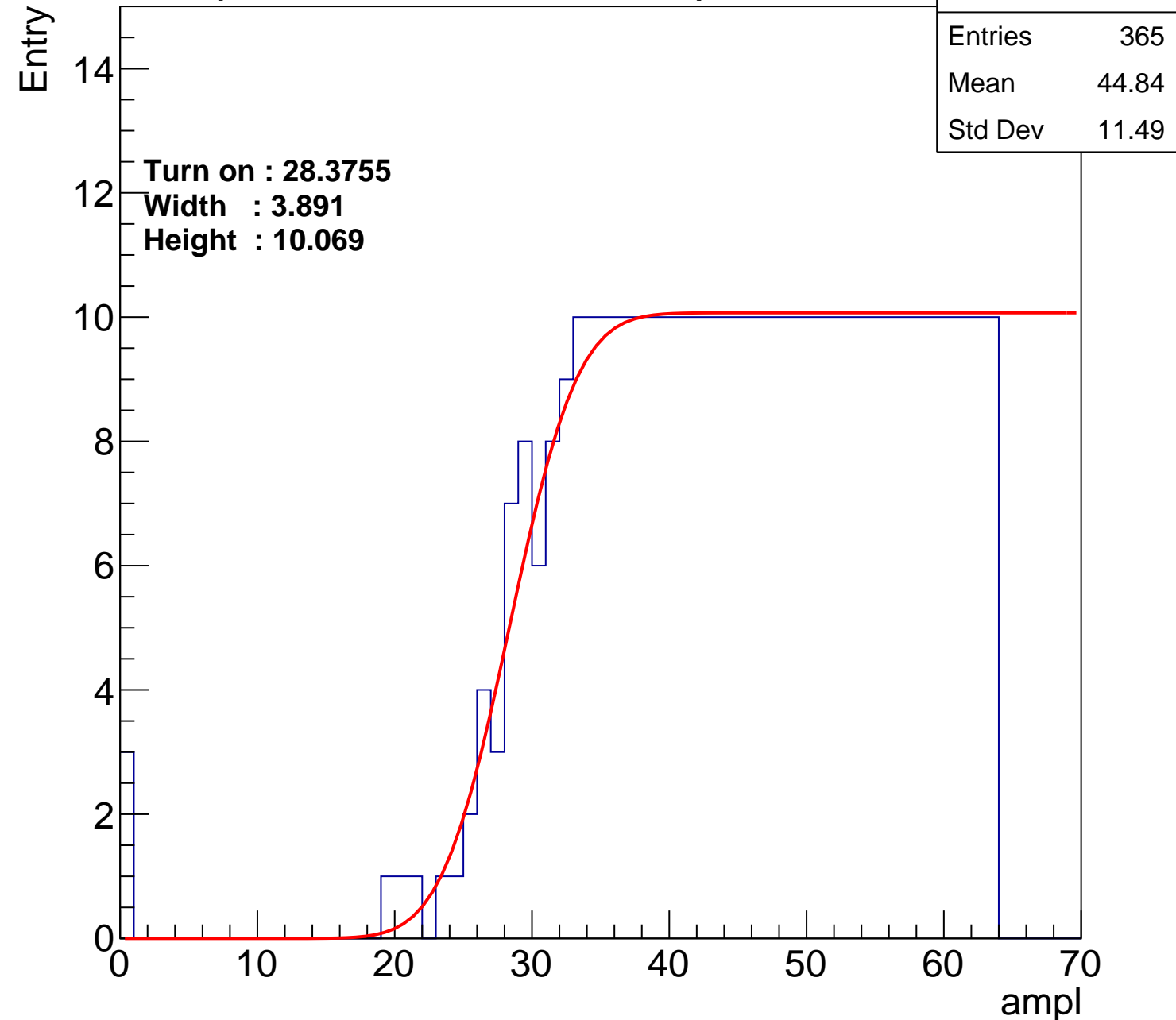
Width : 3.891

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.53
Std Dev	11.73

Turn on : 27.1942

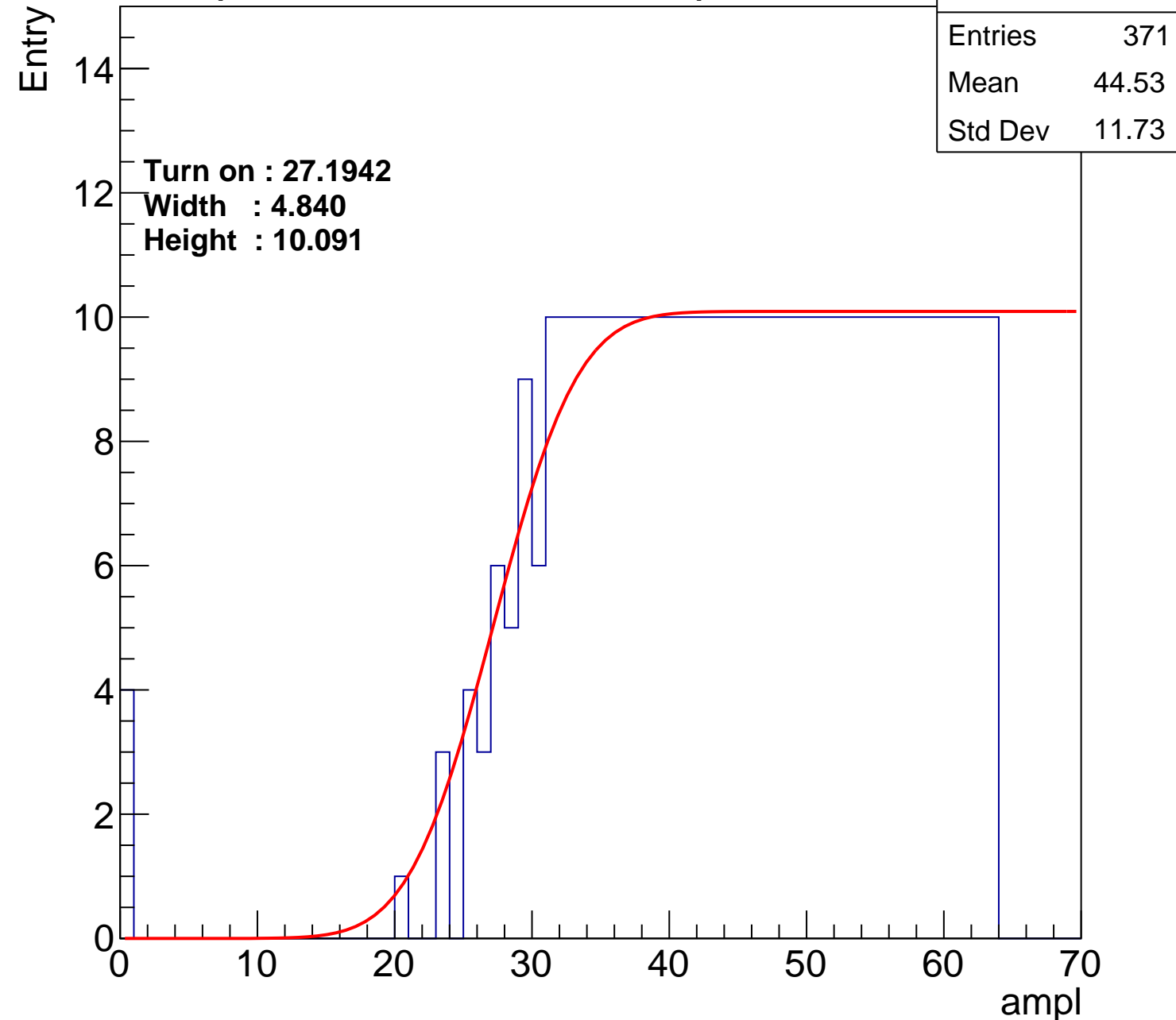
Width : 4.840

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L100S, U23-ch127

calib_packv5_042523_0143.root, FC#4, port A2

Entries	371
Mean	44.53
Std Dev	11.73

Turn on : 27.1942

Width : 4.840

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl

