

B0L000S, U9-ch0

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.54
Std Dev	10.87

Turn on : 29.2969

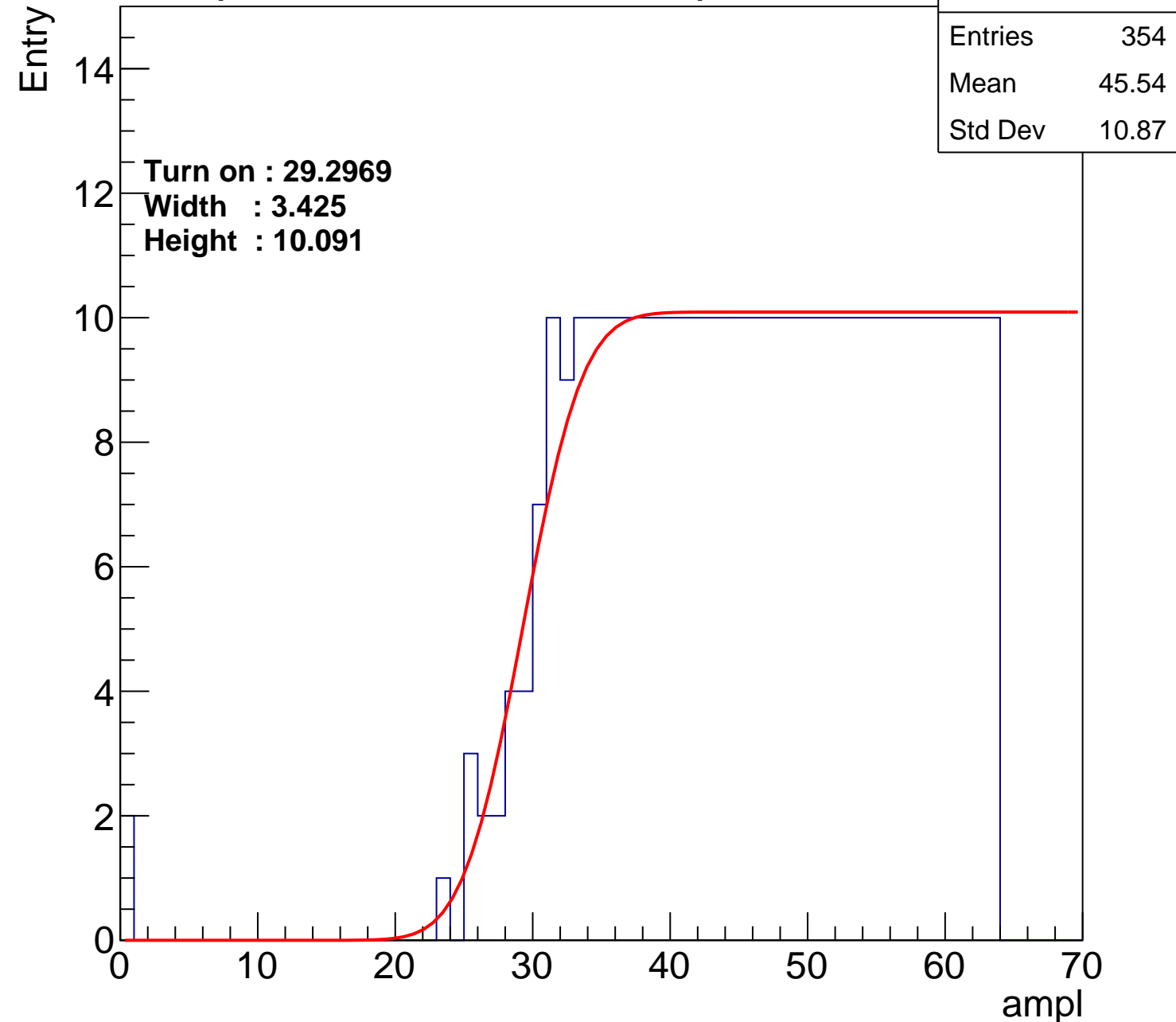
Width : 3.425

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch1

calib_packv5_042523_0143.root, FC#5, port B1

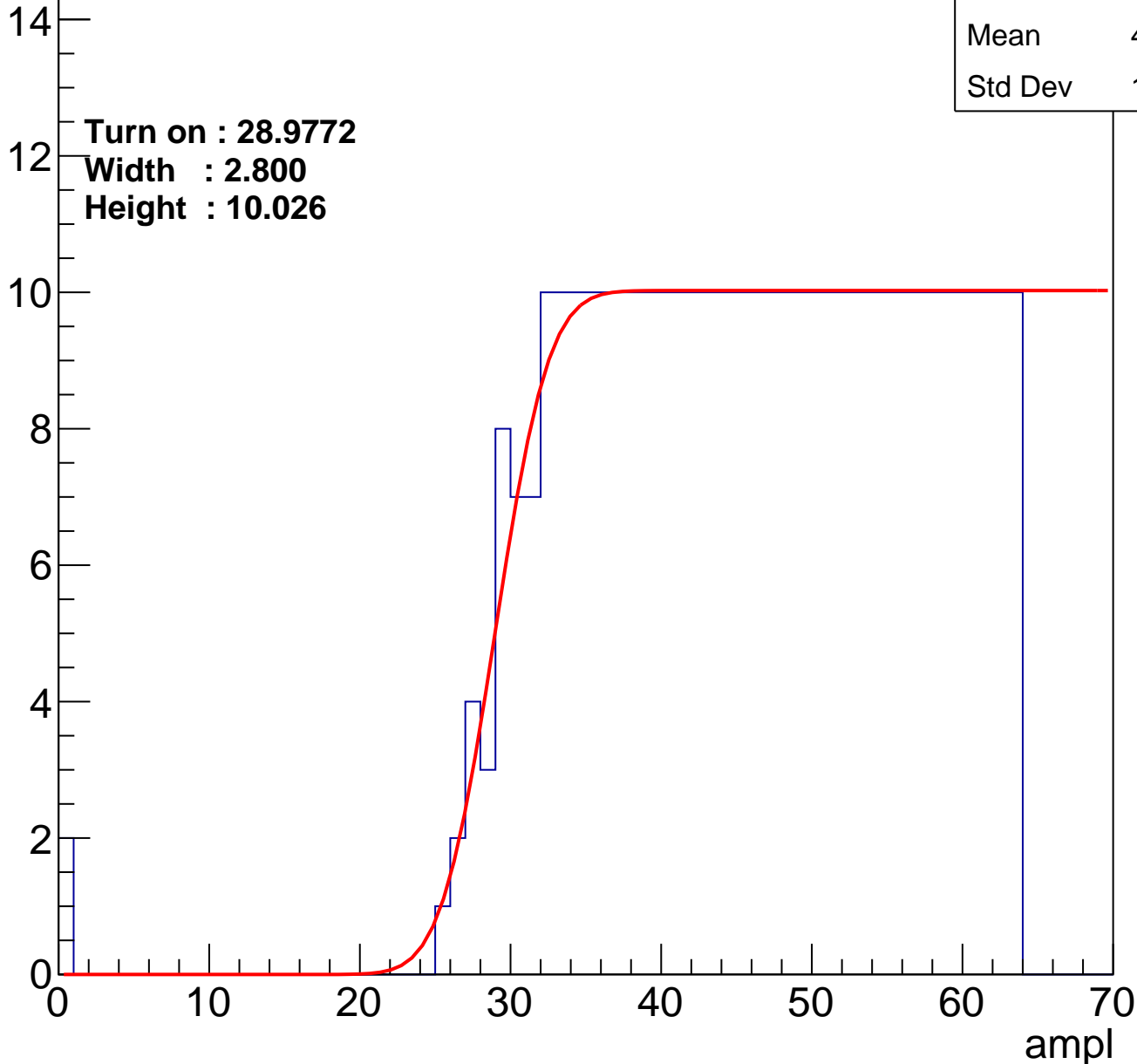
Entries	354
Mean	45.56
Std Dev	10.83

Turn on : 28.9772

Width : 2.800

Height : 10.026

Entry



B0L000S, U9-ch2

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	44.92
Std Dev	11.55

Turn on : 29.1938

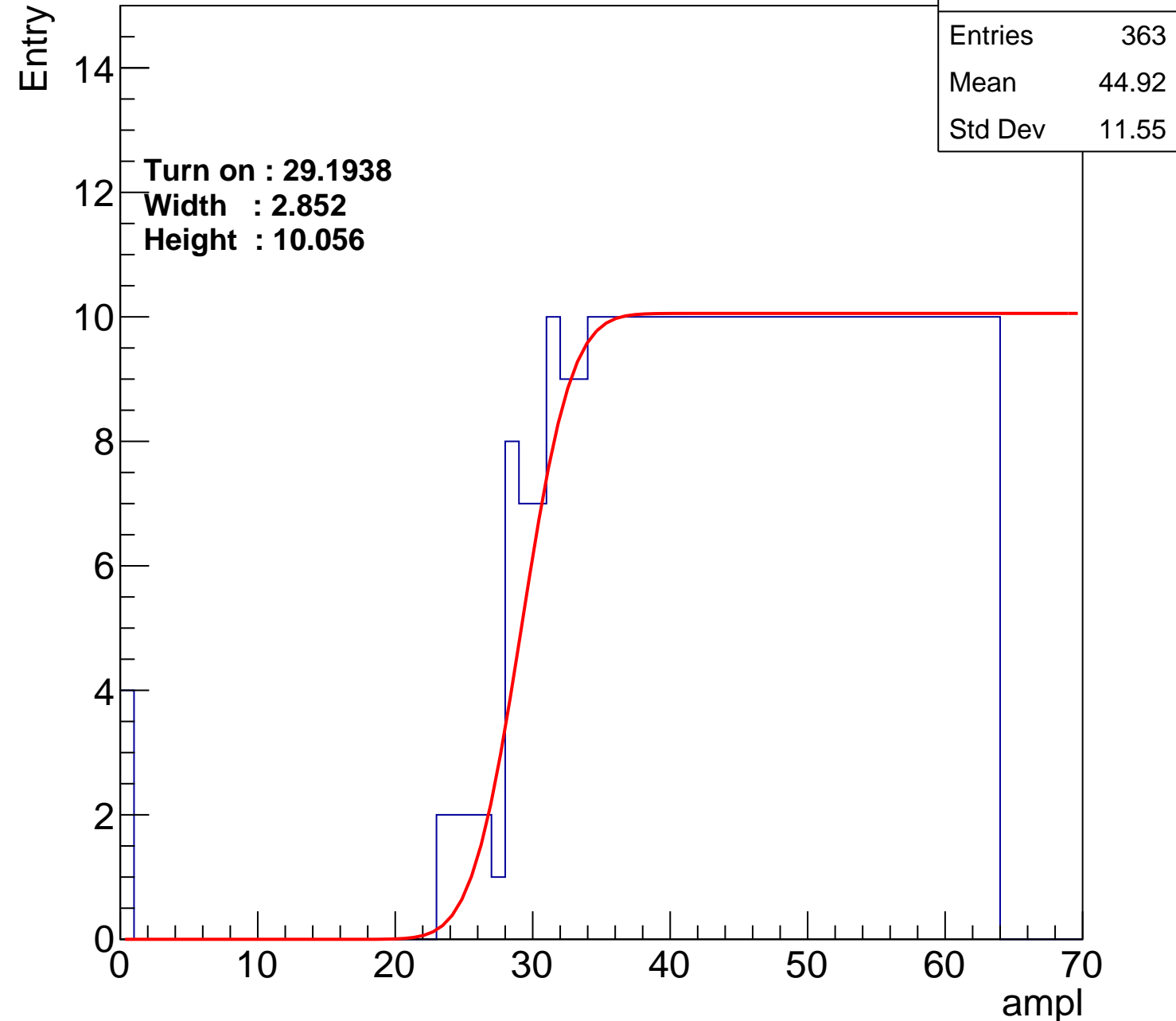
Width : 2.852

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch3

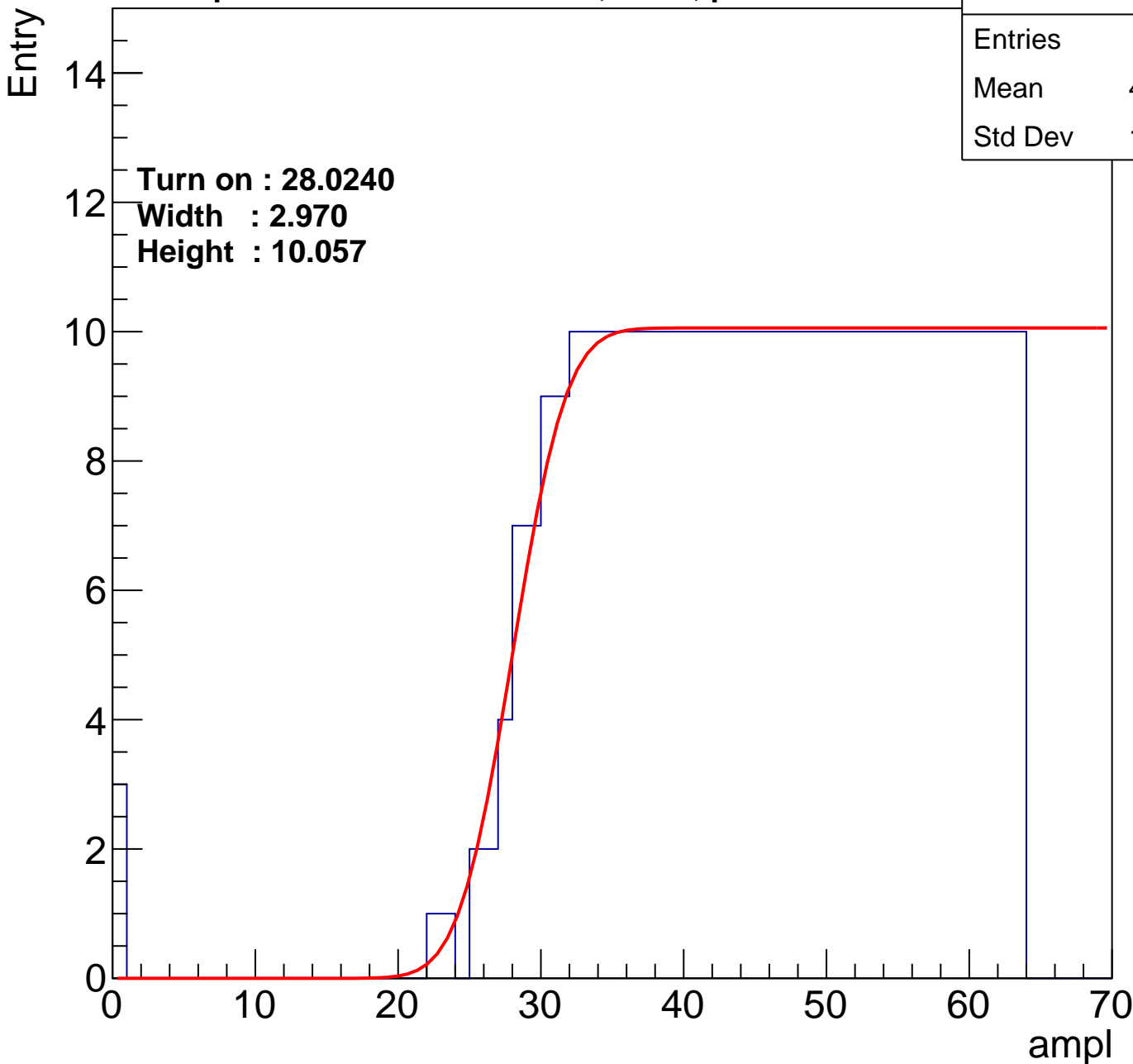
calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.94
Std Dev	11.32

Turn on : 28.0240

Width : 2.970

Height : 10.057



B0L000S, U9-ch4

calib_packv5_042523_0143.root, FC#5, port B1

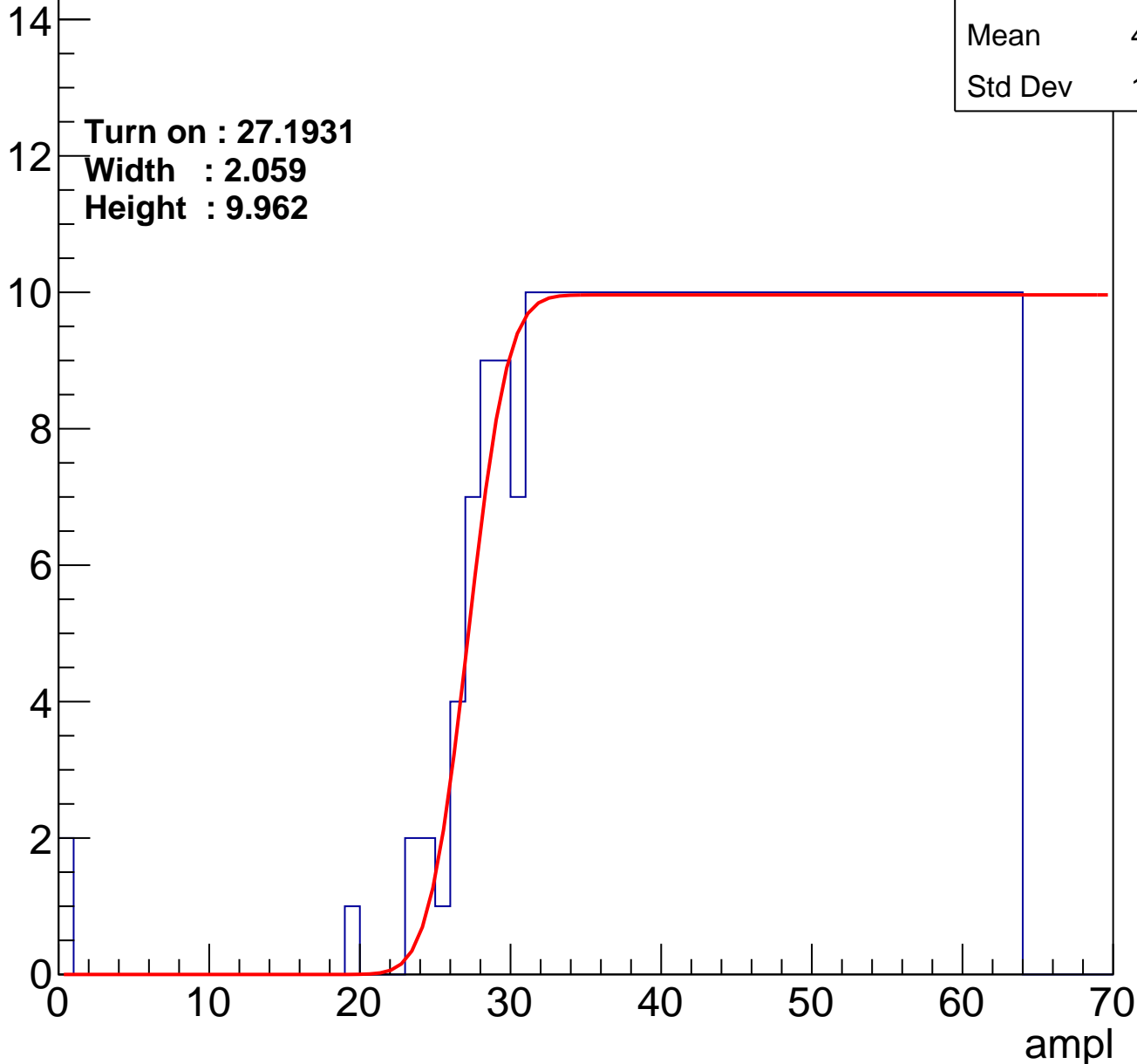
Entries	374
Mean	44.56
Std Dev	11.37

Turn on : 27.1931

Width : 2.059

Height : 9.962

Entry



B0L000S, U9-ch5

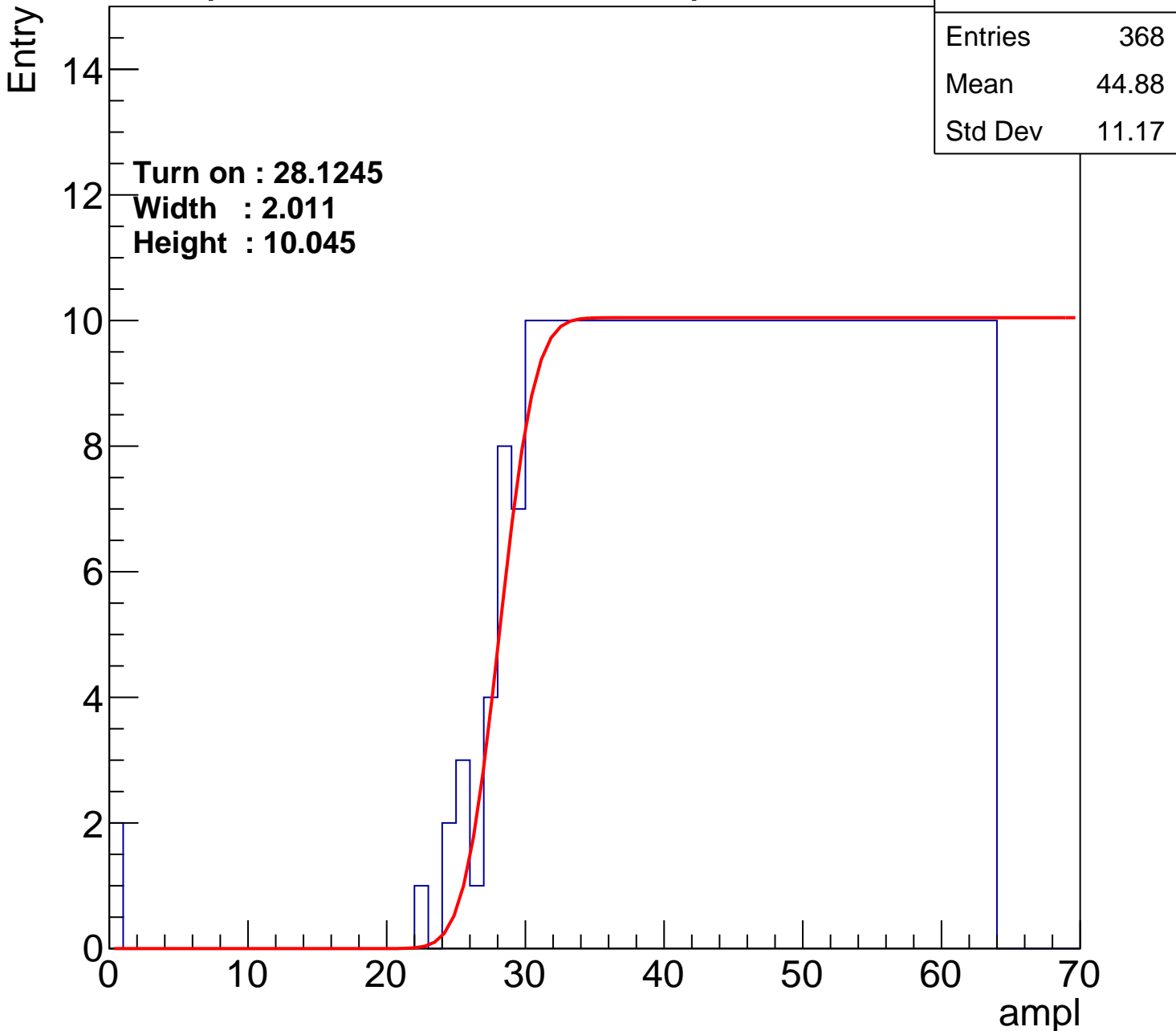
calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 28.1245

Width : 2.011

Height : 10.045



B0L000S, U9-ch6

calib_packv5_042523_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.91

Turn on : 26.2565

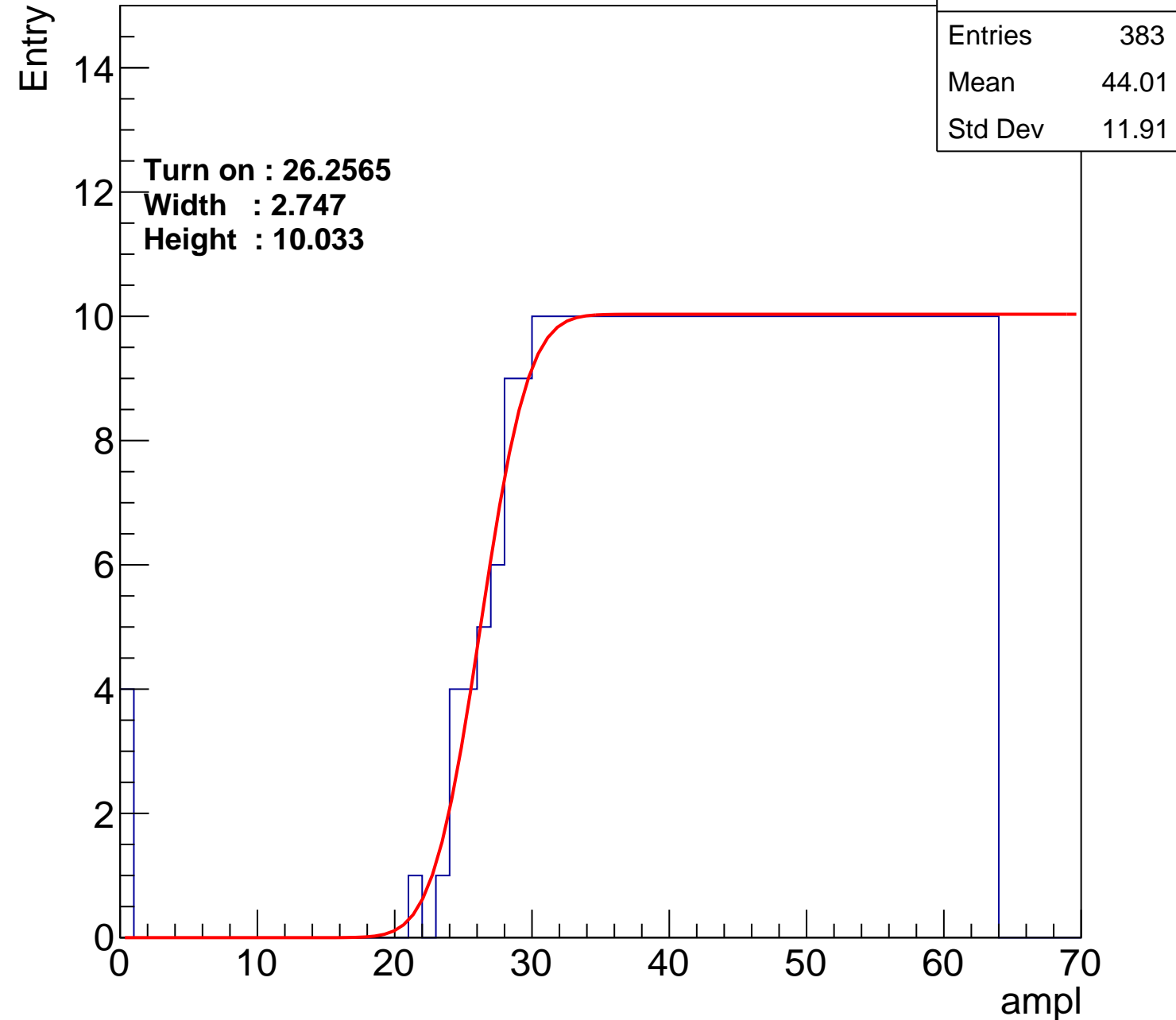
Width : 2.747

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch7

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.09
Std Dev	11.08

Turn on : 27.8210

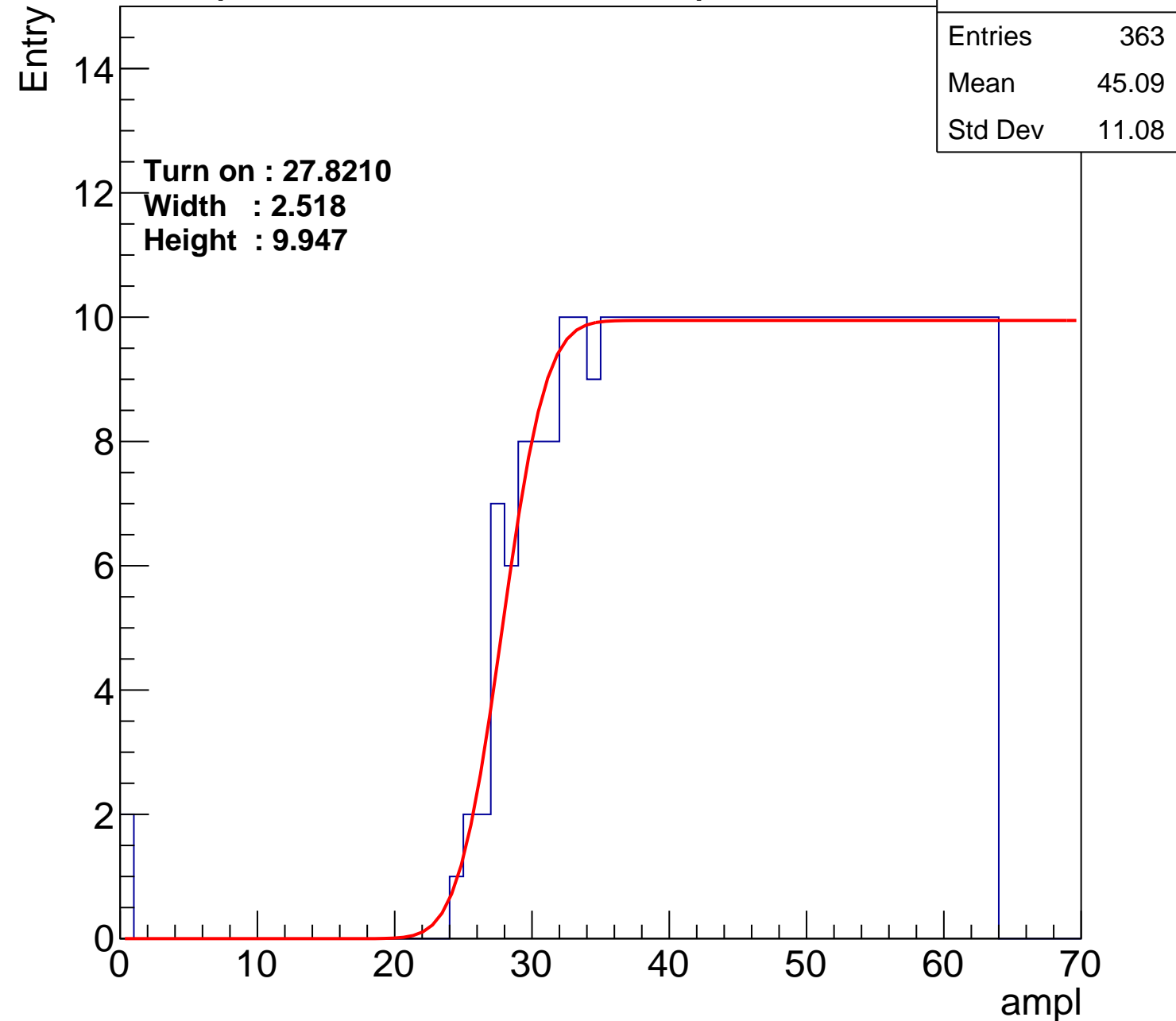
Width : 2.518

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch8

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.29
Std Dev	12.13

Turn on : 27.4655

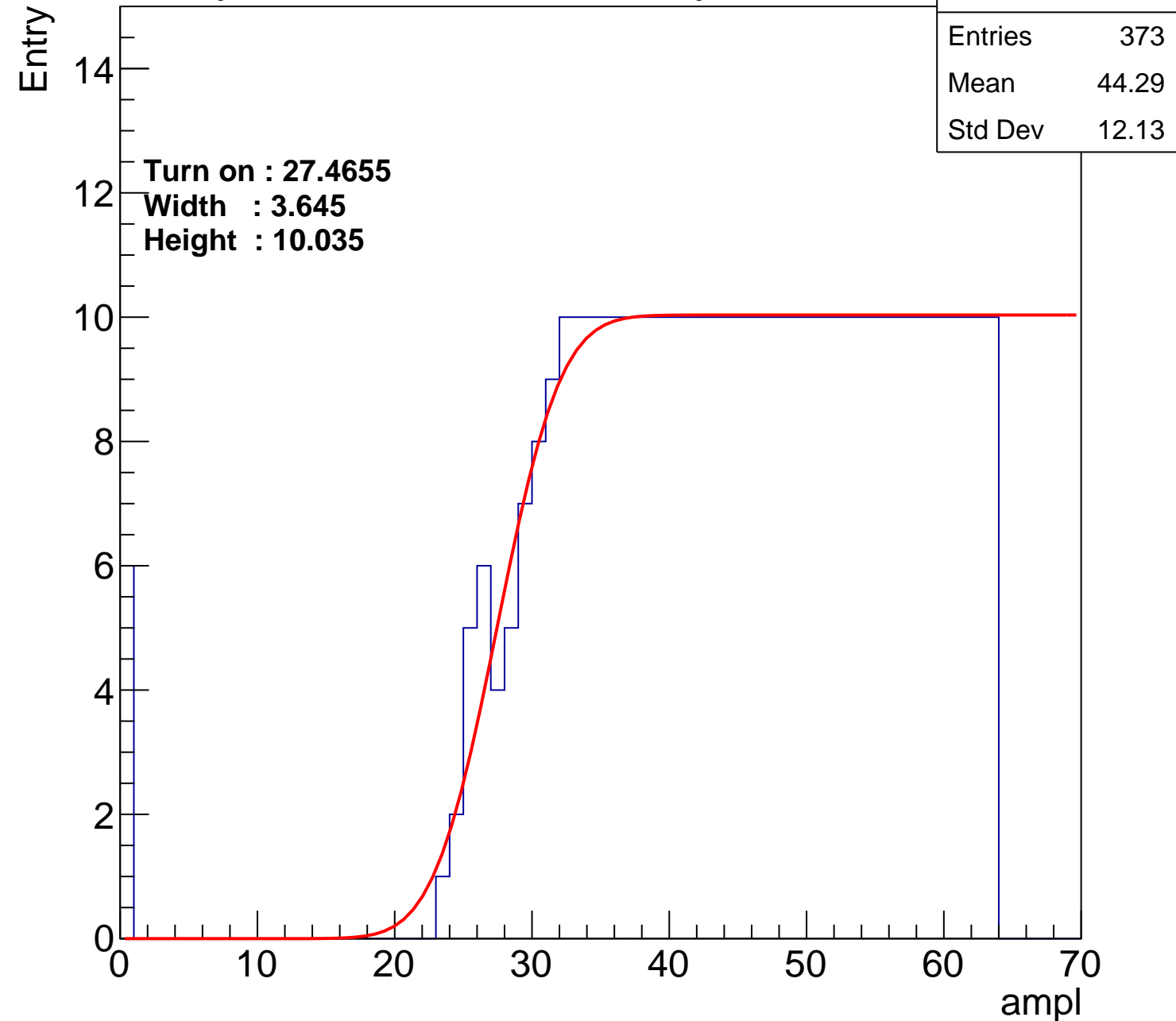
Width : 3.645

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch9

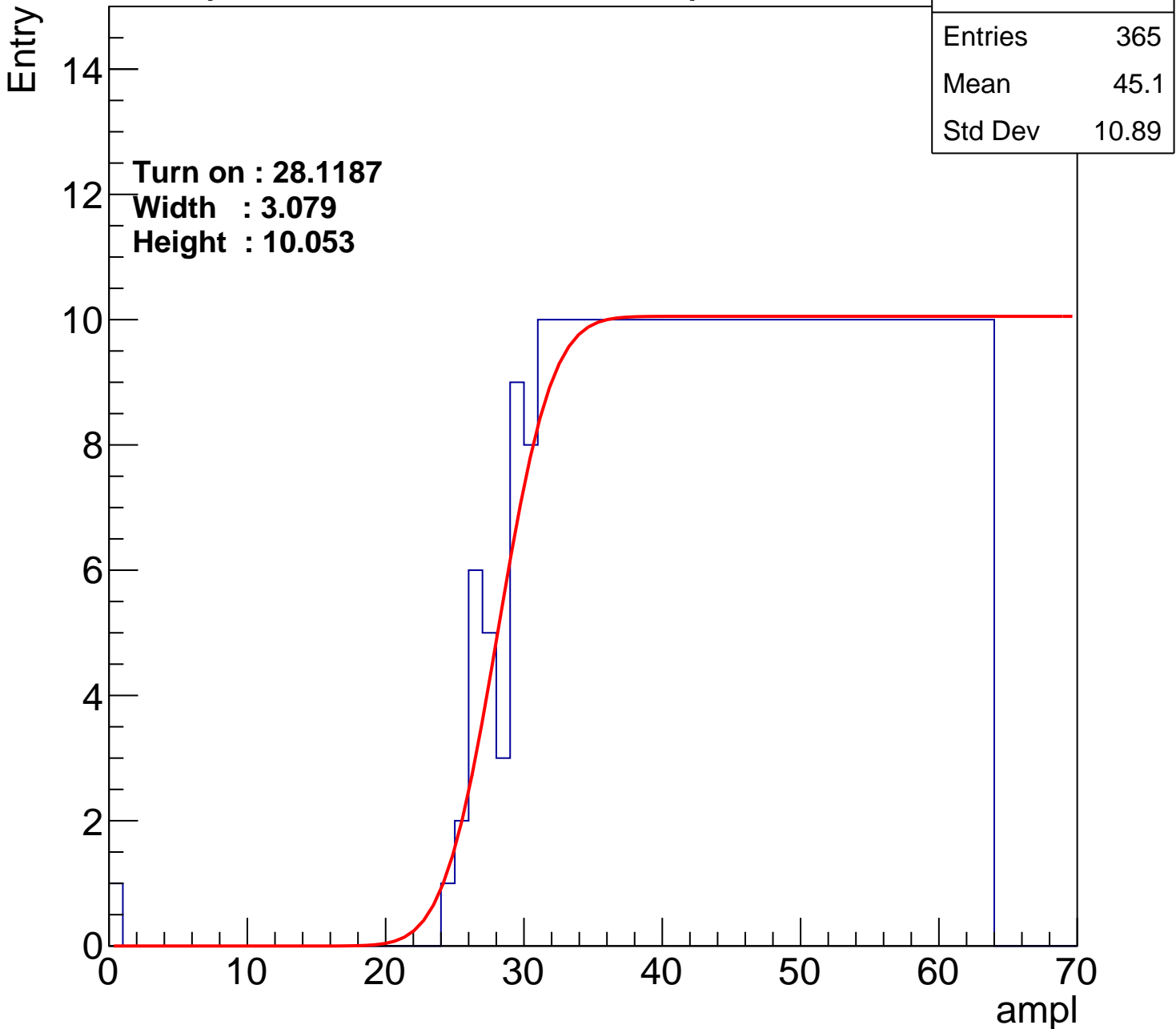
calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.1
Std Dev	10.89

Turn on : 28.1187

Width : 3.079

Height : 10.053



B0L000S, U9-ch10

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.75
Std Dev	11.47

Turn on : 28.6139

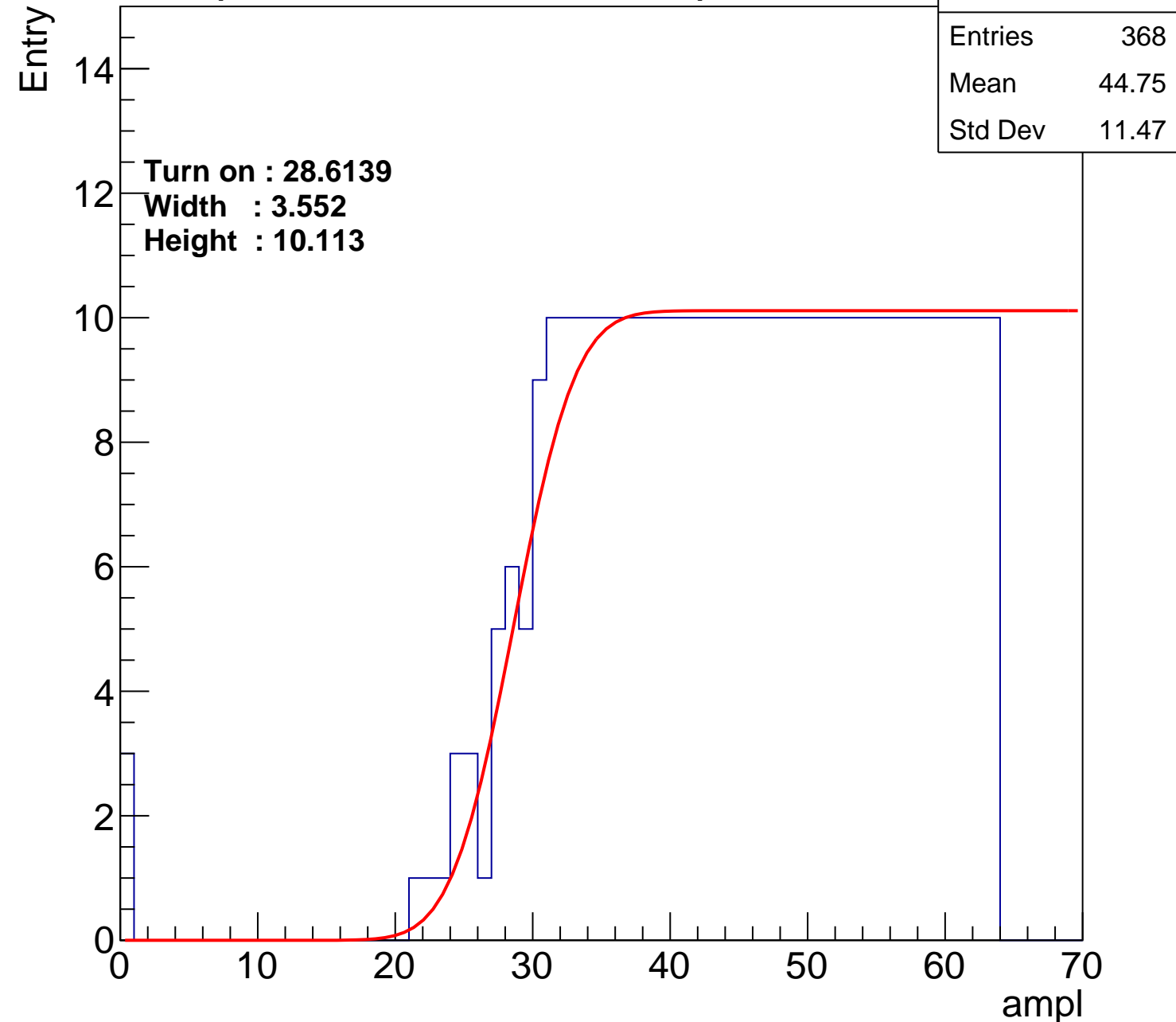
Width : 3.552

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch11

calib_packv5_042523_0143.root, FC#5, port B1

Entries	345
Mean	45.9
Std Dev	10.87

Turn on : 30.3925

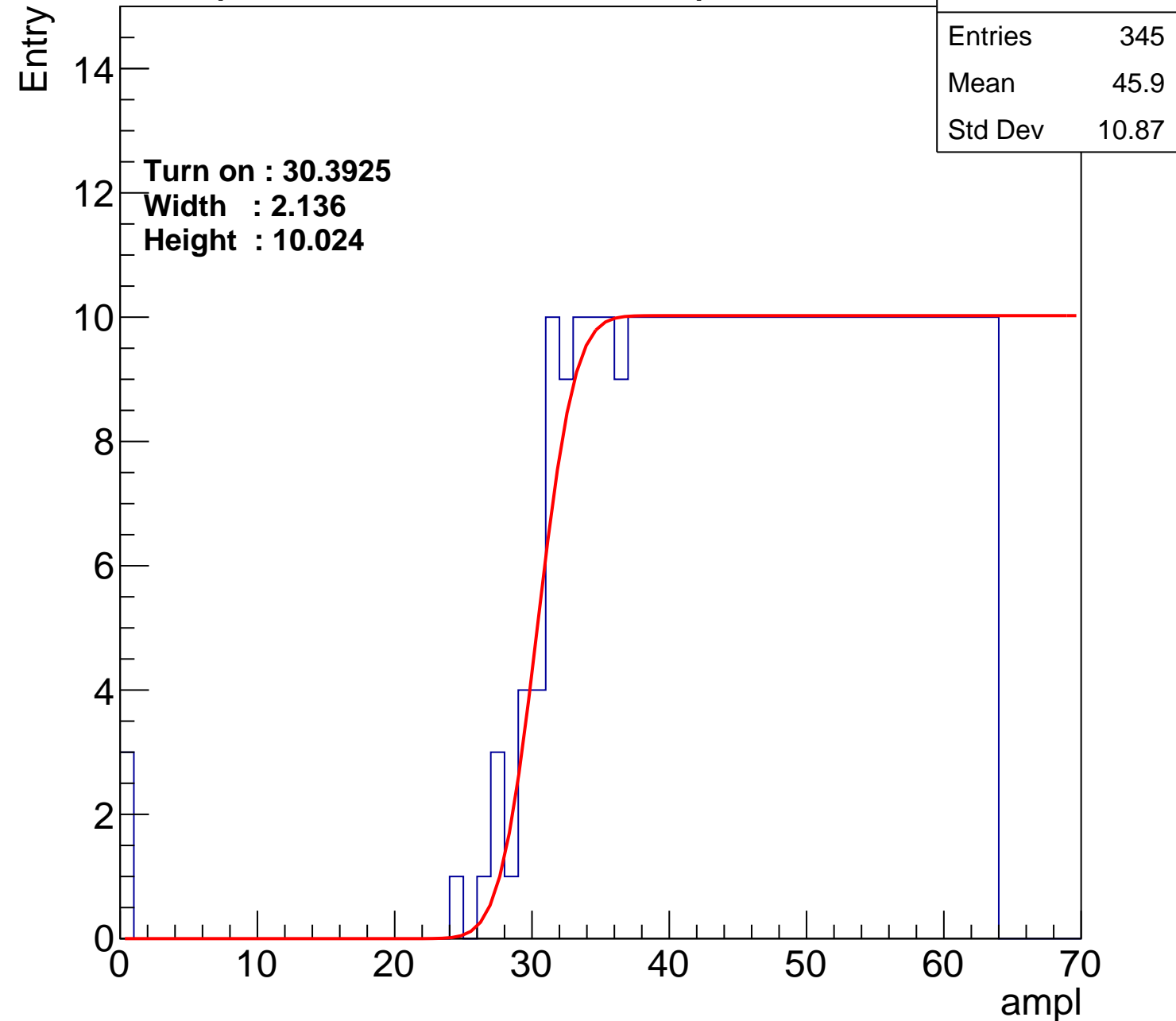
Width : 2.136

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch12

calib_packv5_042523_0143.root, FC#5, port B1

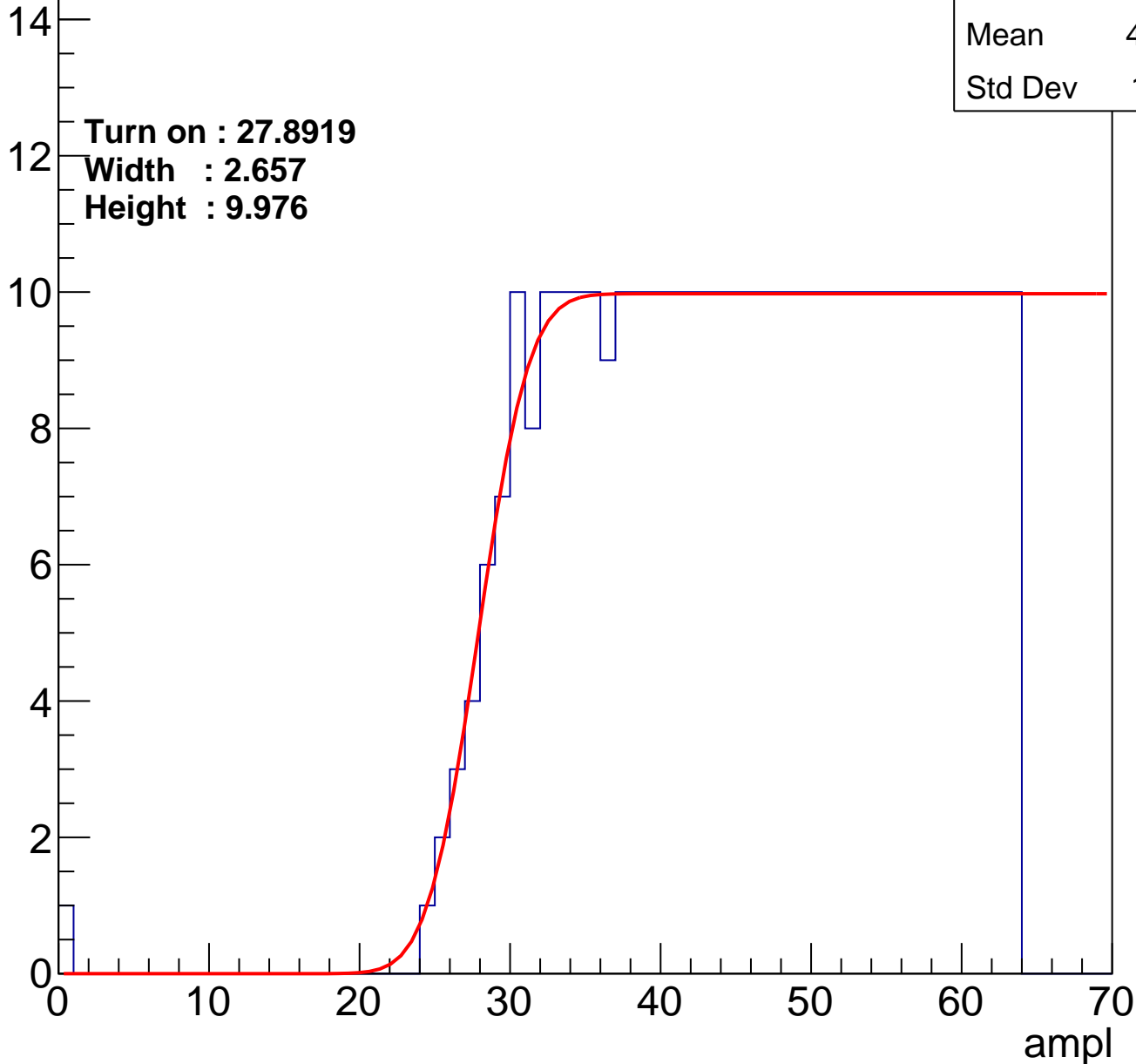
Entry

Entries	361
Mean	45.27
Std Dev	10.81

Turn on : 27.8919

Width : 2.657

Height : 9.976



B0L000S, U9-ch13

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.92
Std Dev	11.64

Turn on : 25.5208

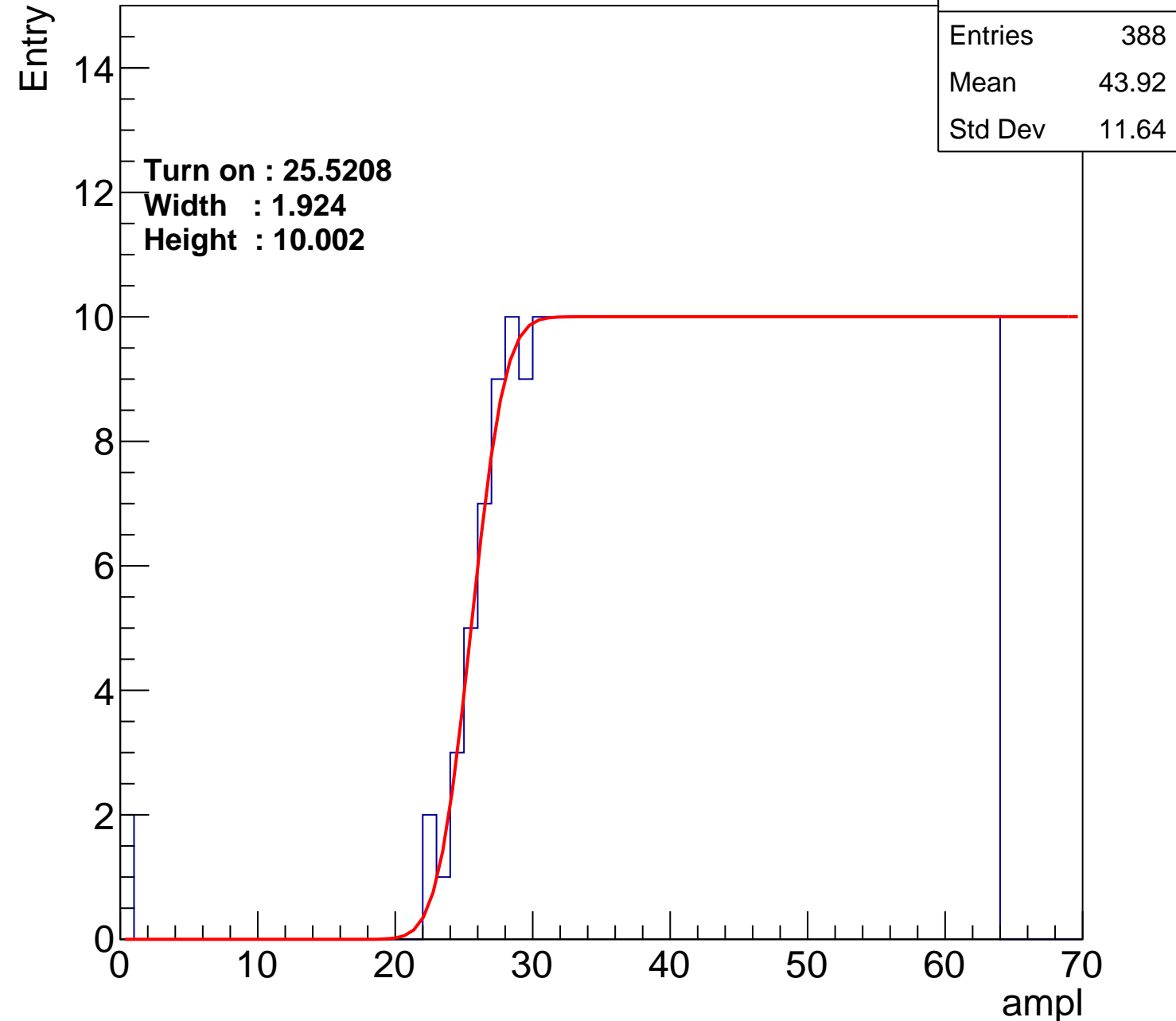
Width : 1.924

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch14

calib_packv5_042523_0143.root, FC#5, port B1

Entries	354
Mean	45.49
Std Dev	11.04

Turn on : 28.9664

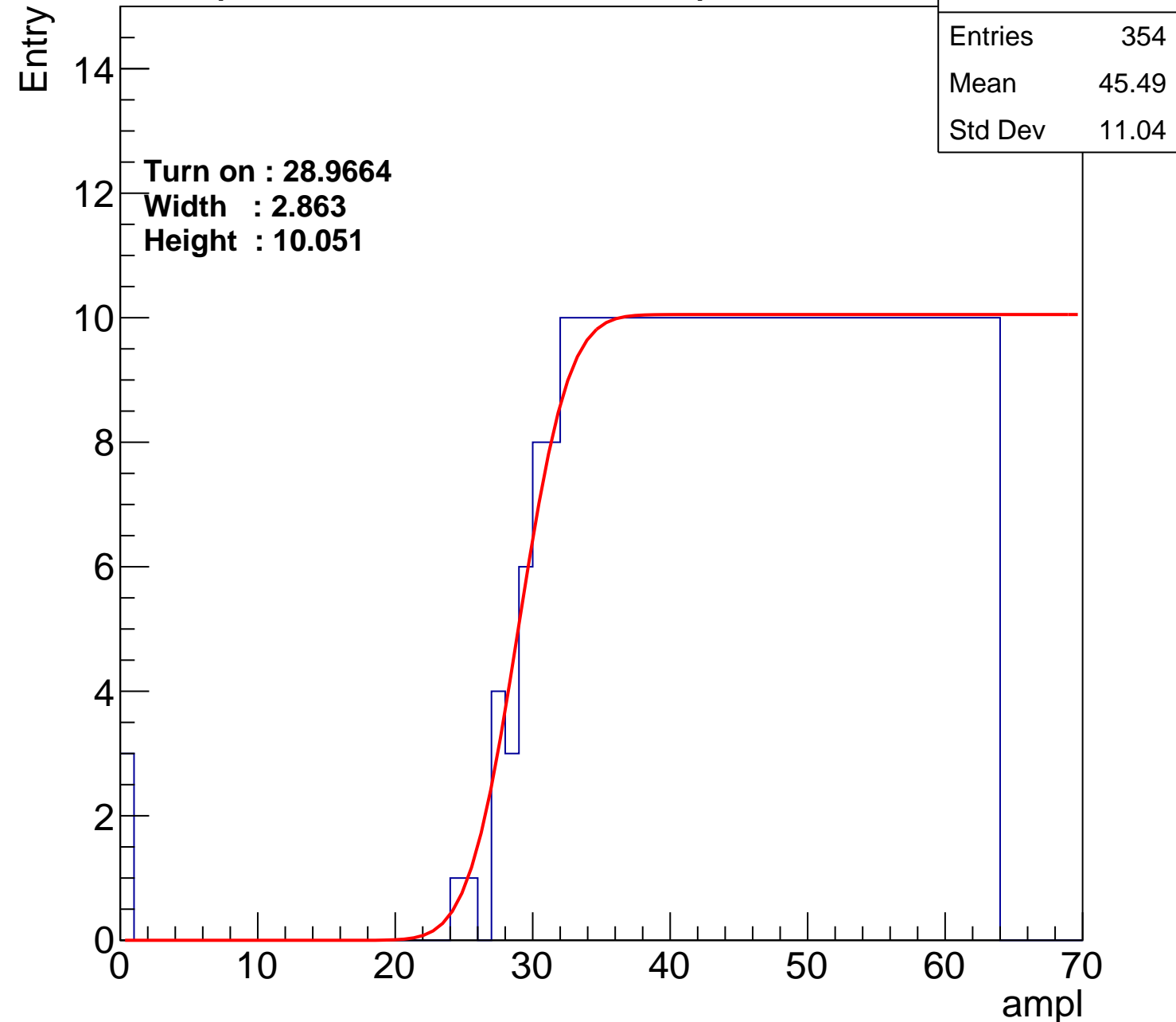
Width : 2.863

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch15

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 27.0235

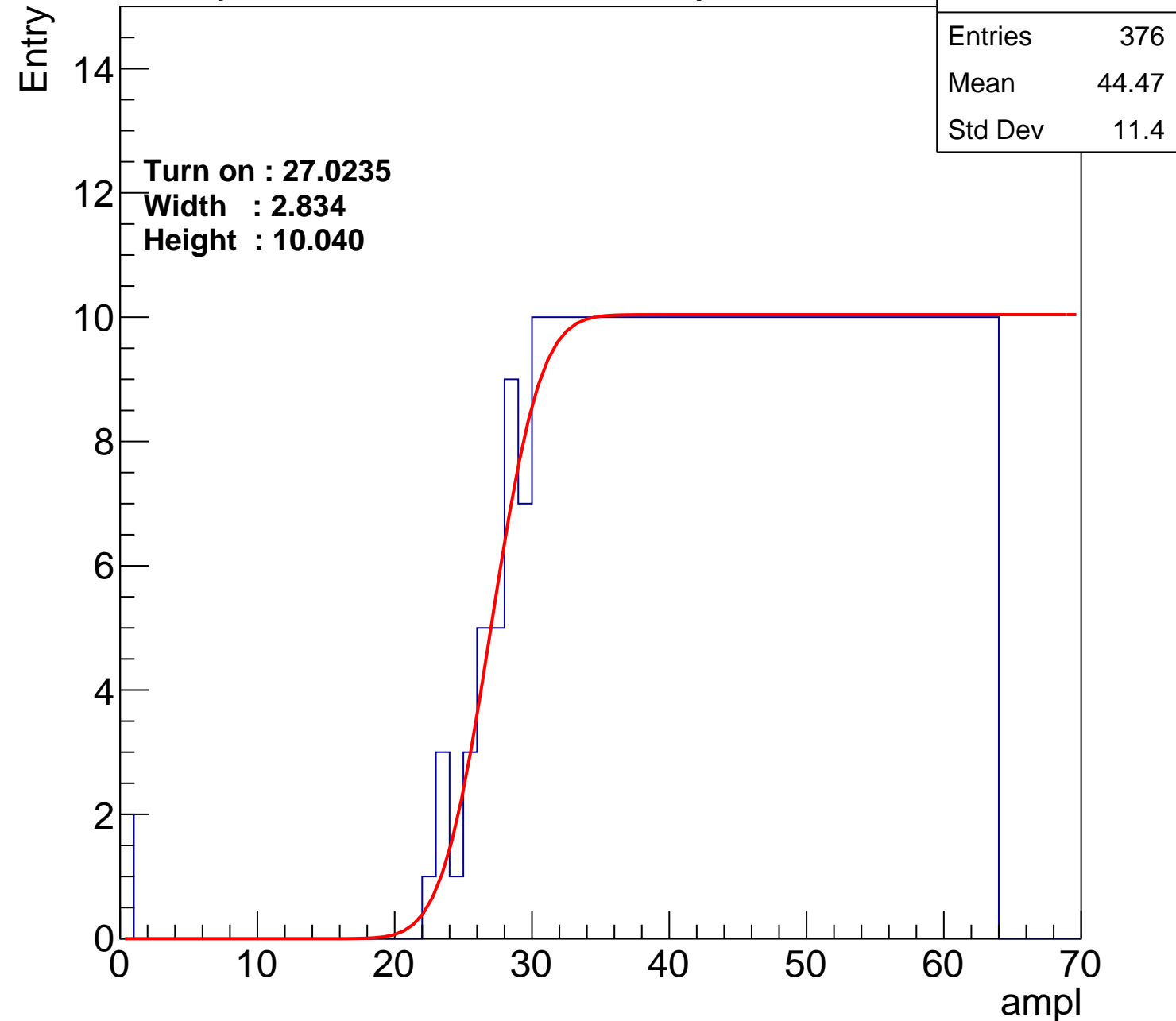
Width : 2.834

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch16

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 26.7018

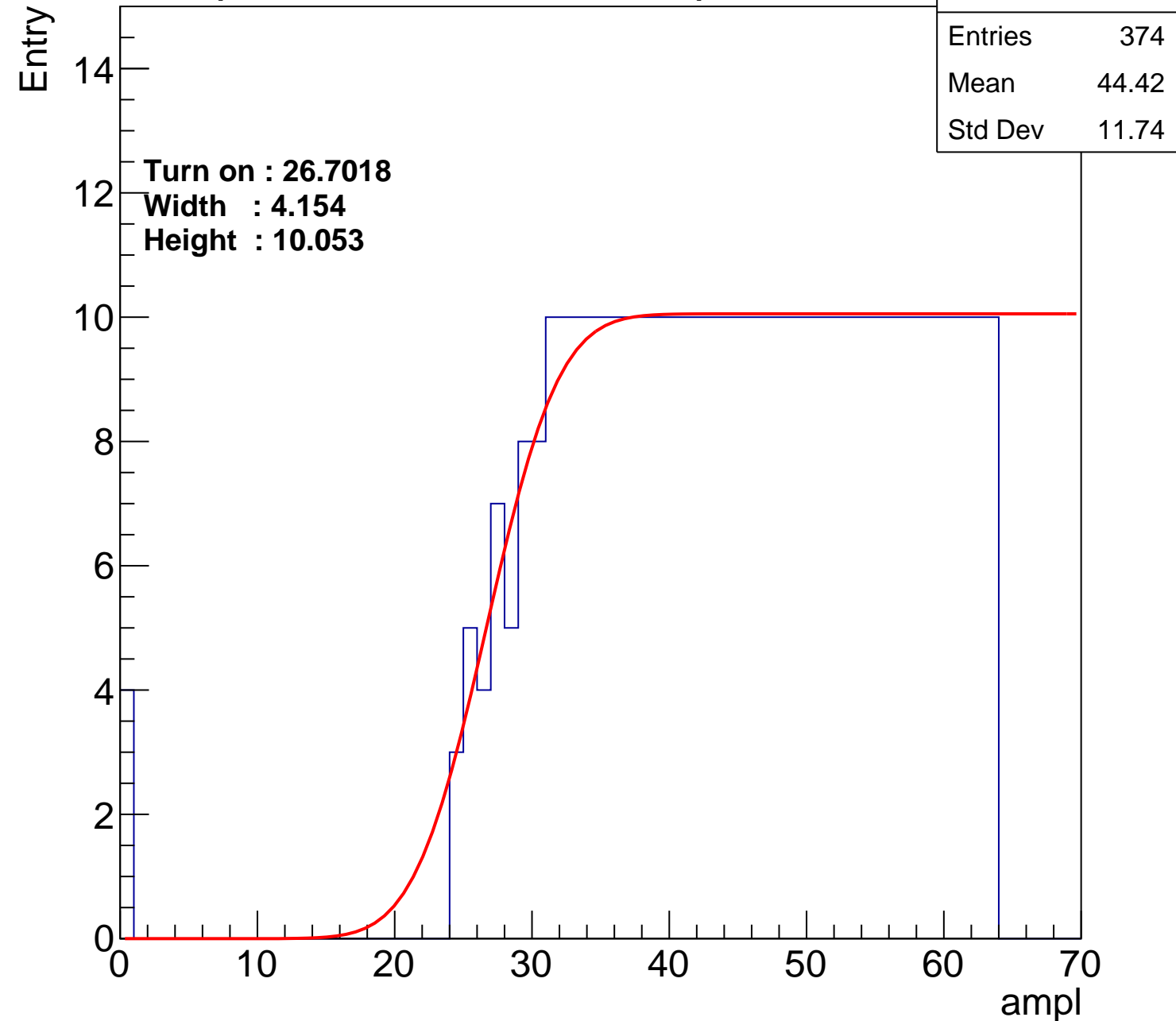
Width : 4.154

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch17

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.09
Std Dev	10.9

Turn on : 27.7378

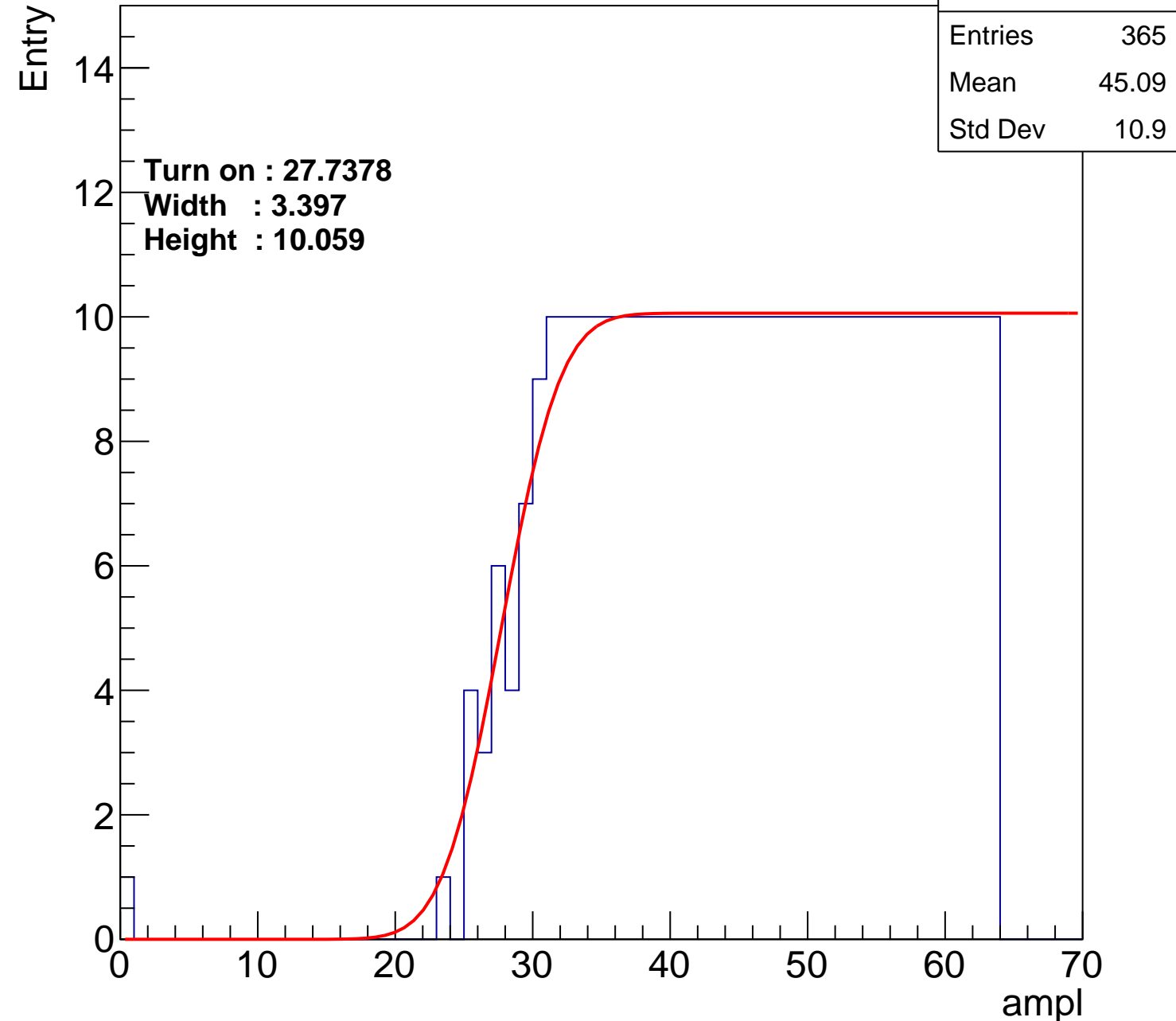
Width : 3.397

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch18

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.15
Std Dev	11.05

Turn on : 28.6194

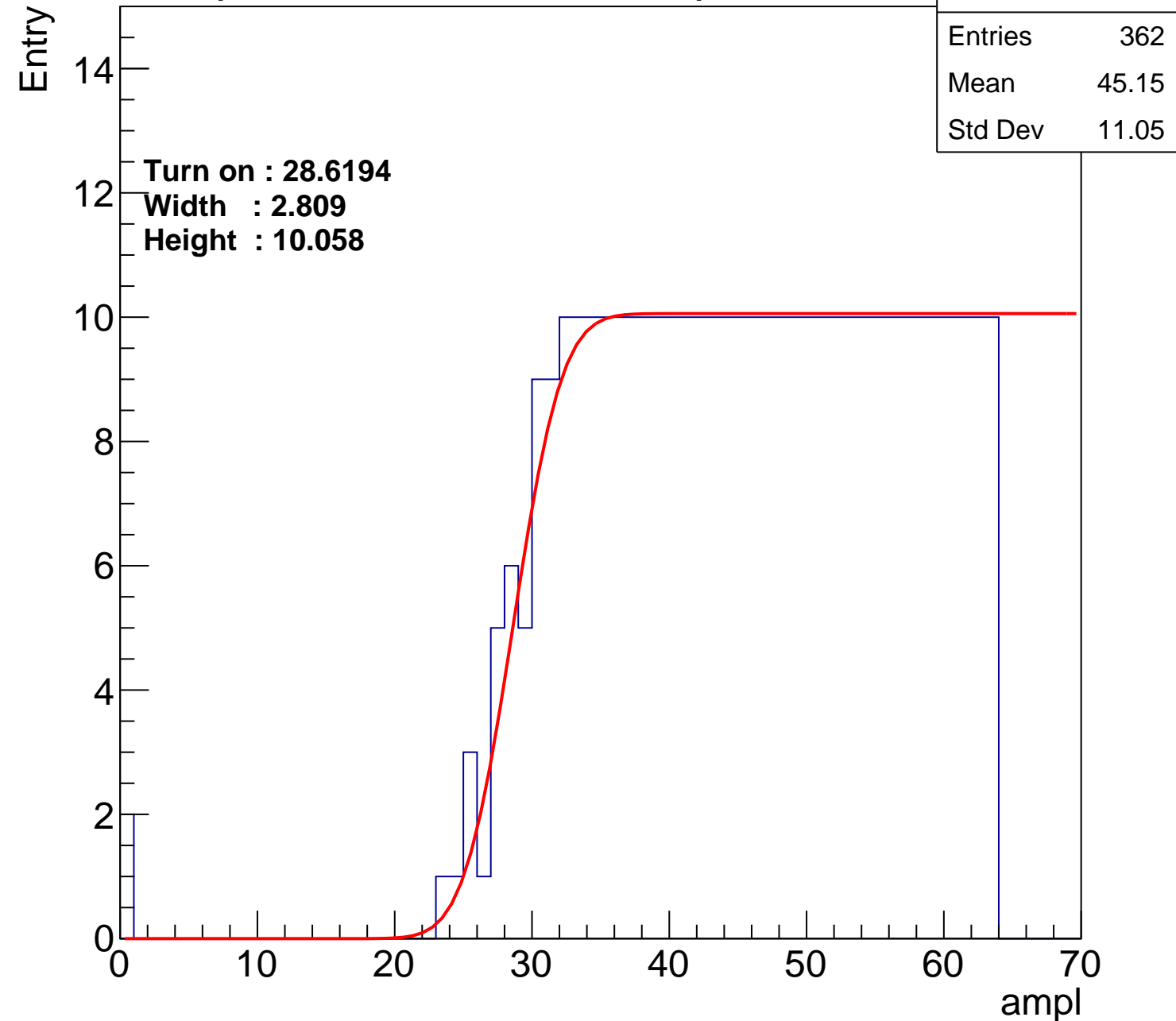
Width : 2.809

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch19

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.12
Std Dev	10.89

Turn on : 27.8796

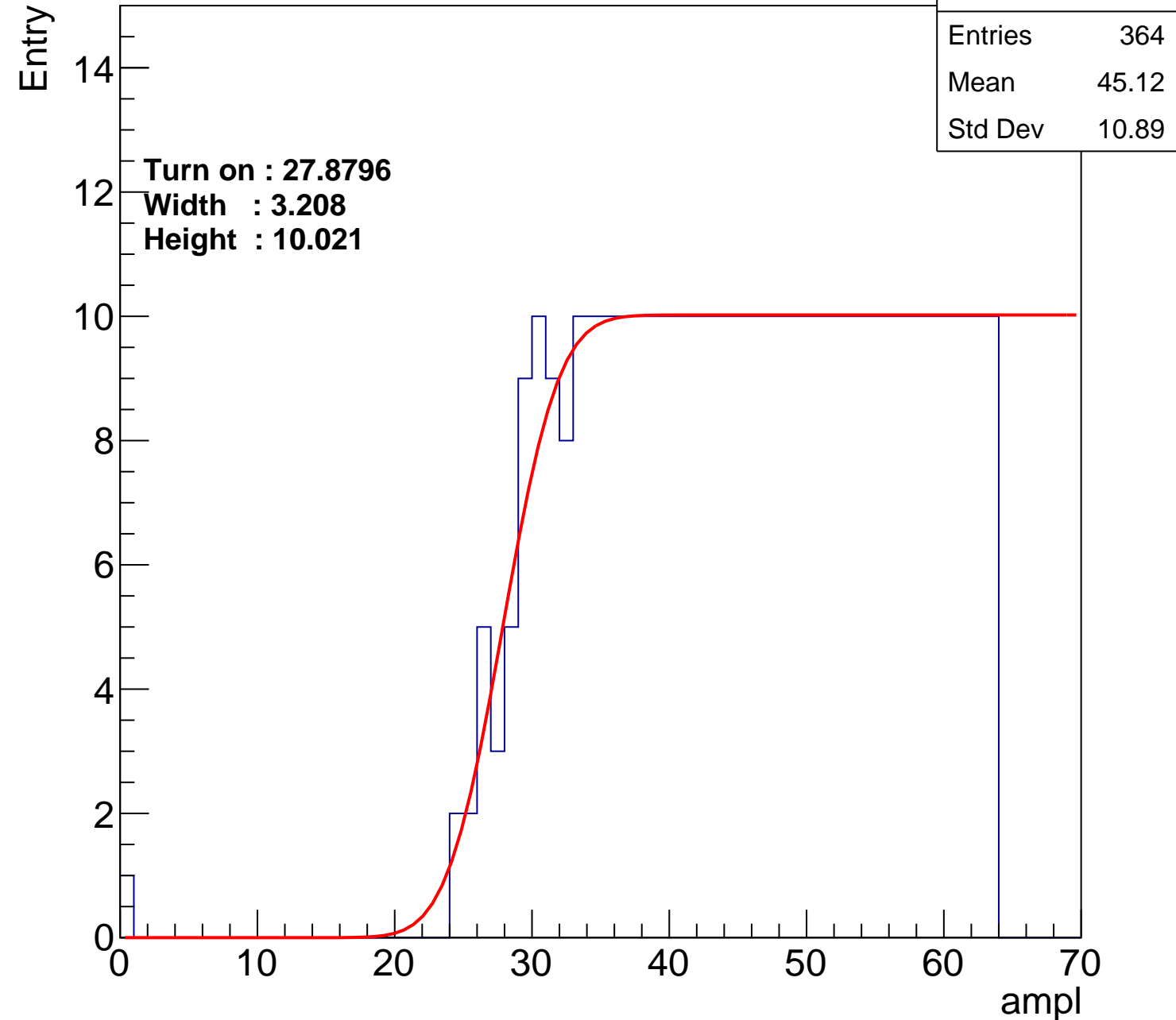
Width : 3.208

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch20

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.04
Std Dev	11.41

Turn on : 28.3440

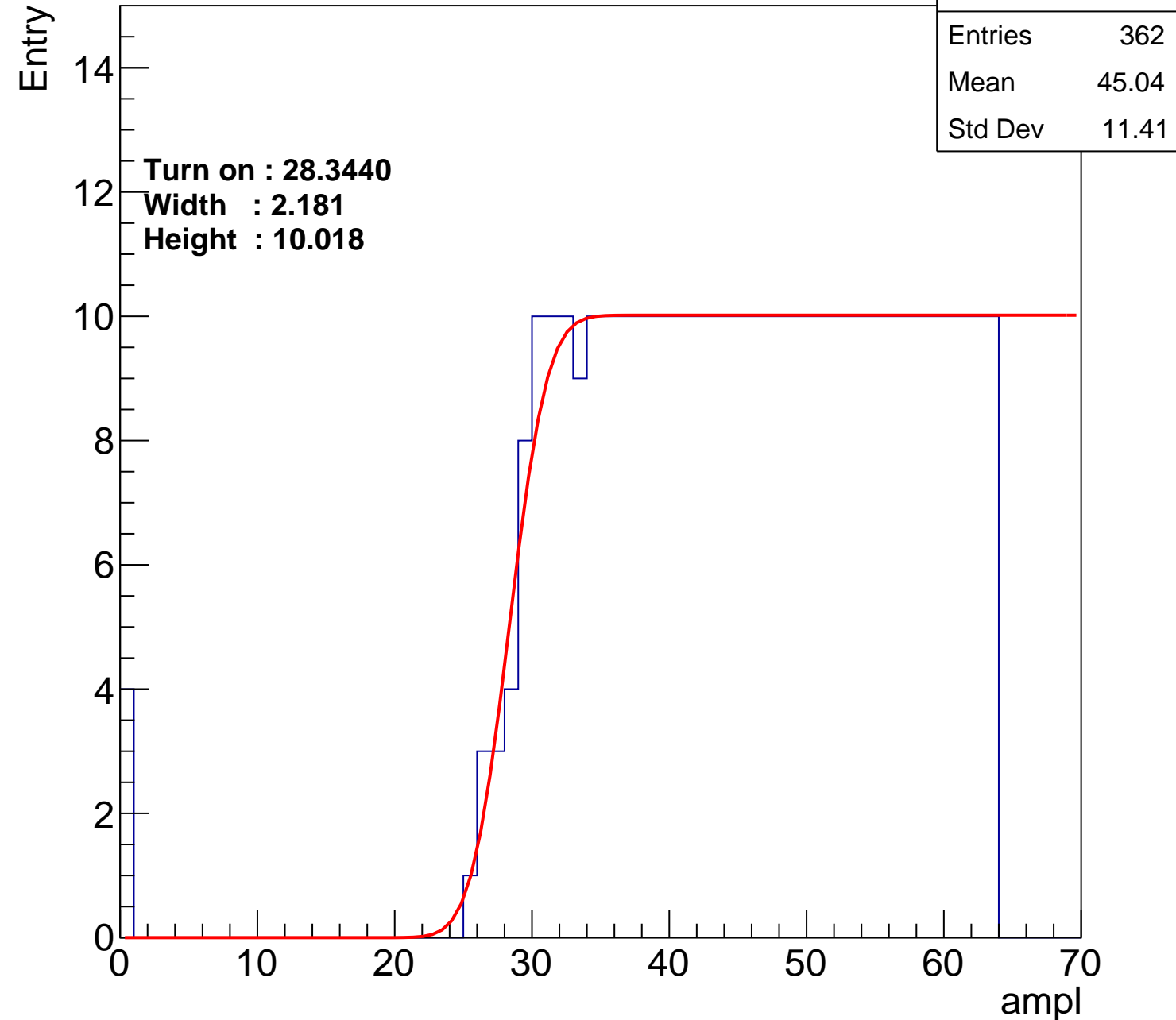
Width : 2.181

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch21

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.48
Std Dev	10.85

Turn on : 28.7690

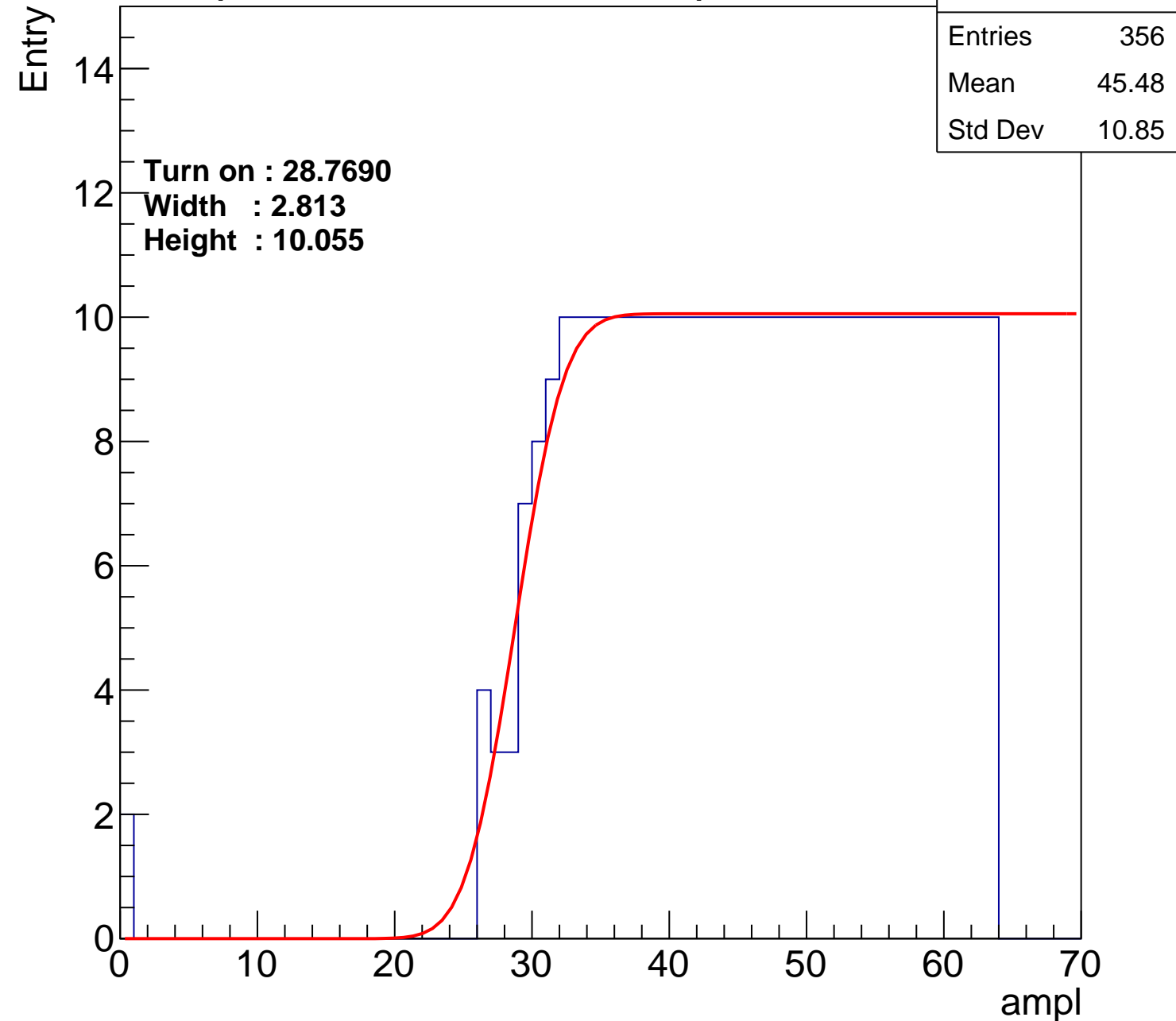
Width : 2.813

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch22

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.37
Std Dev	10.92

Turn on : 28.3793

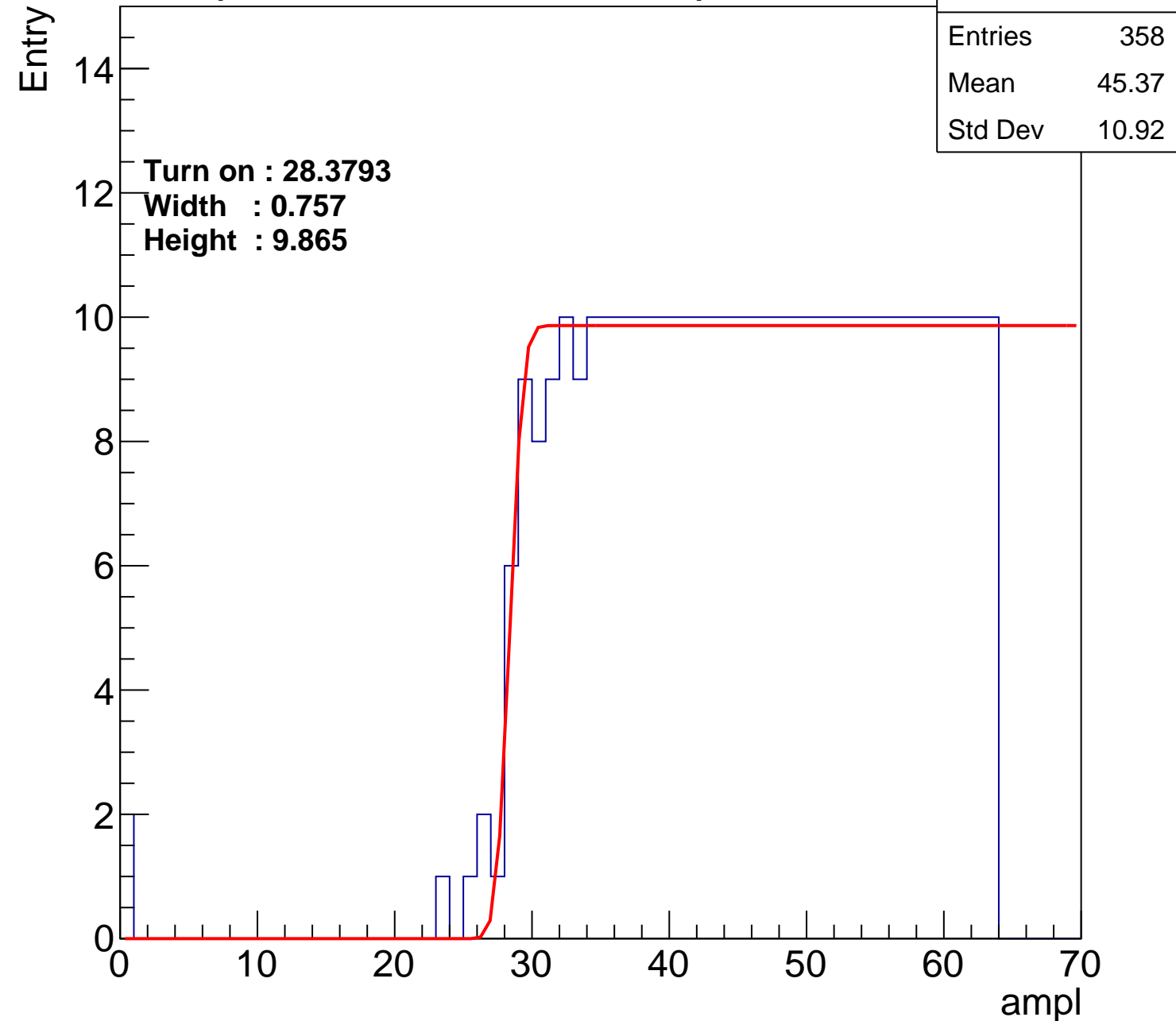
Width : 0.757

Height : 9.865

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch23

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.23
Std Dev	11.38

Turn on : 28.7634

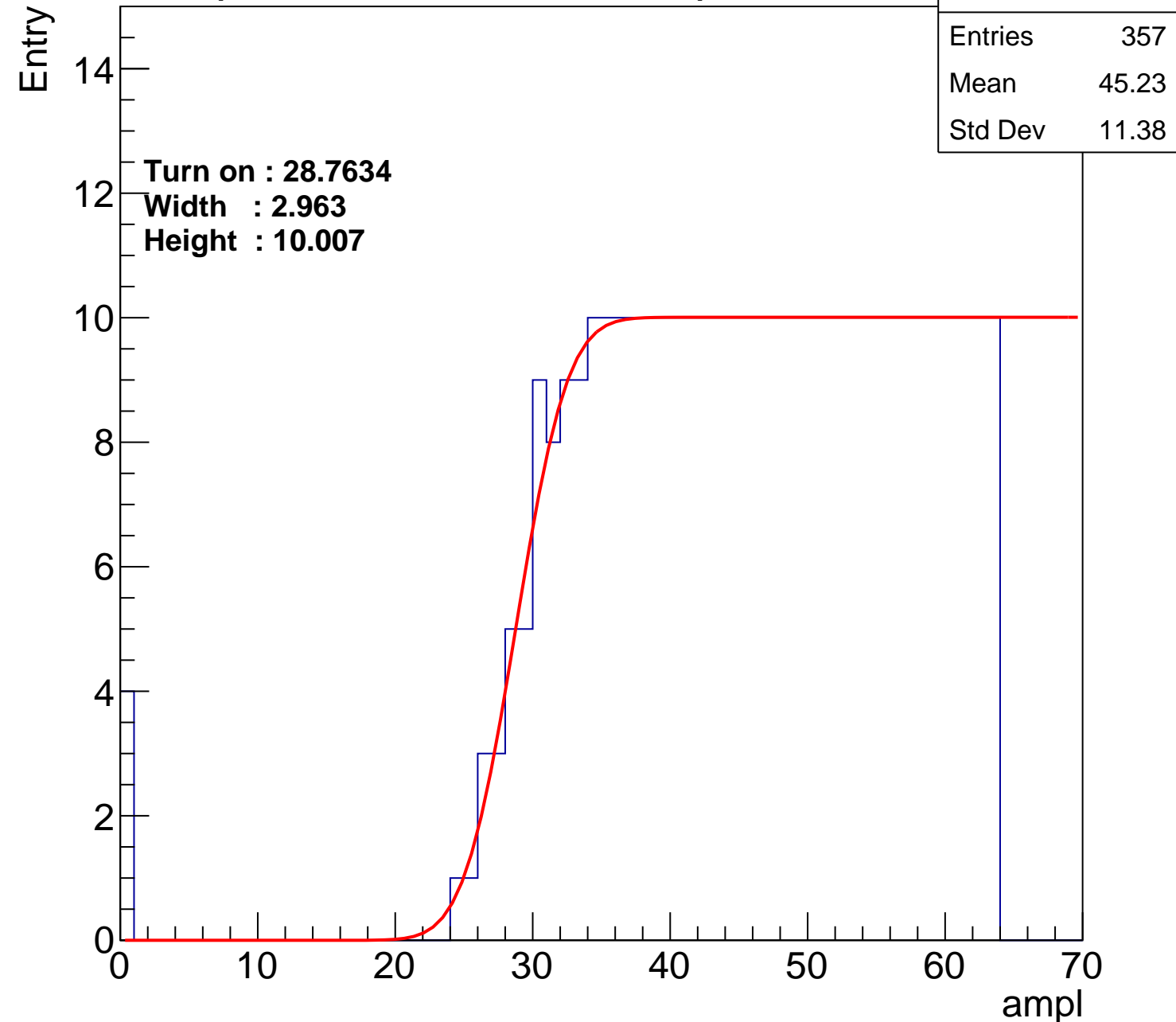
Width : 2.963

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch24

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.2
Std Dev	11.18

Turn on : 28.5142

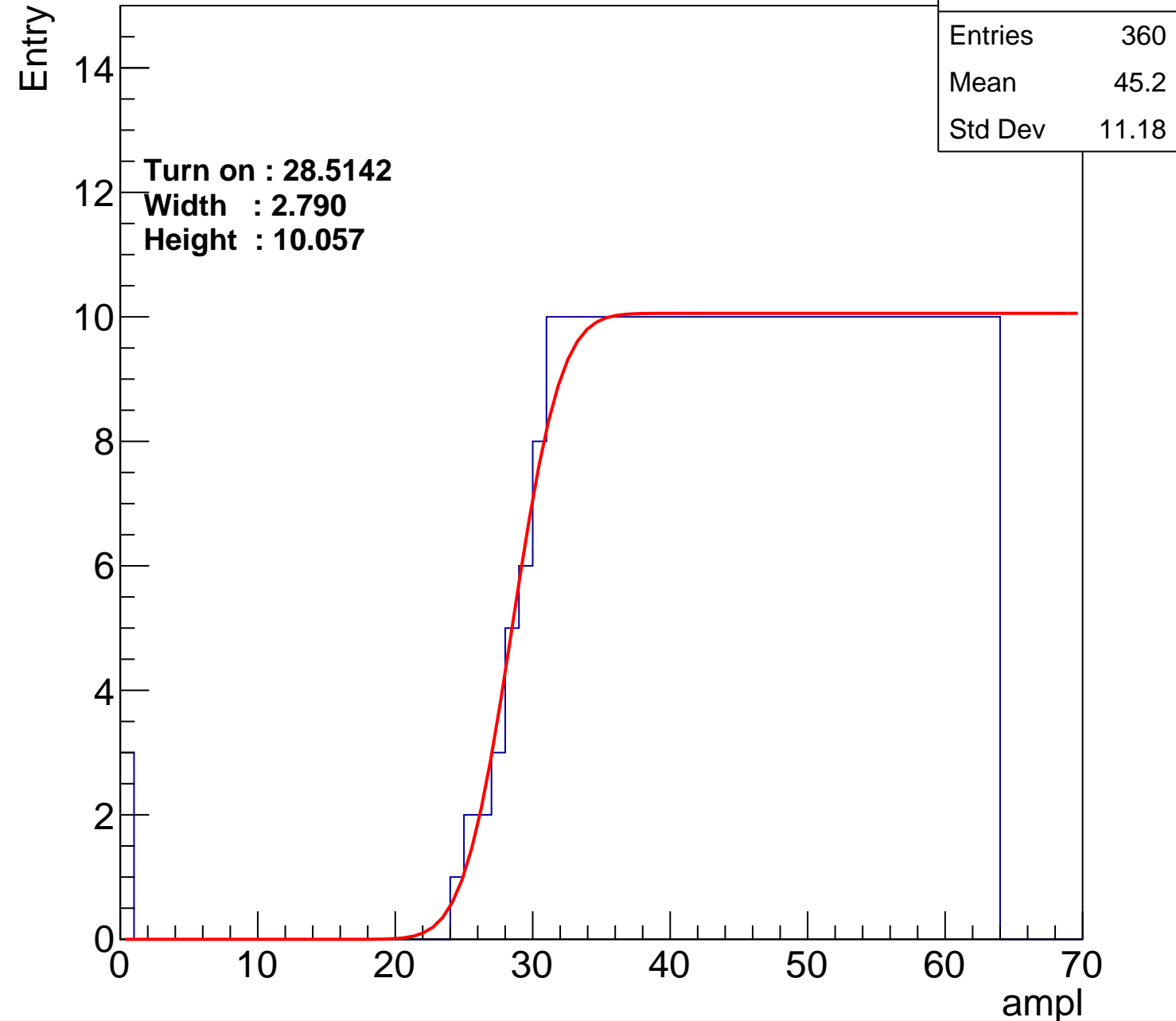
Width : 2.790

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch25

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.17
Std Dev	11.61

Turn on : 26.6648

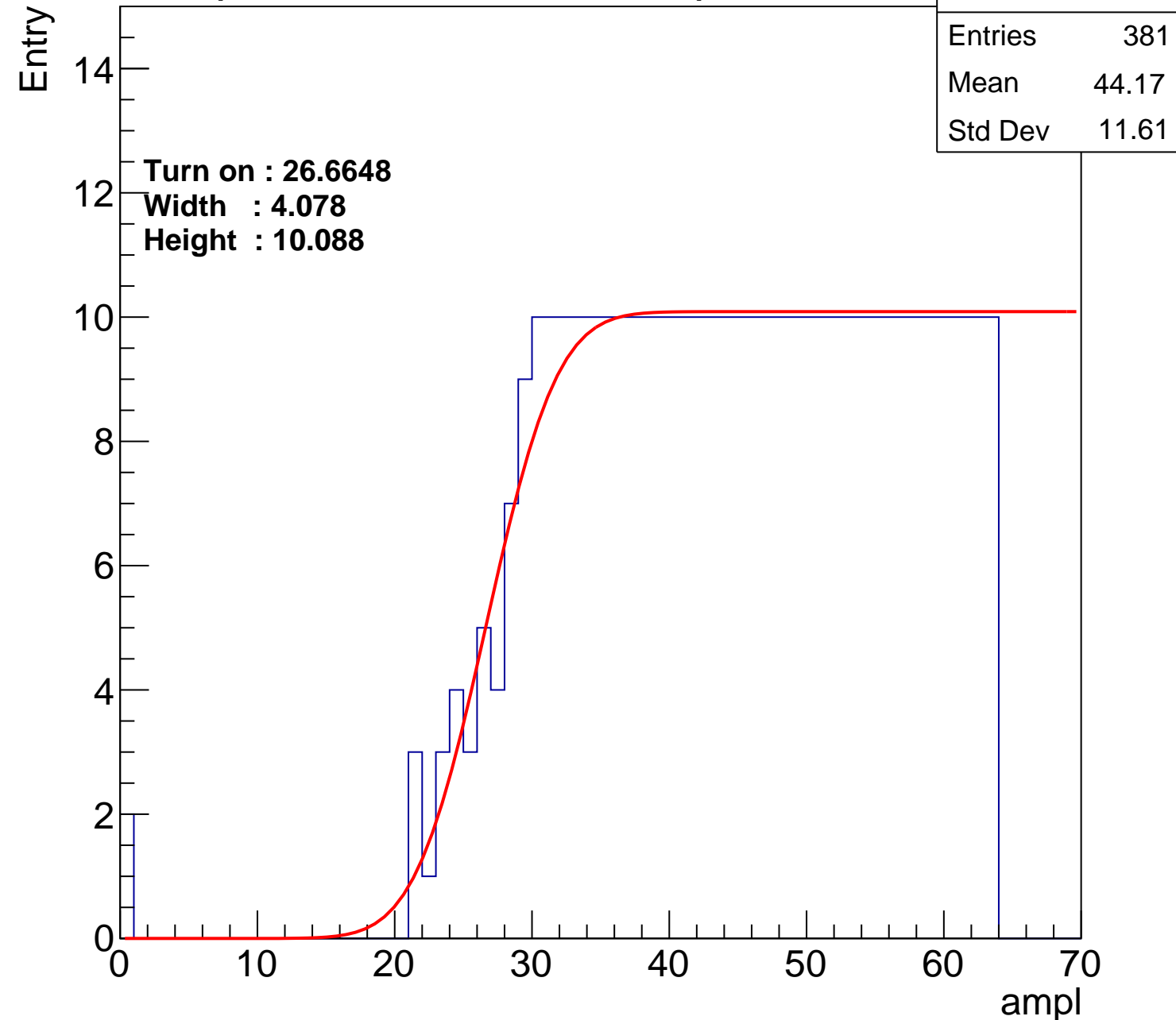
Width : 4.078

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch26

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.39
Std Dev	10.88

Turn on : 28.0274

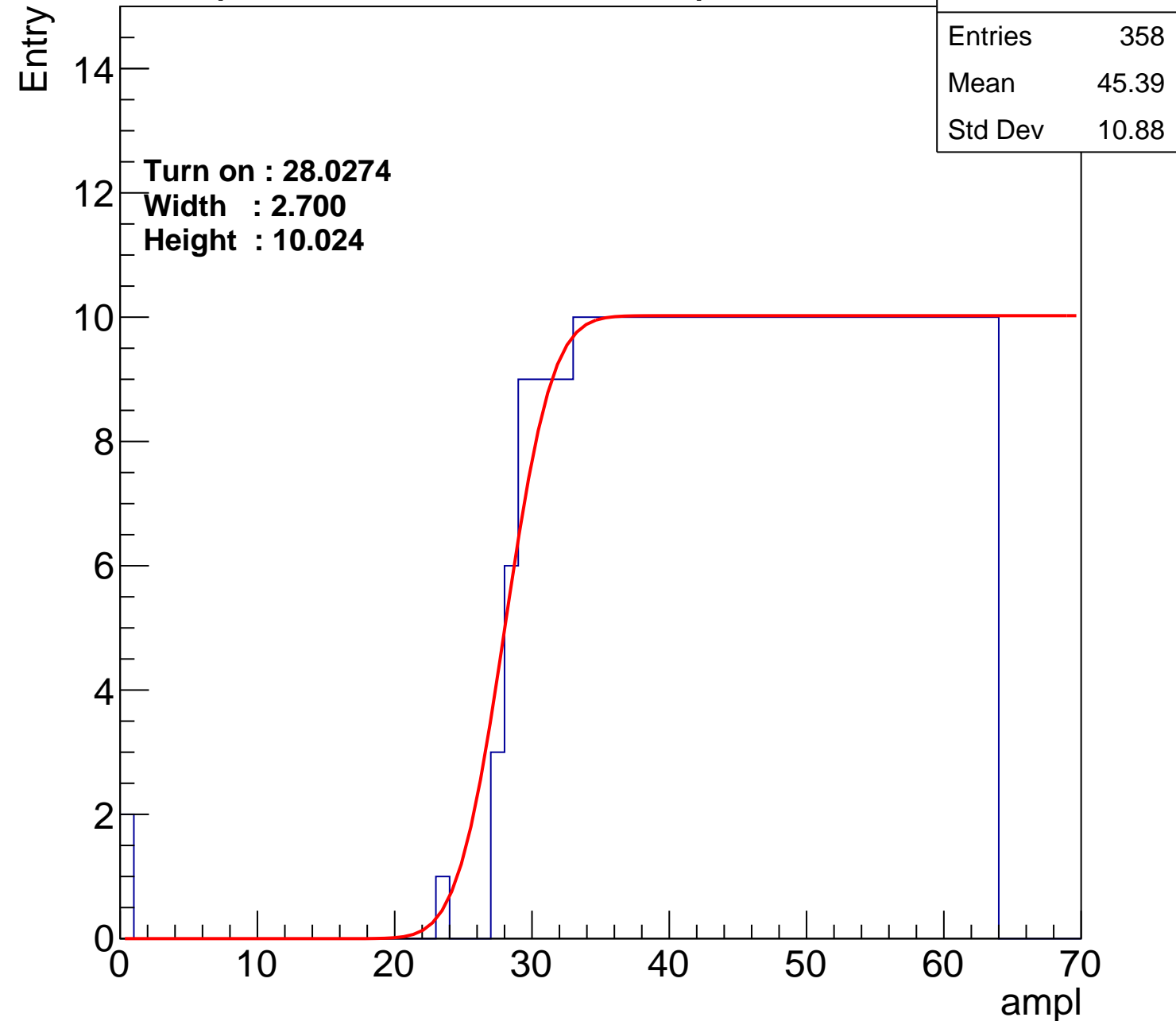
Width : 2.700

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch27

calib_packv5_042523_0143.root, FC#5, port B1

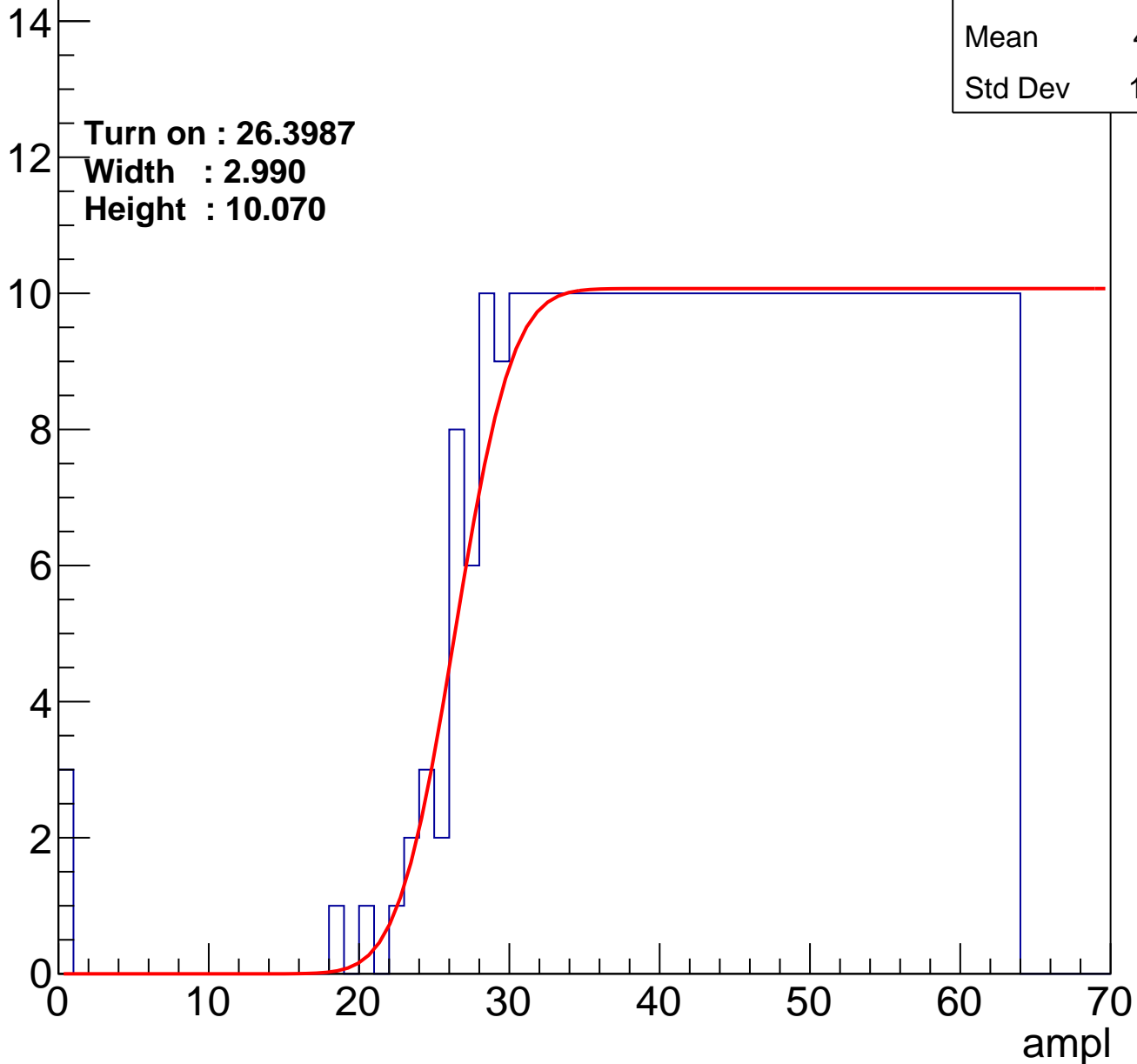
Entry

Entries	386
Mean	43.91
Std Dev	11.84

Turn on : 26.3987

Width : 2.990

Height : 10.070



B0L000S, U9-ch28

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 27.1818

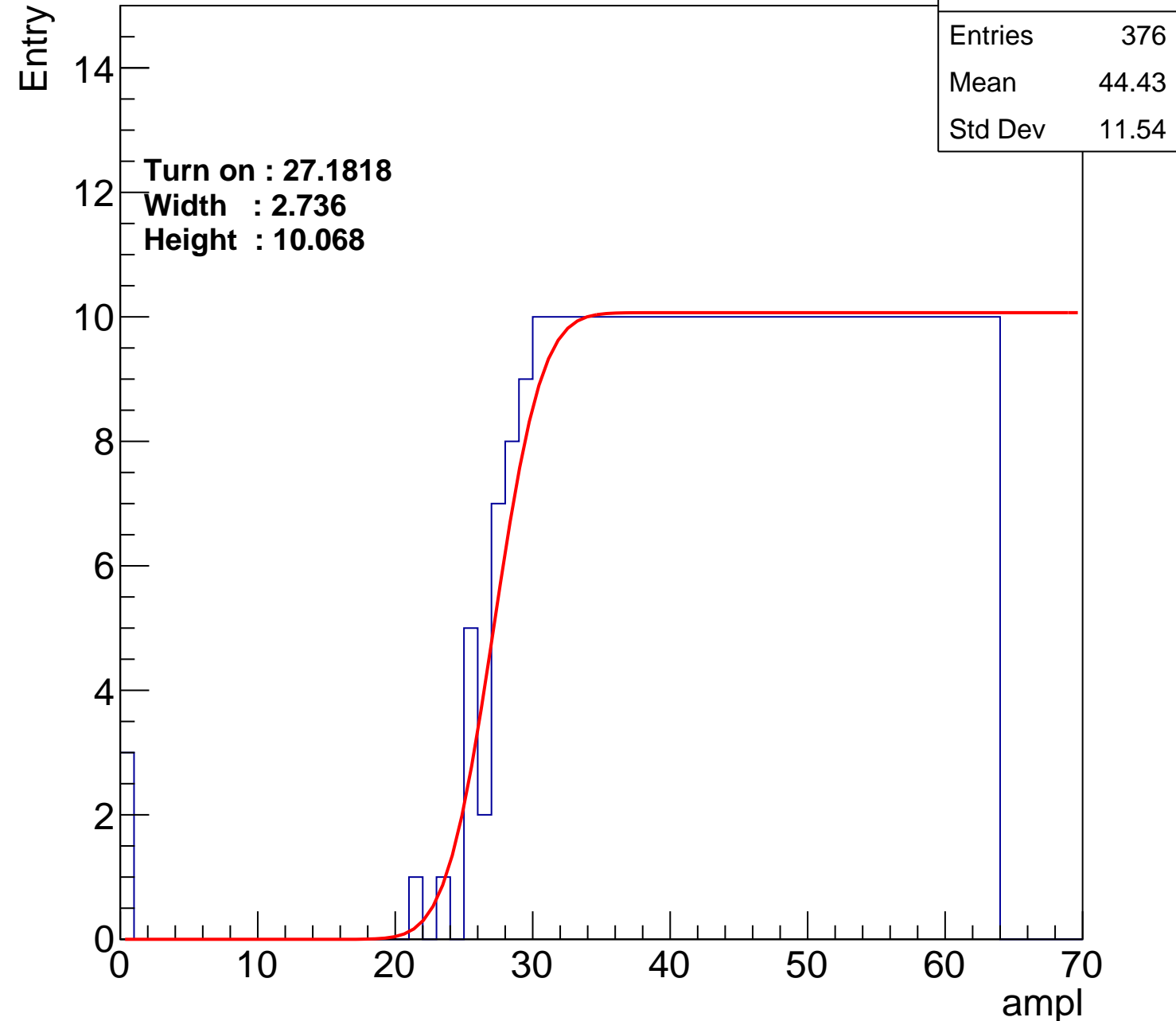
Width : 2.736

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch29

calib_packv5_042523_0143.root, FC#5, port B1

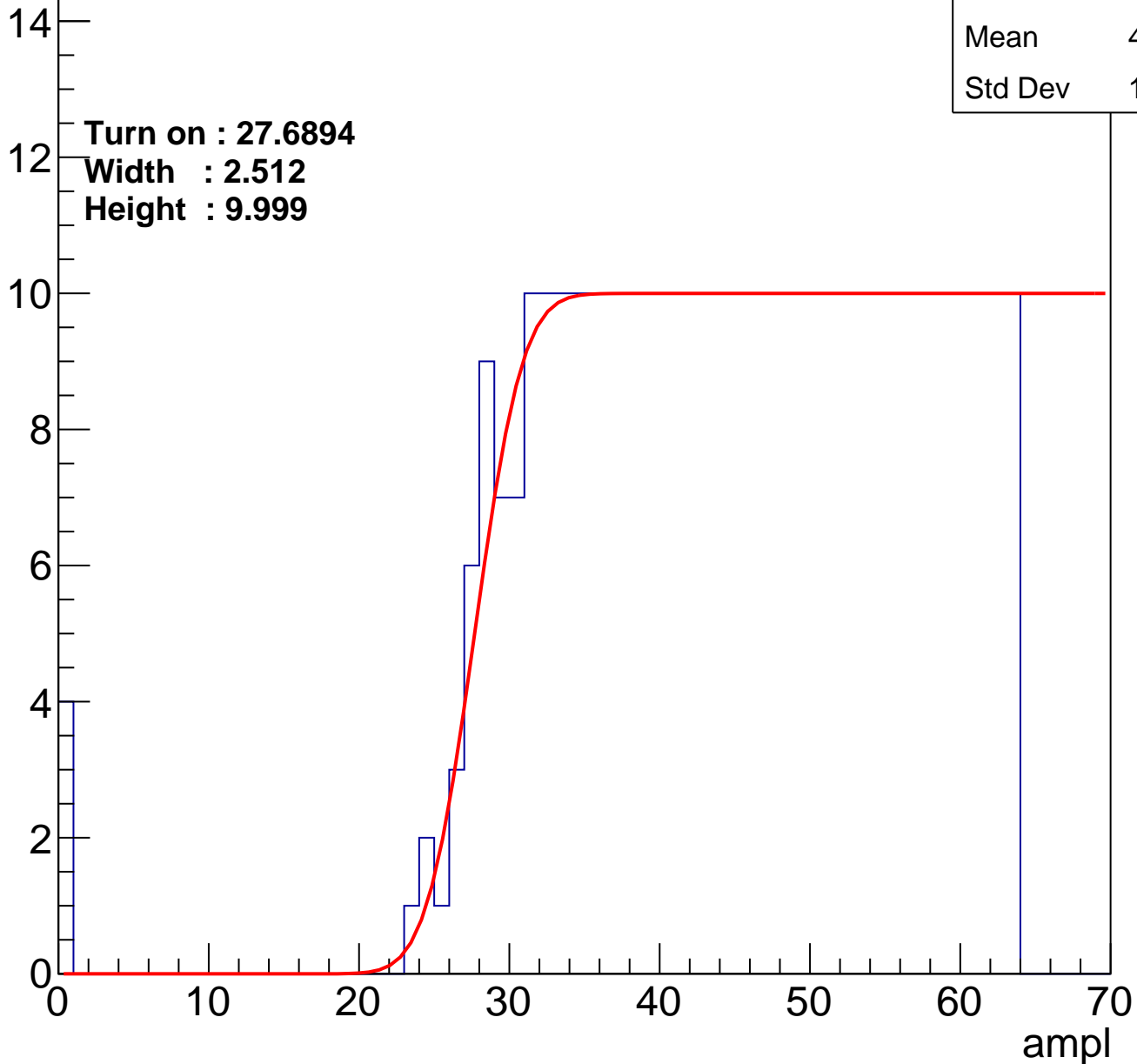
Entry

Entries	370
Mean	44.62
Std Dev	11.63

Turn on : 27.6894

Width : 2.512

Height : 9.999



B0L000S, U9-ch30

calib_packv5_042523_0143.root, FC#5, port B1

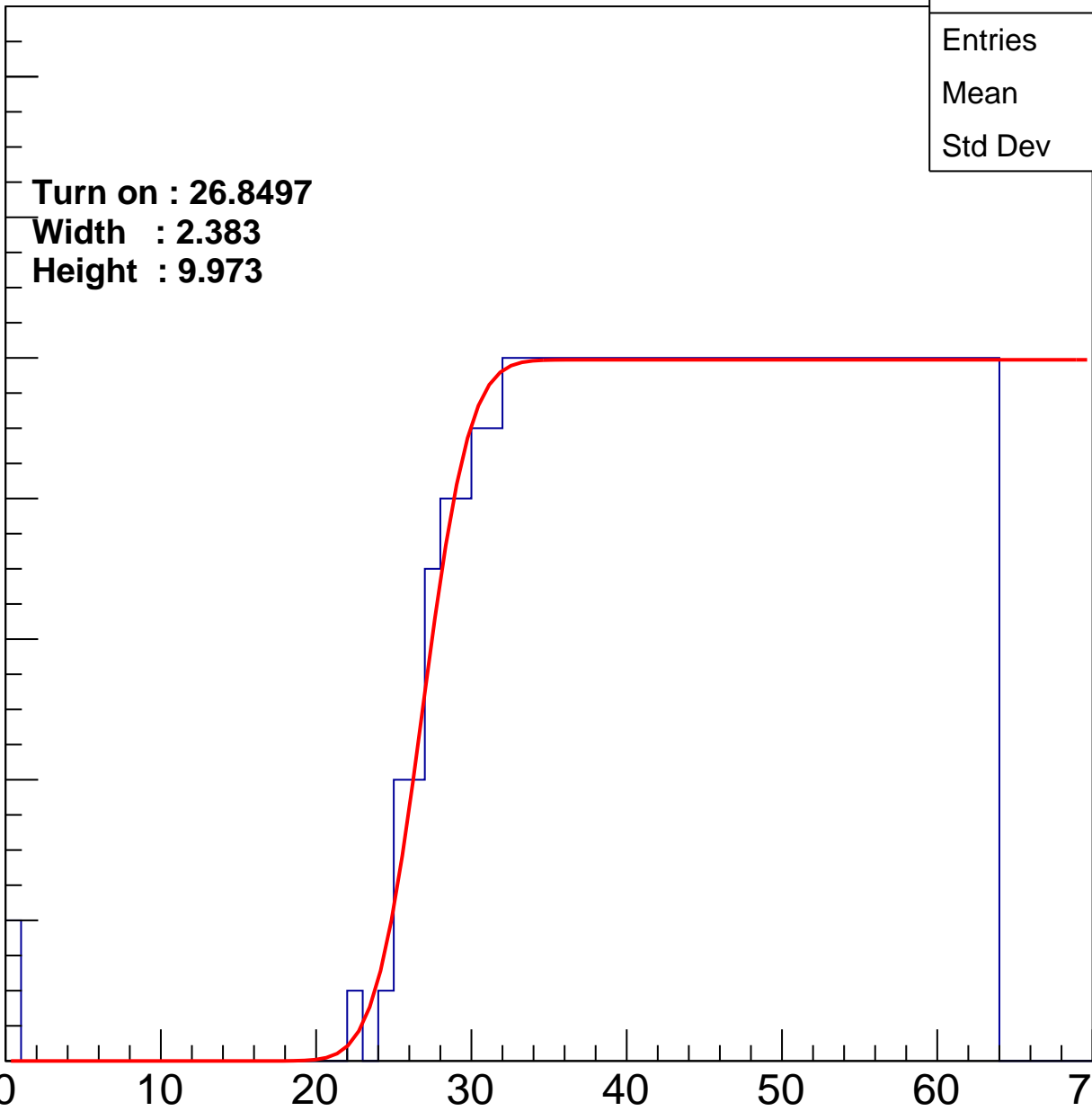
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8497
Width : 2.383
Height : 9.973

Entries	373
Mean	44.62
Std Dev	11.31

ampl



B0L000S, U9-ch31

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.2730

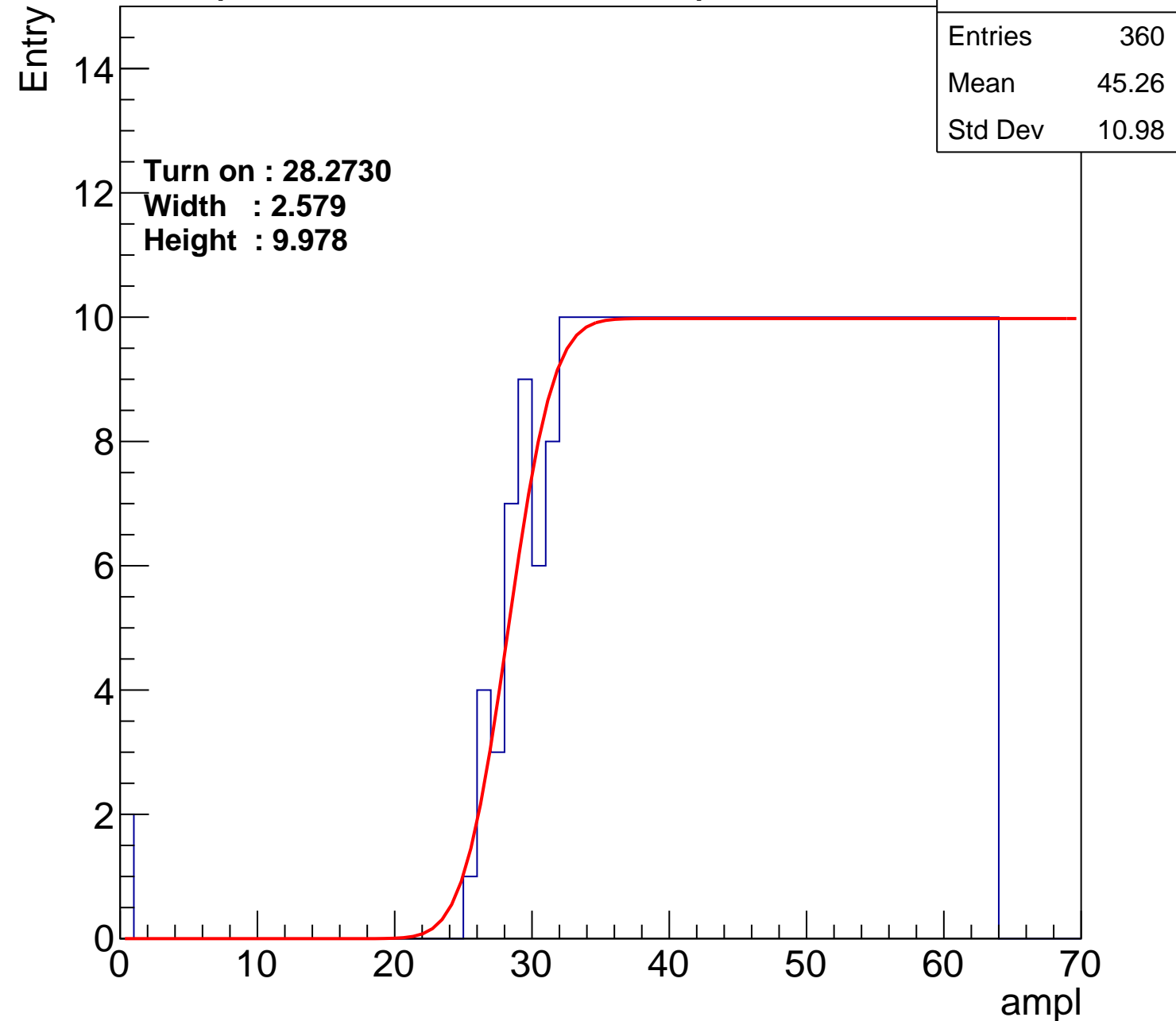
Width : 2.579

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch32

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.76
Std Dev	11.58

Turn on : 28.0564

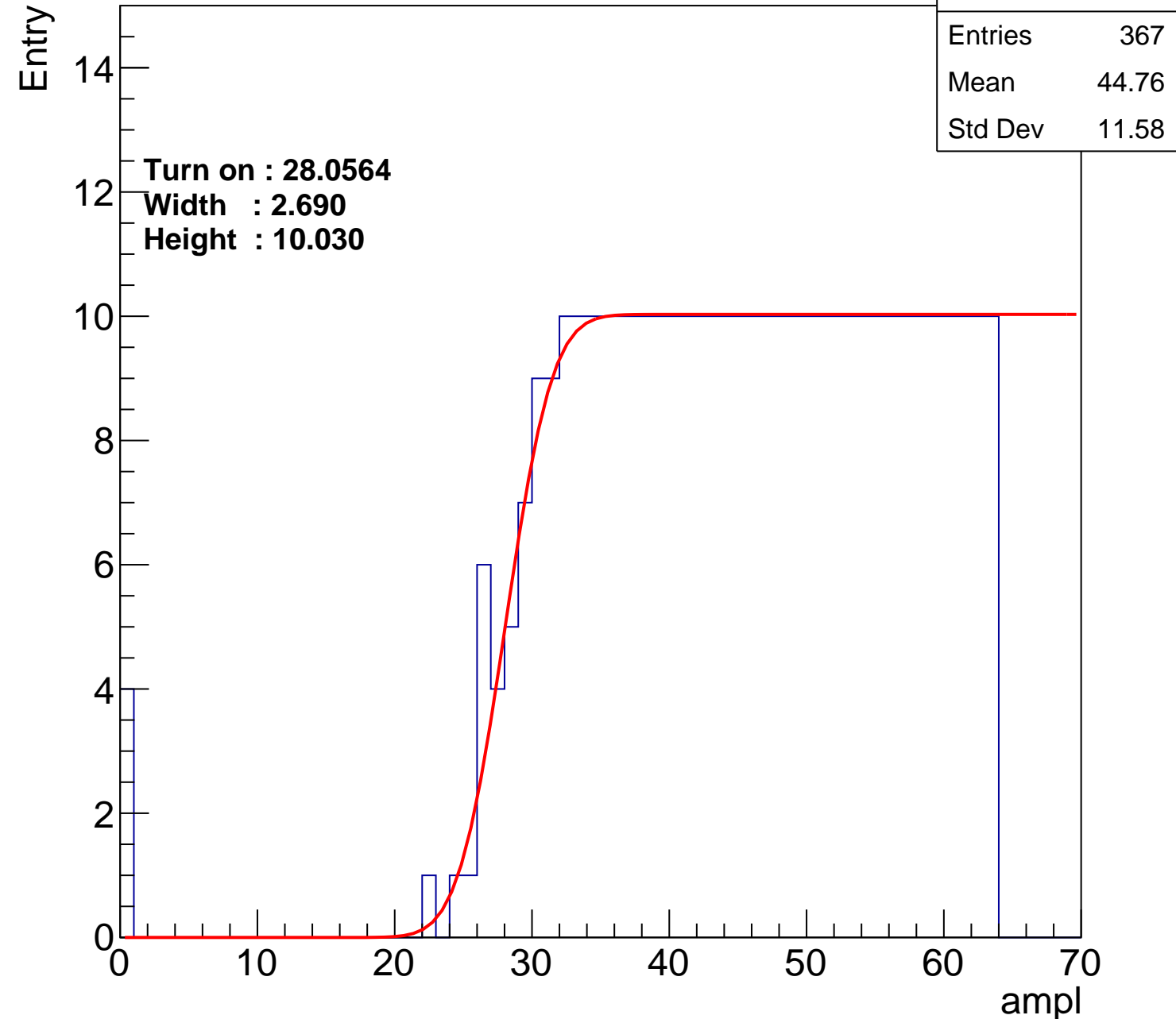
Width : 2.690

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch33

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.14
Std Dev	11.37

Turn on : 28.4209

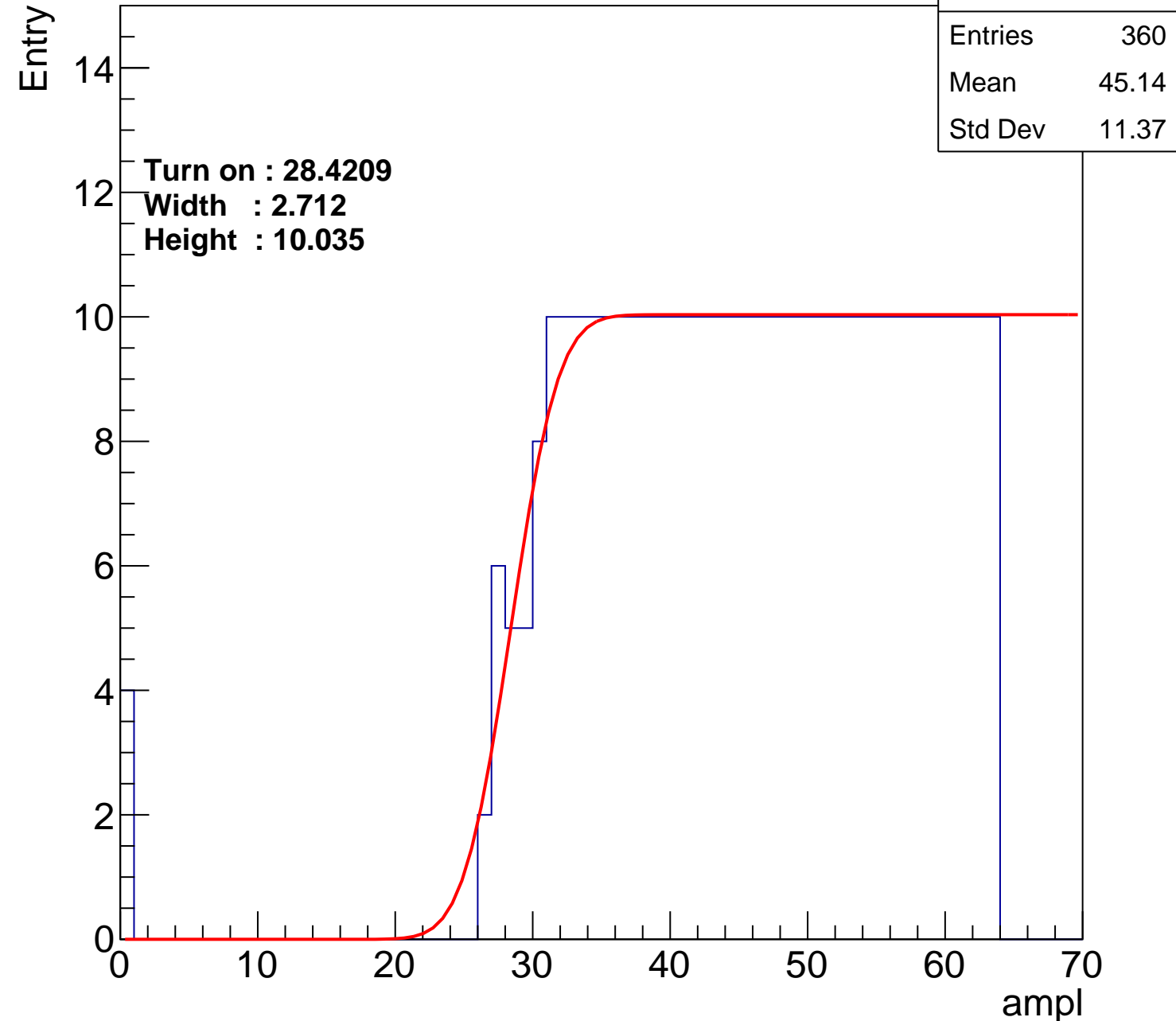
Width : 2.712

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch34

calib_packv5_042523_0143.root, FC#5, port B1

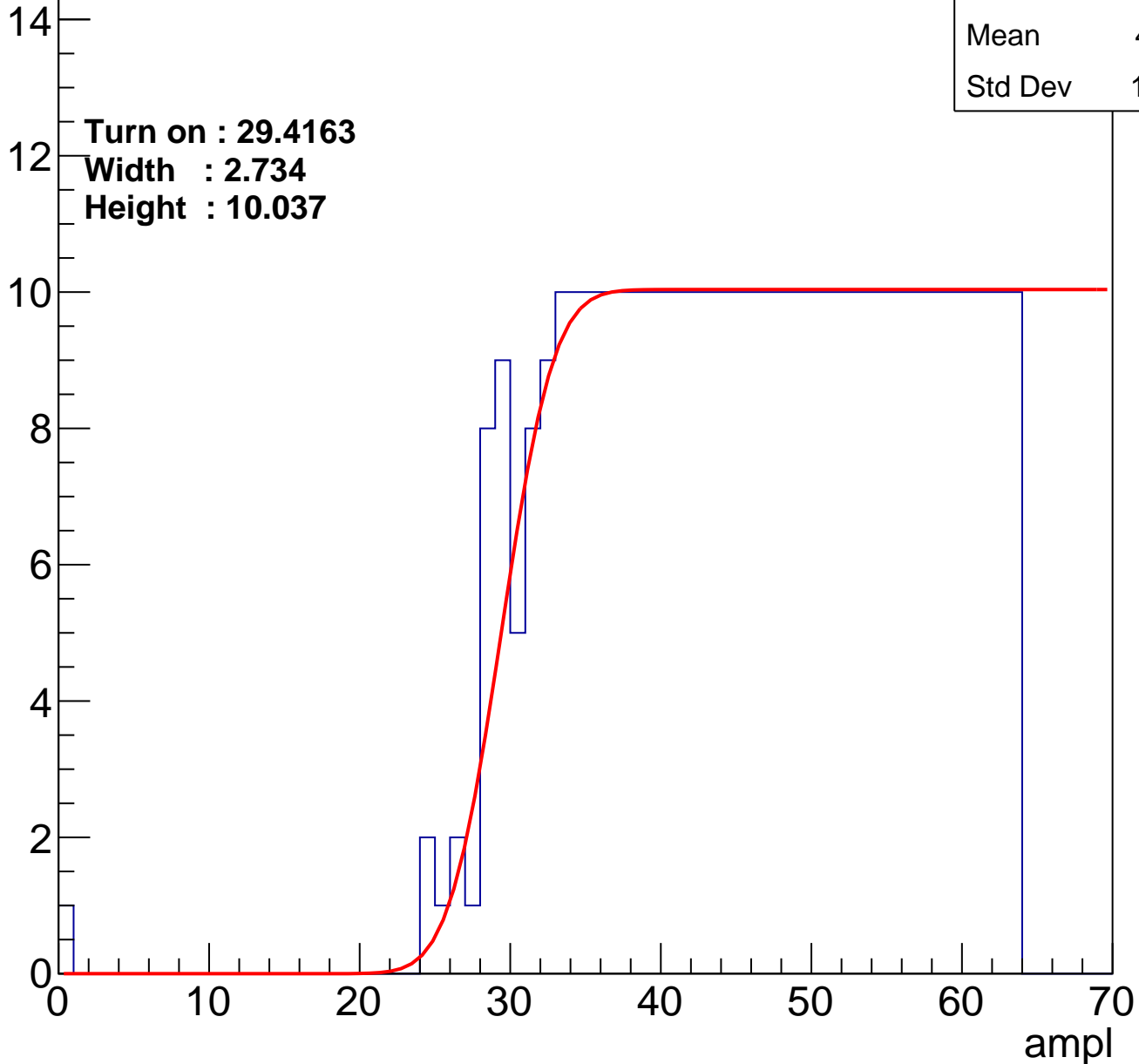
Entries	356
Mean	45.51
Std Dev	10.69

Turn on : 29.4163

Width : 2.734

Height : 10.037

Entry



B0L000S, U9-ch35

calib_packv5_042523_0143.root, FC#5, port B1

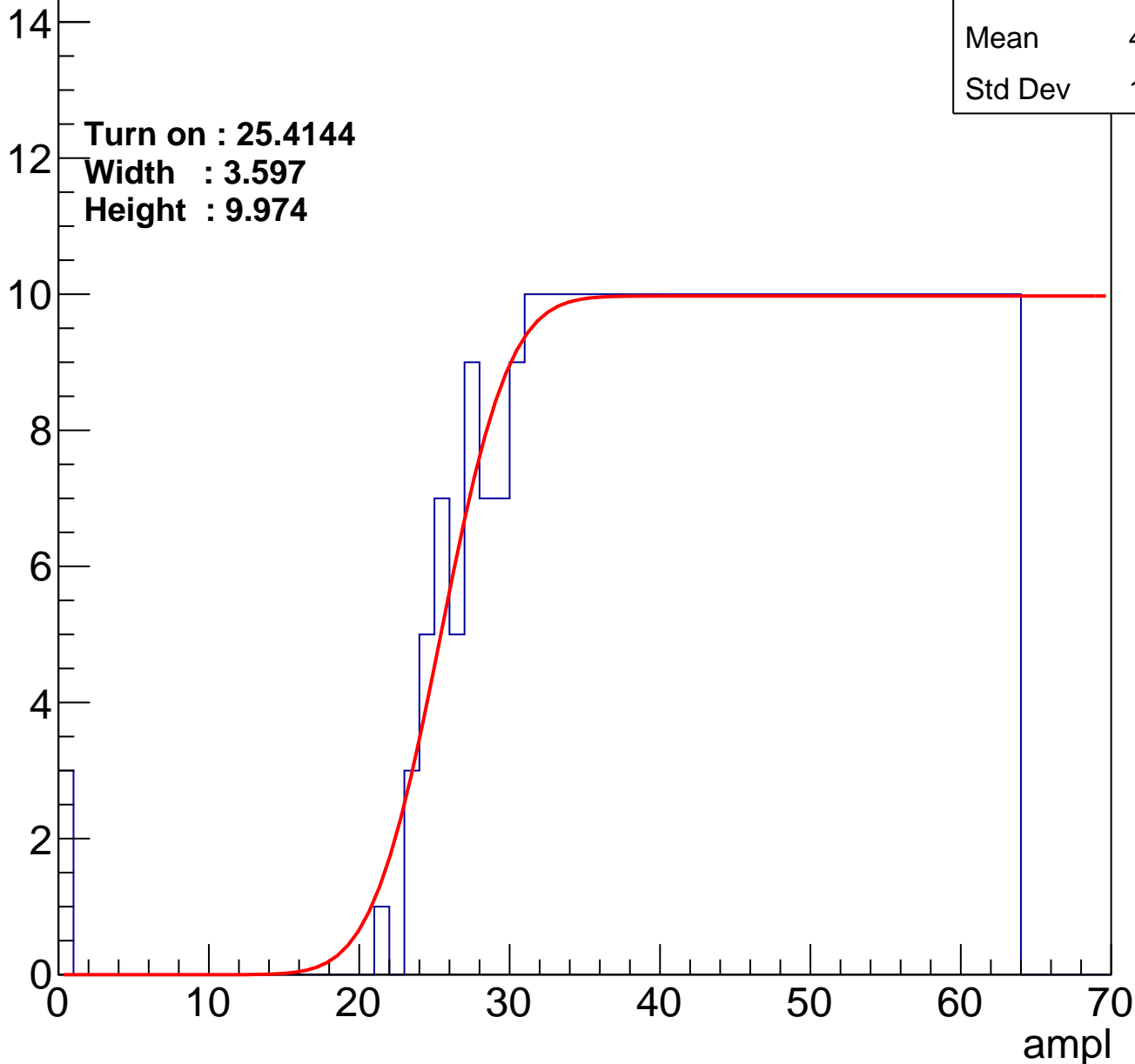
Entry

Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 25.4144

Width : 3.597

Height : 9.974



B0L000S, U9-ch36

calib_packv5_042523_0143.root, FC#5, port B1

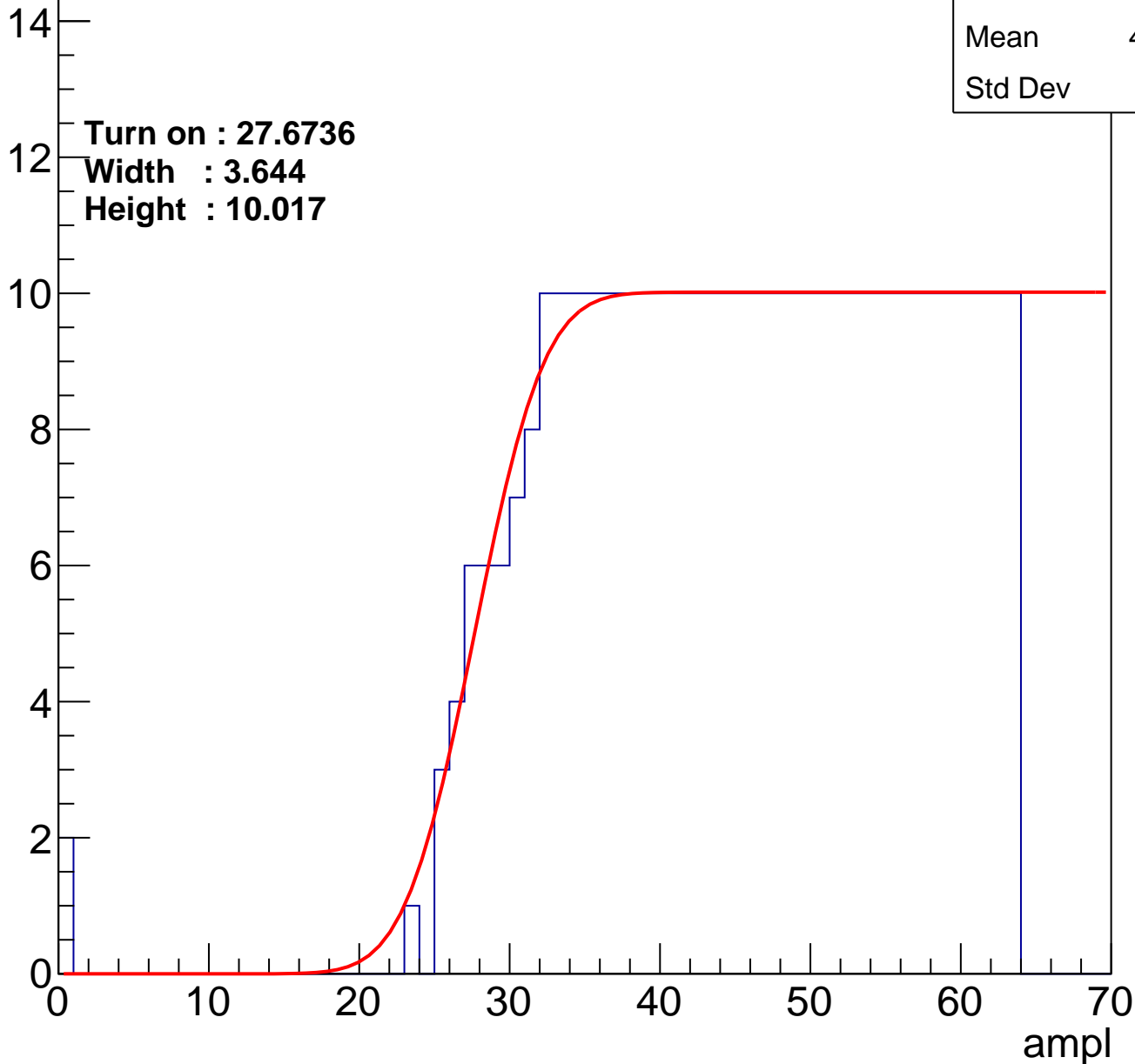
Entry

Entries	363
Mean	45.08
Std Dev	11.11

Turn on : 27.6736

Width : 3.644

Height : 10.017



B0L000S, U9-ch37

calib_packv5_042523_0143.root, FC#5, port B1

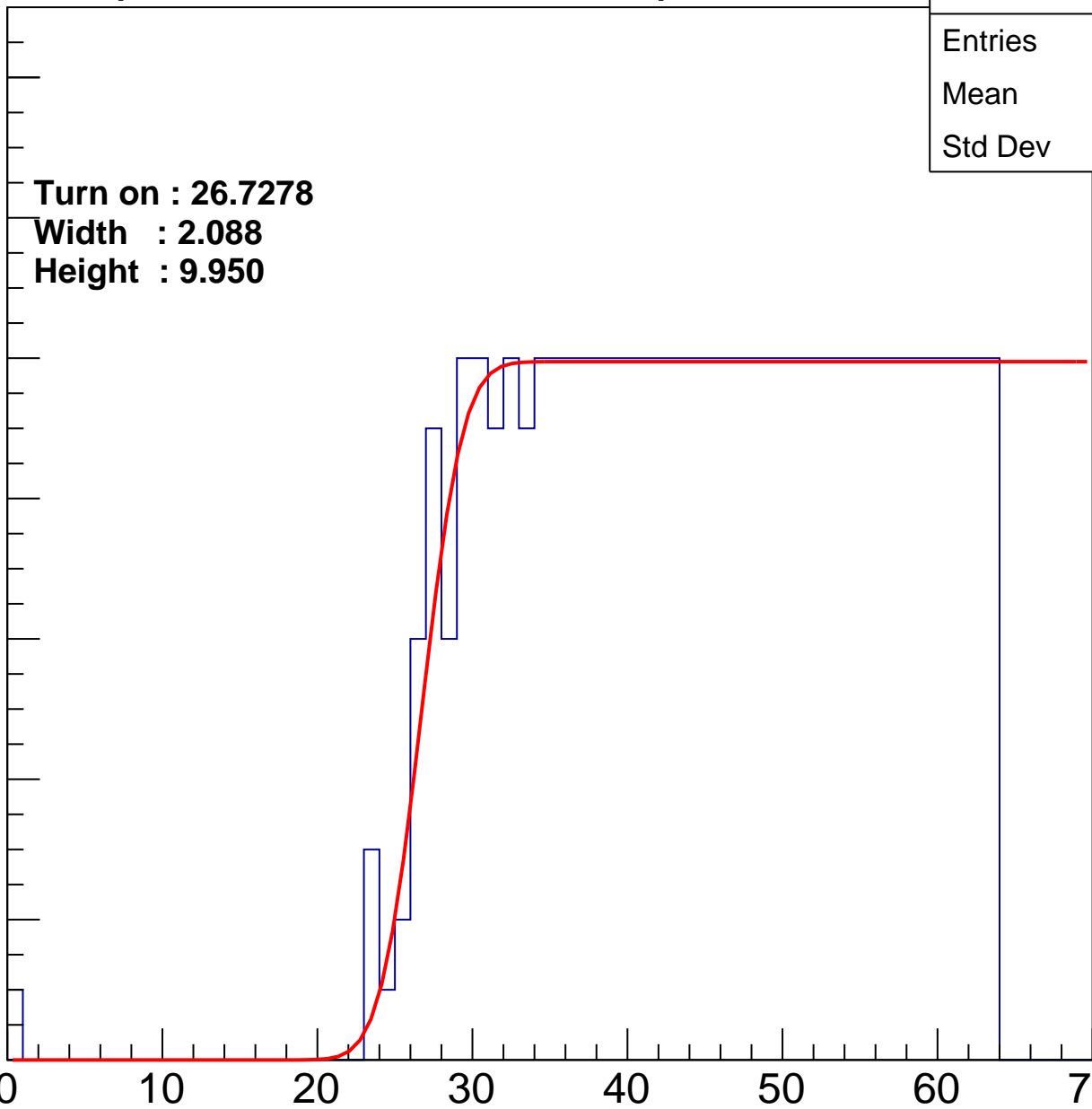
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7278
Width : 2.088
Height : 9.950

Entries	376
Mean	44.54
Std Dev	11.2

ampl



B0L000S, U9-ch38

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.35
Std Dev	11.43

Turn on : 26.1922

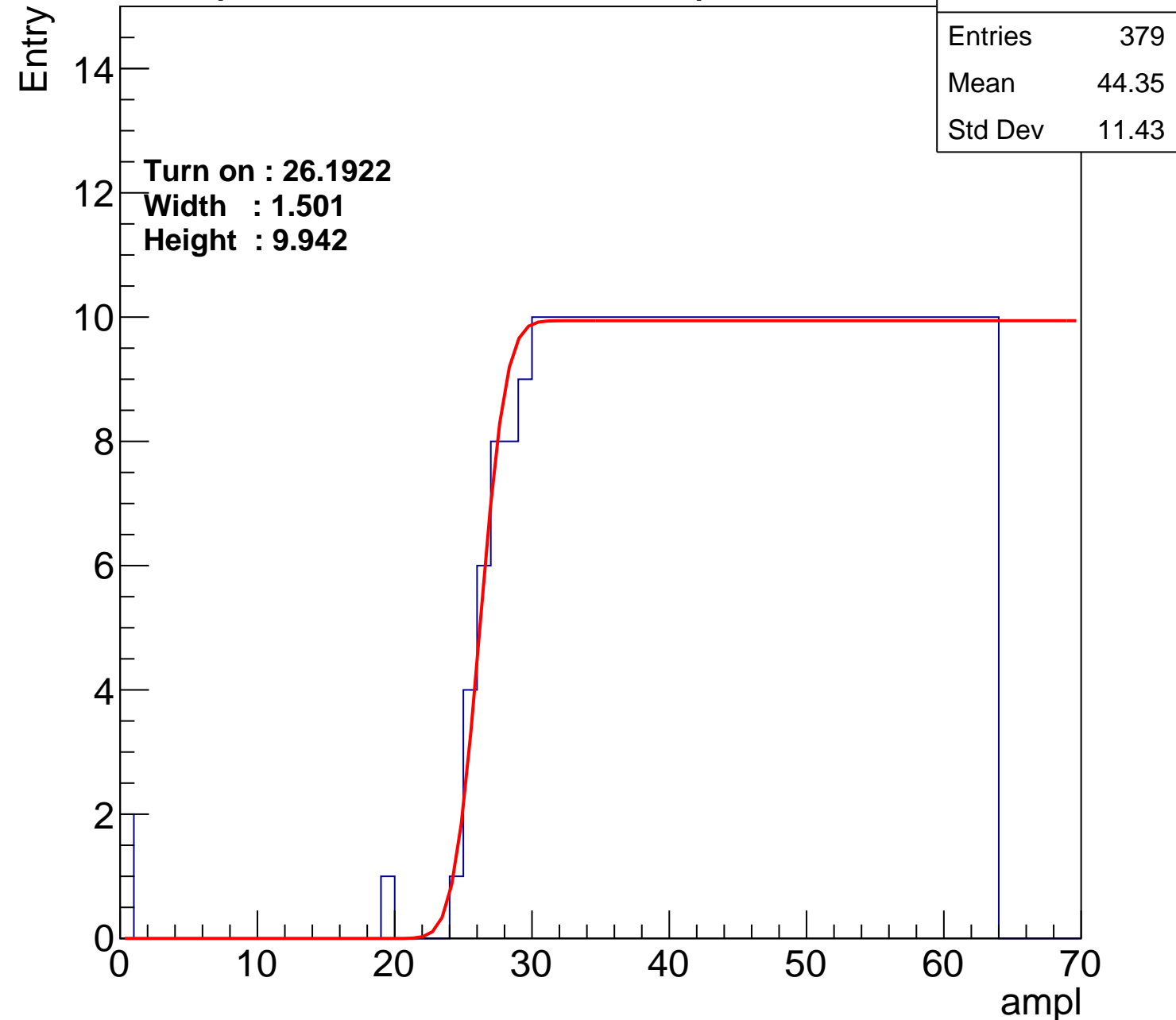
Width : 1.501

Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch39

calib_packv5_042523_0143.root, FC#5, port B1

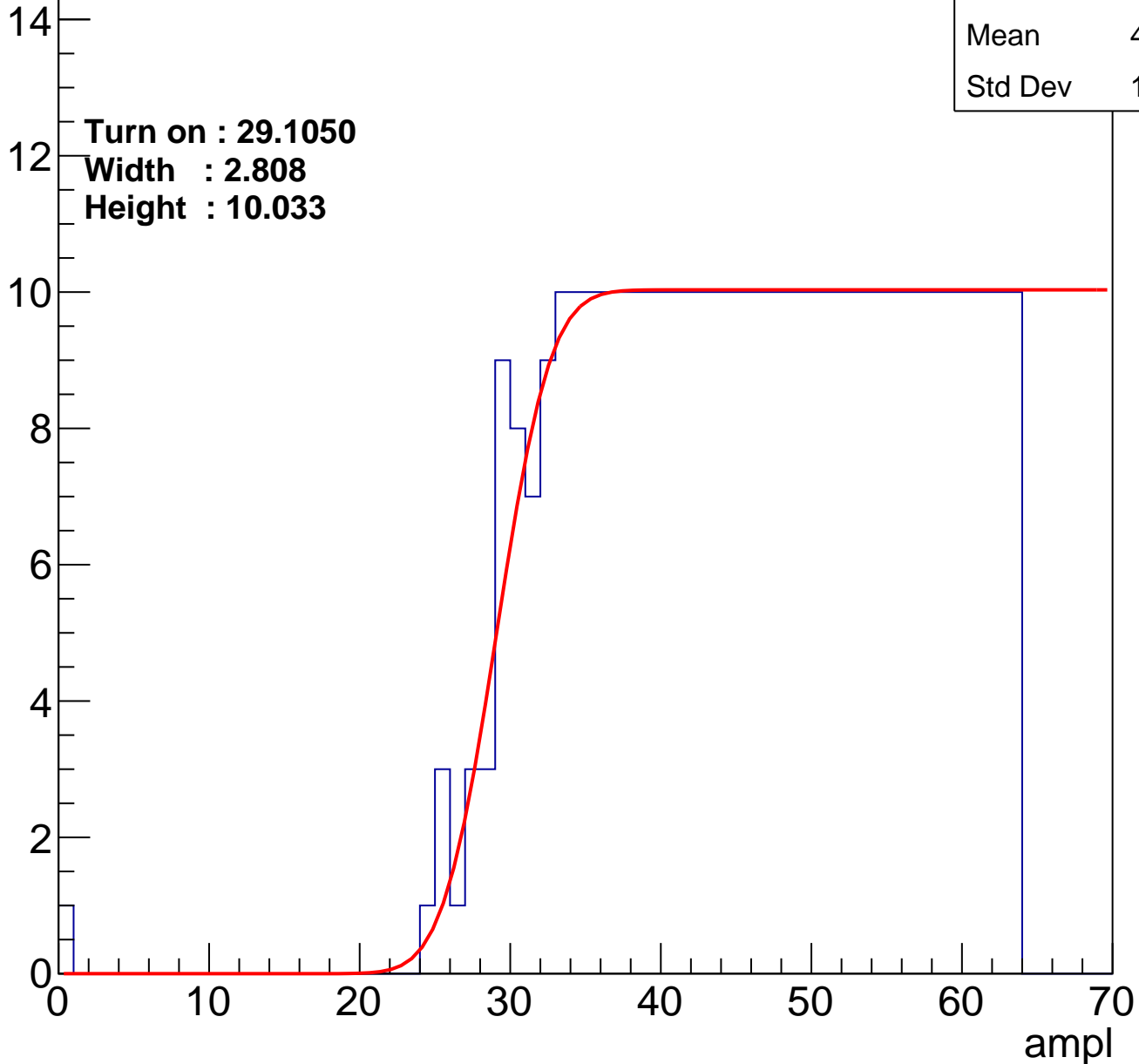
Entries	355
Mean	45.57
Std Dev	10.66

Turn on : 29.1050

Width : 2.808

Height : 10.033

Entry



B0L000S, U9-ch40

calib_packv5_042523_0143.root, FC#5, port B1

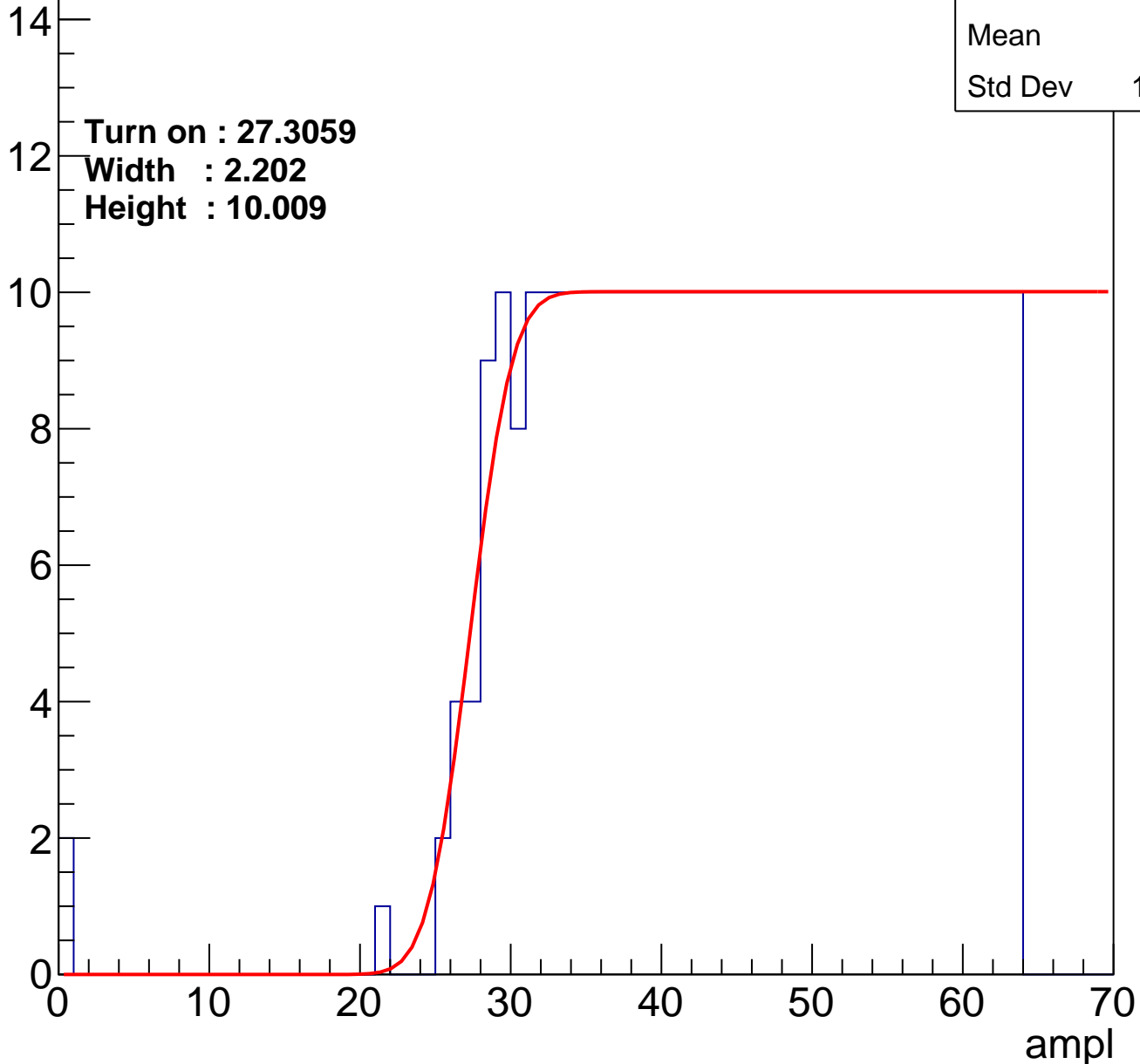
Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.3059

Width : 2.202

Height : 10.009

Entry



B0L000S, U9-ch41

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.55
Std Dev	11.26

Turn on : 29.5840

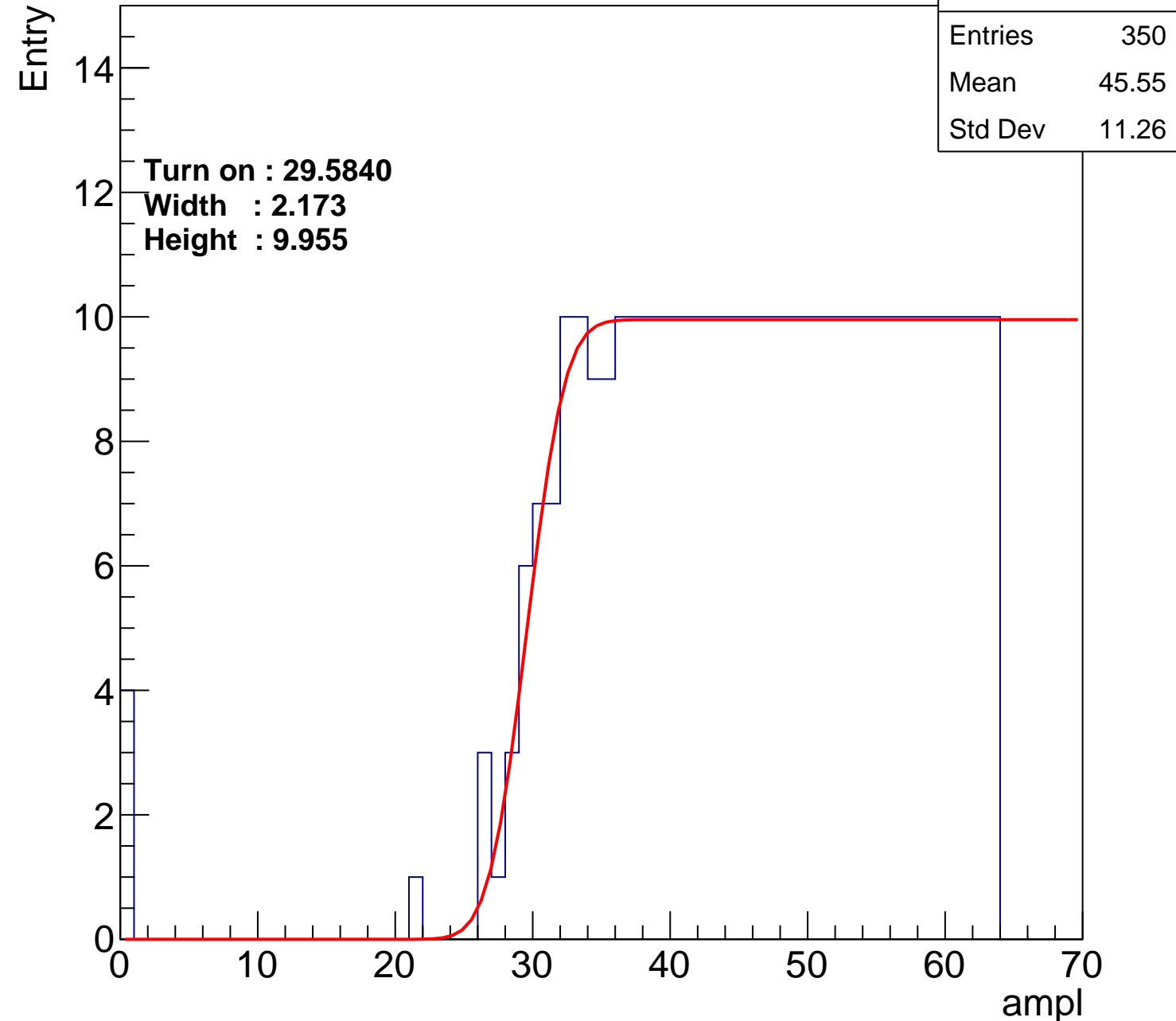
Width : 2.173

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch42

calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.69
Std Dev	11.58

Turn on : 27.9168

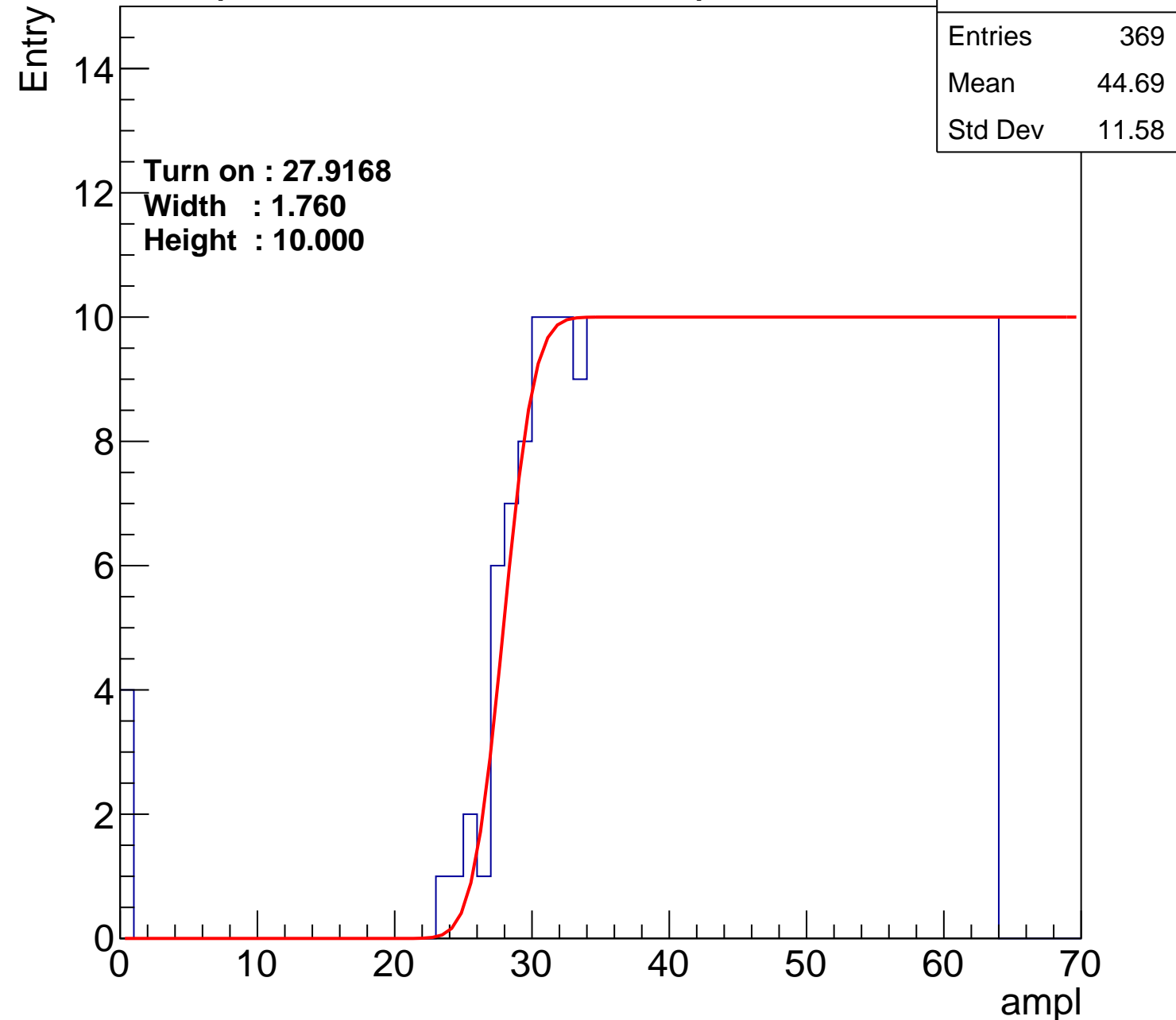
Width : 1.760

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch43

calib_packv5_042523_0143.root, FC#5, port B1

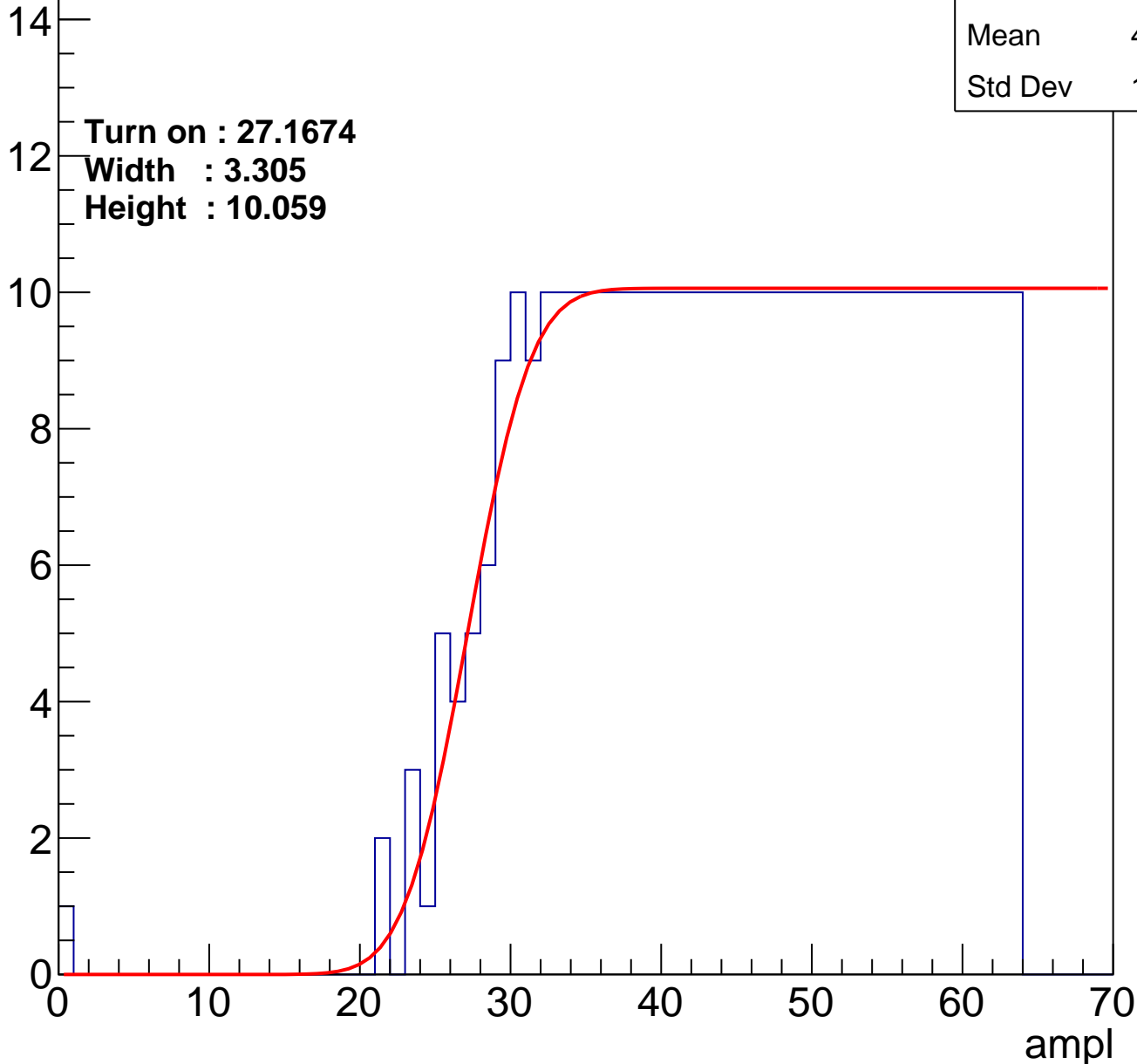
Entry

Entries	375
Mean	44.55
Std Dev	11.24

Turn on : 27.1674

Width : 3.305

Height : 10.059



B0L000S, U9-ch44

calib_packv5_042523_0143.root, FC#5, port B1

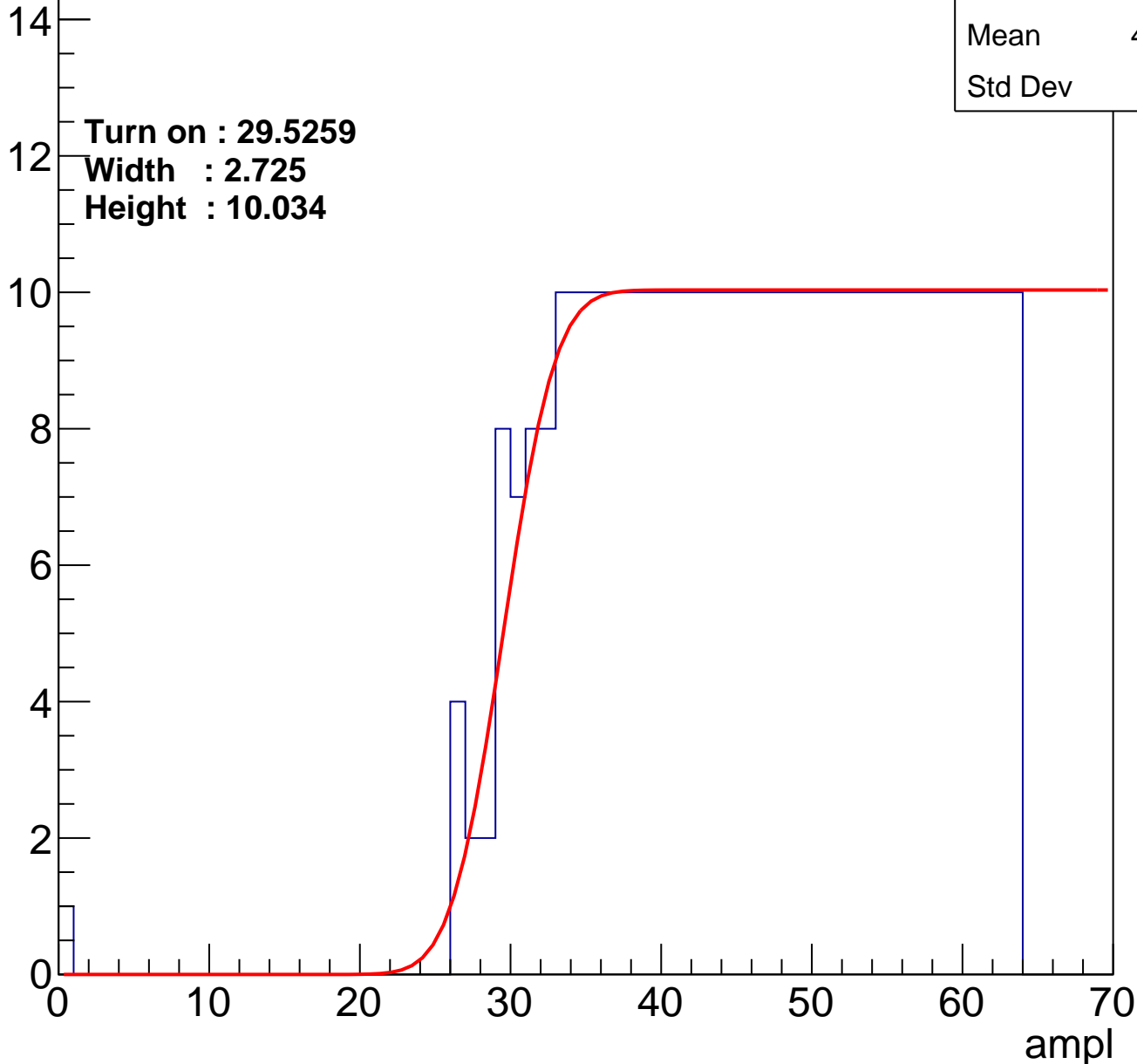
Entries	350
Mean	45.83
Std Dev	10.5

Turn on : 29.5259

Width : 2.725

Height : 10.034

Entry



B0L000S, U9-ch45

calib_packv5_042523_0143.root, FC#5, port B1

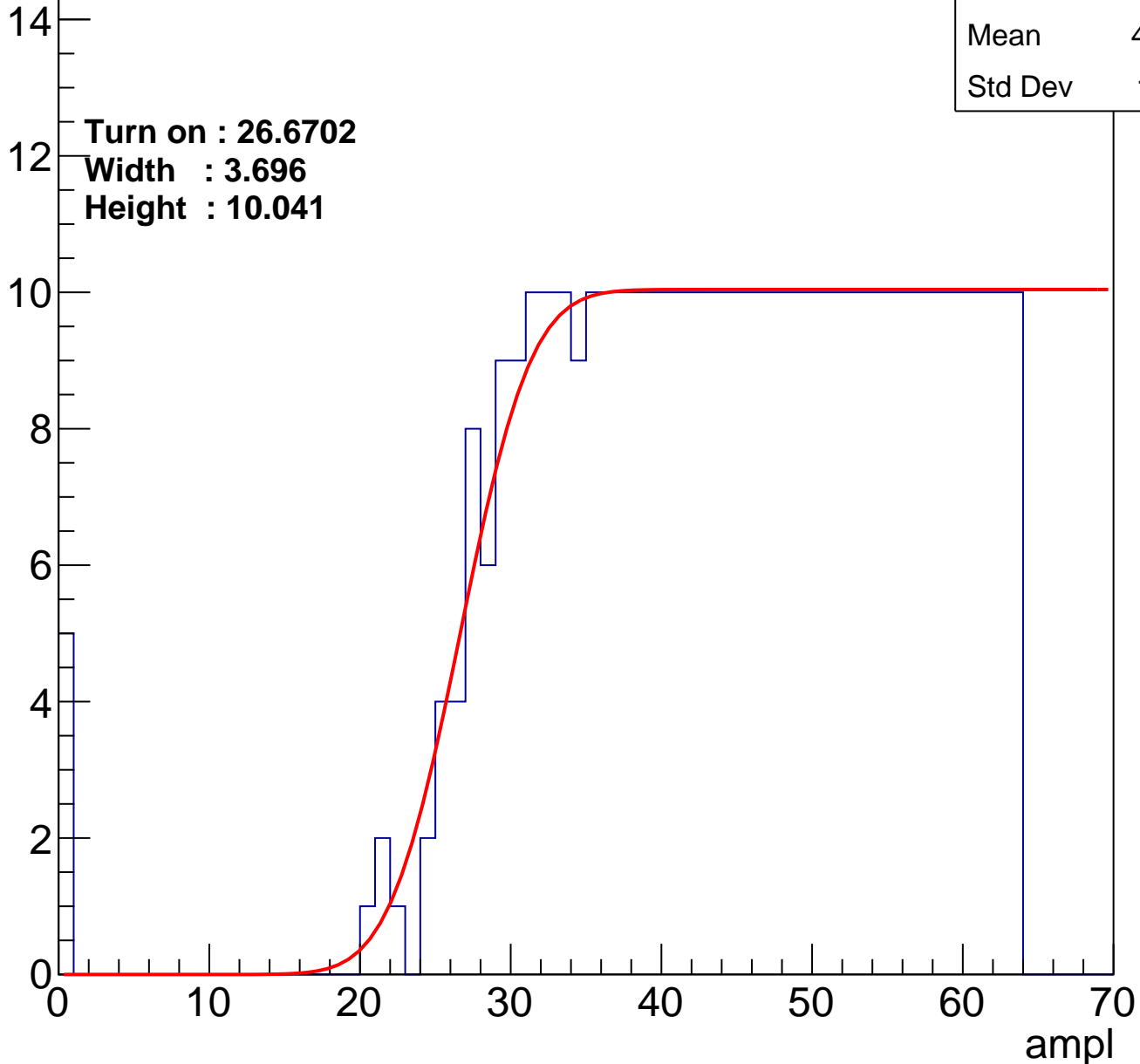
Entries	380
Mean	44.02
Std Dev	12.11

Turn on : 26.6702

Width : 3.696

Height : 10.041

Entry



B0L000S, U9-ch46

calib_packv5_042523_0143.root, FC#5, port B1

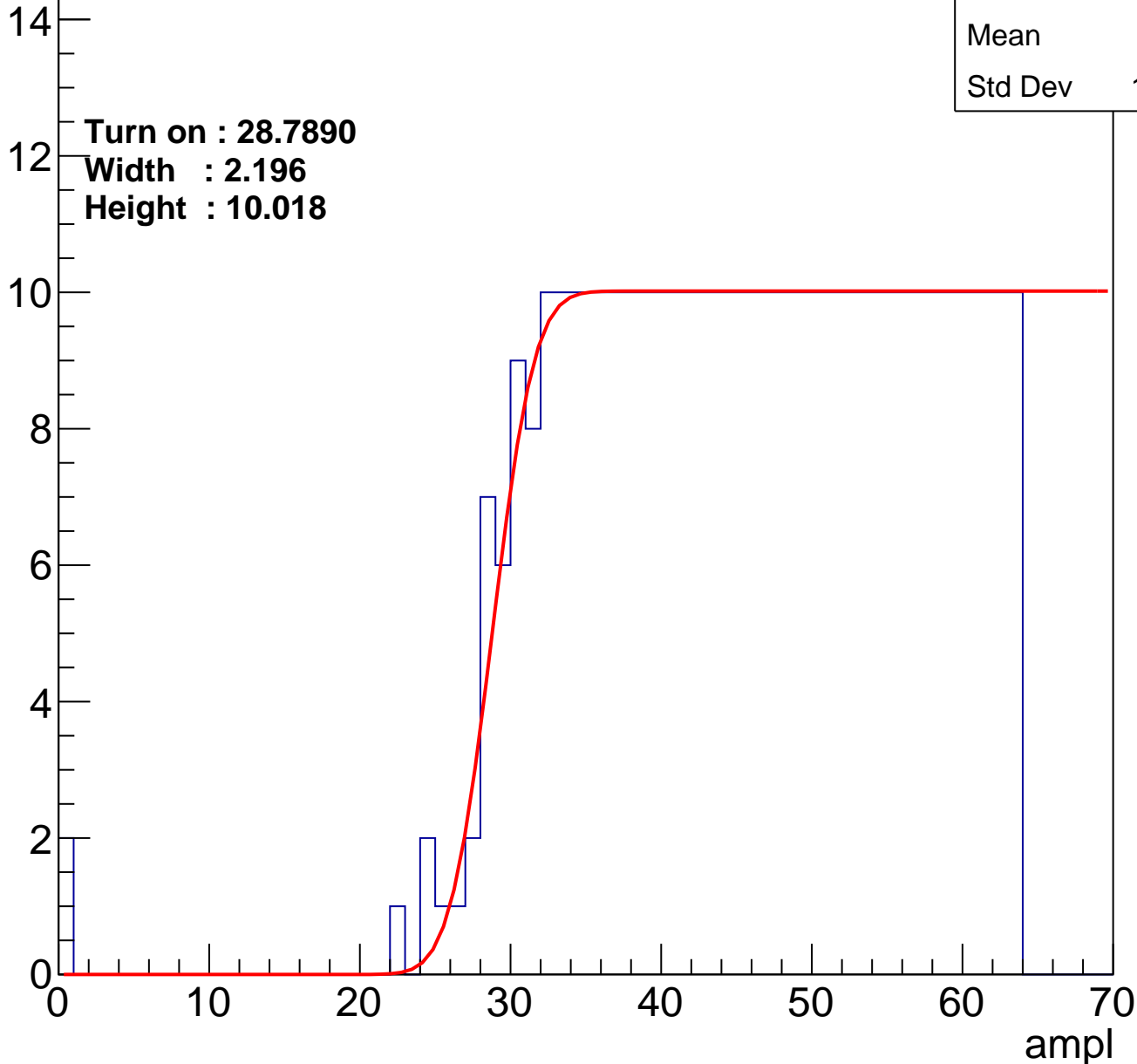
Entries	359
Mean	45.3
Std Dev	10.98

Turn on : 28.7890

Width : 2.196

Height : 10.018

Entry



B0L000S, U9-ch47

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.27
Std Dev	10.76

Turn on : 28.2727

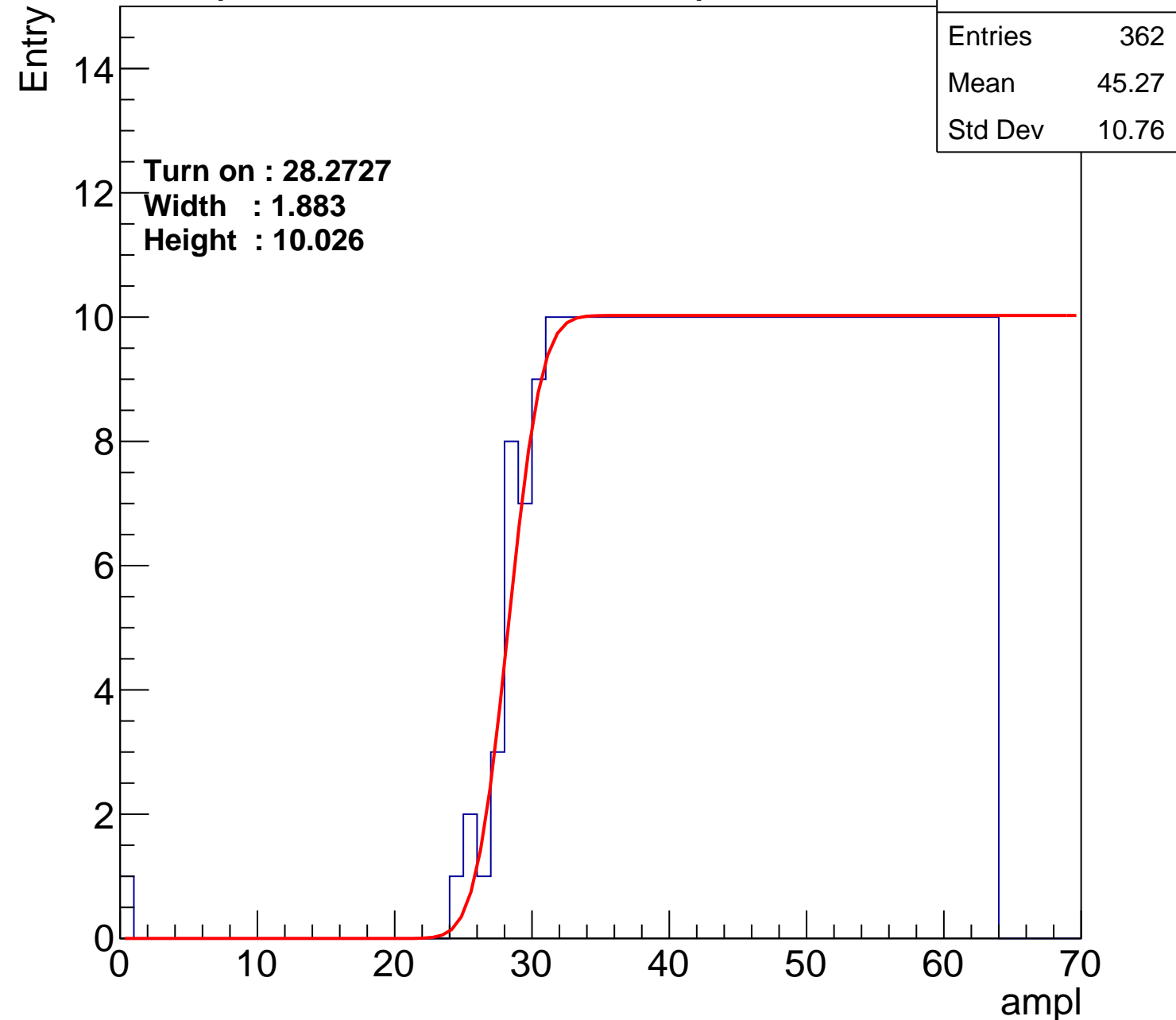
Width : 1.883

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch48

calib_packv5_042523_0143.root, FC#5, port B1

Entries	384
Mean	44.02
Std Dev	11.69

Turn on : 25.8063

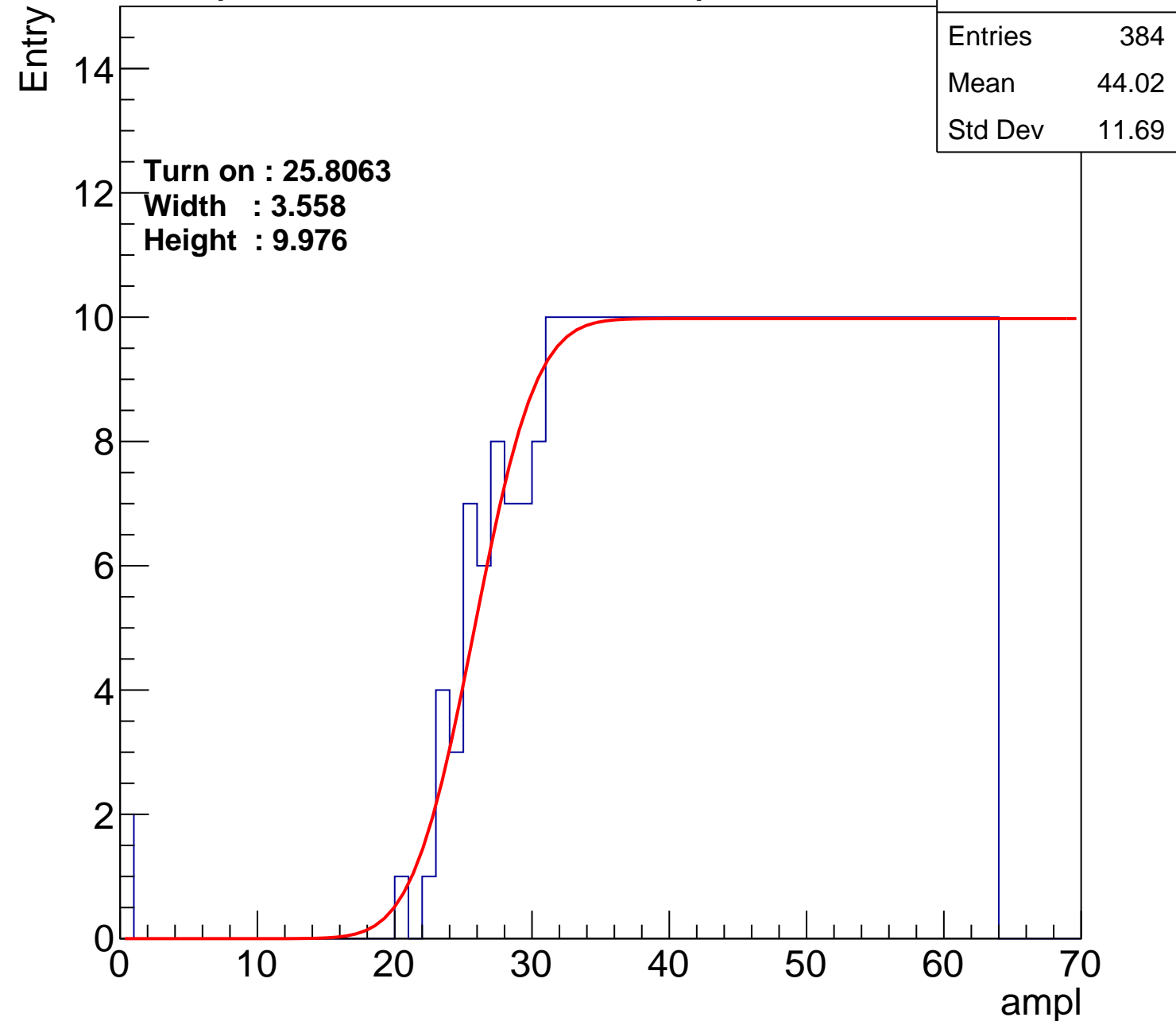
Width : 3.558

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch49

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.51
Std Dev	10.7

Turn on : 29.0385

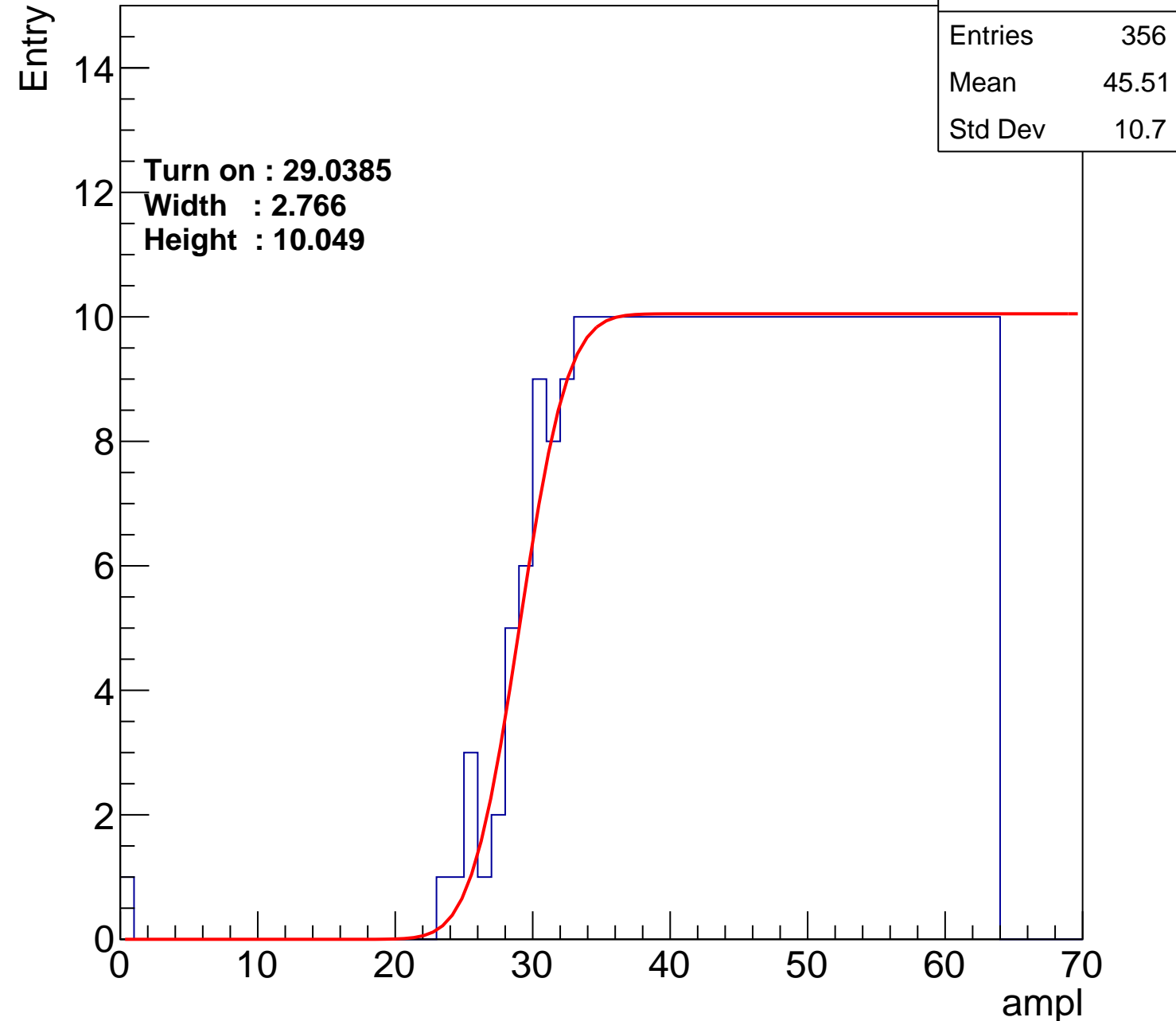
Width : 2.766

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch50

calib_packv5_042523_0143.root, FC#5, port B1

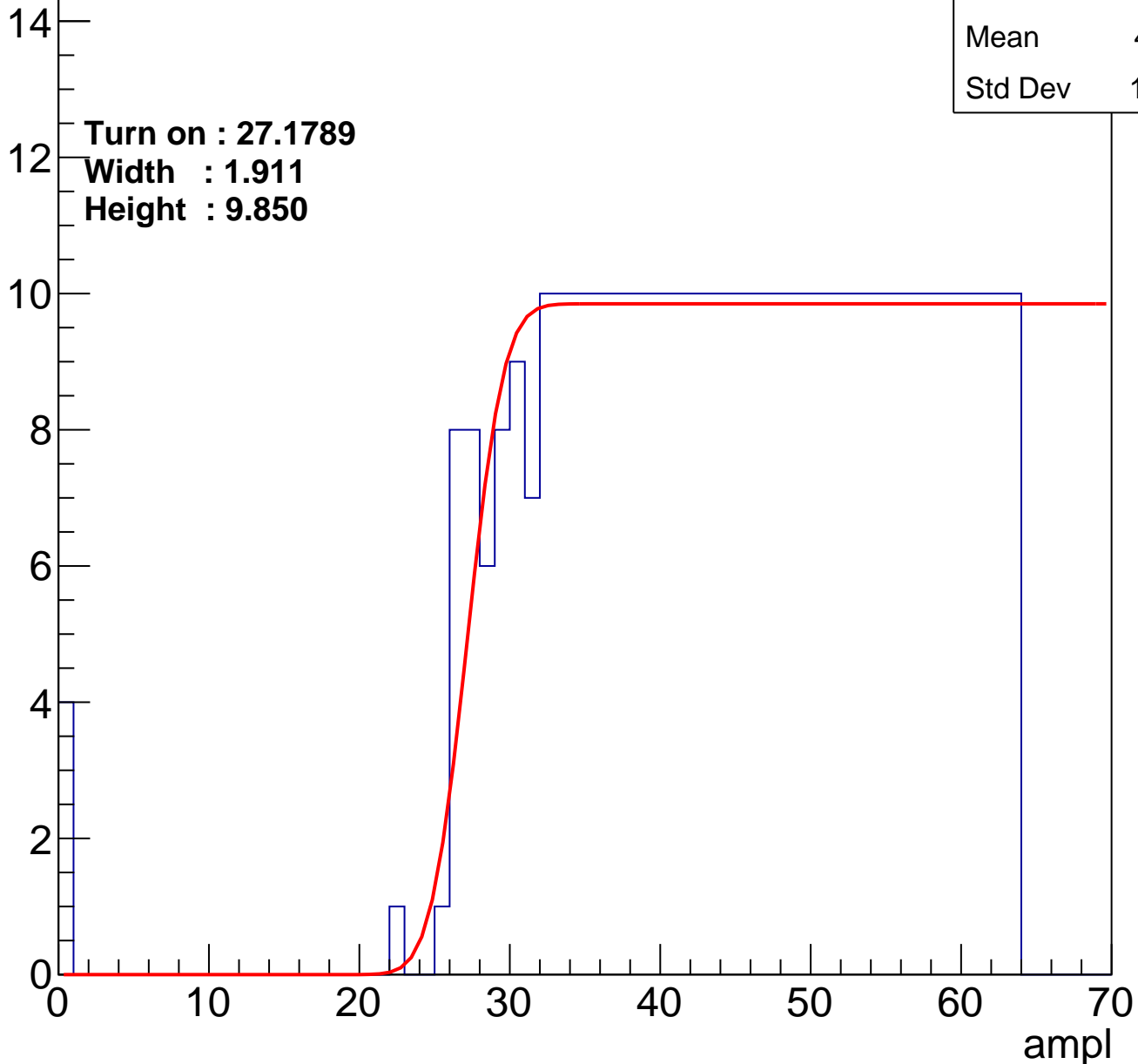
Entry

Entries	372
Mean	44.51
Std Dev	11.69

Turn on : 27.1789

Width : 1.911

Height : 9.850



B0L000S, U9-ch51

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.42
Std Dev	10.73

Turn on : 28.5908

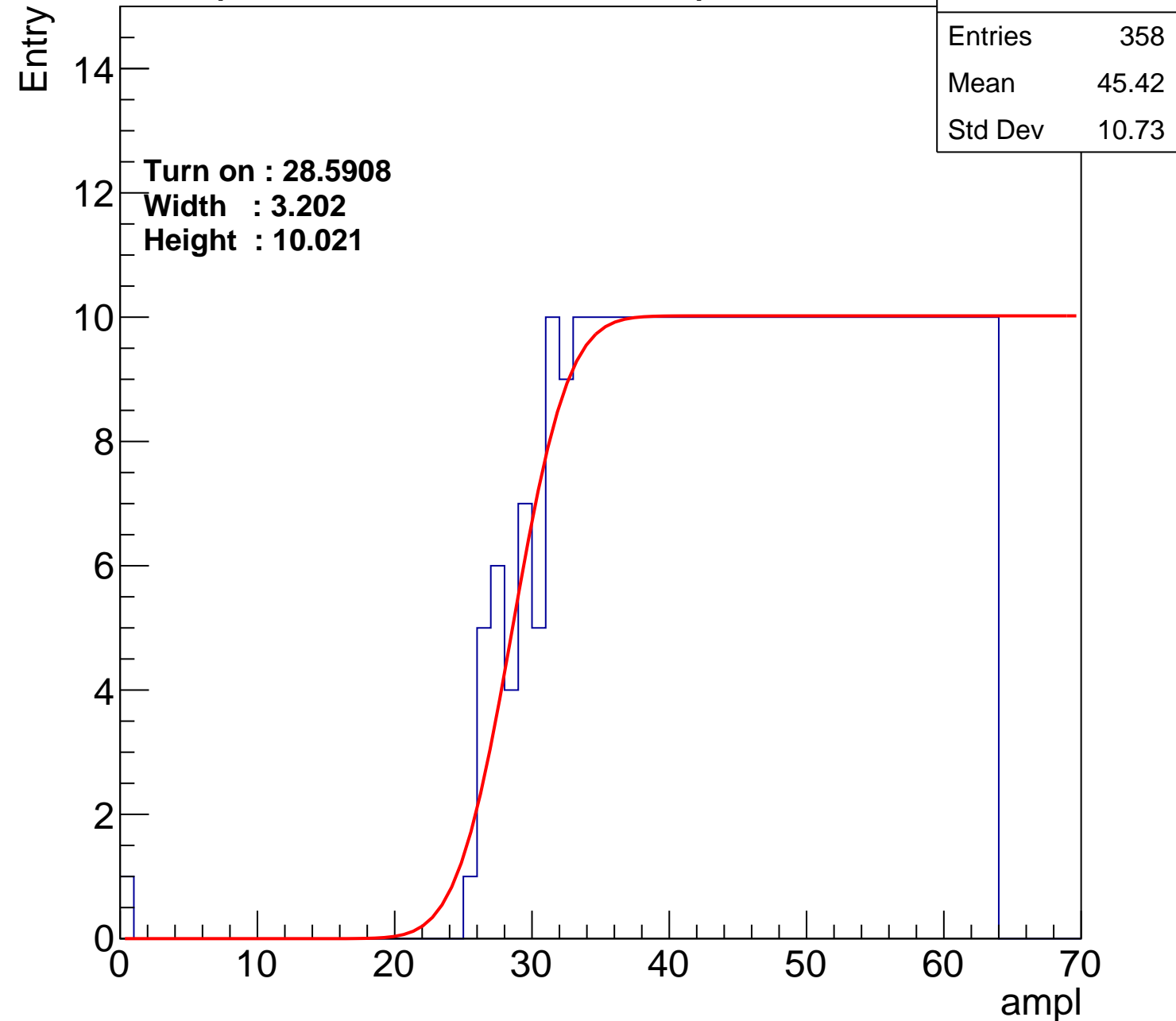
Width : 3.202

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch52

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.66
Std Dev	11.31

Turn on : 26.8107

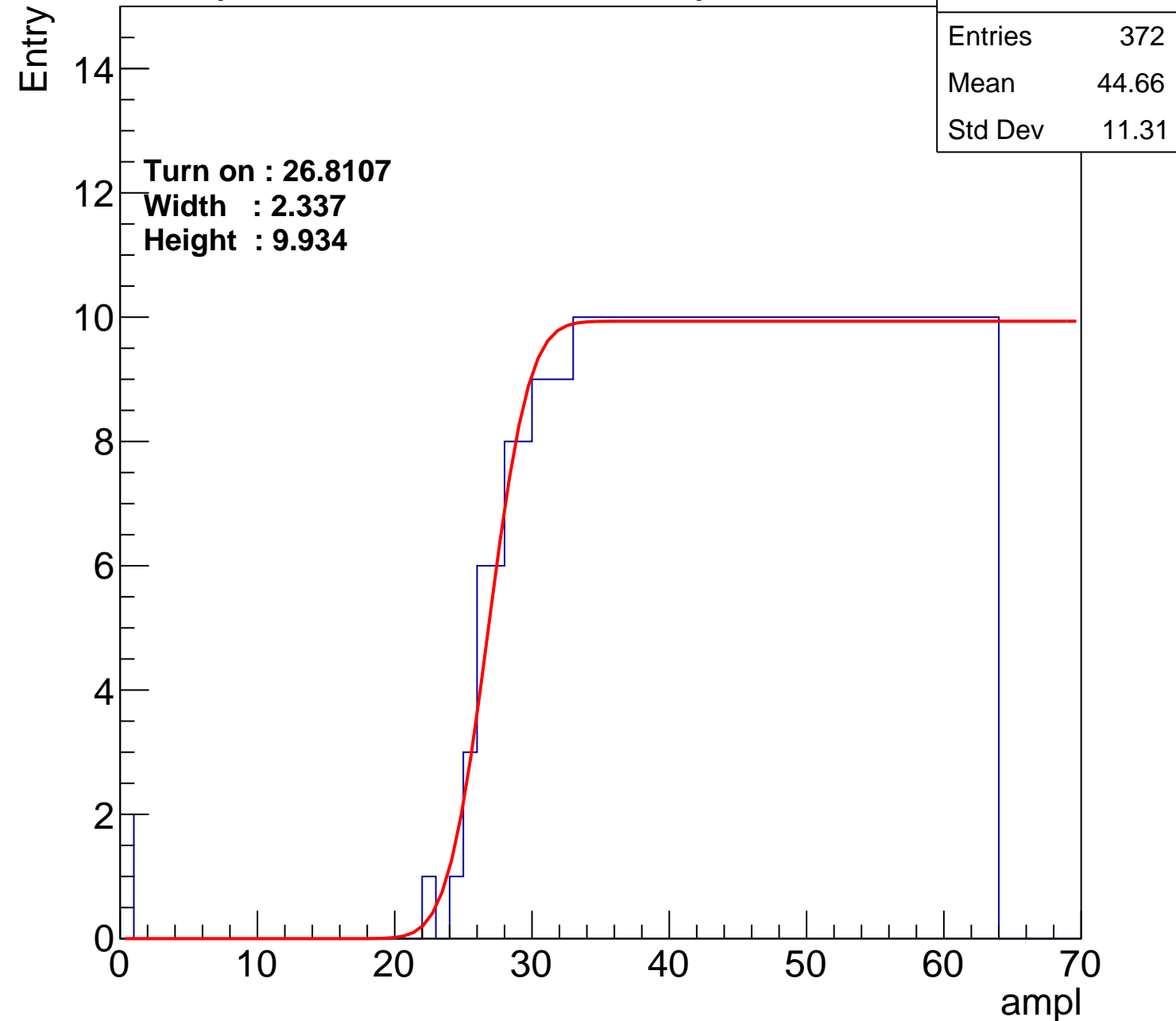
Width : 2.337

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch53

calib_packv5_042523_0143.root, FC#5, port B1

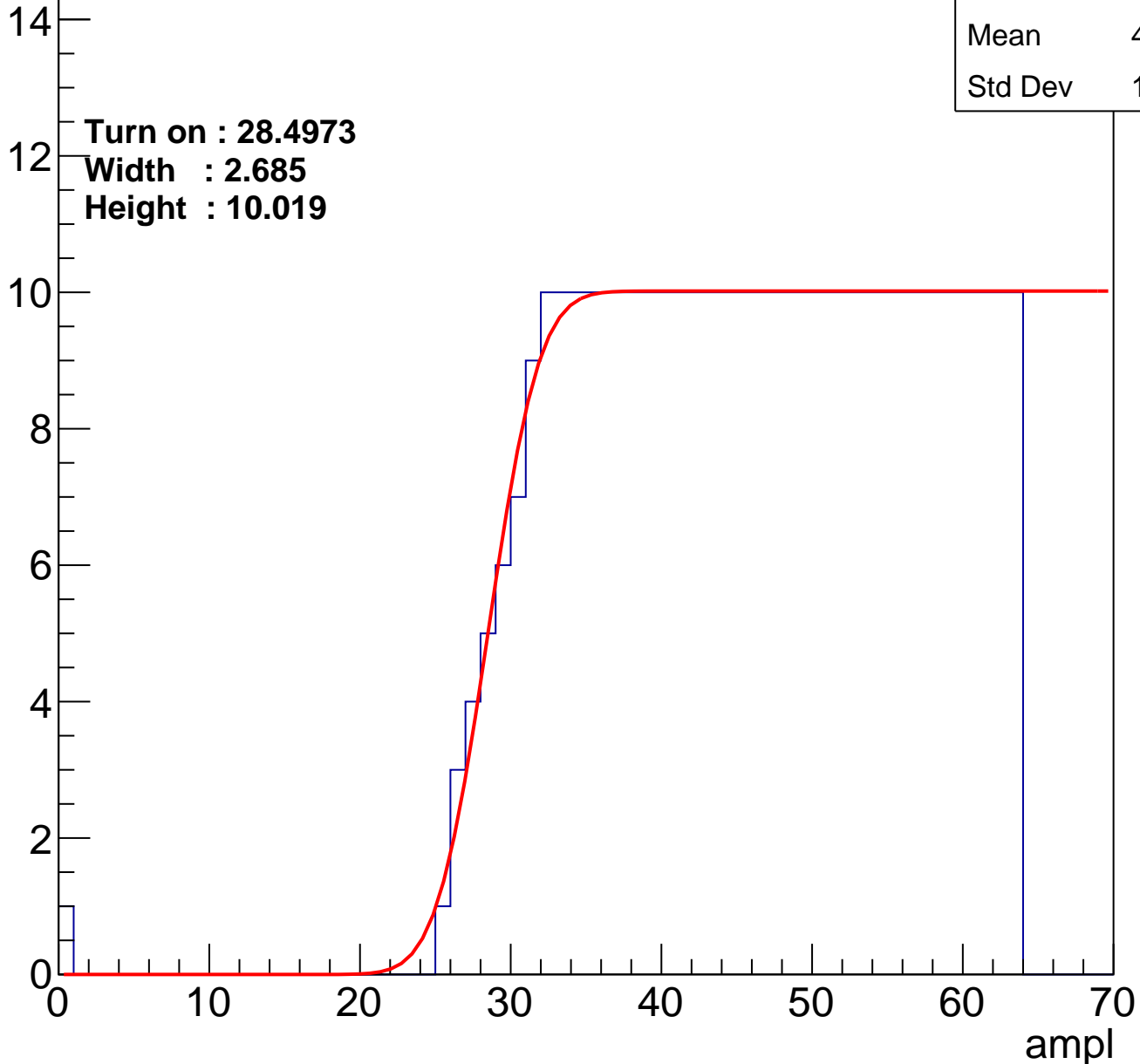
Entries	356
Mean	45.54
Std Dev	10.64

Turn on : 28.4973

Width : 2.685

Height : 10.019

Entry



B0L000S, U9-ch54

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.18
Std Dev	11.05

Turn on : 28.1893

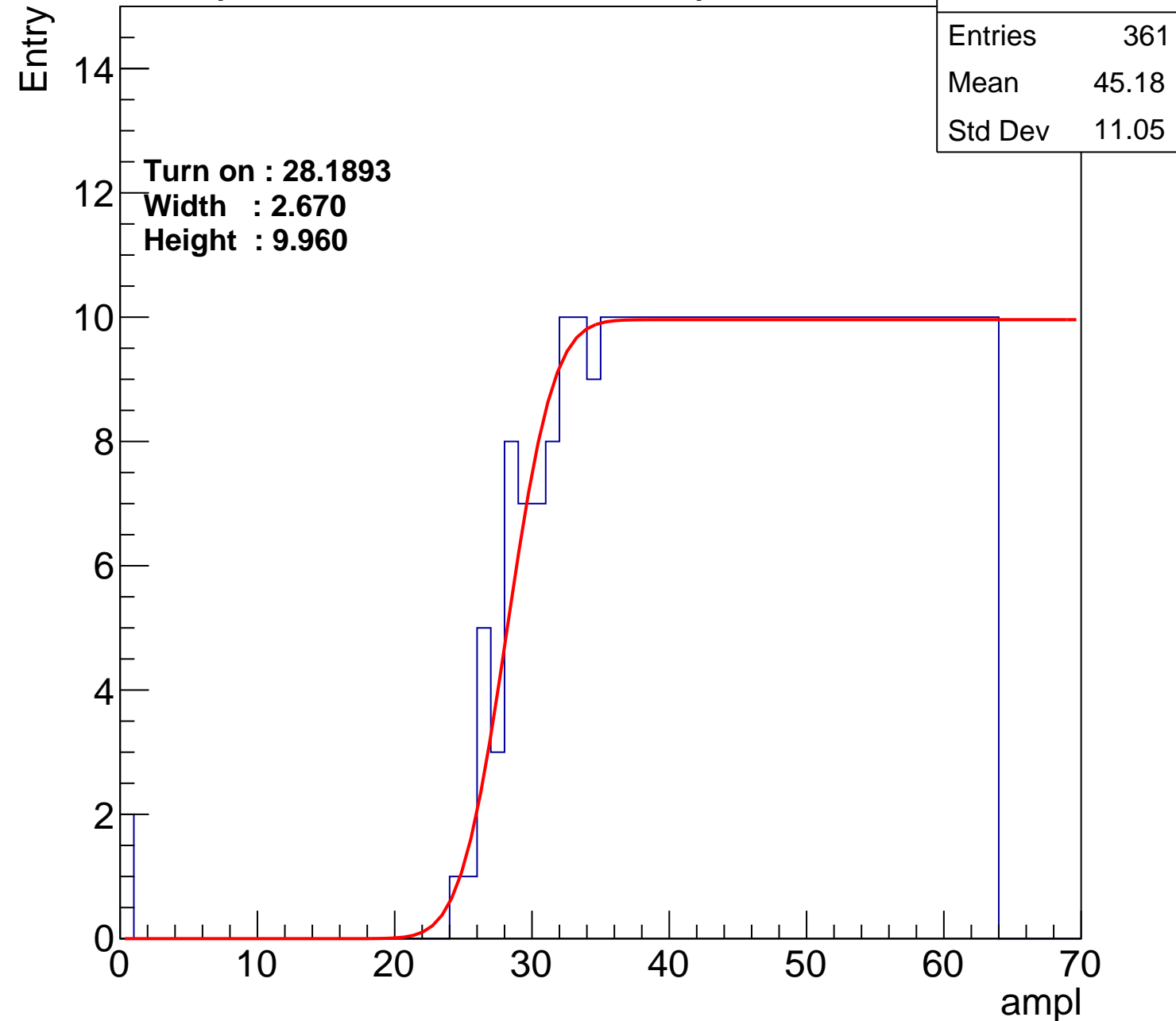
Width : 2.670

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch55

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	45.09
Std Dev	10.9

Turn on : 28.0772

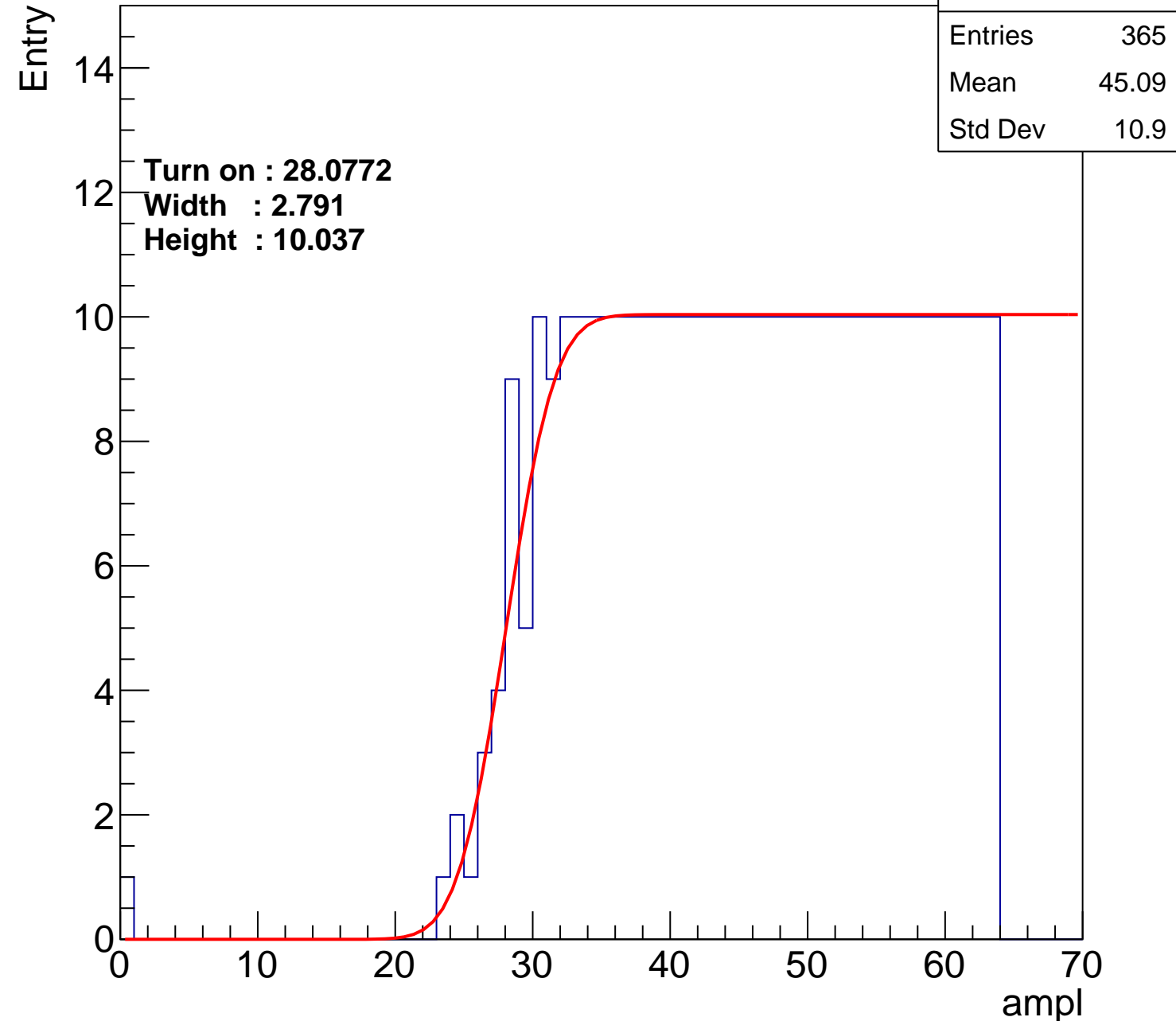
Width : 2.791

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch56

calib_packv5_042523_0143.root, FC#5, port B1

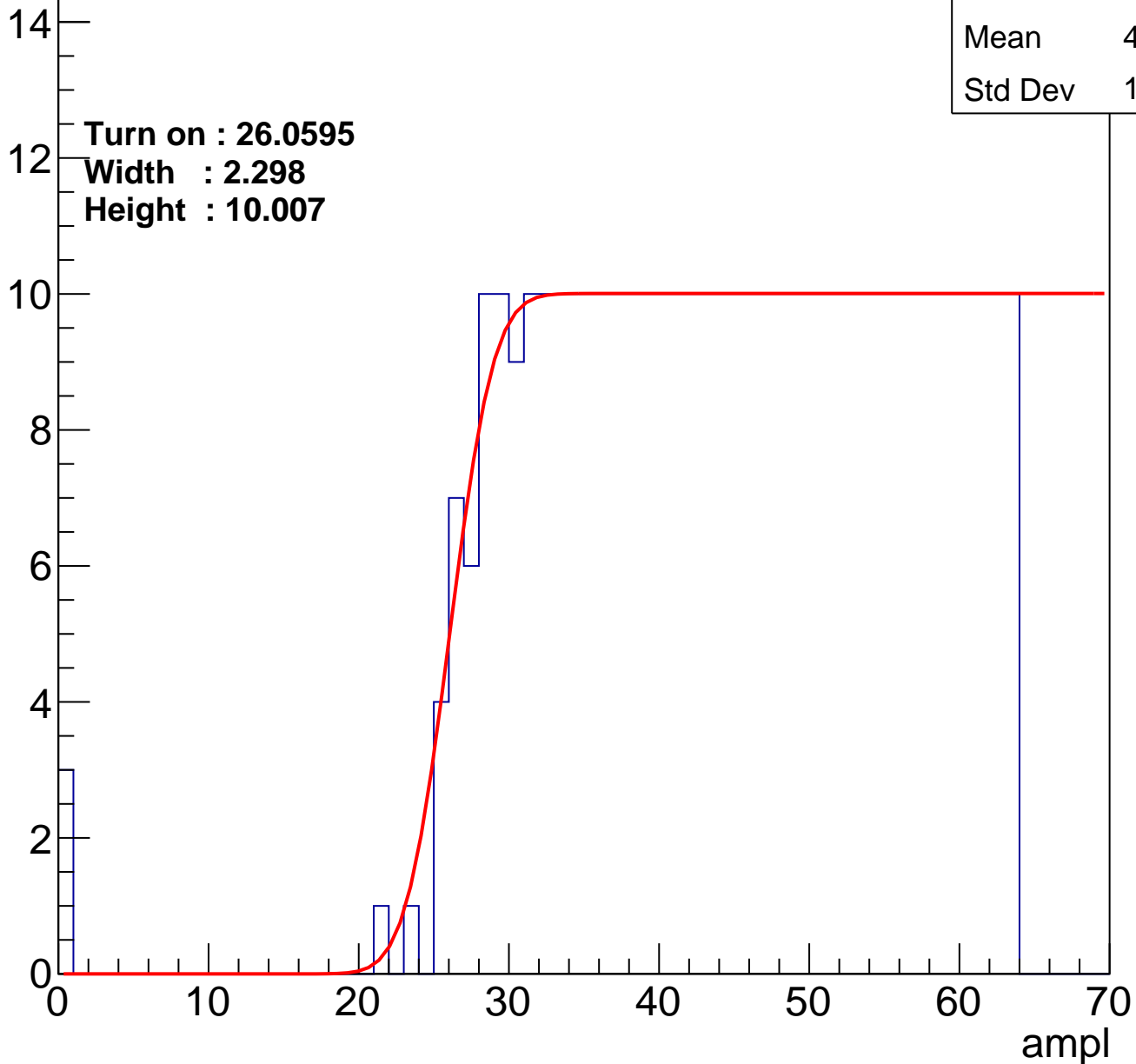
Entry

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 26.0595

Width : 2.298

Height : 10.007



B0L000S, U9-ch57

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.56
Std Dev	10.67

Turn on : 29.0021

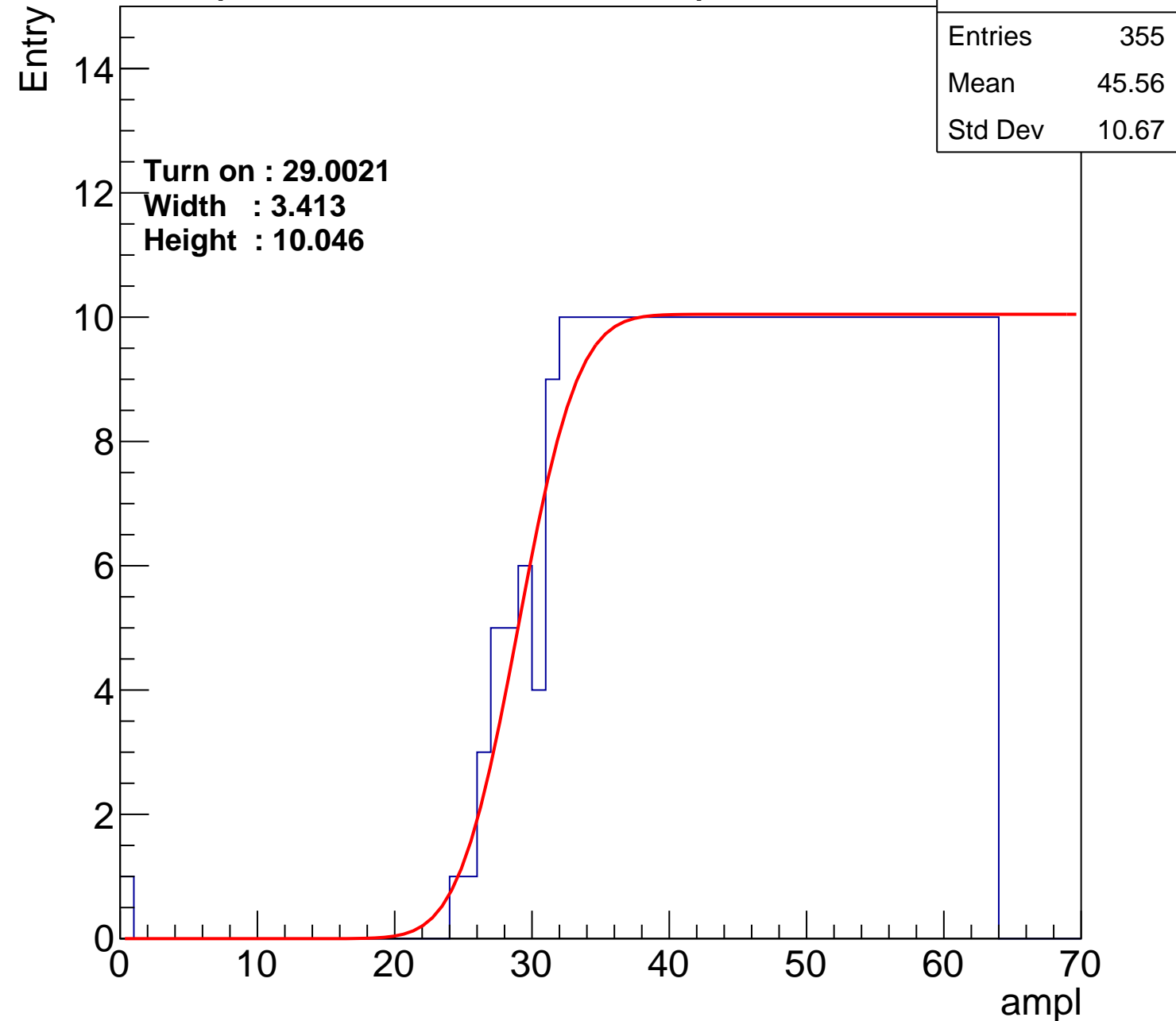
Width : 3.413

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch58

calib_packv5_042523_0143.root, FC#5, port B1

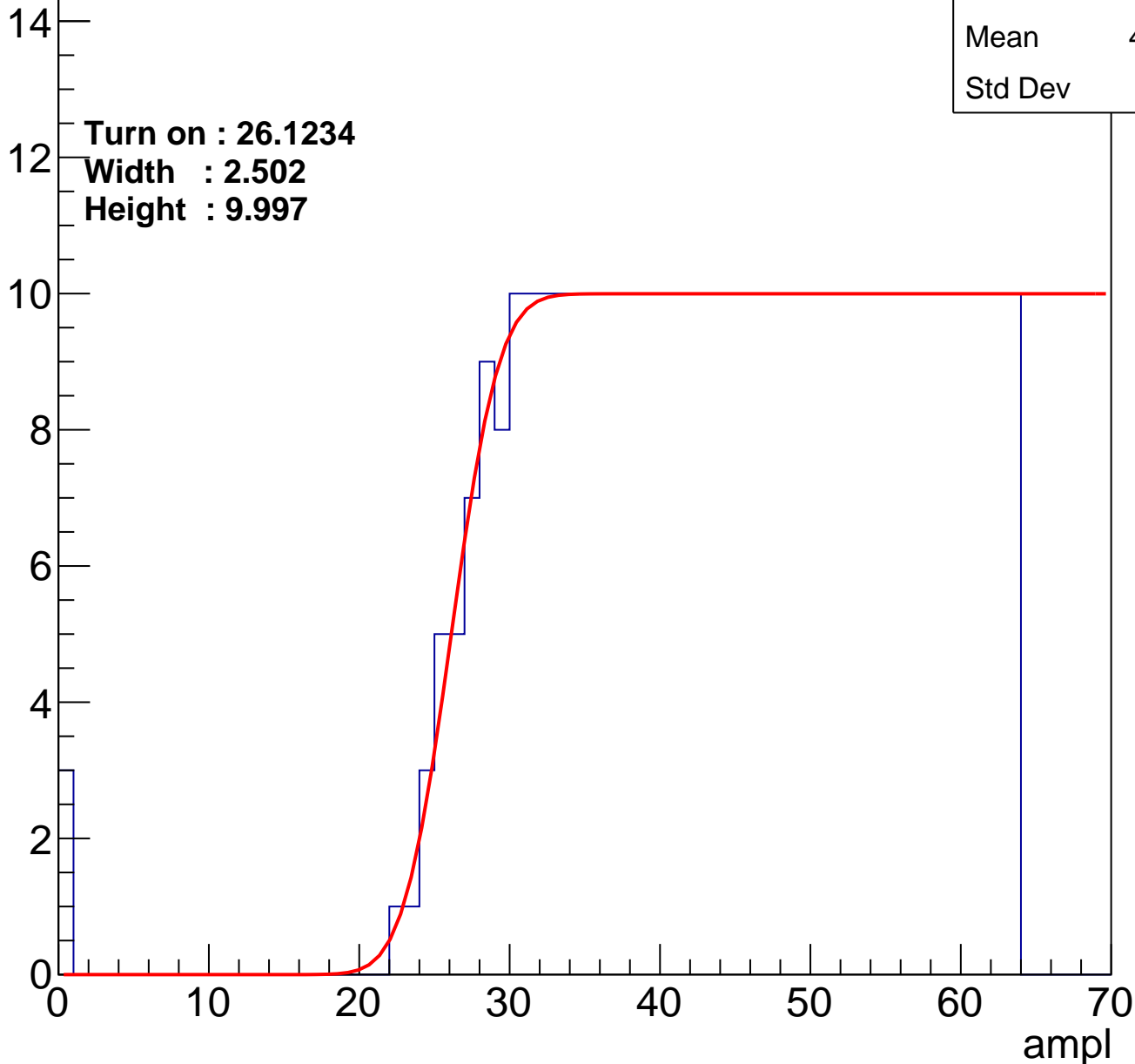
Entry

Entries	382
Mean	44.12
Std Dev	11.7

Turn on : 26.1234

Width : 2.502

Height : 9.997



B0L000S, U9-ch59

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.54
Std Dev	11.66

Turn on : 27.4678

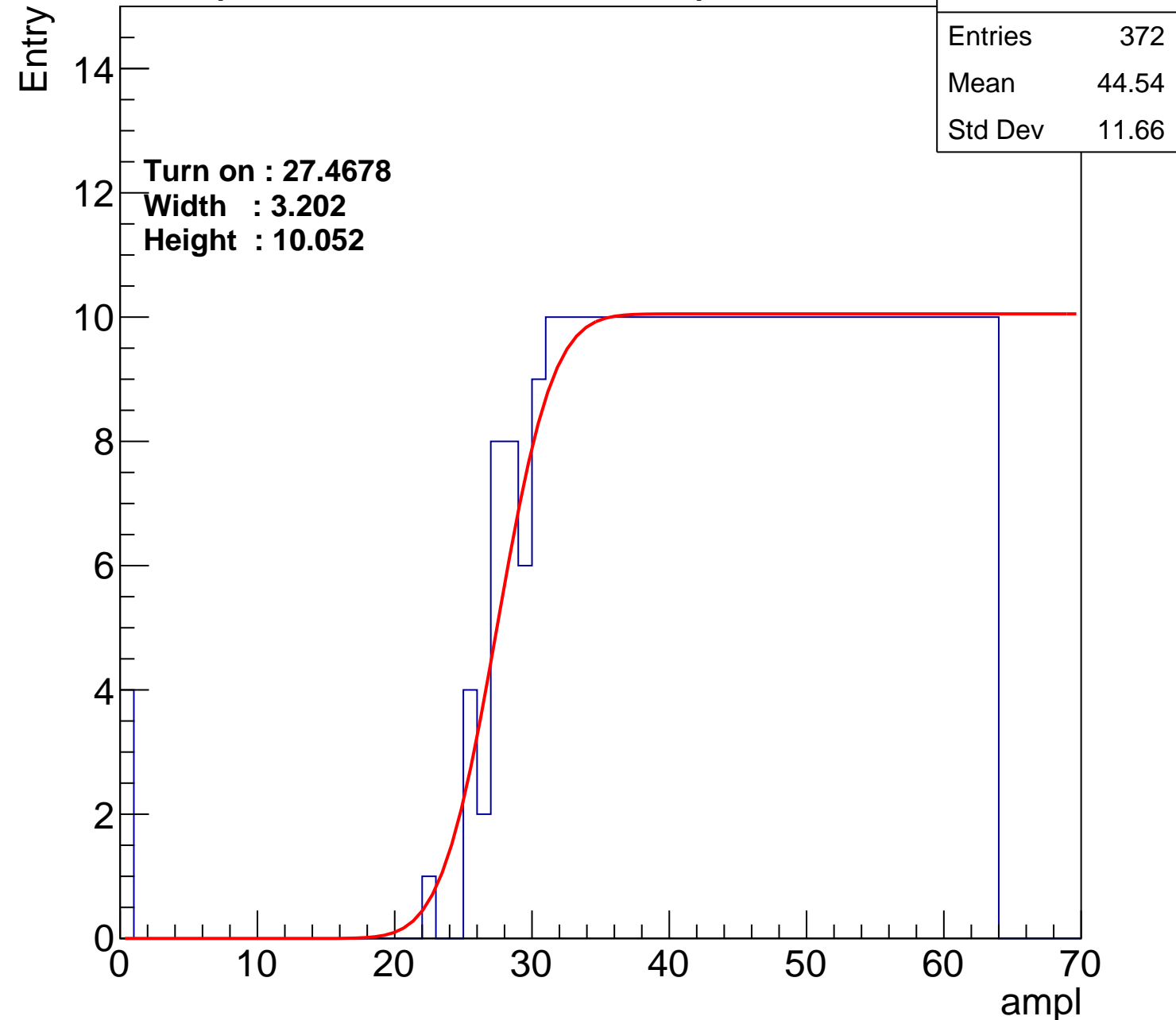
Width : 3.202

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch60

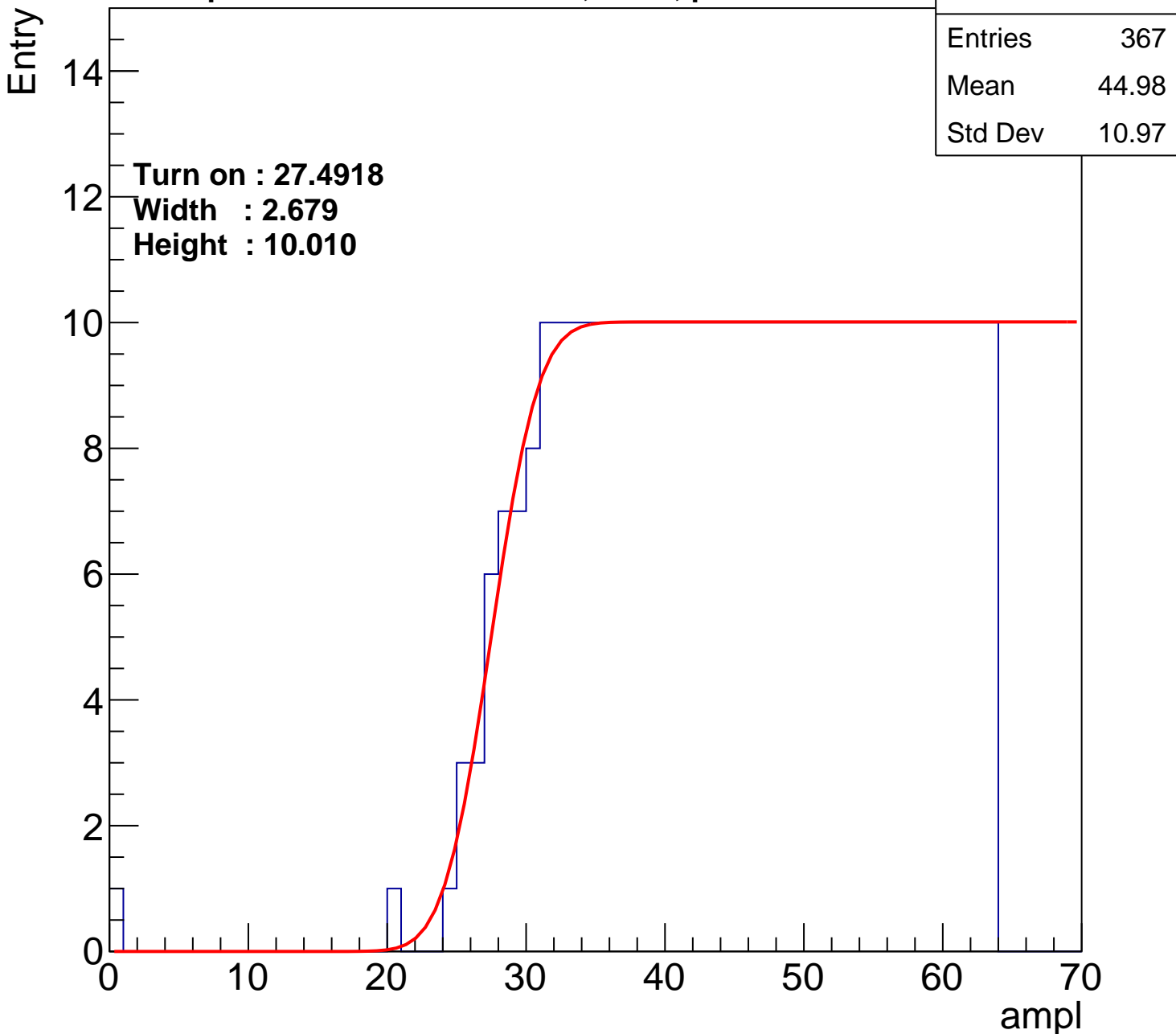
calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.98
Std Dev	10.97

Turn on : 27.4918

Width : 2.679

Height : 10.010



B0L000S, U9-ch61

calib_packv5_042523_0143.root, FC#5, port B1

Entries	374
Mean	44.61
Std Dev	11.27

Turn on : 27.0299

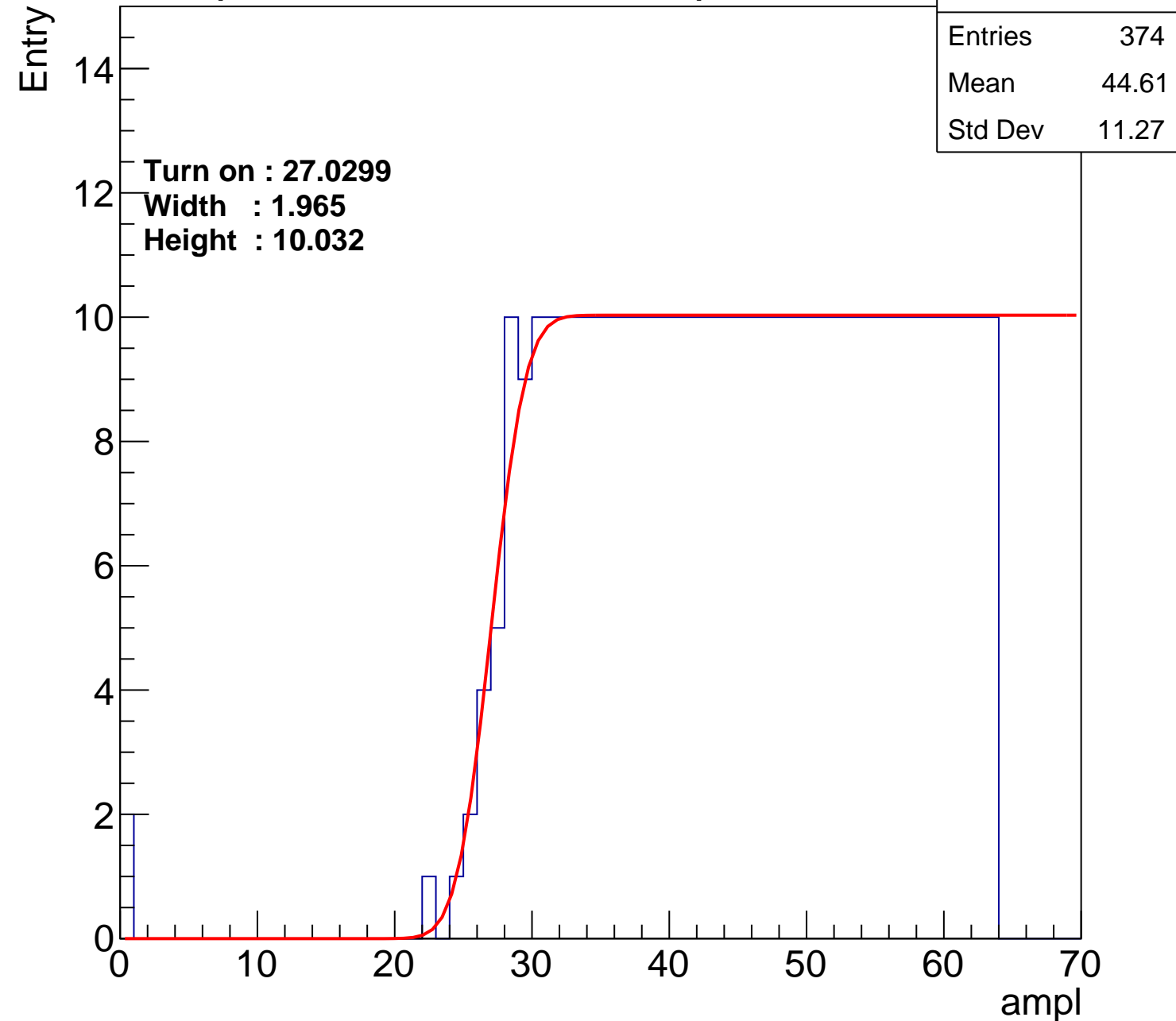
Width : 1.965

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch62

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.57
Std Dev	10.87

Turn on : 29.4514

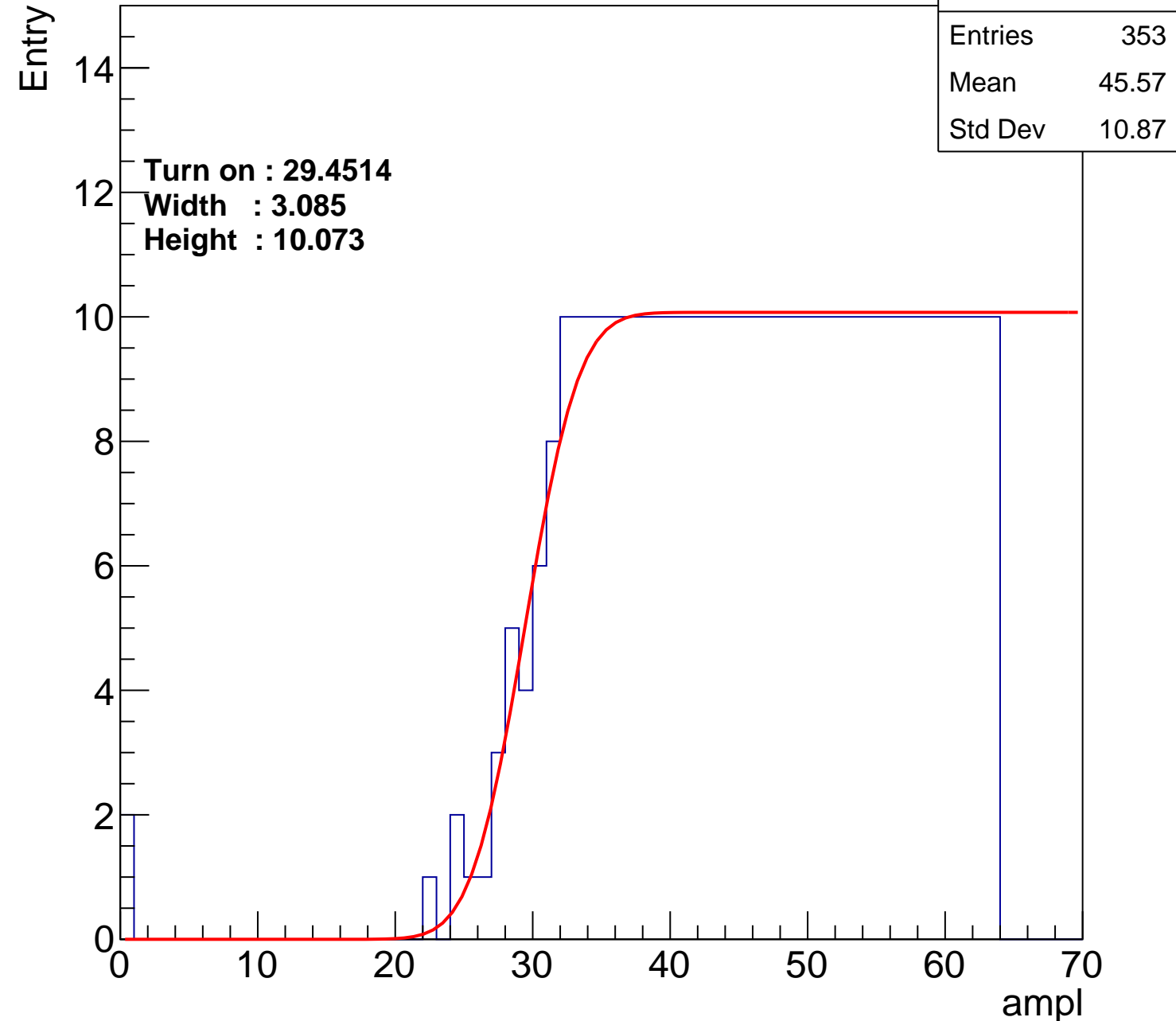
Width : 3.085

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch63

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.7602

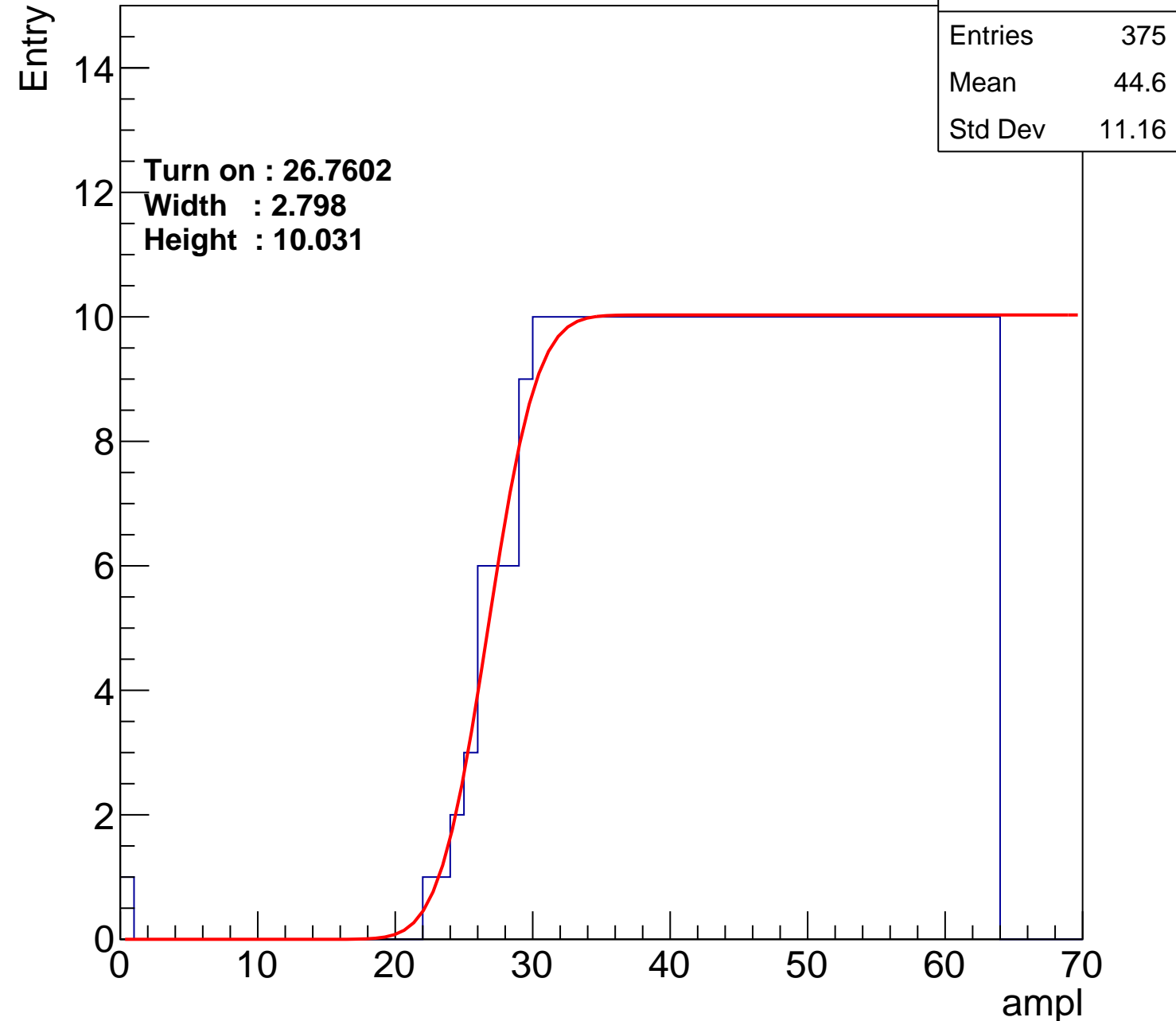
Width : 2.798

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch64

calib_packv5_042523_0143.root, FC#5, port B1

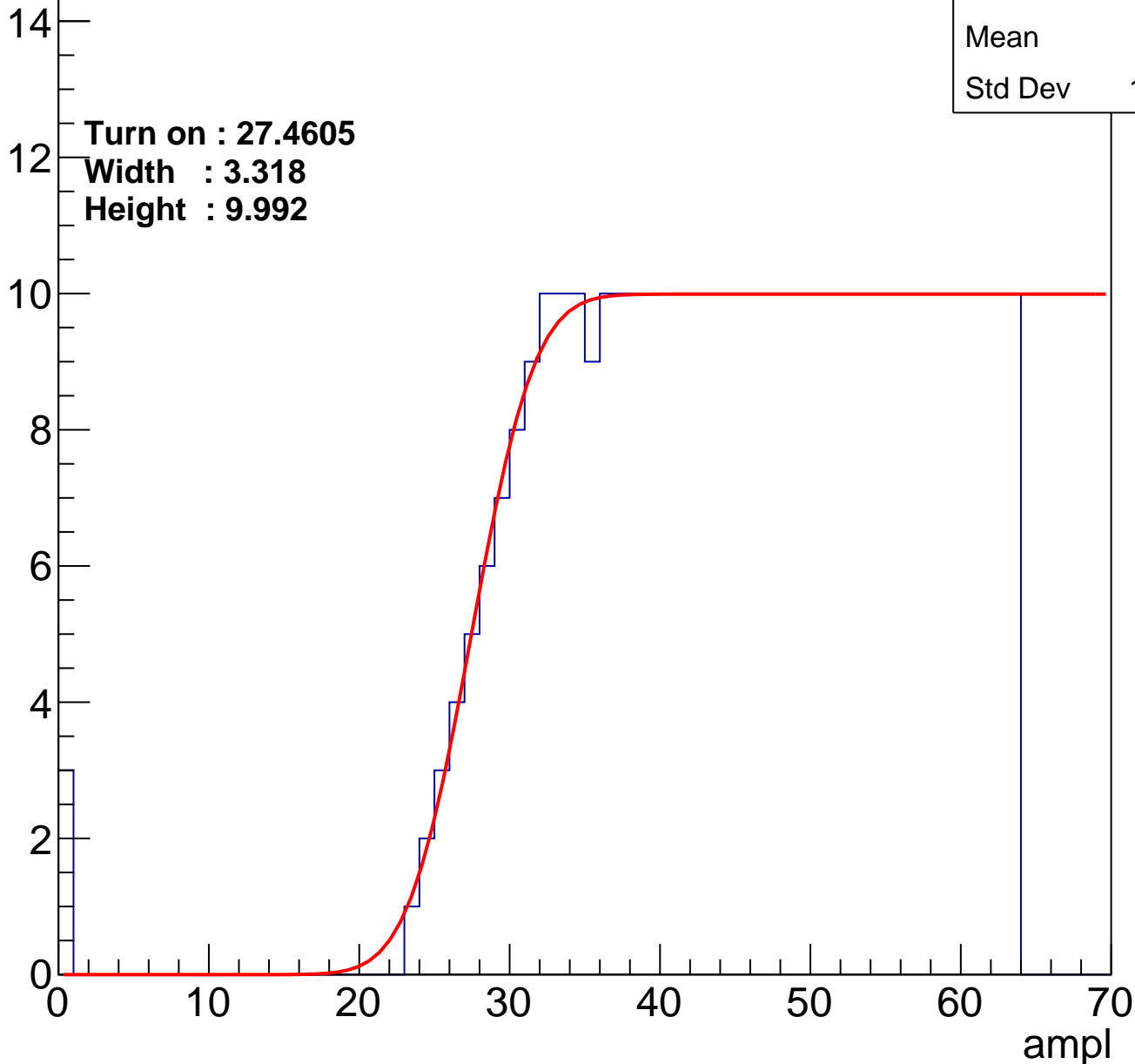
Entry

Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 27.4605

Width : 3.318

Height : 9.992



B0L000S, U9-ch65

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.25
Std Dev	11.41

Turn on : 26.4478

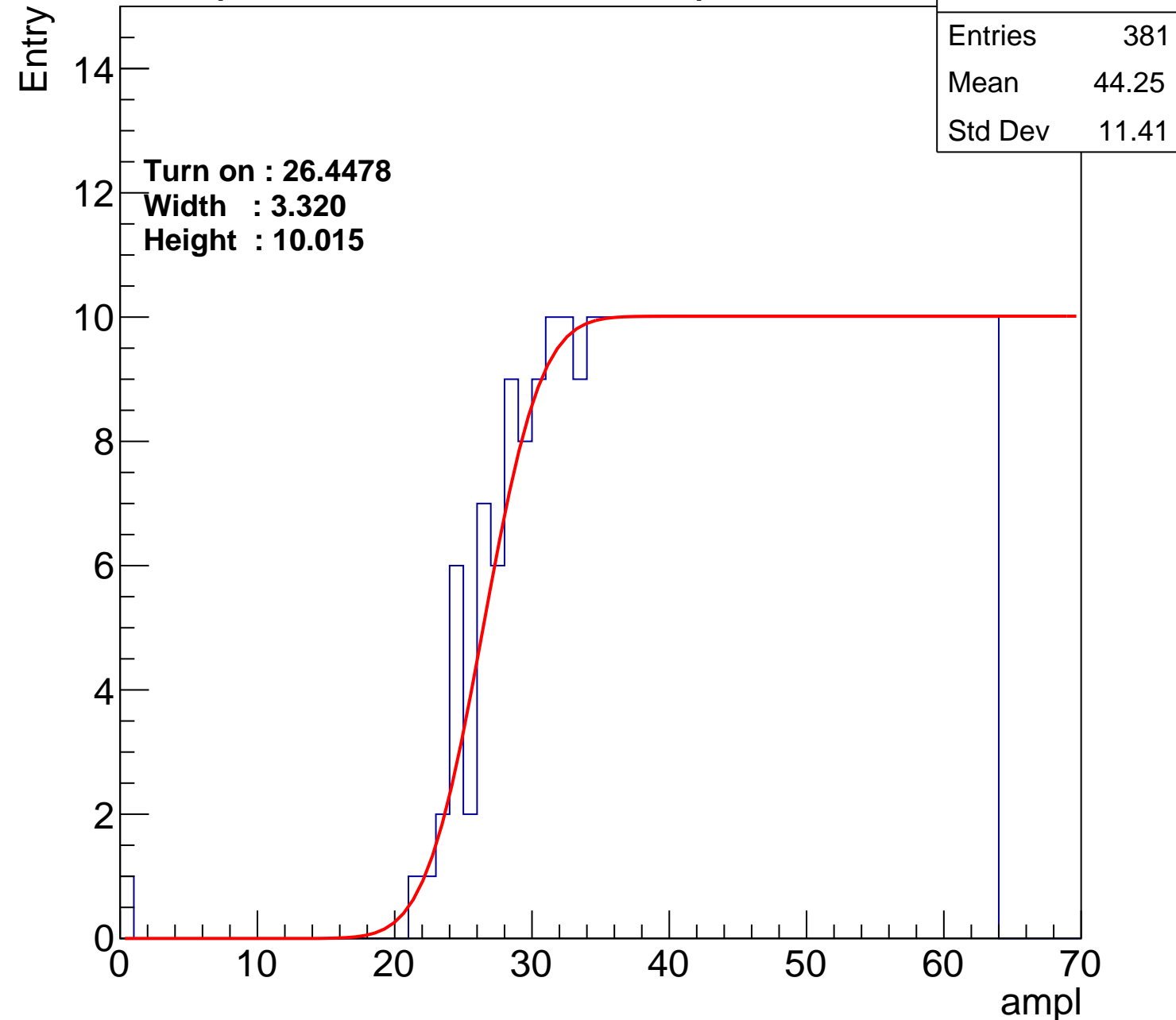
Width : 3.320

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch66

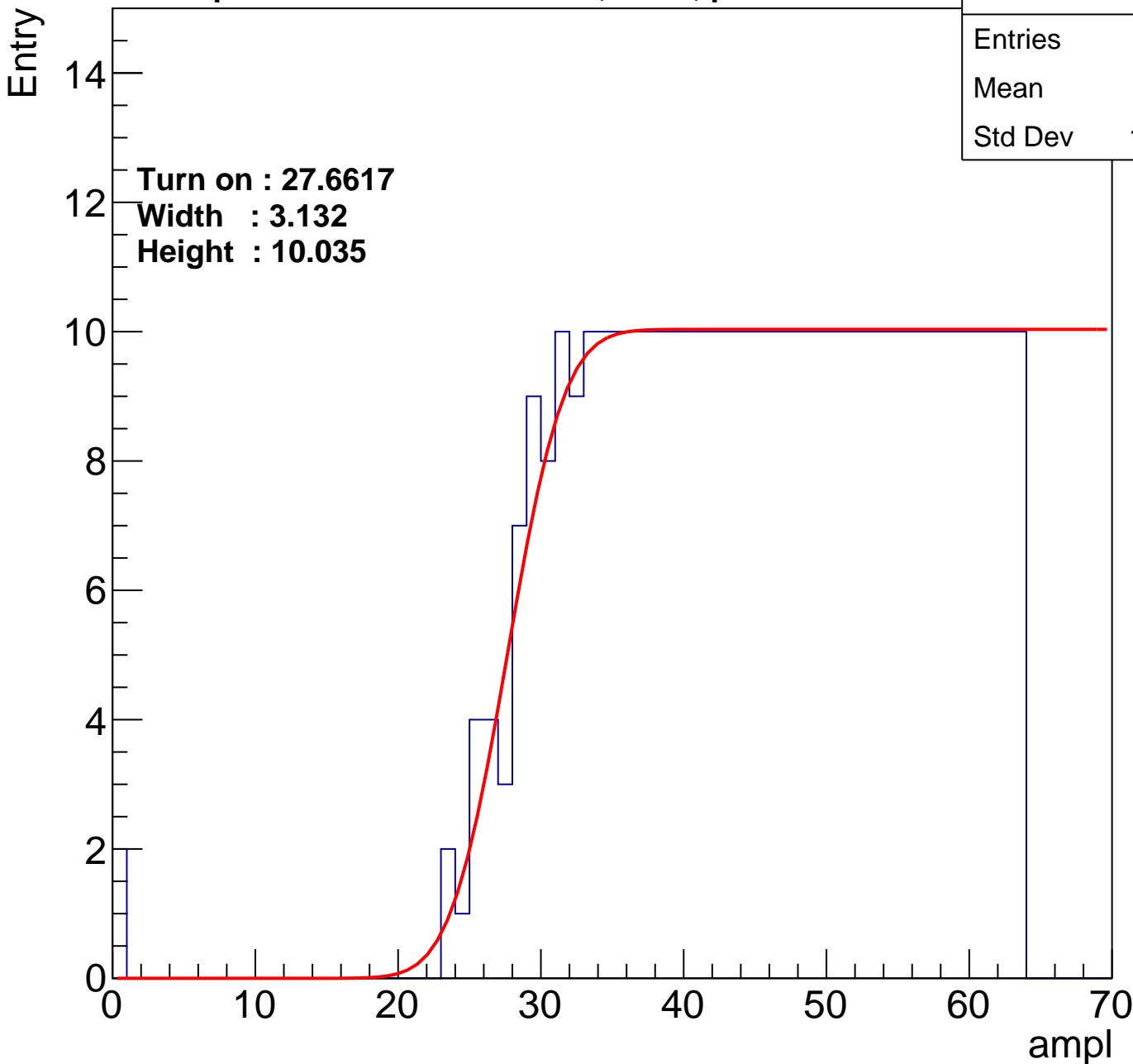
calib_packv5_042523_0143.root, FC#5, port B1

Entries	369
Mean	44.8
Std Dev	11.24

Turn on : 27.6617

Width : 3.132

Height : 10.035



B0L000S, U9-ch67

calib_packv5_042523_0143.root, FC#5, port B1

Entries	353
Mean	45.58
Std Dev	10.86

Turn on : 29.0419

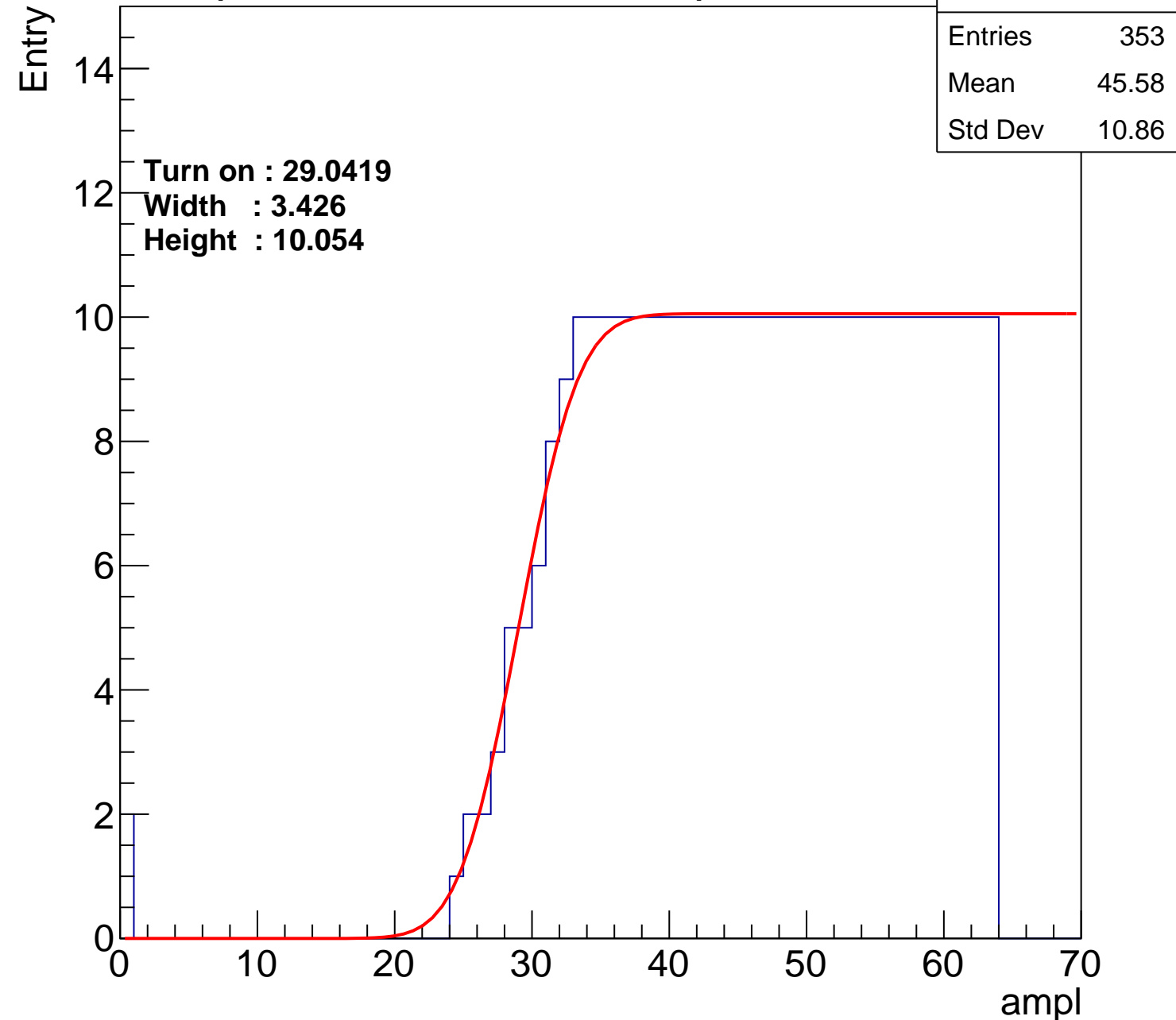
Width : 3.426

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch68

calib_packv5_042523_0143.root, FC#5, port B1

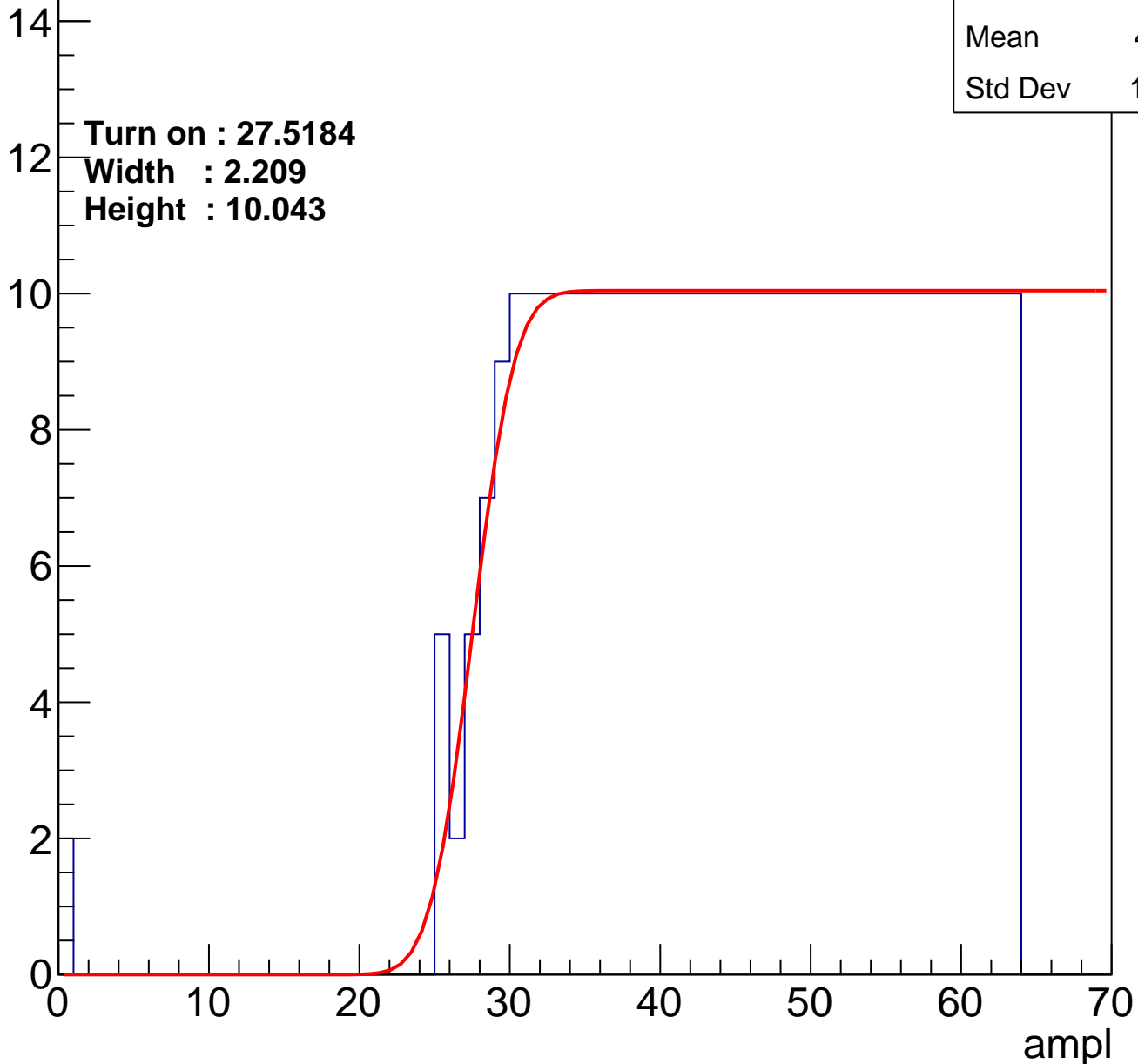
Entry

Entries	370
Mean	44.81
Std Dev	11.17

Turn on : 27.5184

Width : 2.209

Height : 10.043



B0L000S, U9-ch69

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.06
Std Dev	12.01

Turn on : 26.6391

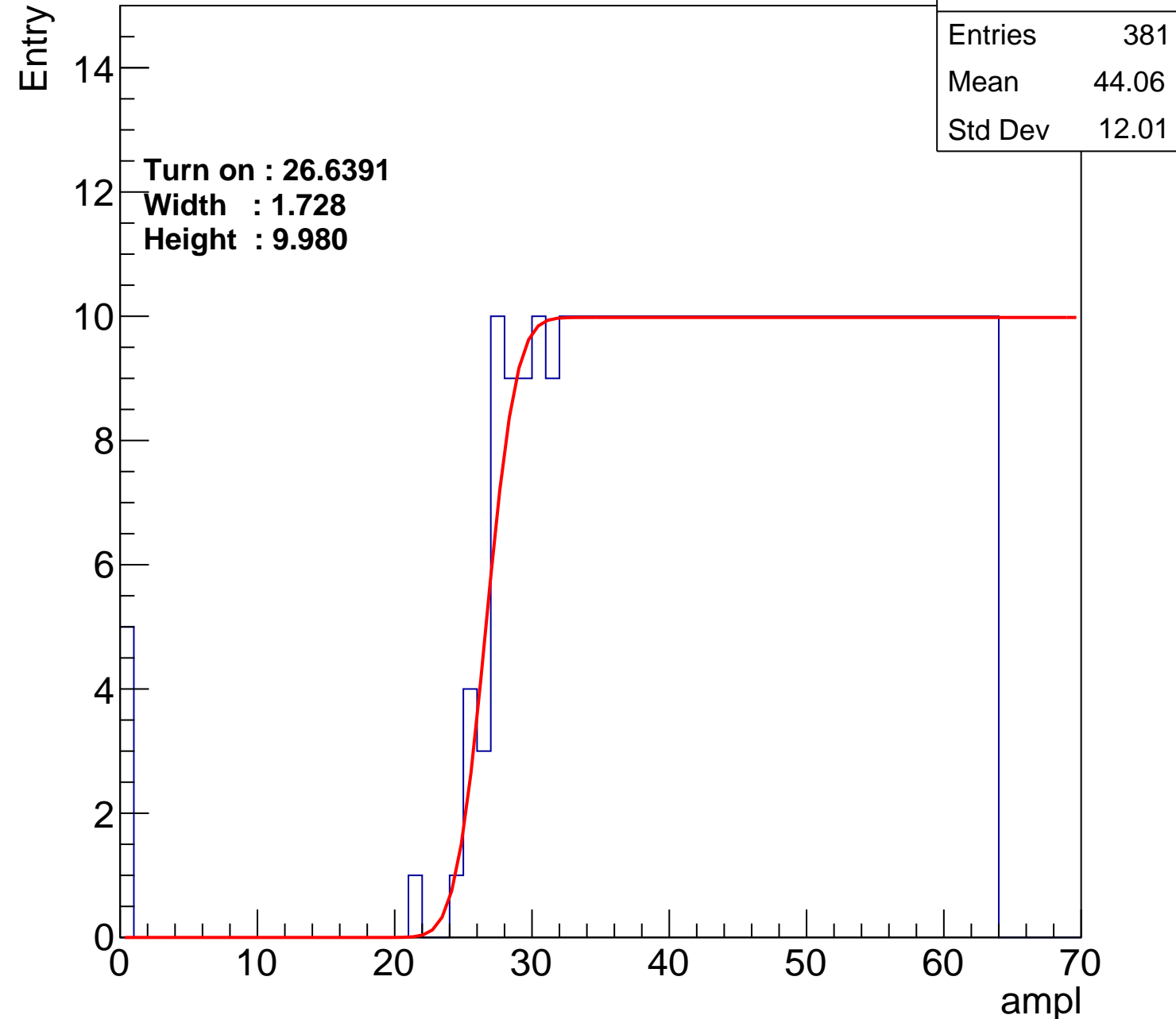
Width : 1.728

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch70

calib_packv5_042523_0143.root, FC#5, port B1

Entries	385
Mean	43.81
Std Dev	12.17

Turn on : 25.7631

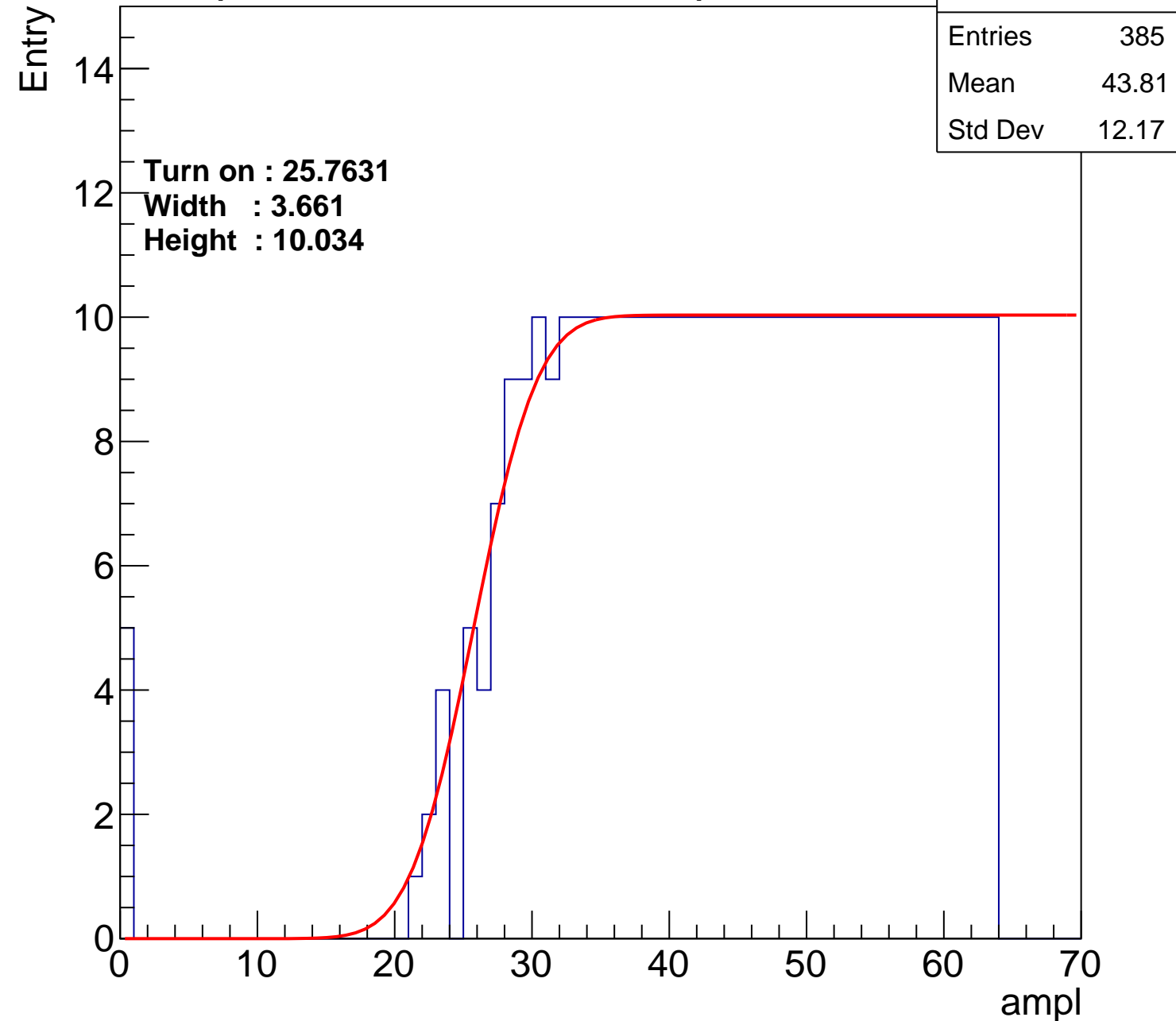
Width : 3.661

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch71

calib_packv5_042523_0143.root, FC#5, port B1

Entries	350
Mean	45.81
Std Dev	10.53

Turn on : 29.3672

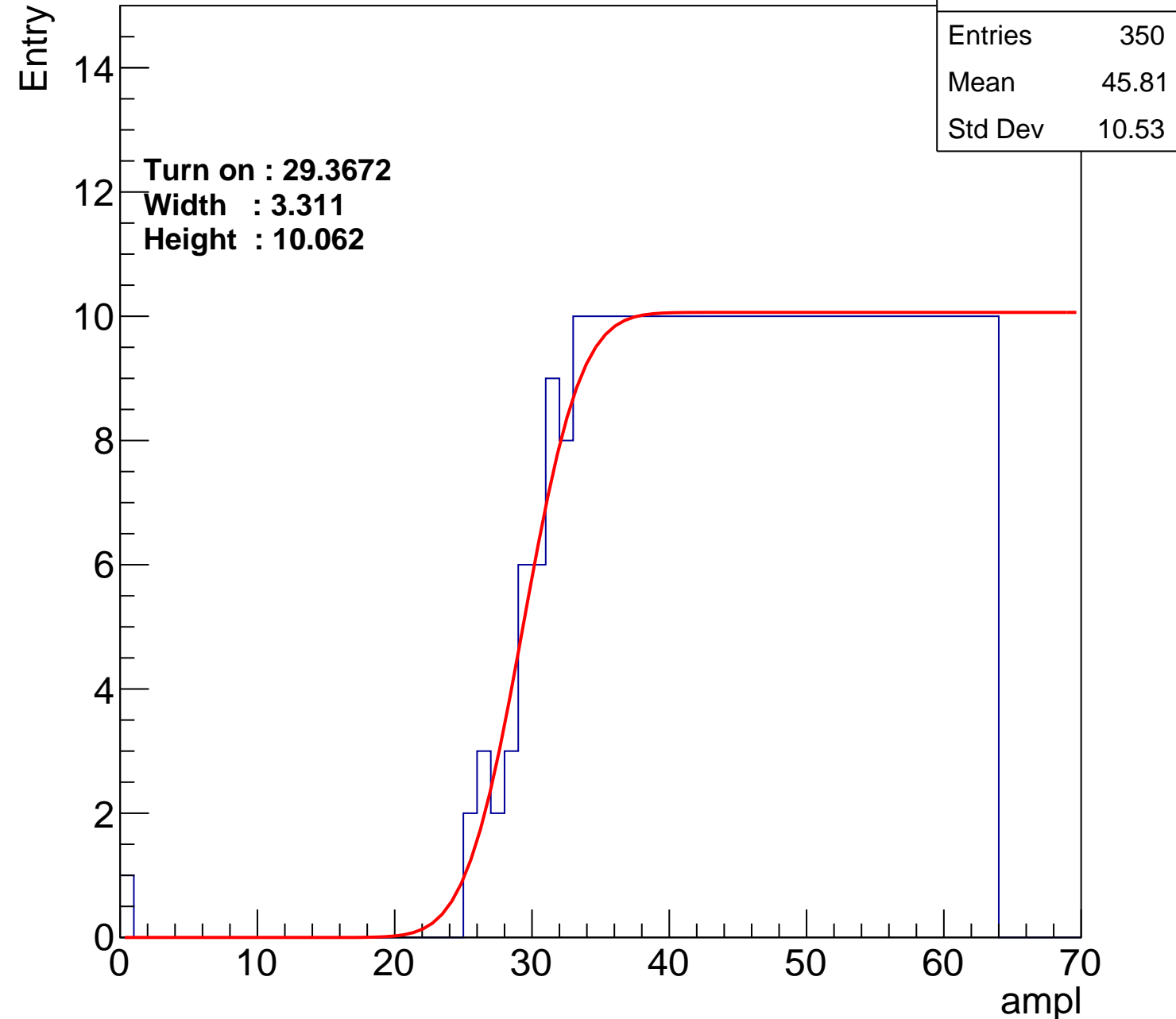
Width : 3.311

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch72

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.98
Std Dev	10.97

Turn on : 27.1320

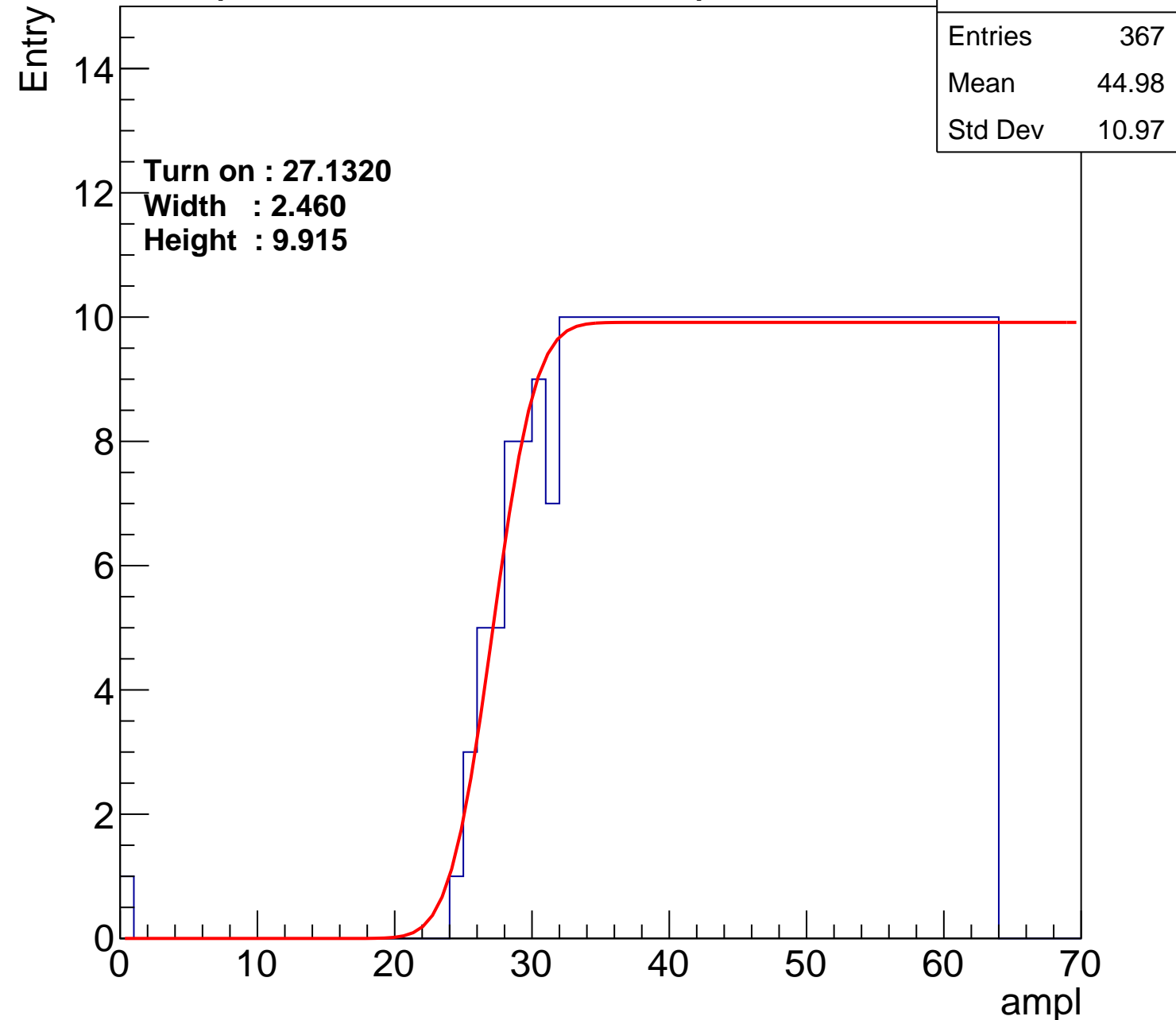
Width : 2.460

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch73

calib_packv5_042523_0143.root, FC#5, port B1

Entries	375
Mean	44.3
Std Dev	11.95

Turn on : 27.6548

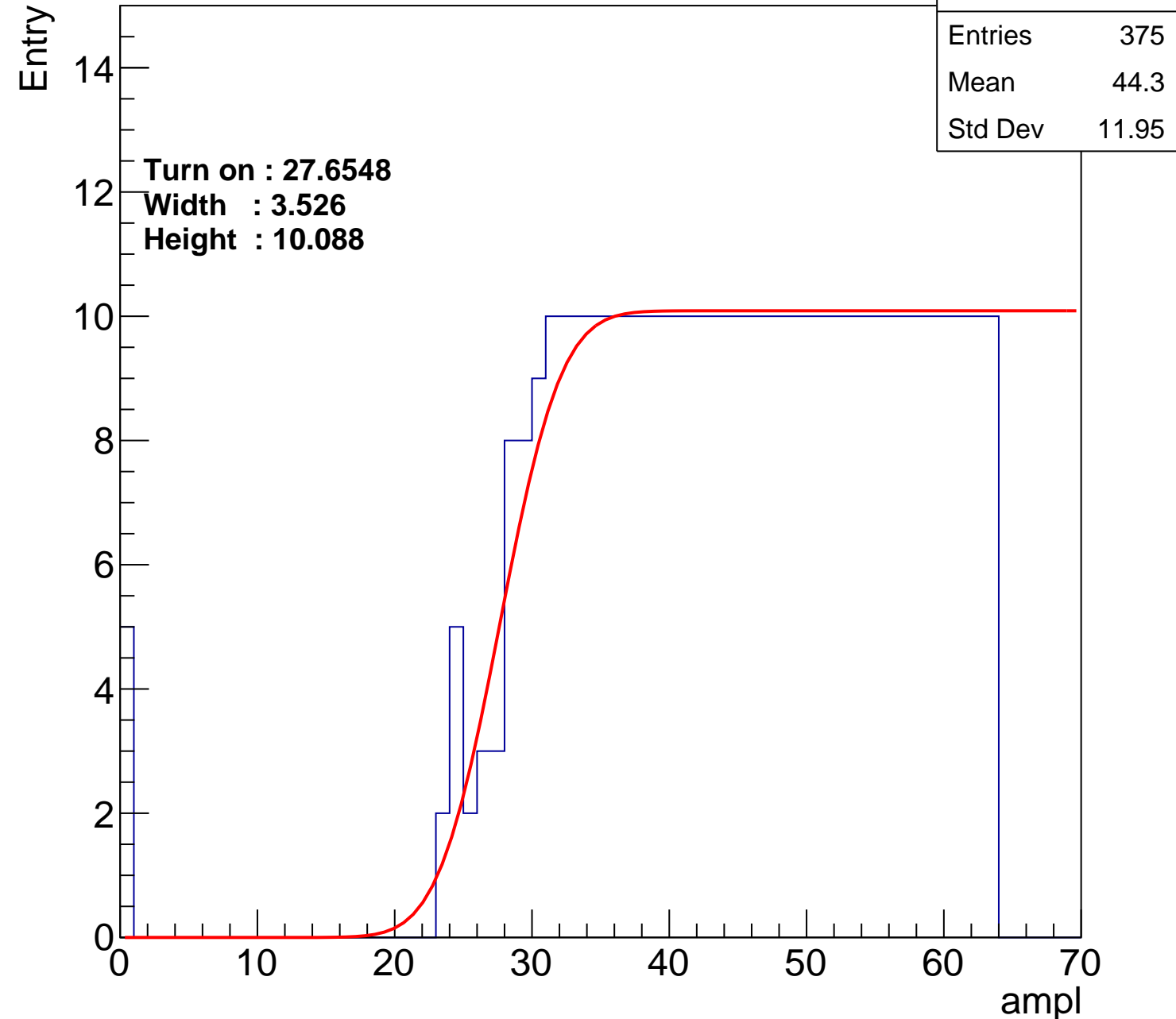
Width : 3.526

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch74

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
---------	-----

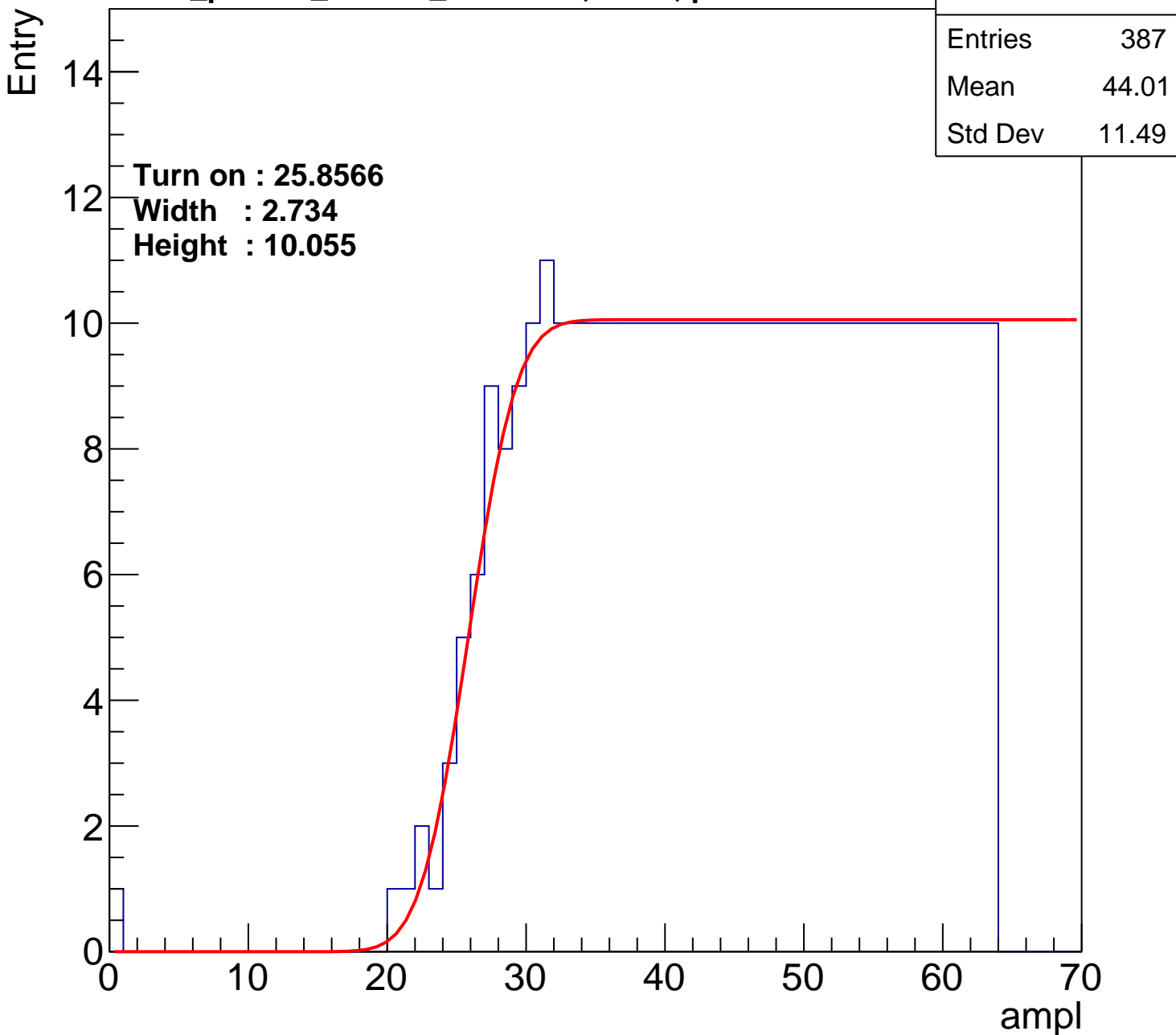
Mean	44.01
------	-------

Std Dev	11.49
---------	-------

Turn on : 25.8566

Width : 2.734

Height : 10.055



B0L000S, U9-ch75

calib_packv5_042523_0143.root, FC#5, port B1

Entries	373
Mean	44.45
Std Dev	11.74

Turn on : 27.2392

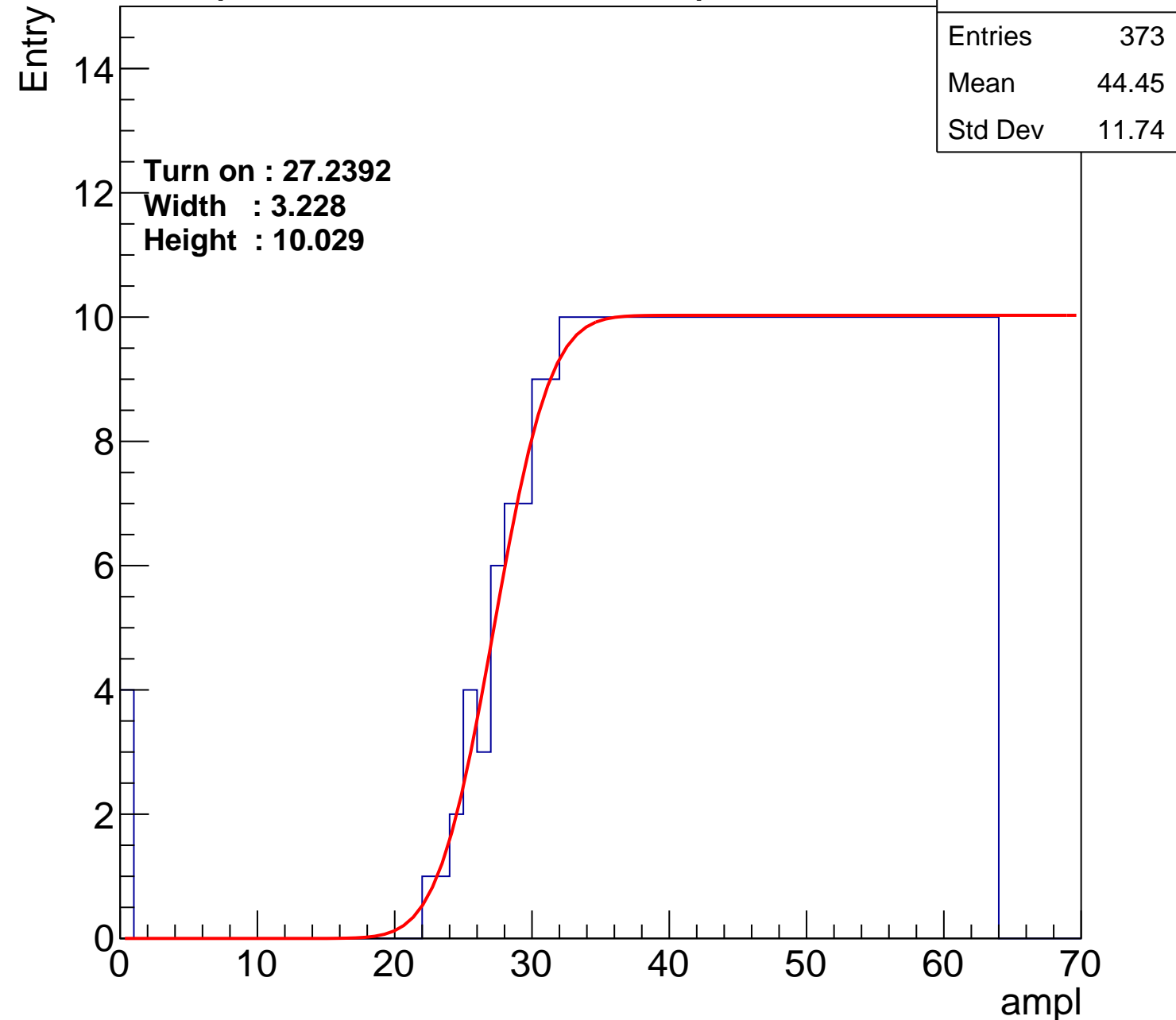
Width : 3.228

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch76

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.84
Std Dev	11.55

Turn on : 27.5206

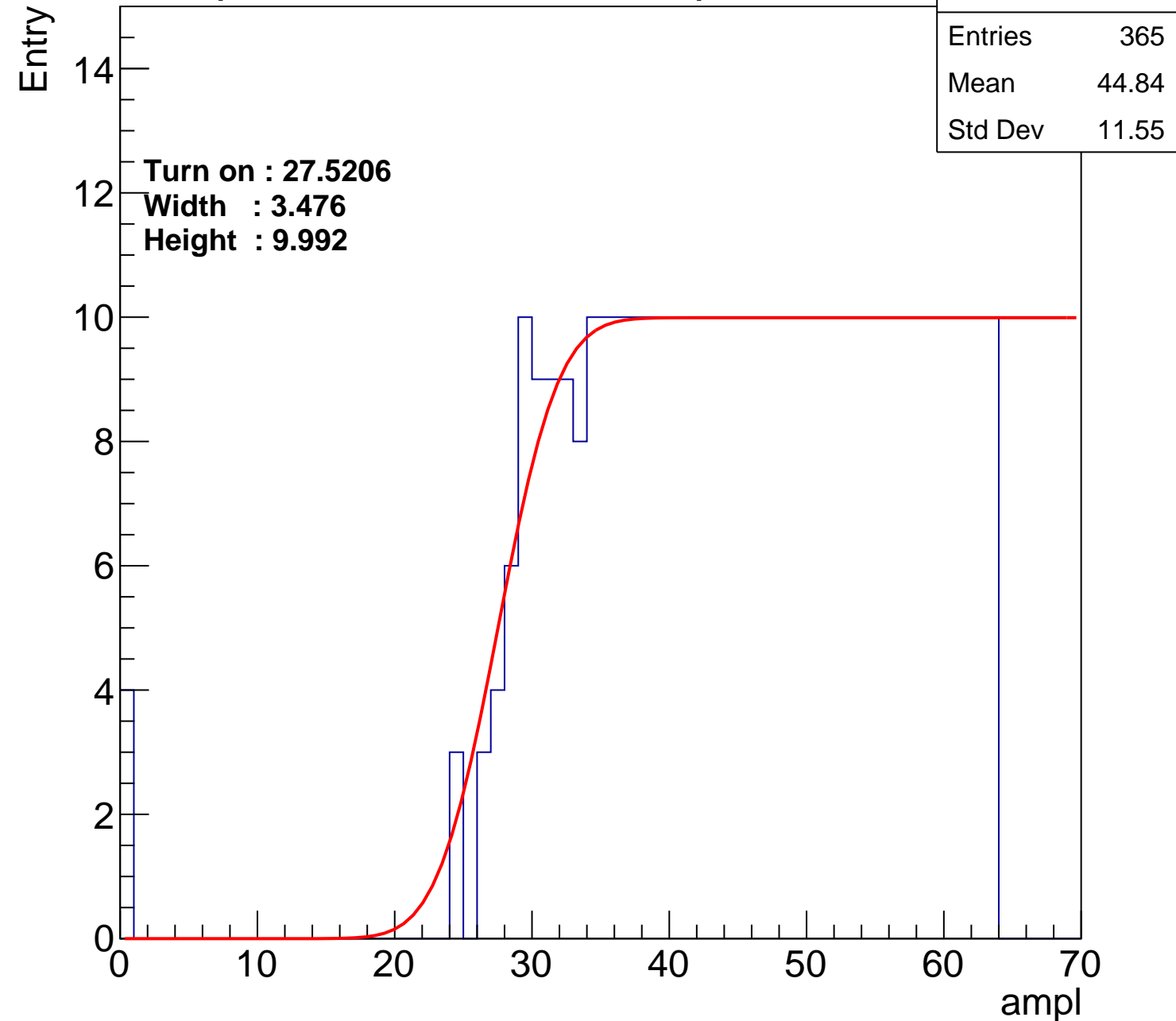
Width : 3.476

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch77

calib_packv5_042523_0143.root, FC#5, port B1

Entries	358
Mean	45.23
Std Dev	11.23

Turn on : 29.2629

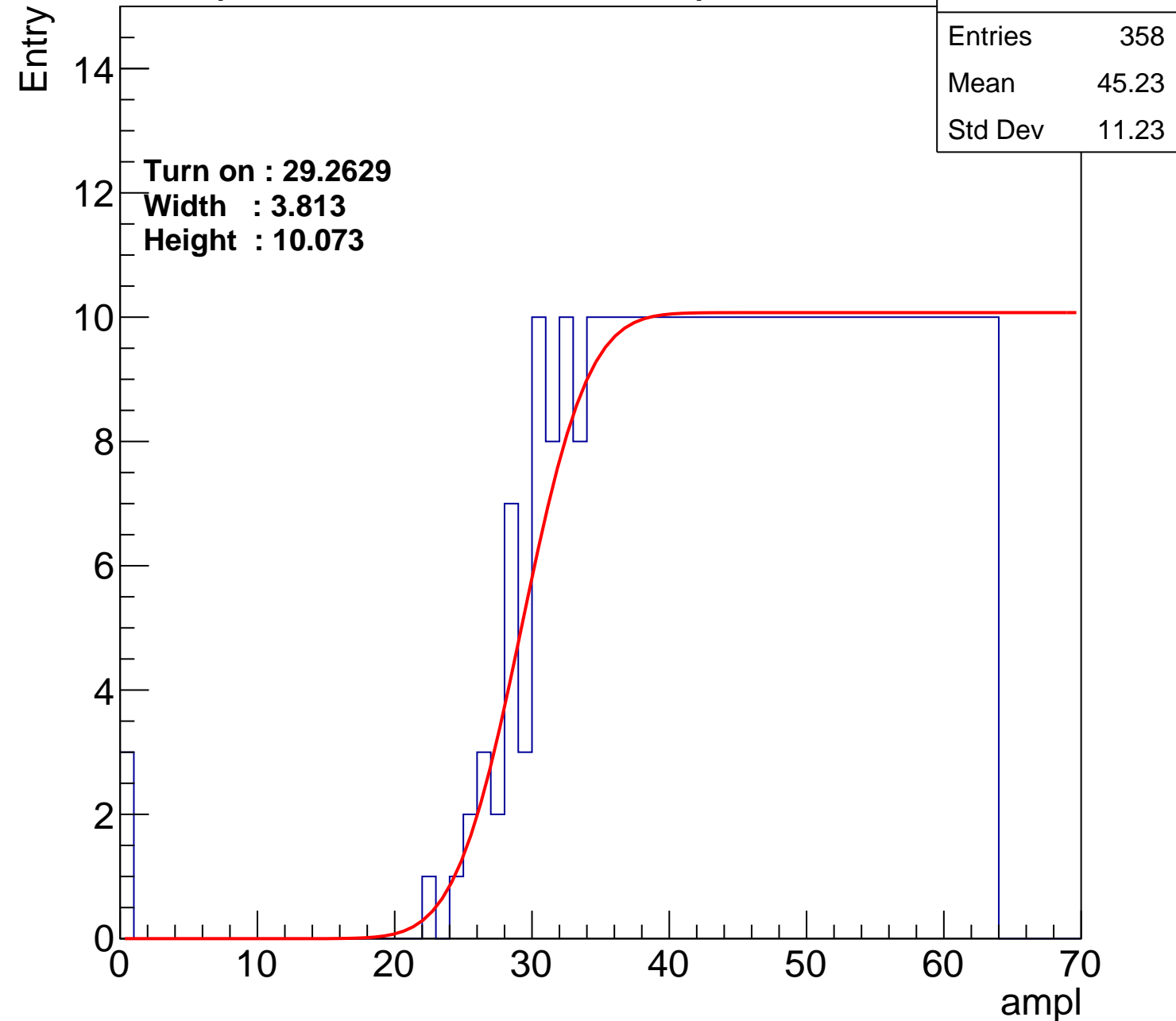
Width : 3.813

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch78

calib_packv5_042523_0143.root, FC#5, port B1

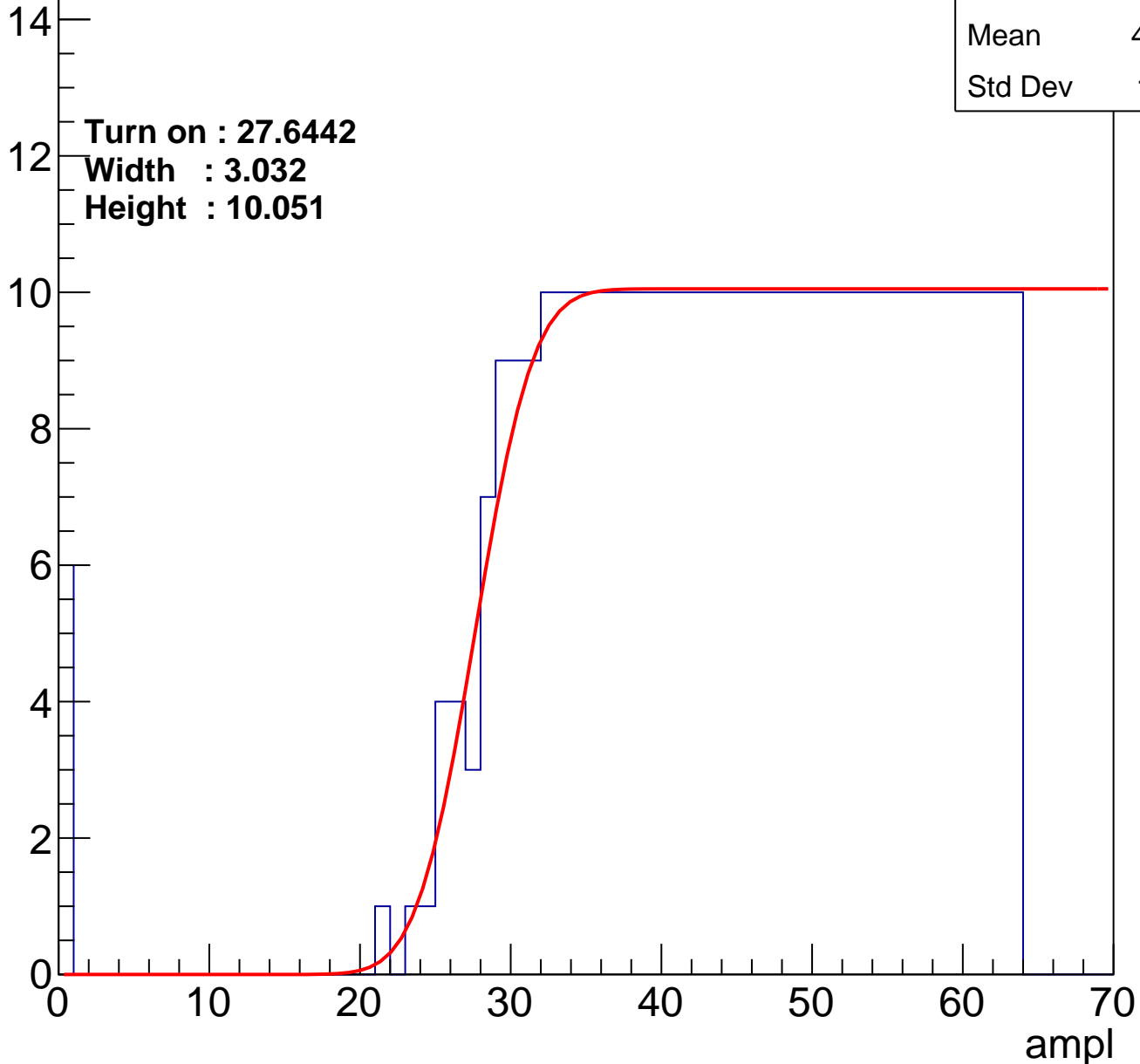
Entries	374
Mean	44.28
Std Dev	12.11

Turn on : 27.6442

Width : 3.032

Height : 10.051

Entry



B0L000S, U9-ch79

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
---------	-----

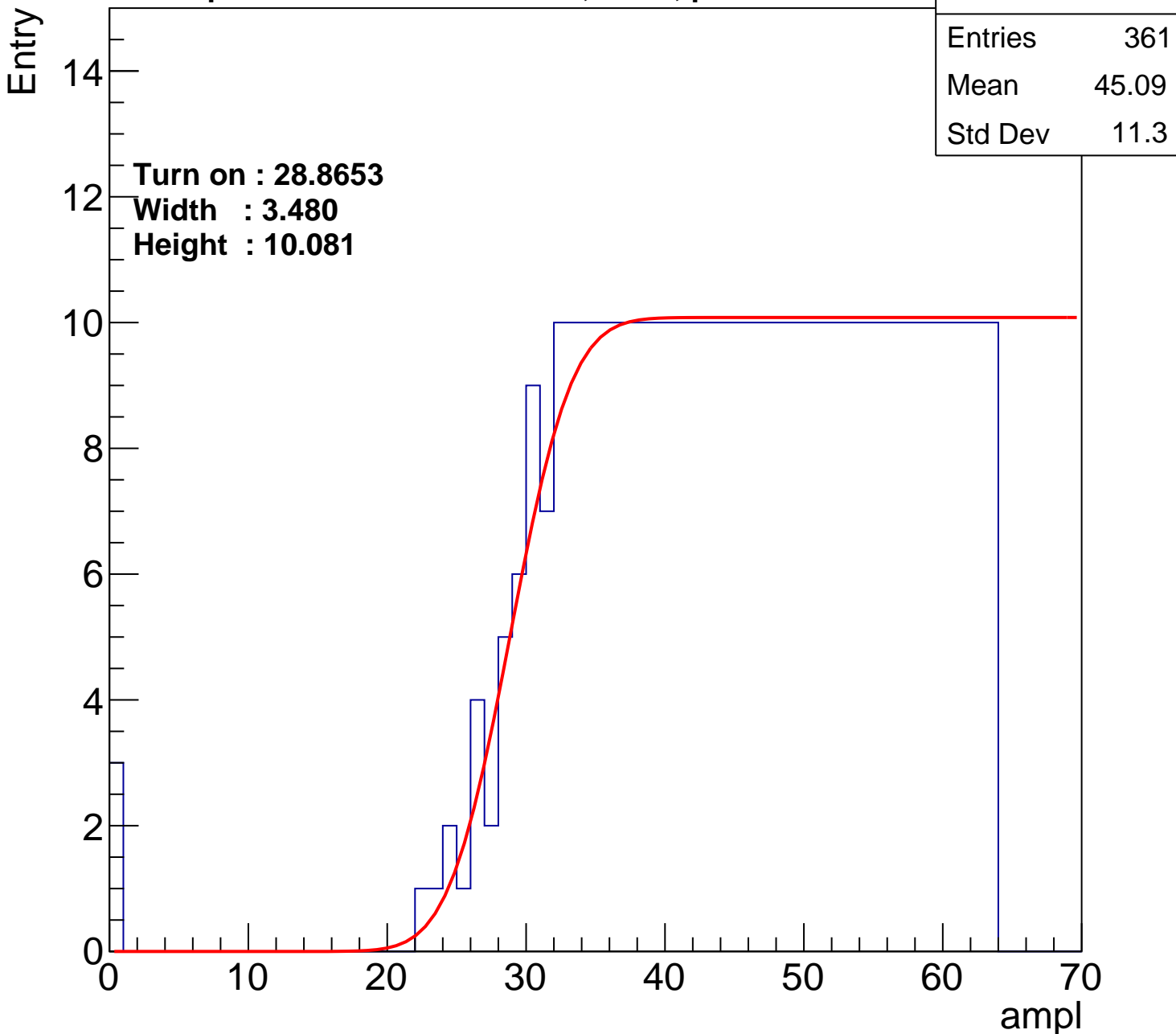
Mean	45.09
------	-------

Std Dev	11.3
---------	------

Turn on : 28.8653

Width : 3.480

Height : 10.081



B0L000S, U9-ch80

calib_packv5_042523_0143.root, FC#5, port B1

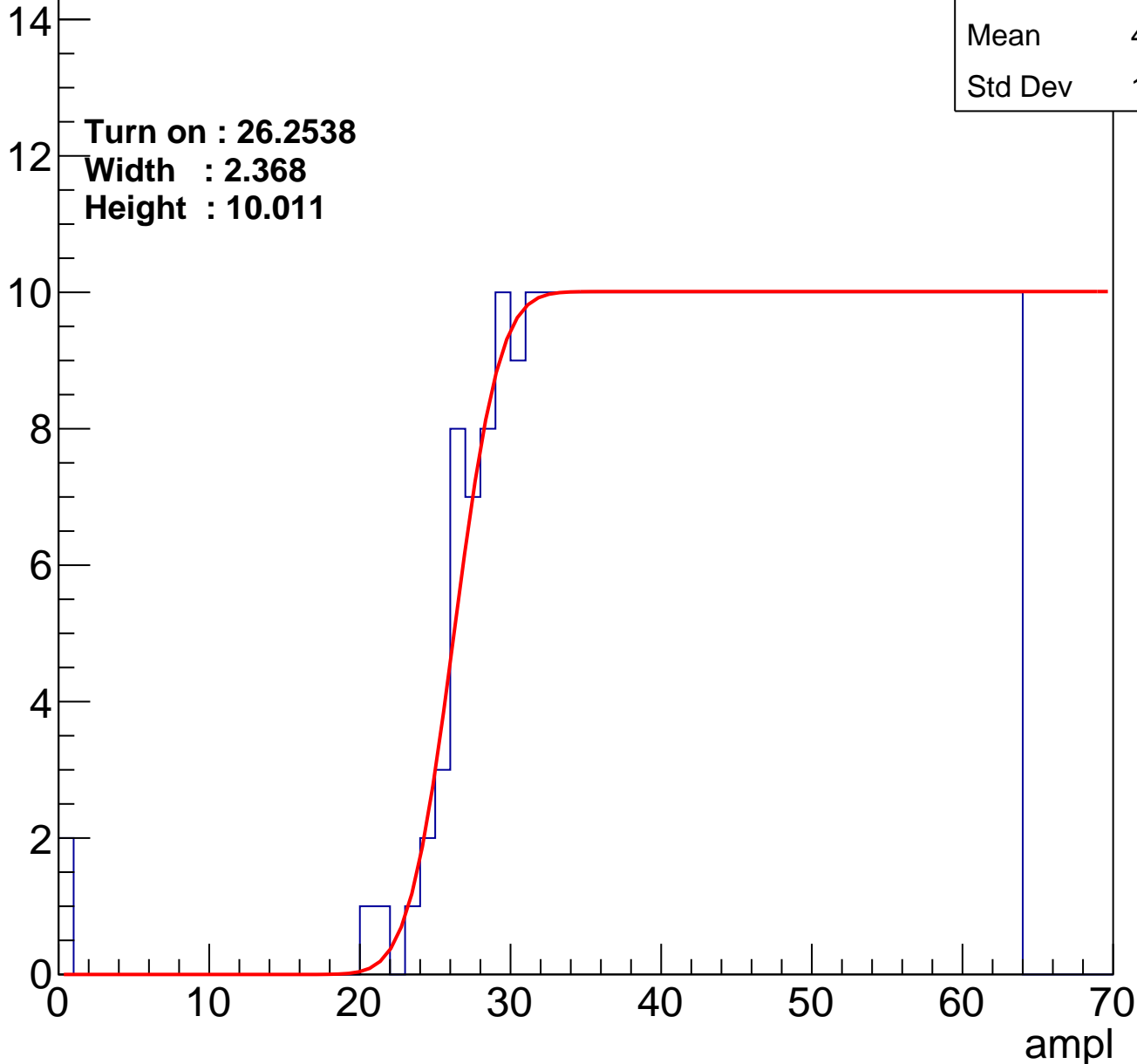
Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.2538

Width : 2.368

Height : 10.011

Entry



B0L000S, U9-ch81

calib_packv5_042523_0143.root, FC#5, port B1

Entries	376
Mean	44.43
Std Dev	11.45

Turn on : 26.9313

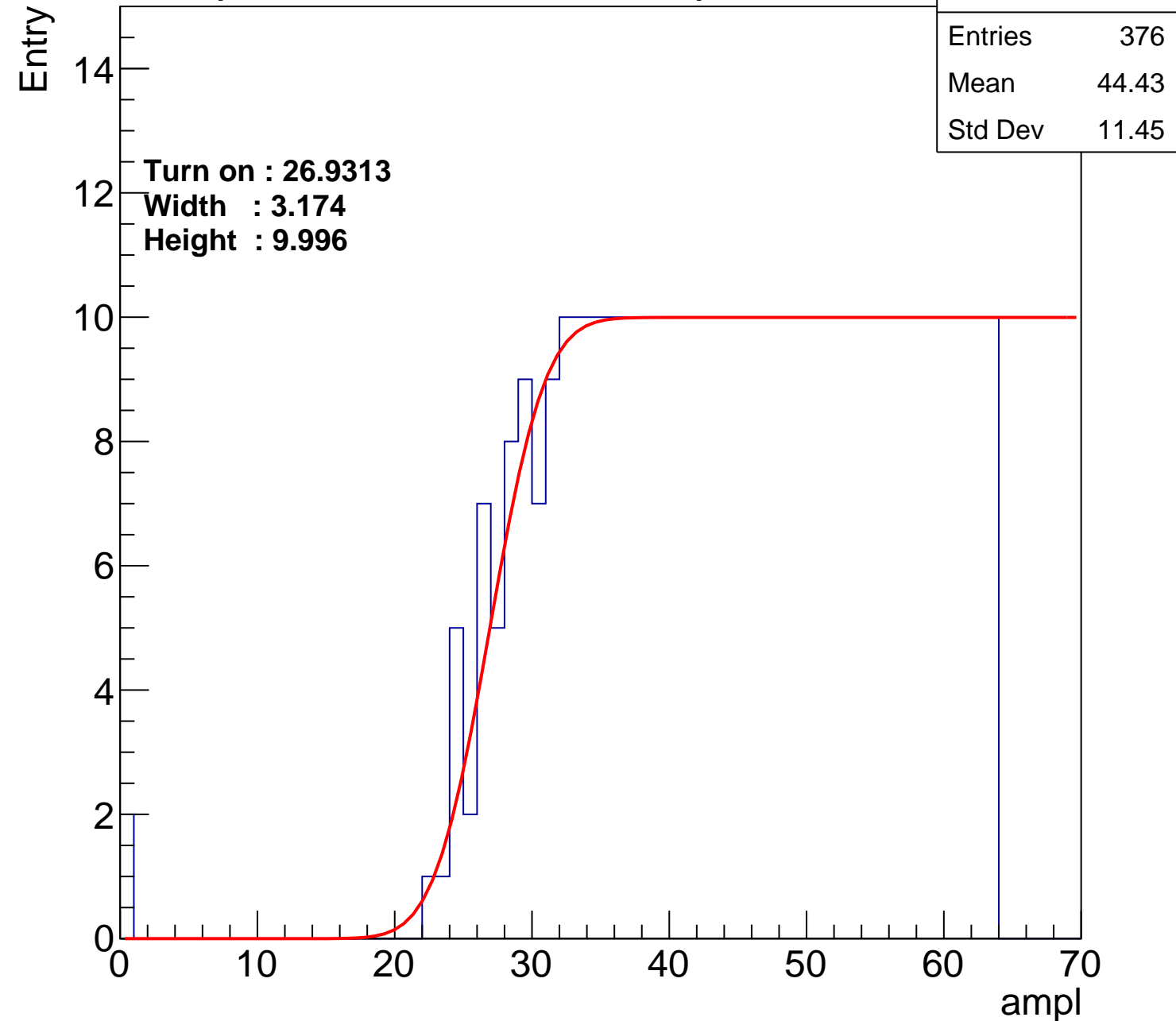
Width : 3.174

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch82

calib_packv5_042523_0143.root, FC#5, port B1

Entries	357
Mean	45.36
Std Dev	10.98

Turn on : 28.3979

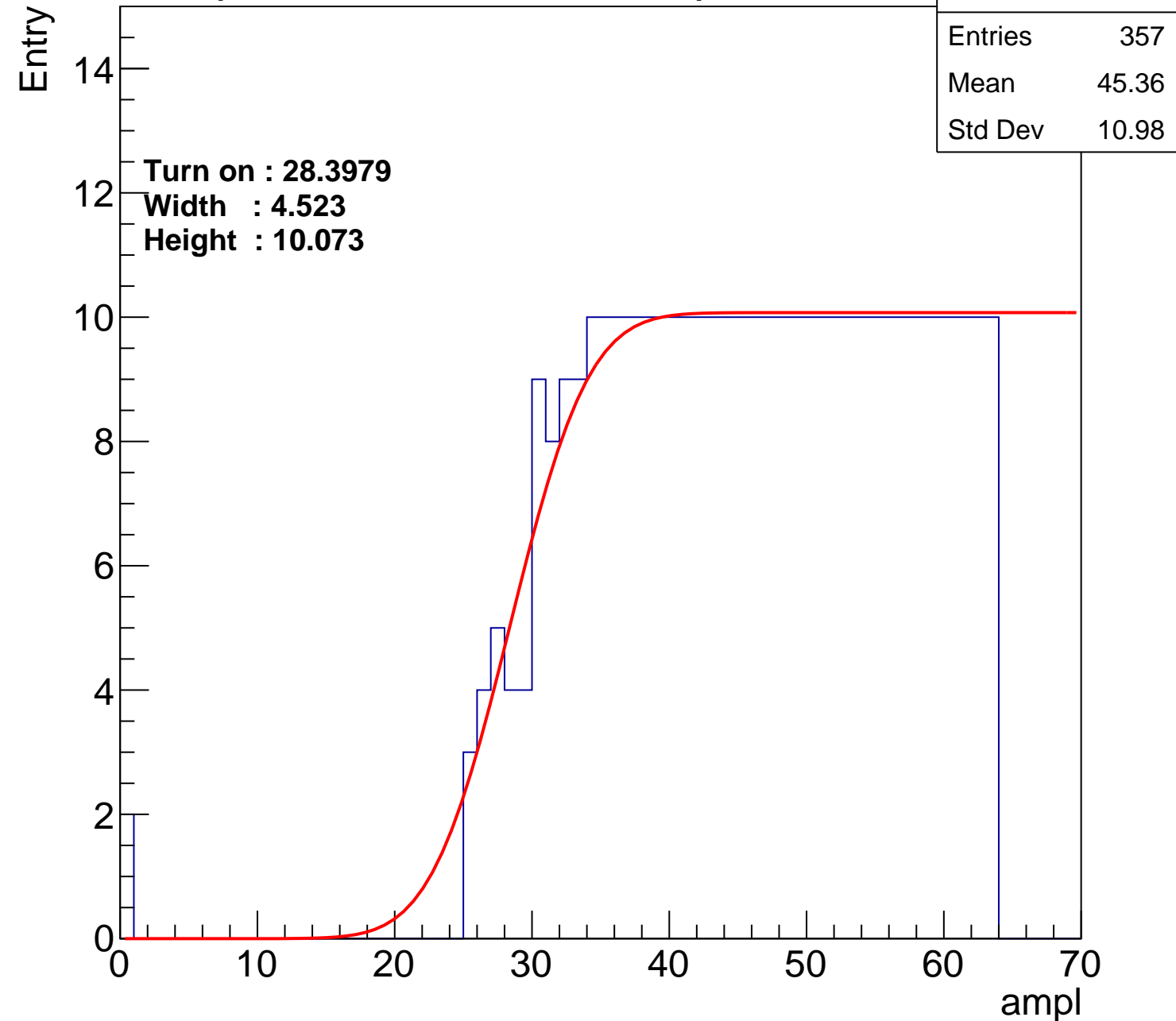
Width : 4.523

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch83

calib_packv5_042523_0143.root, FC#5, port B1

Entry

14

12

10

8

6

4

2

0

Turn on : 26.5554

Width : 2.079

Height : 9.948

Entries	377
Mean	44.38
Std Dev	11.57

ampl

0 10 20 30 40 50 60 70

B0L000S, U9-ch84

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.89
Std Dev	11.4

Turn on : 27.8889

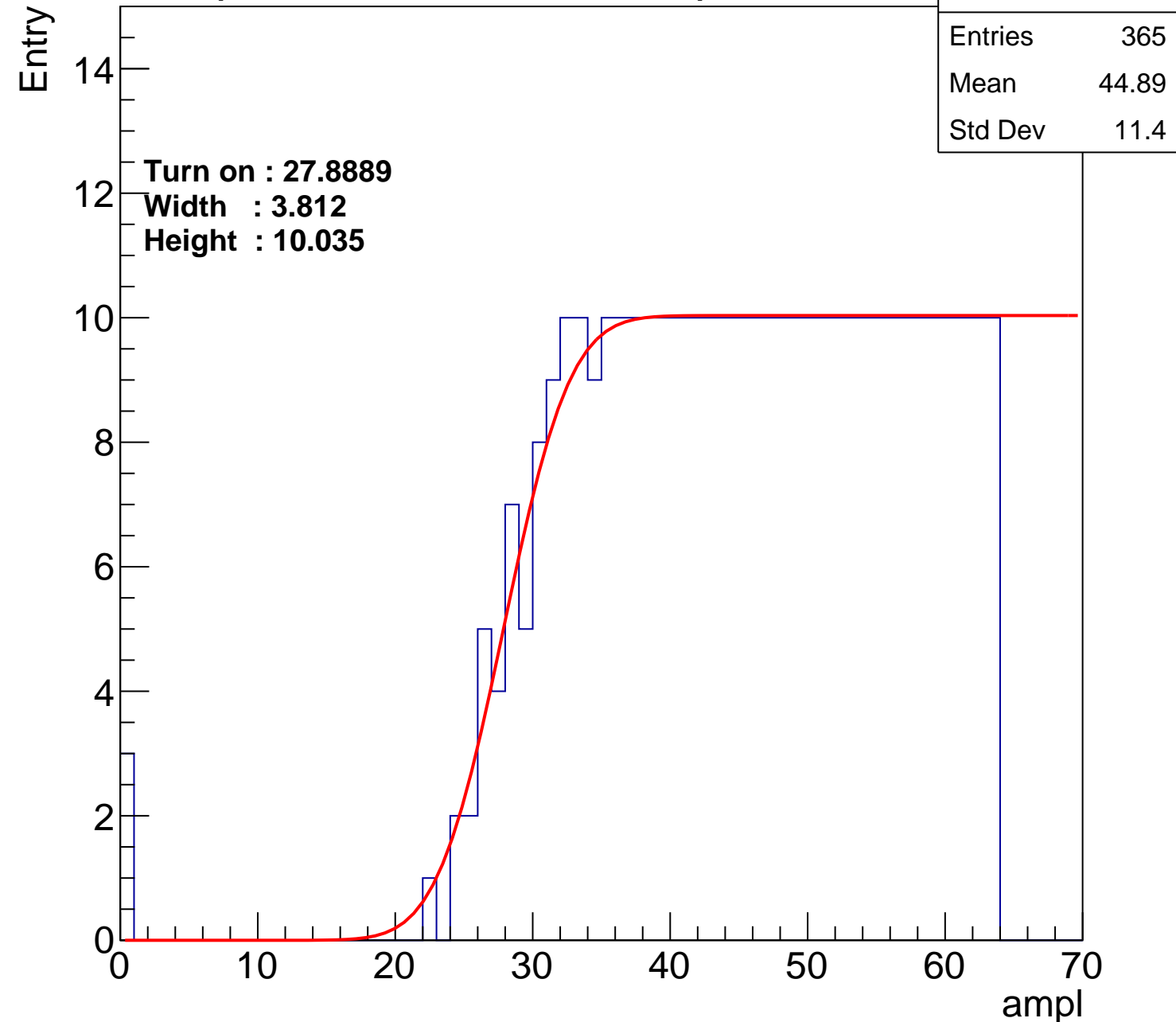
Width : 3.812

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch85

calib_packv5_042523_0143.root, FC#5, port B1

Entries	388
Mean	43.86
Std Dev	11.73

Turn on : 25.5871

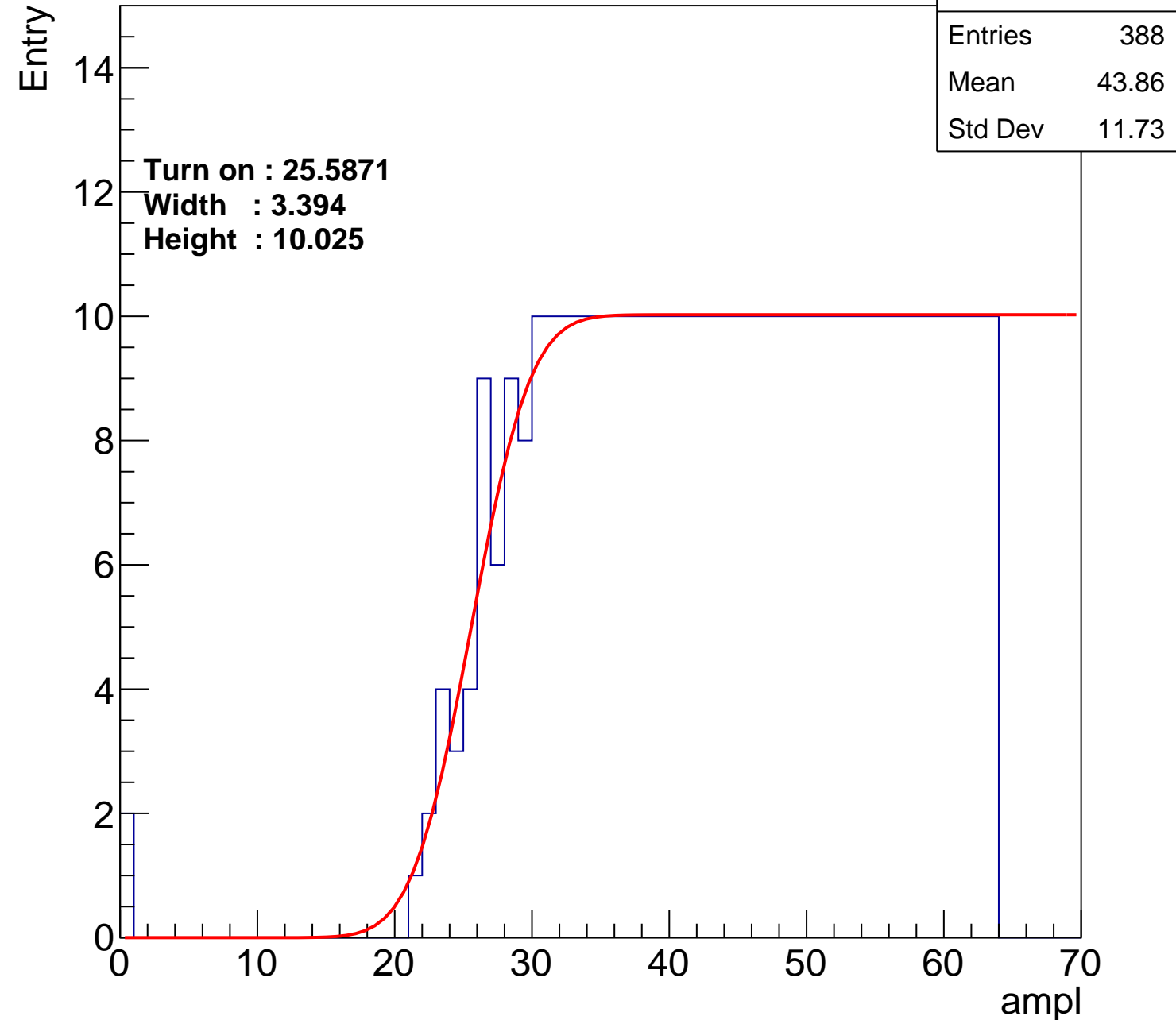
Width : 3.394

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch86

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
Mean	44.84
Std Dev	11.56

Turn on : 28.2097

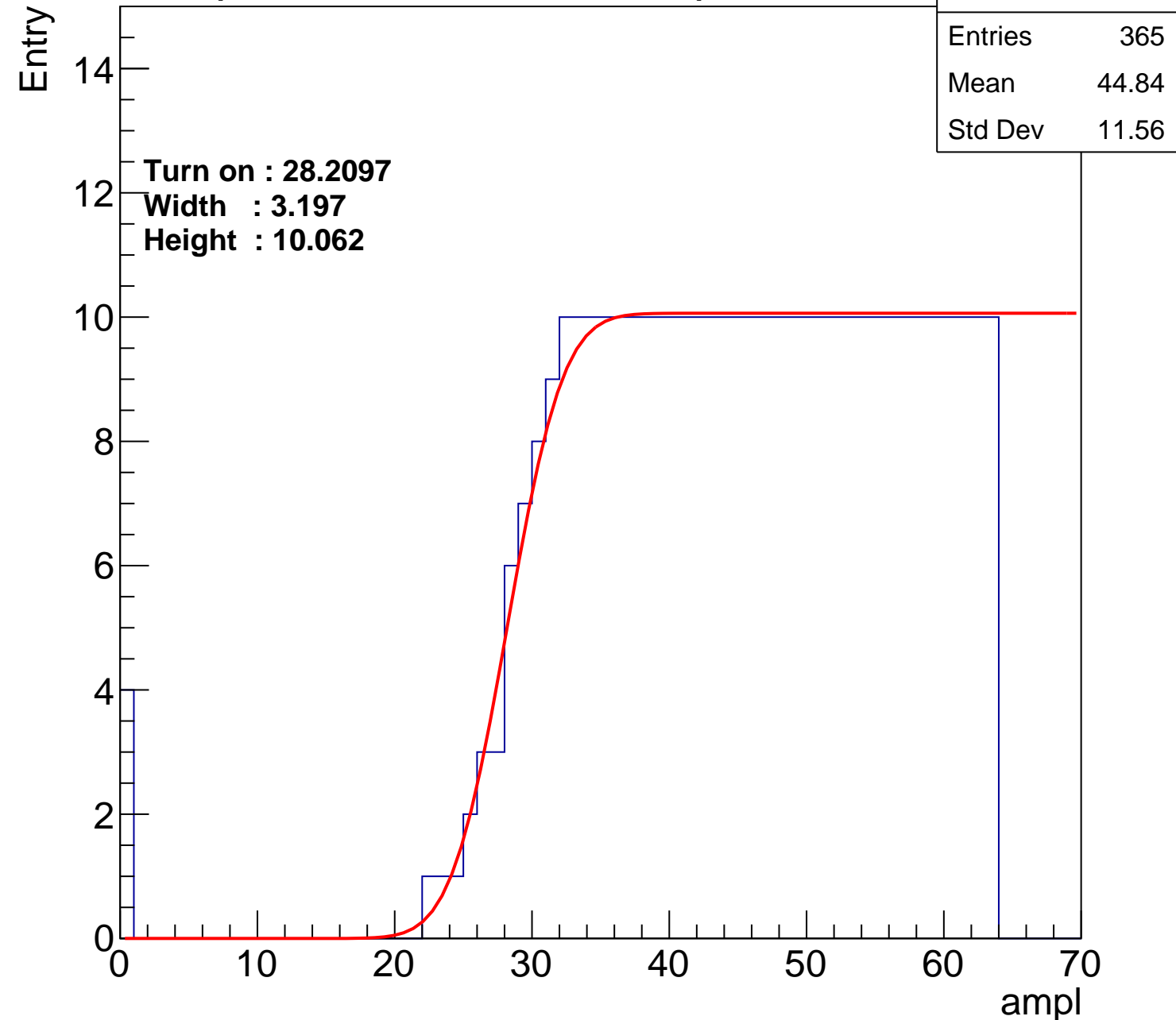
Width : 3.197

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch87

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.12
Std Dev	11.11

Turn on : 28.2770

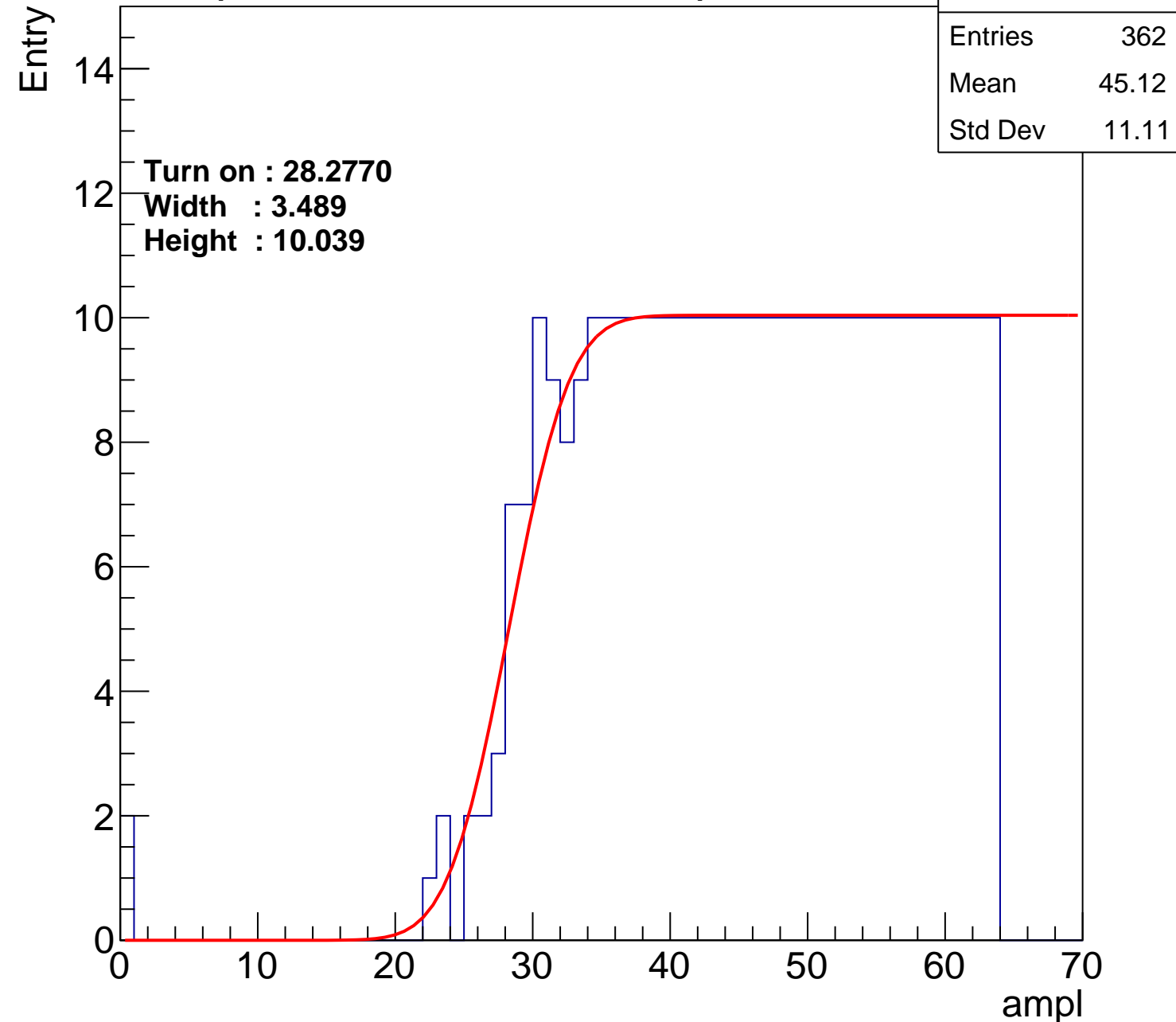
Width : 3.489

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch88

calib_packv5_042523_0143.root, FC#5, port B1

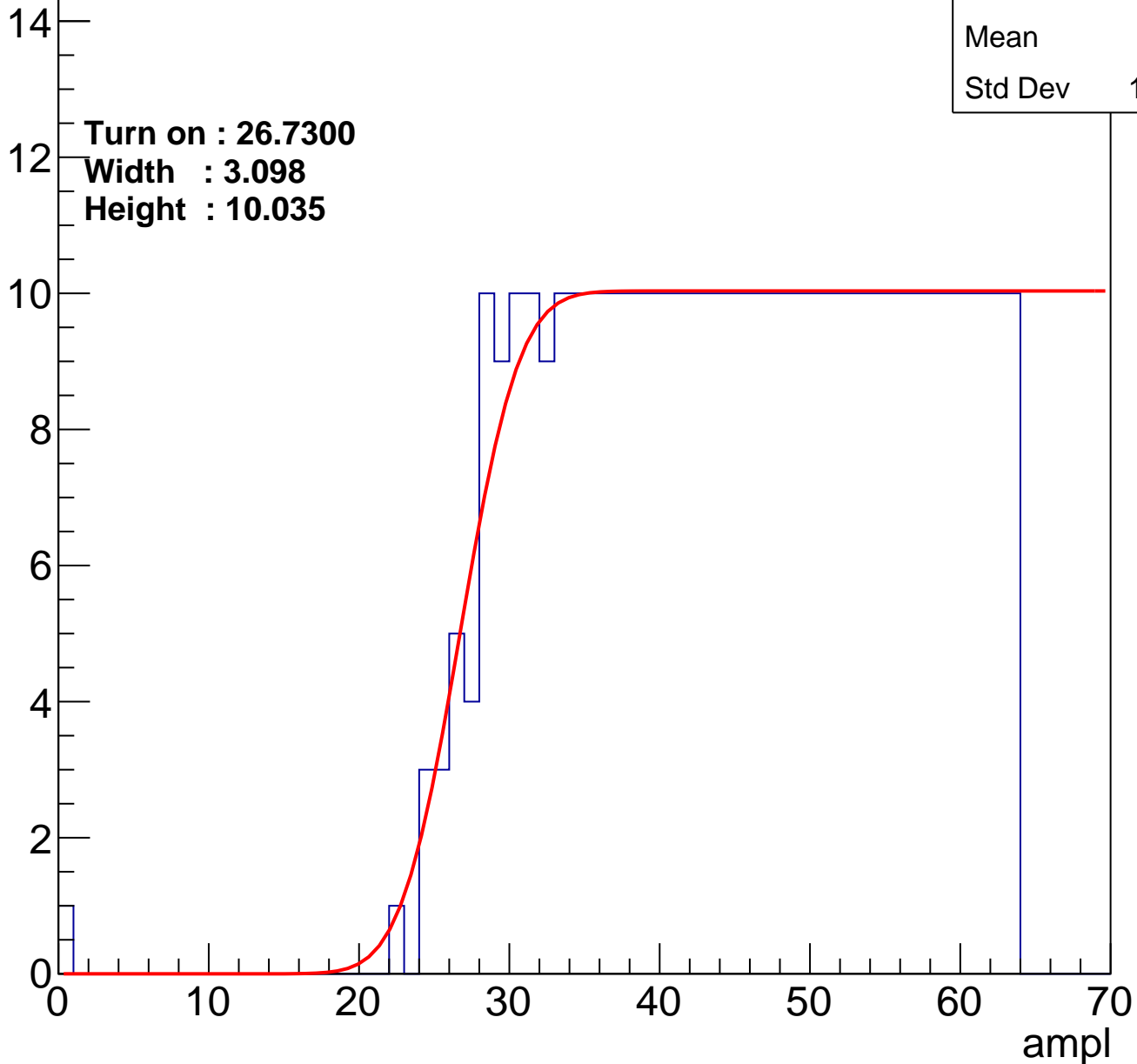
Entries	375
Mean	44.6
Std Dev	11.15

Turn on : 26.7300

Width : 3.098

Height : 10.035

Entry



B0L000S, U9-ch89

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.14
Std Dev	10.93

Turn on : 28.4662

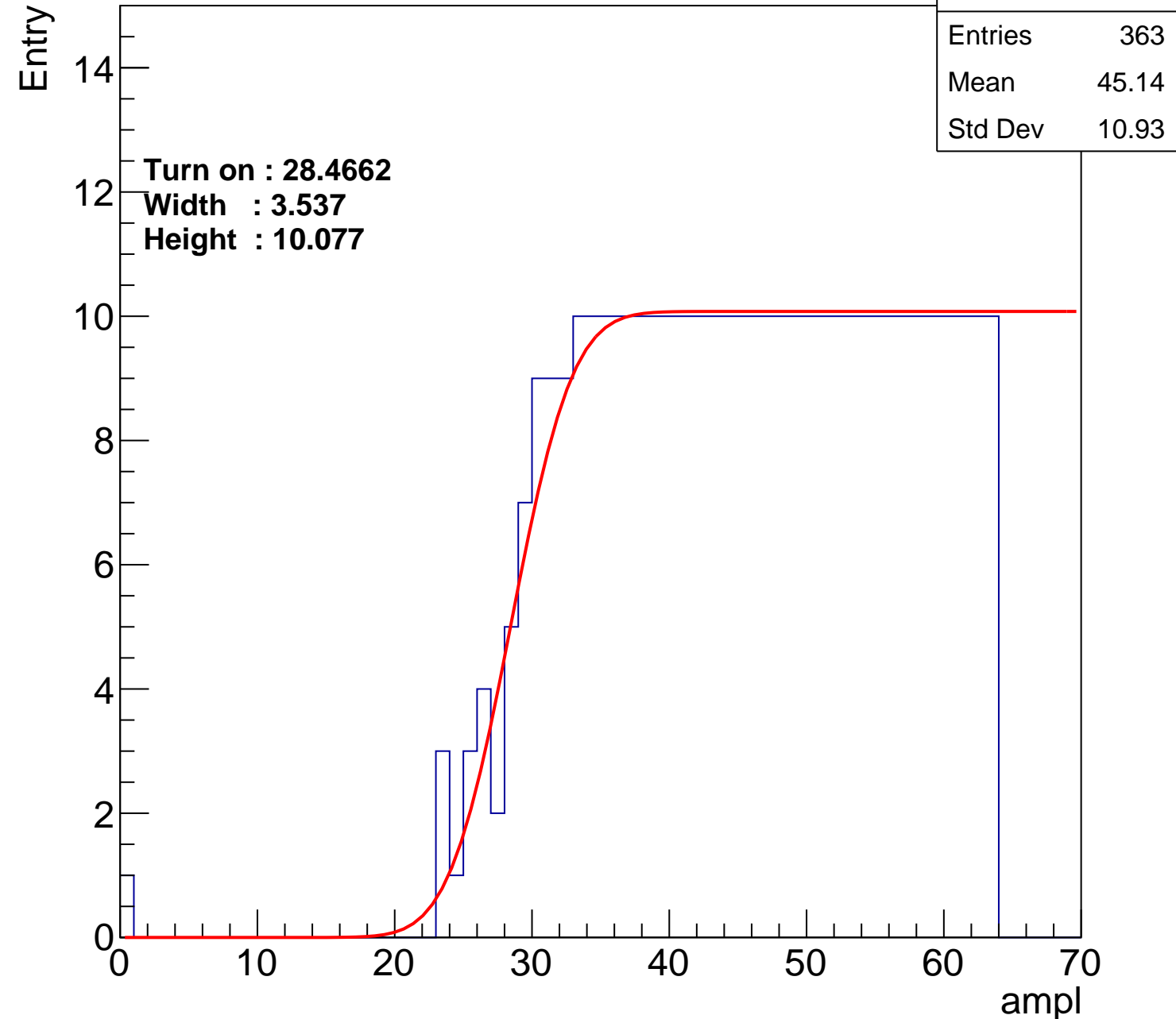
Width : 3.537

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch90

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.19
Std Dev	11.03

Turn on : 28.0646

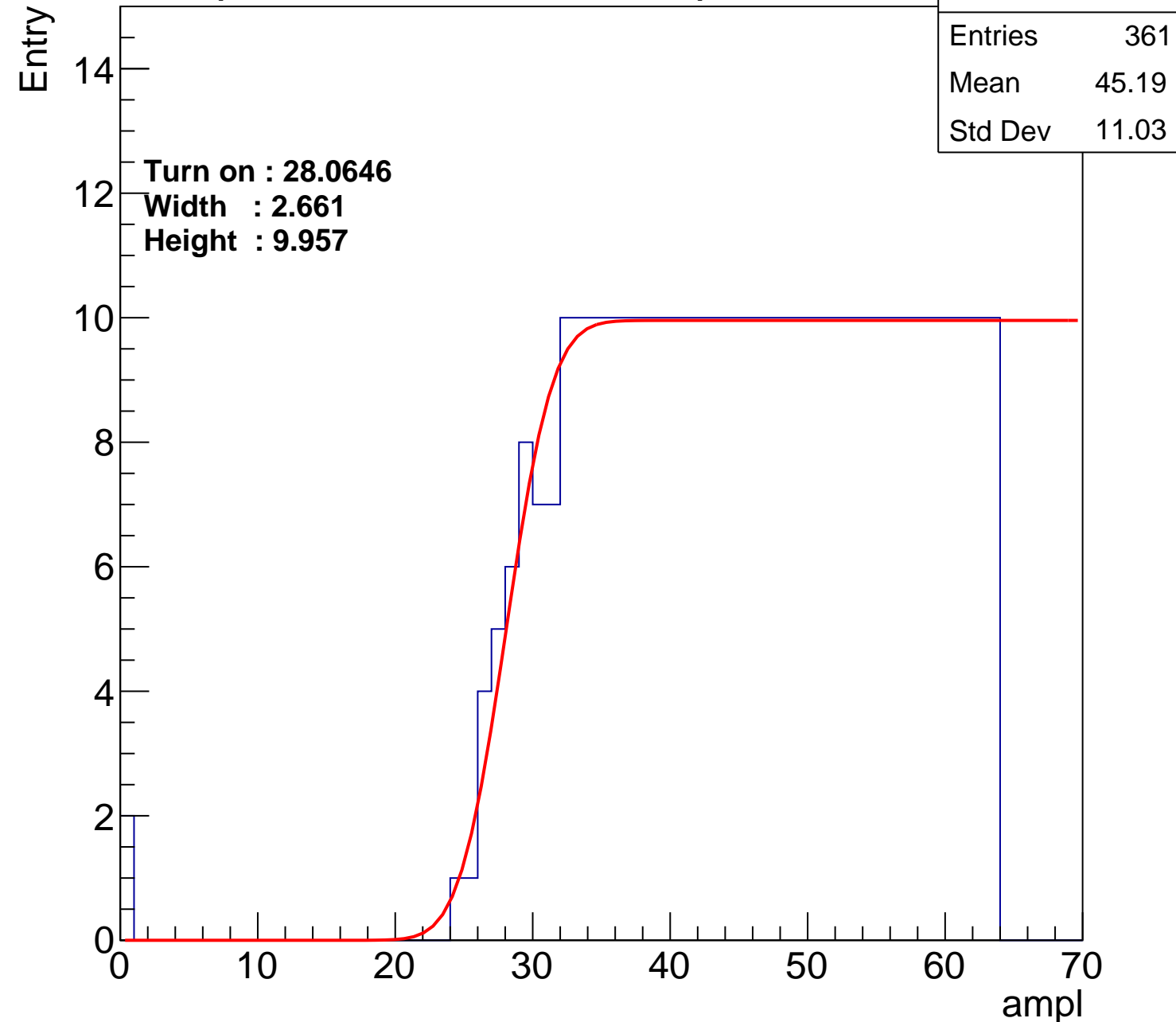
Width : 2.661

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch91

calib_packv5_042523_0143.root, FC#5, port B1

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.8576

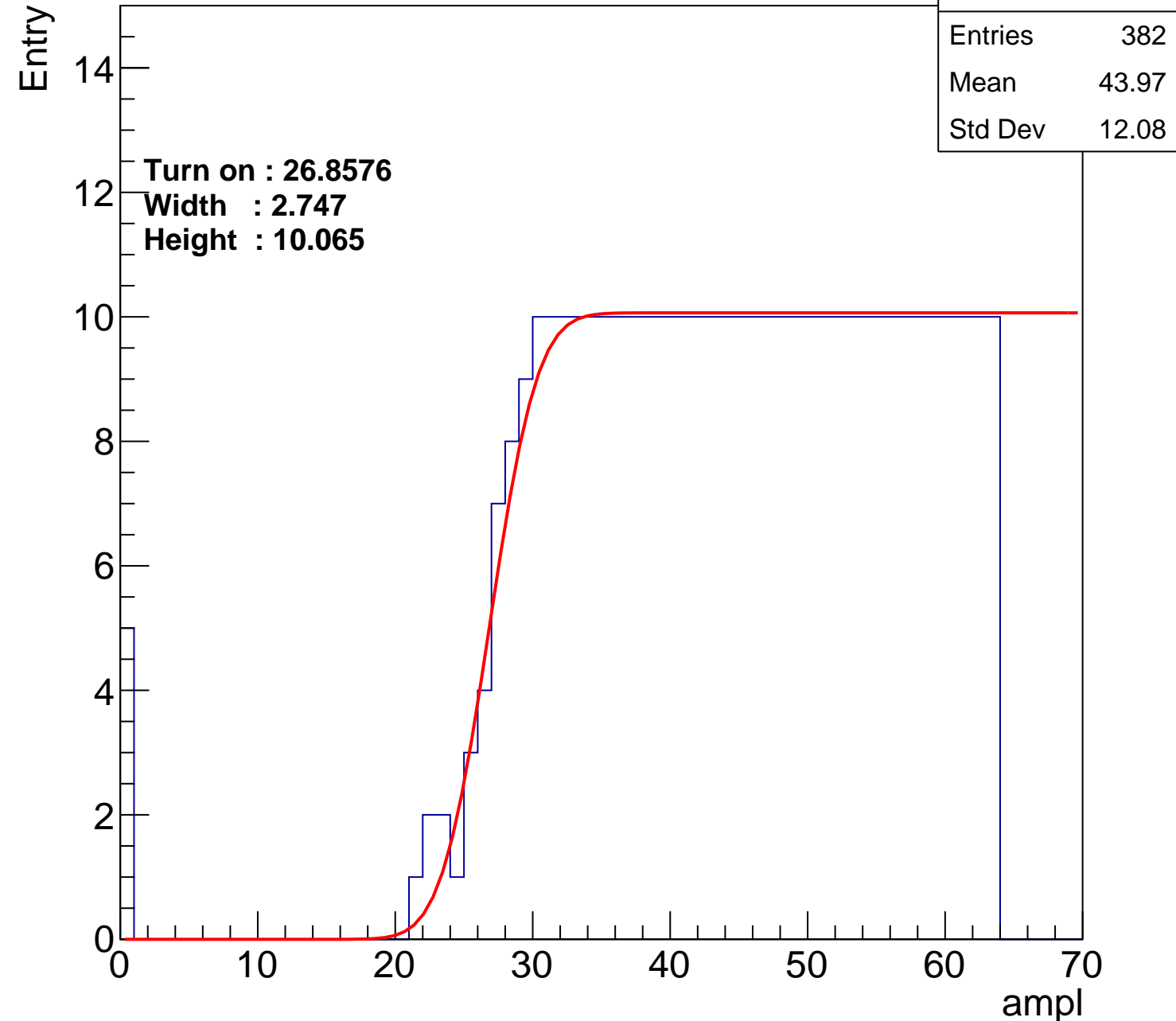
Width : 2.747

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch92

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	44.15
Std Dev	11.91

Turn on : 27.2505

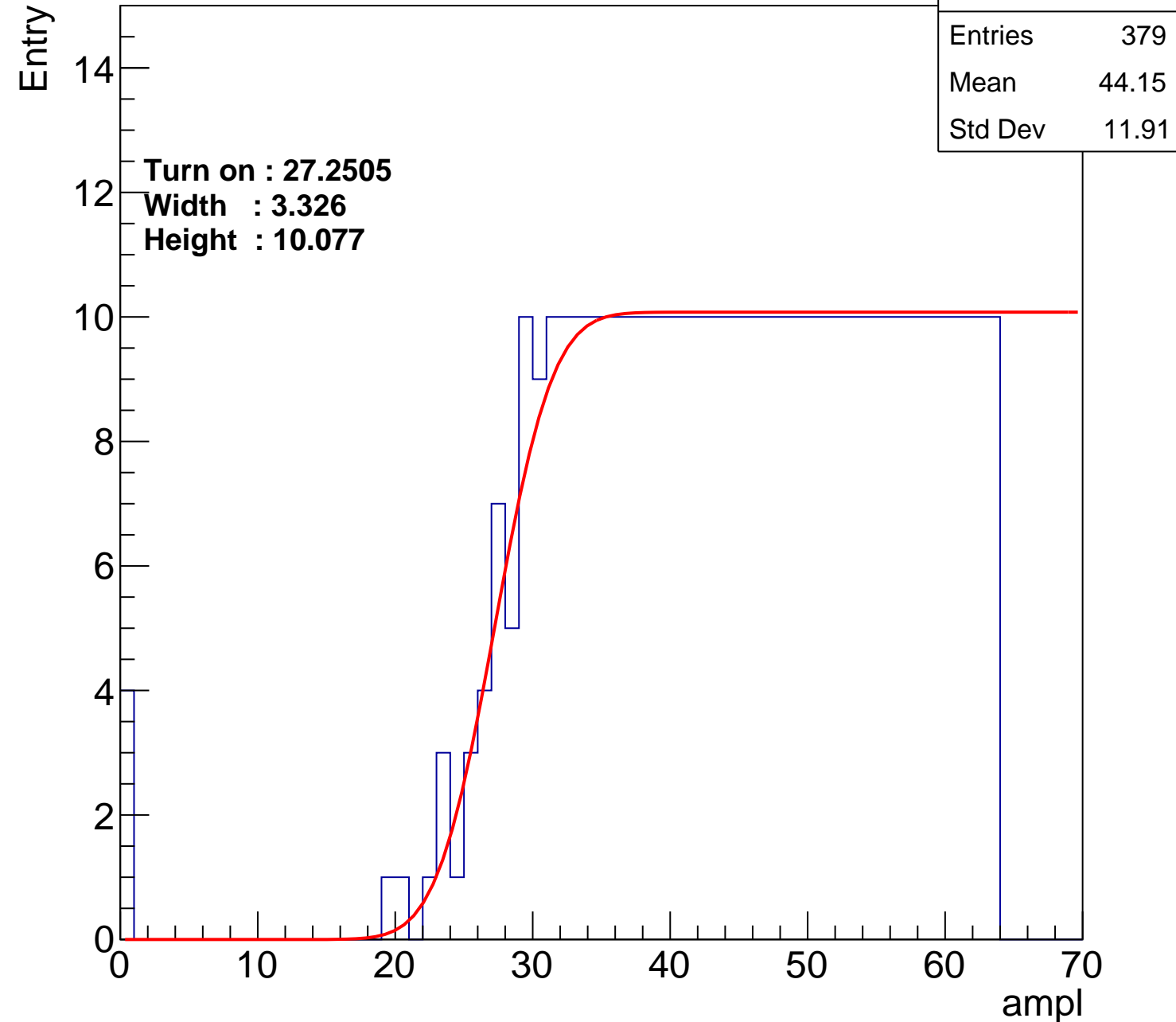
Width : 3.326

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch93

calib_packv5_042523_0143.root, FC#5, port B1

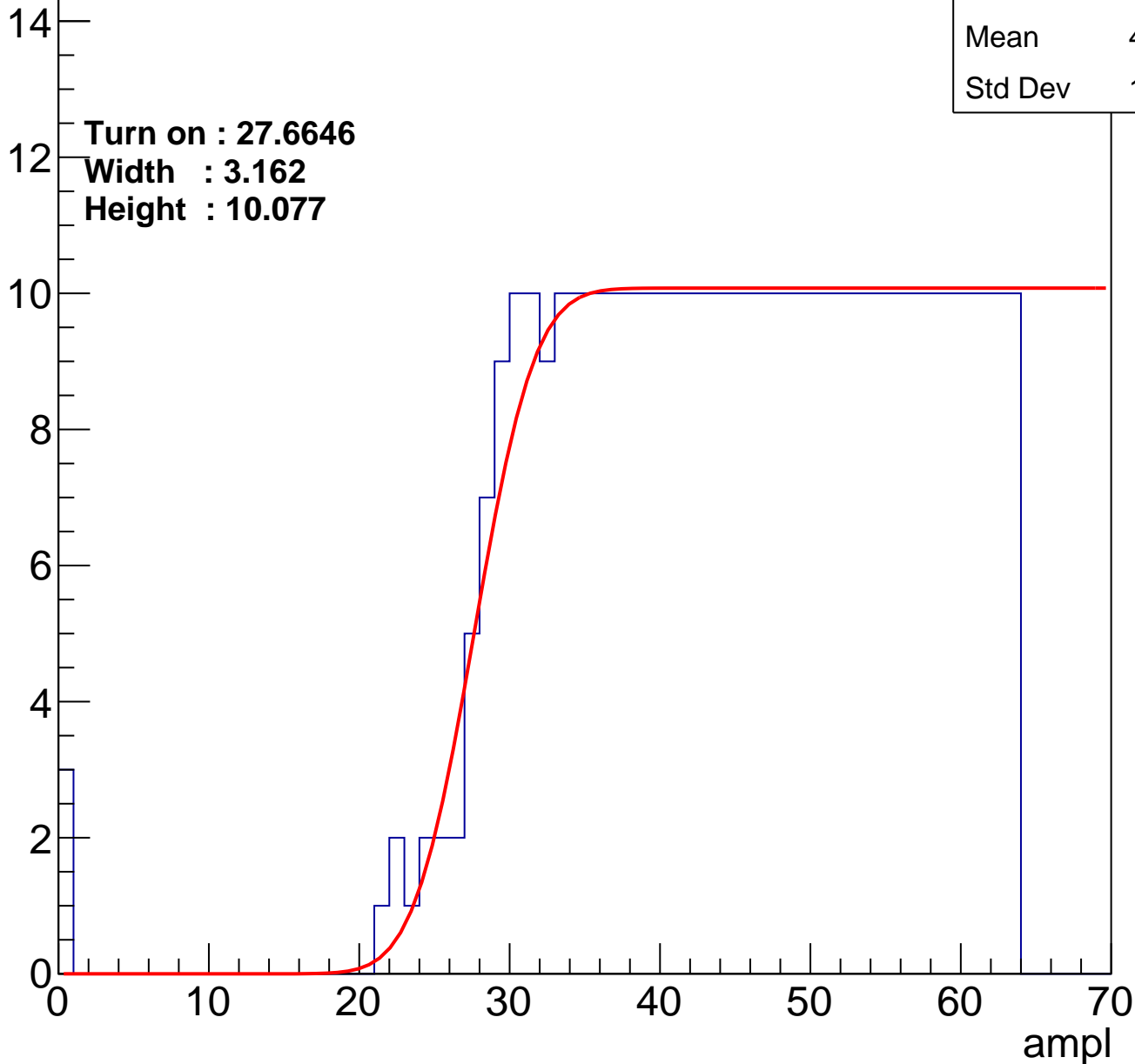
Entry

Entries	373
Mean	44.53
Std Dev	11.55

Turn on : 27.6646

Width : 3.162

Height : 10.077



B0L000S, U9-ch94

calib_packv5_042523_0143.root, FC#5, port B1

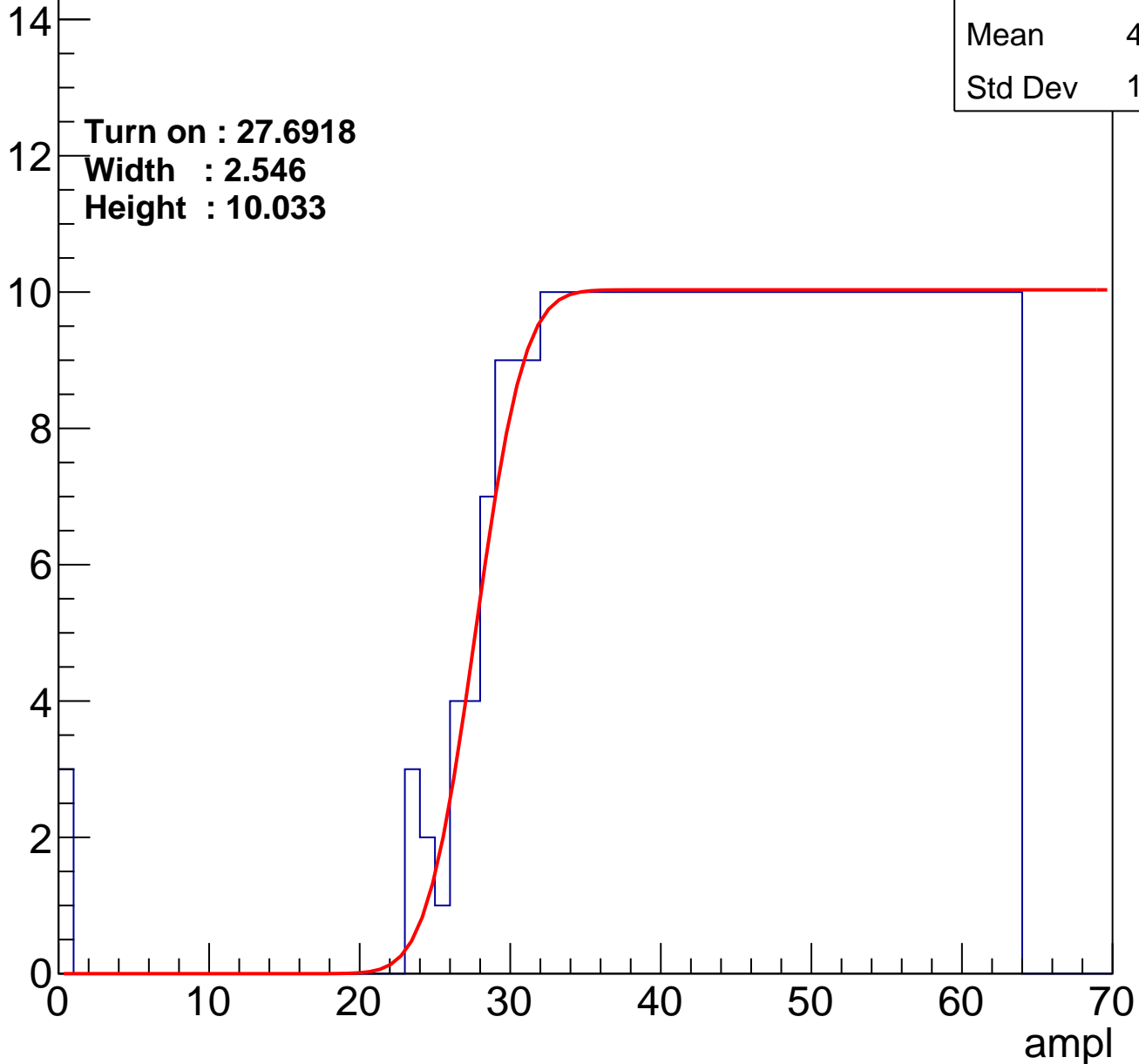
Entries	371
Mean	44.64
Std Dev	11.48

Turn on : 27.6918

Width : 2.546

Height : 10.033

Entry



B0L000S, U9-ch95

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.65
Std Dev	11.69

Turn on : 27.8634

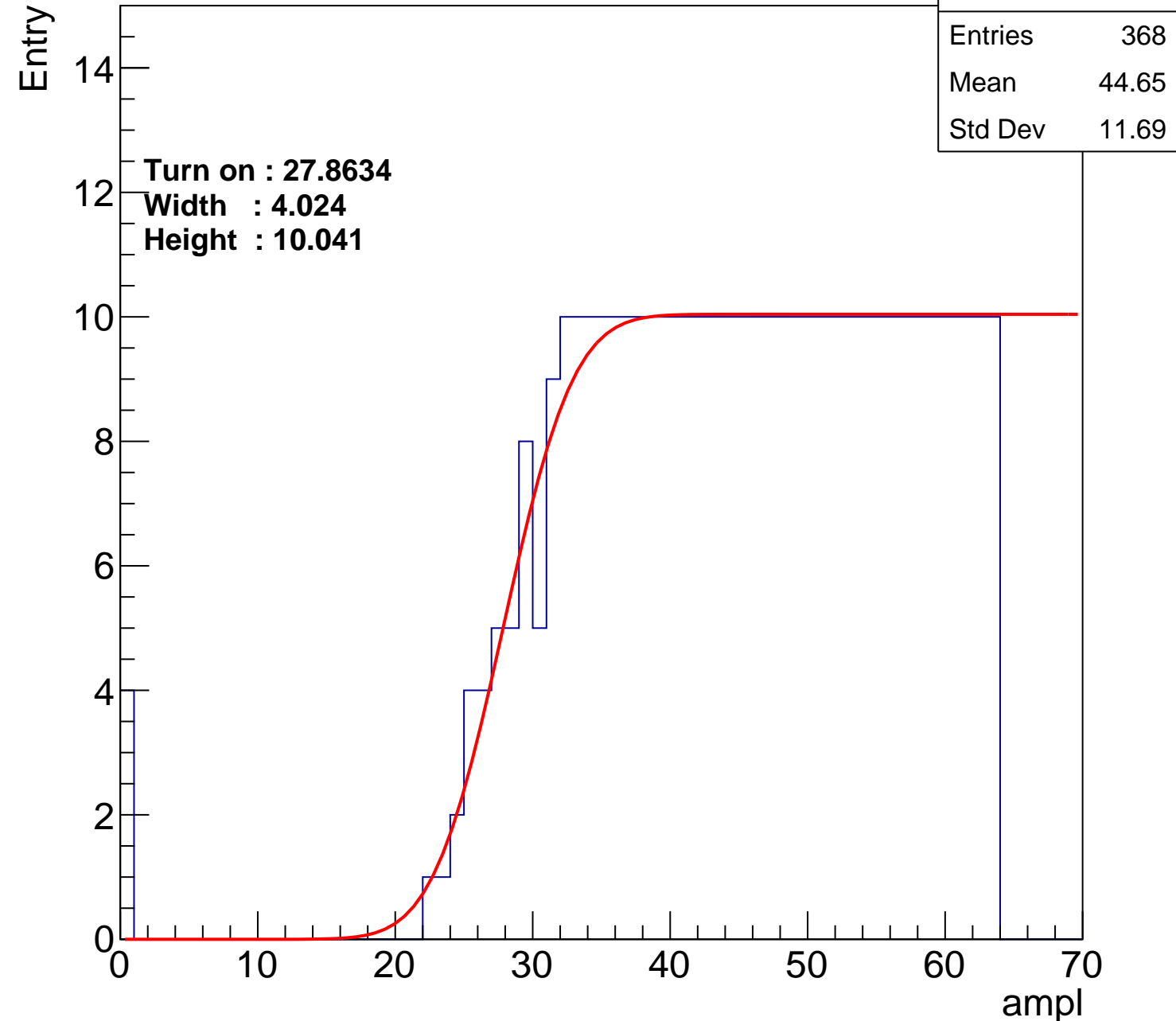
Width : 4.024

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch96

calib_packv5_042523_0143.root, FC#5, port B1

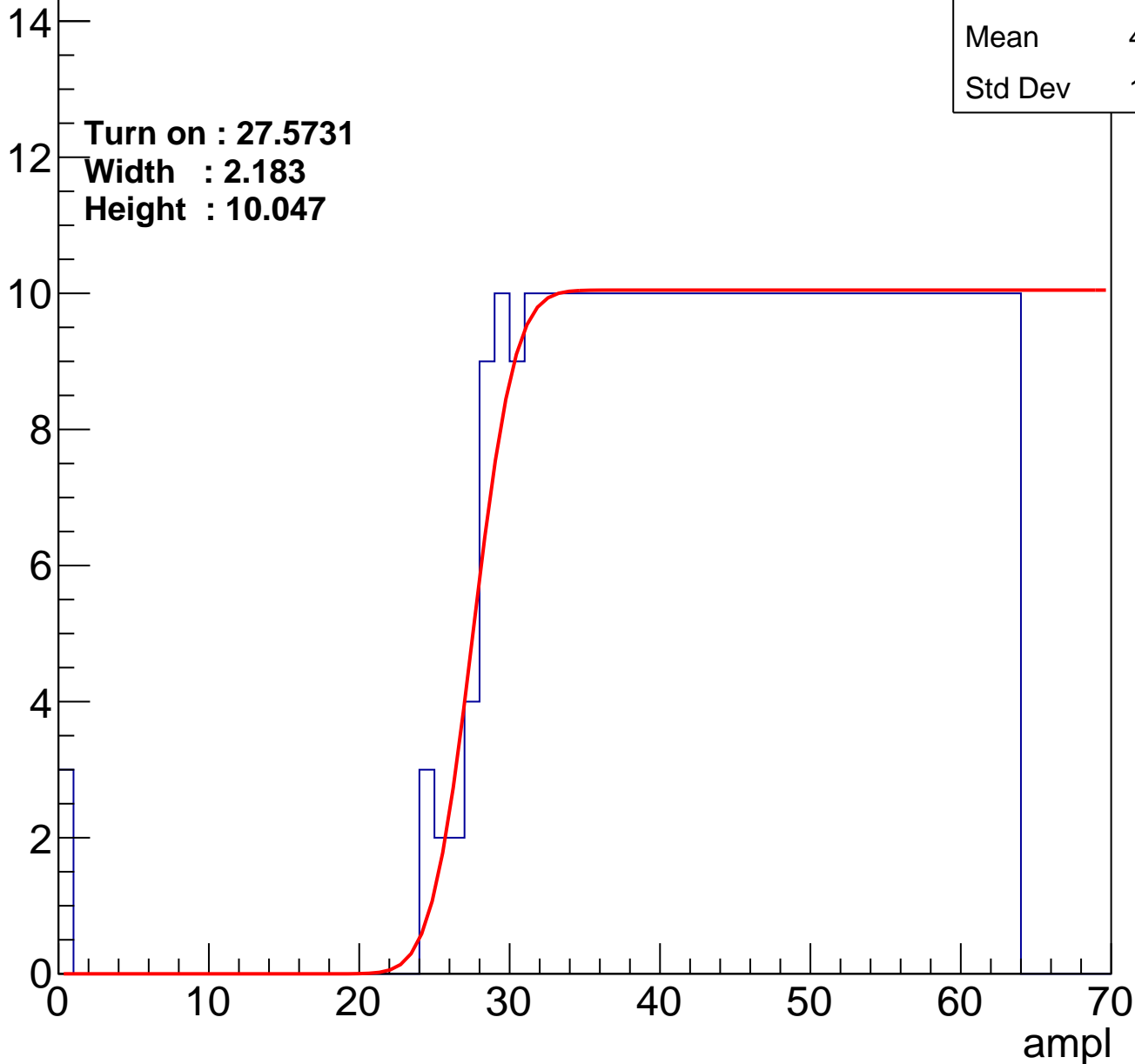
Entry

Entries	372
Mean	44.63
Std Dev	11.43

Turn on : 27.5731

Width : 2.183

Height : 10.047



B0L000S, U9-ch97

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.14
Std Dev	11.73

Turn on : 26.5504

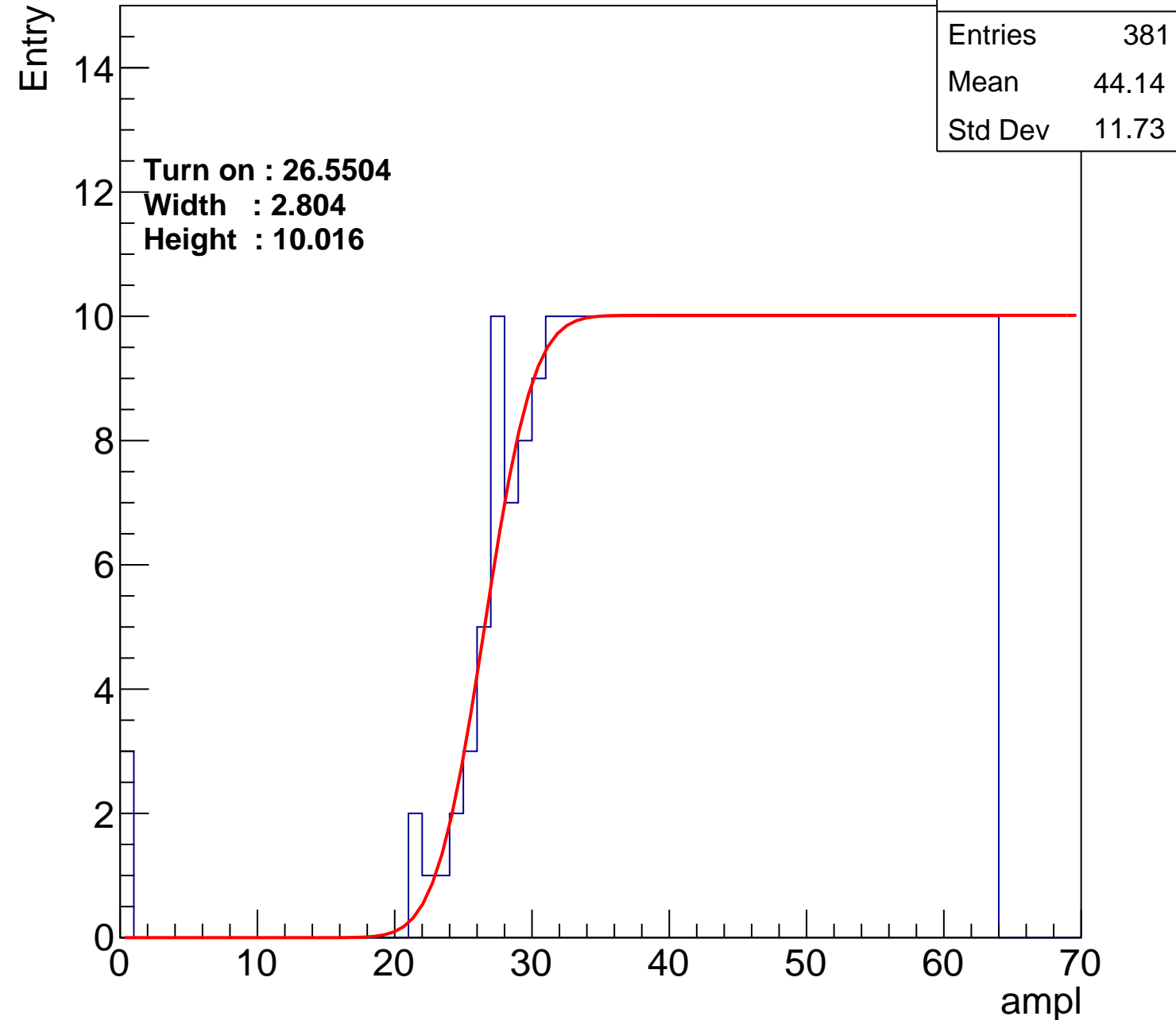
Width : 2.804

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch98

calib_packv5_042523_0143.root, FC#5, port B1

Entries	352
Mean	45.54
Std Dev	11.07

Turn on : 29.4498

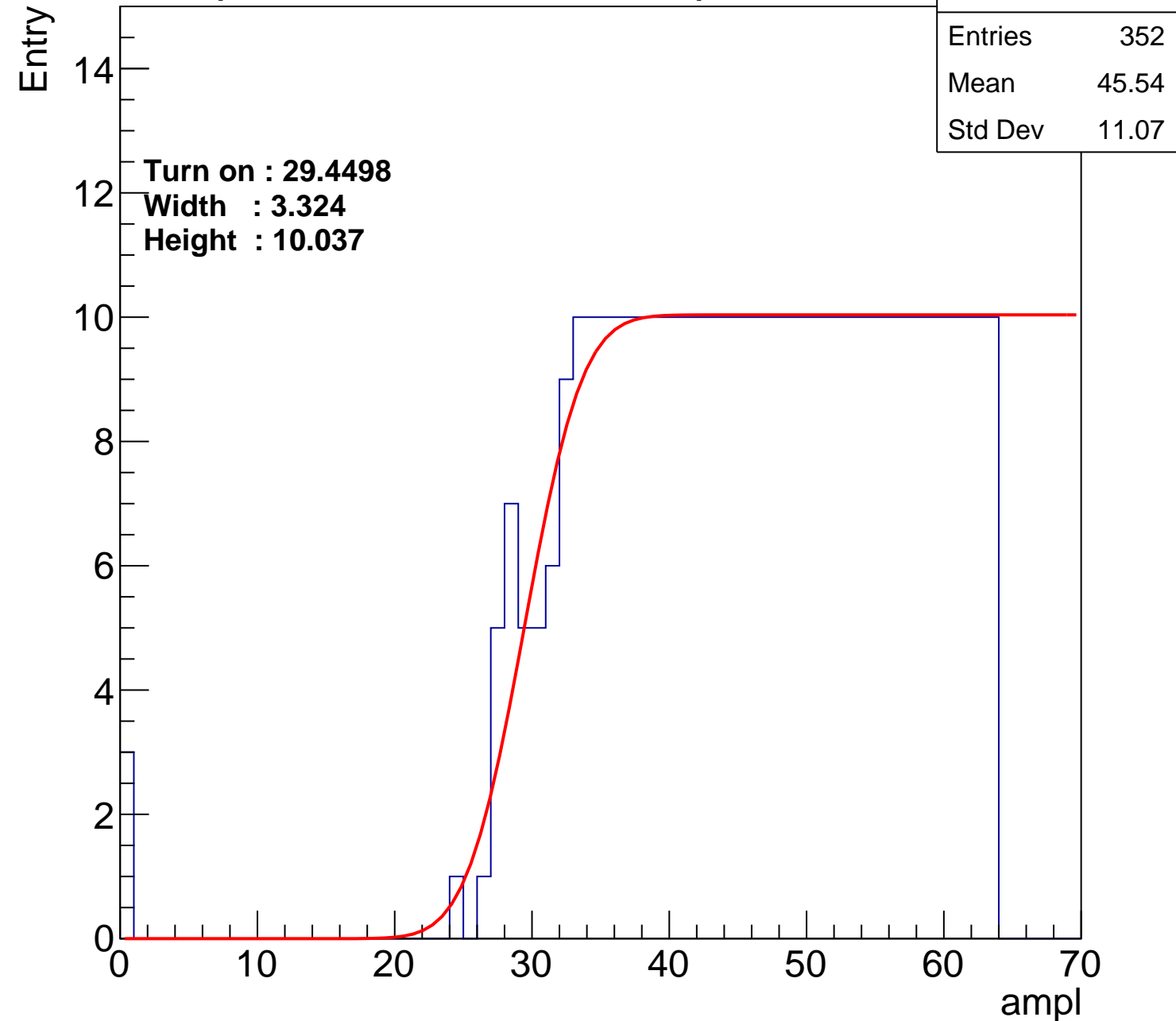
Width : 3.324

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch99

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.33
Std Dev	11.18

Turn on : 29.0192

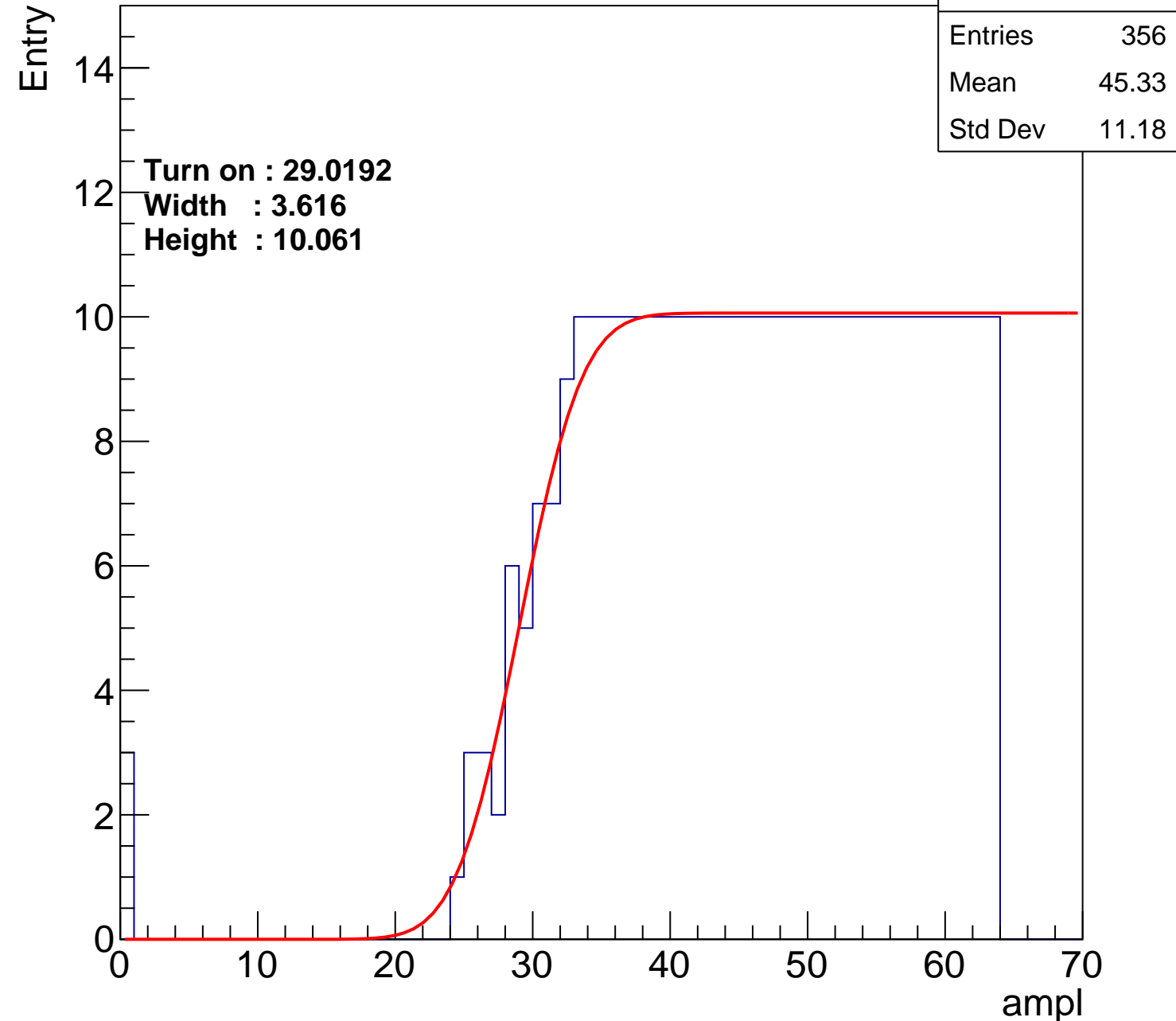
Width : 3.616

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch100

calib_packv5_042523_0143.root, FC#5, port B1

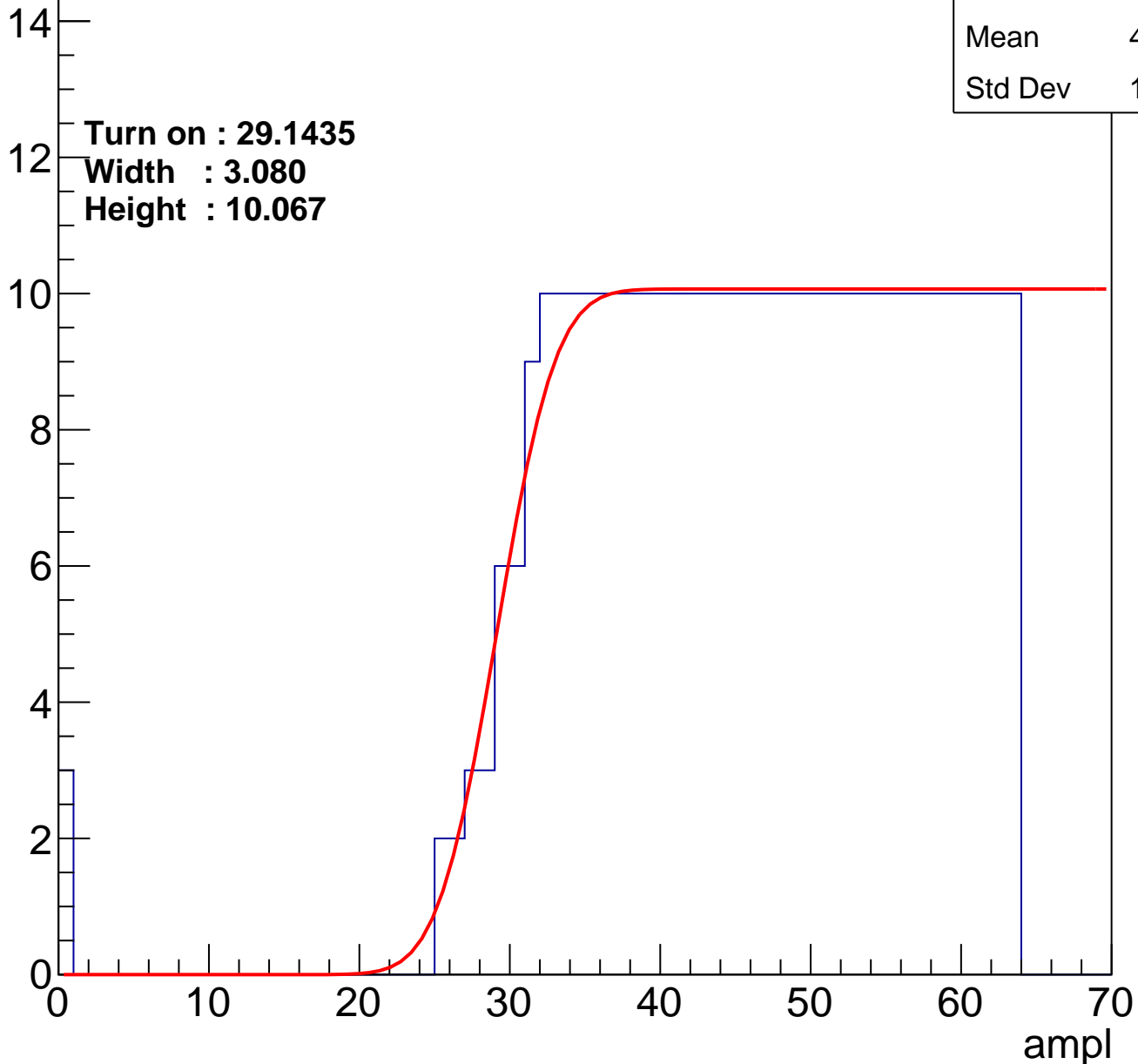
Entries	354
Mean	45.48
Std Dev	11.06

Turn on : 29.1435

Width : 3.080

Height : 10.067

Entry



B0L000S, U9-ch101

calib_packv5_042523_0143.root, FC#5, port B1

Entries	399
Mean	43.21
Std Dev	12.25

Turn on : 24.6508

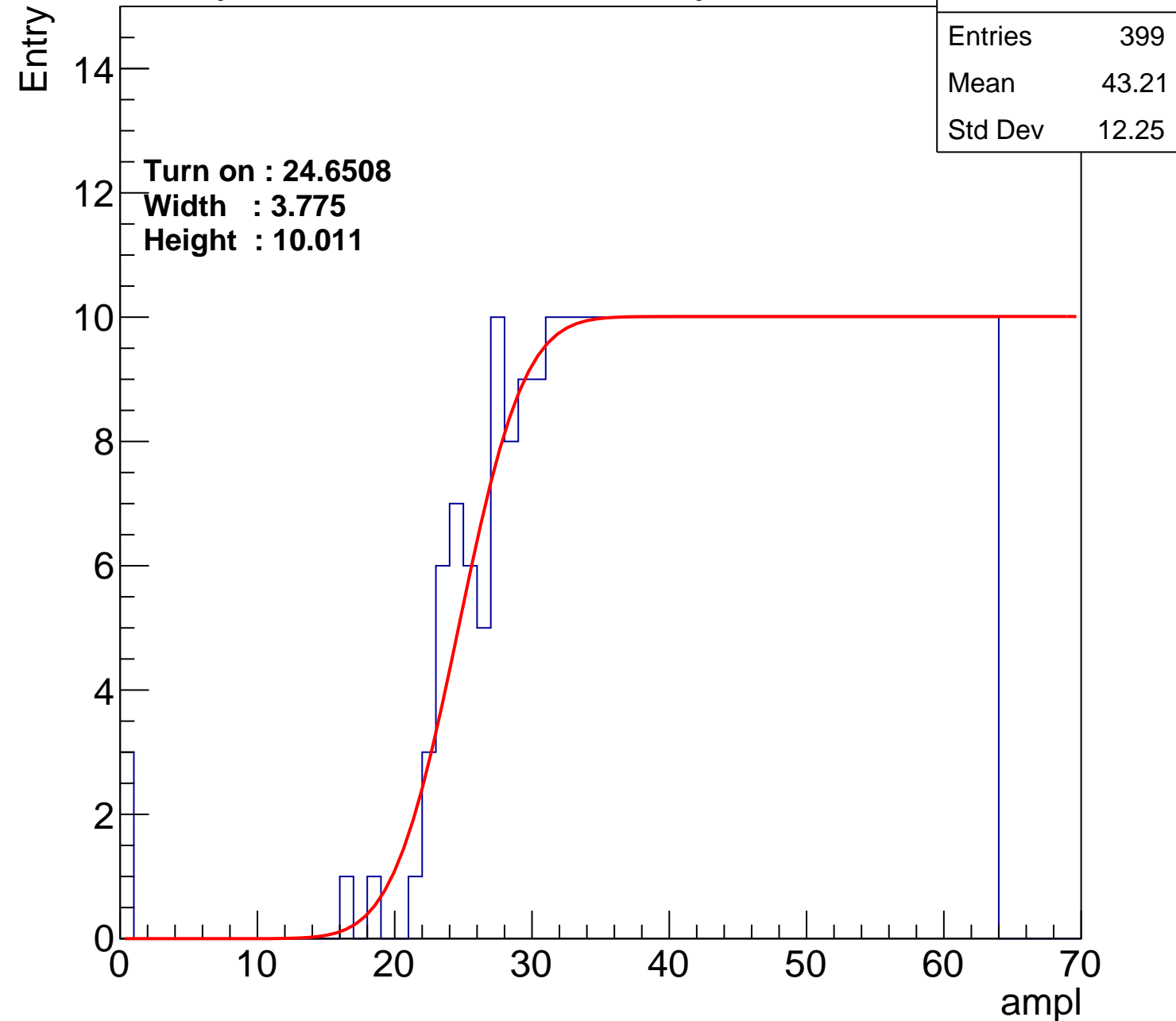
Width : 3.775

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch102

calib_packv5_042523_0143.root, FC#5, port B1

Entries	378
Mean	44.31
Std Dev	11.63

Turn on : 27.1721

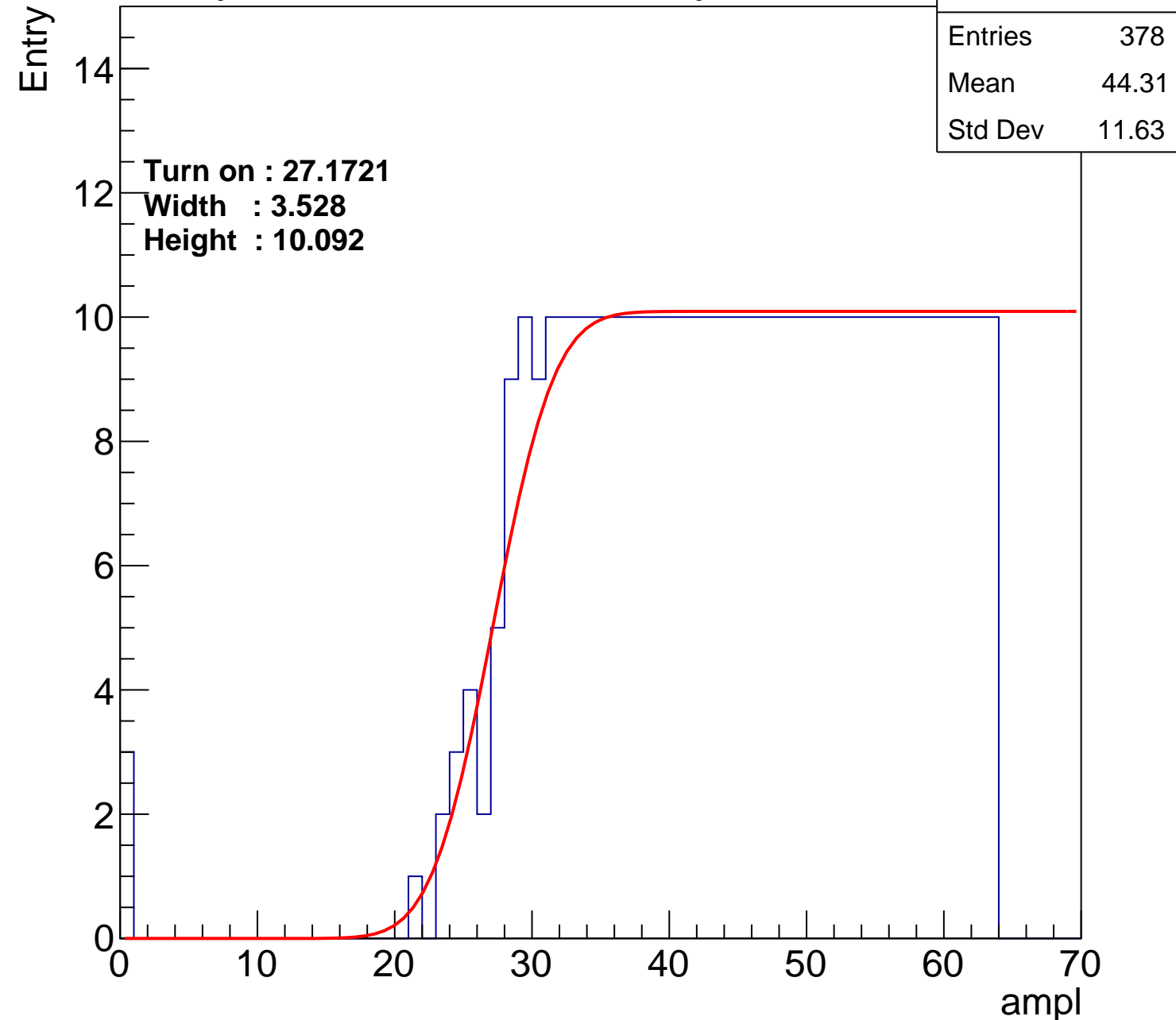
Width : 3.528

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch103

calib_packv5_042523_0143.root, FC#5, port B1

Entries	372
Mean	44.48
Std Dev	11.74

Turn on : 27.2298

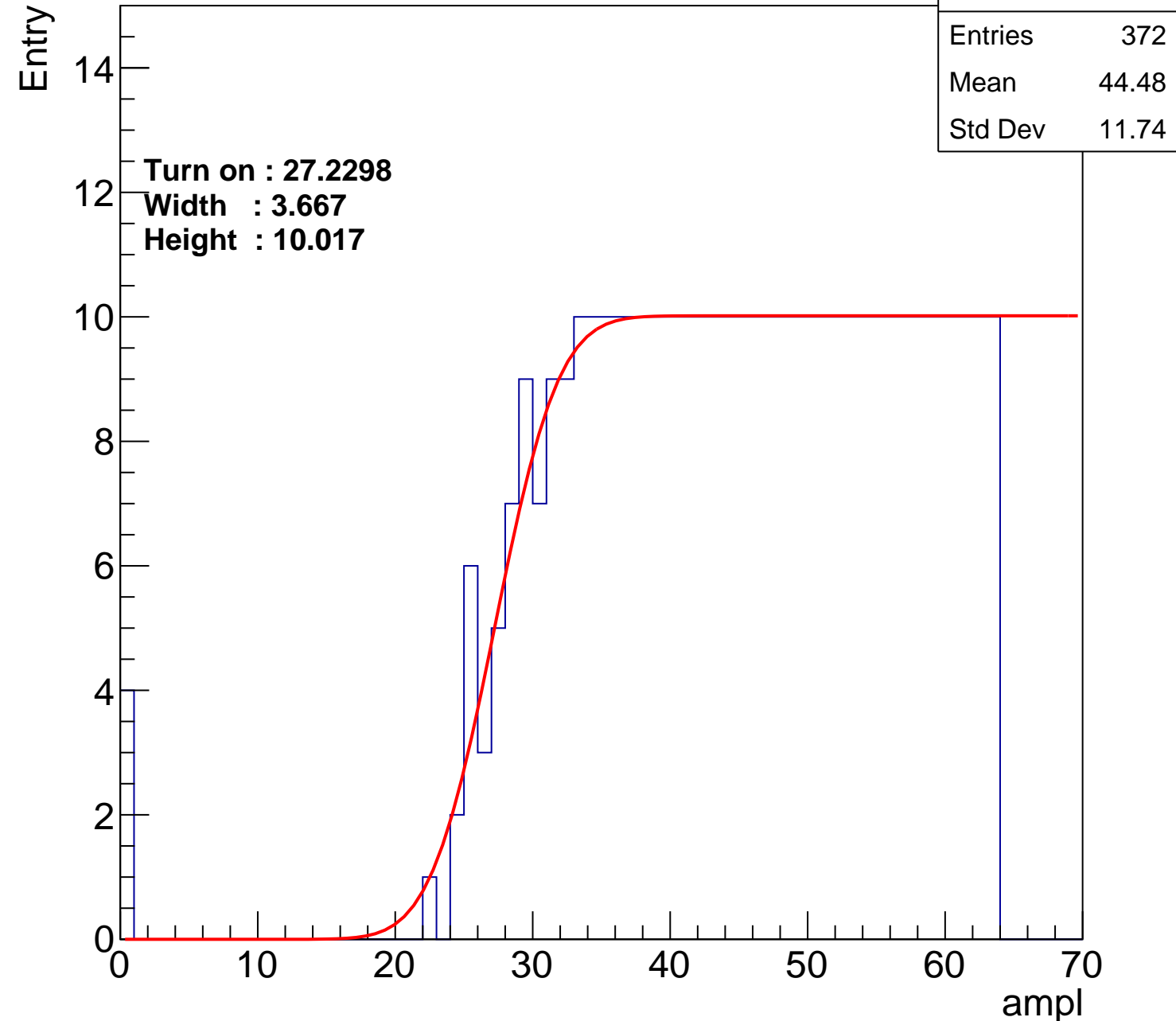
Width : 3.667

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch104

calib_packv5_042523_0143.root, FC#5, port B1

Entries	368
Mean	44.93
Std Dev	10.99

Turn on : 27.1273

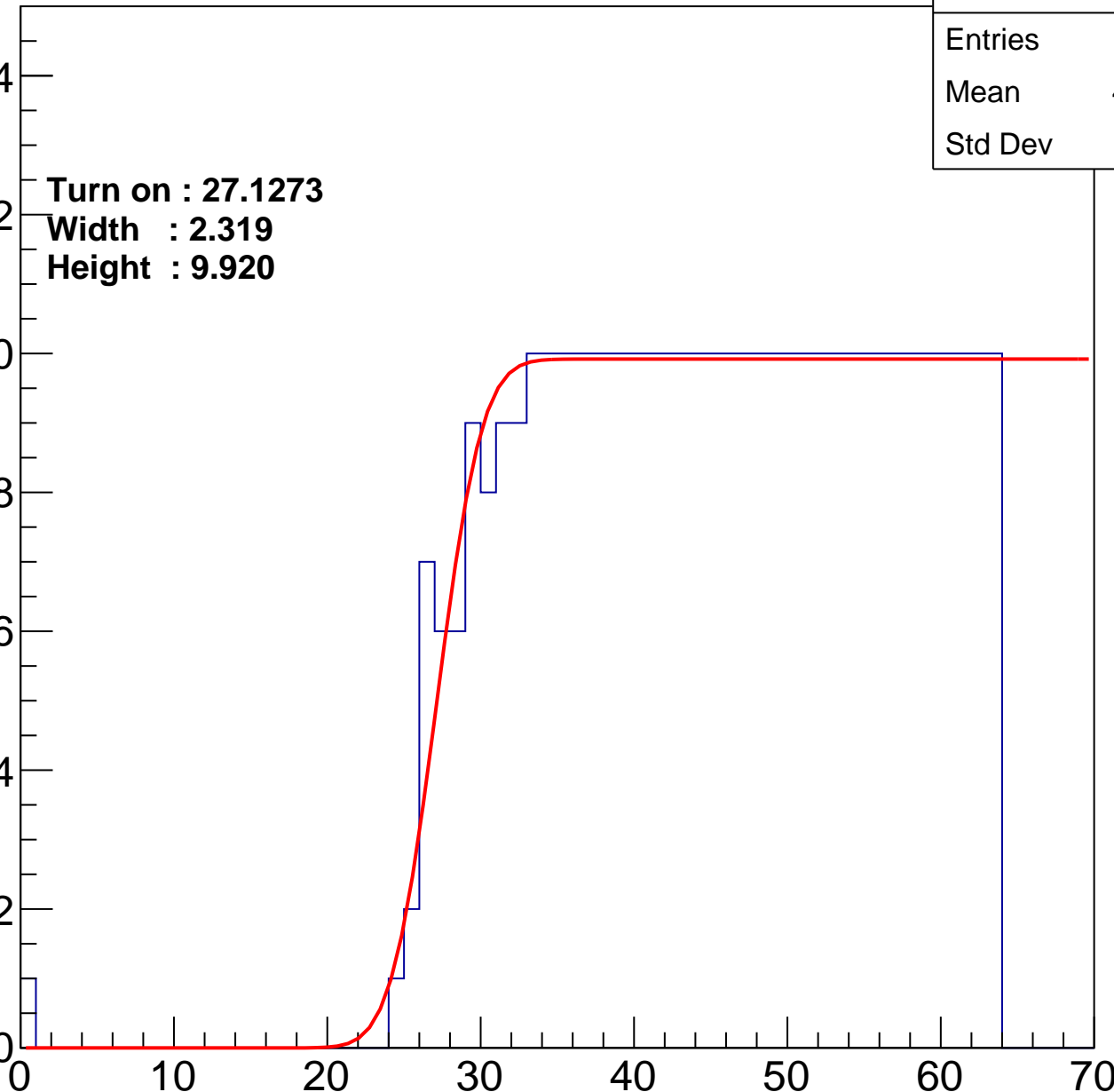
Width : 2.319

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch105

calib_packv5_042523_0143.root, FC#5, port B1

Entries	364
Mean	45.07
Std Dev	10.97

Turn on : 27.6102

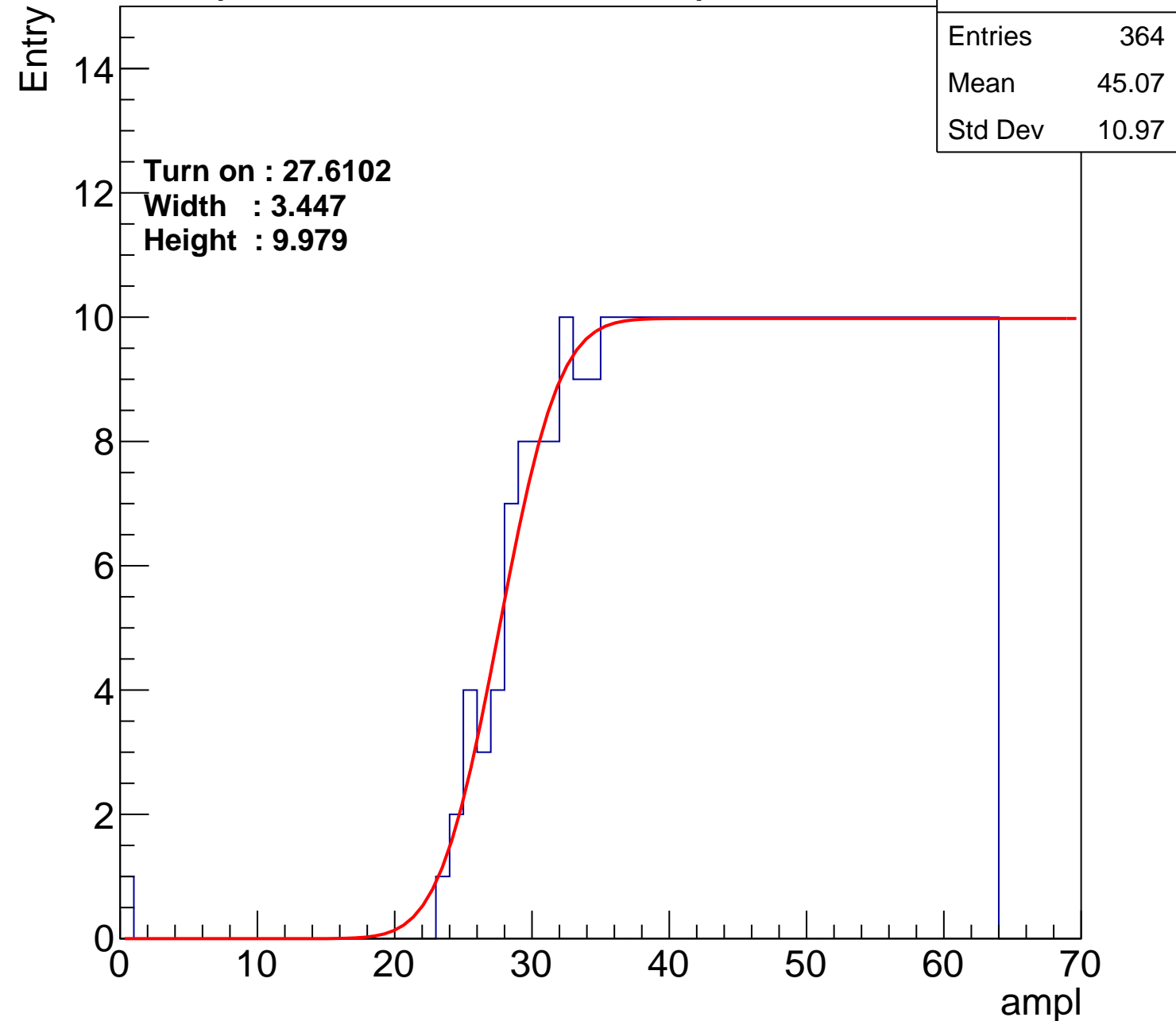
Width : 3.447

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch106

calib_packv5_042523_0143.root, FC#5, port B1

Entries	381
Mean	44.21
Std Dev	11.55

Turn on : 26.4679

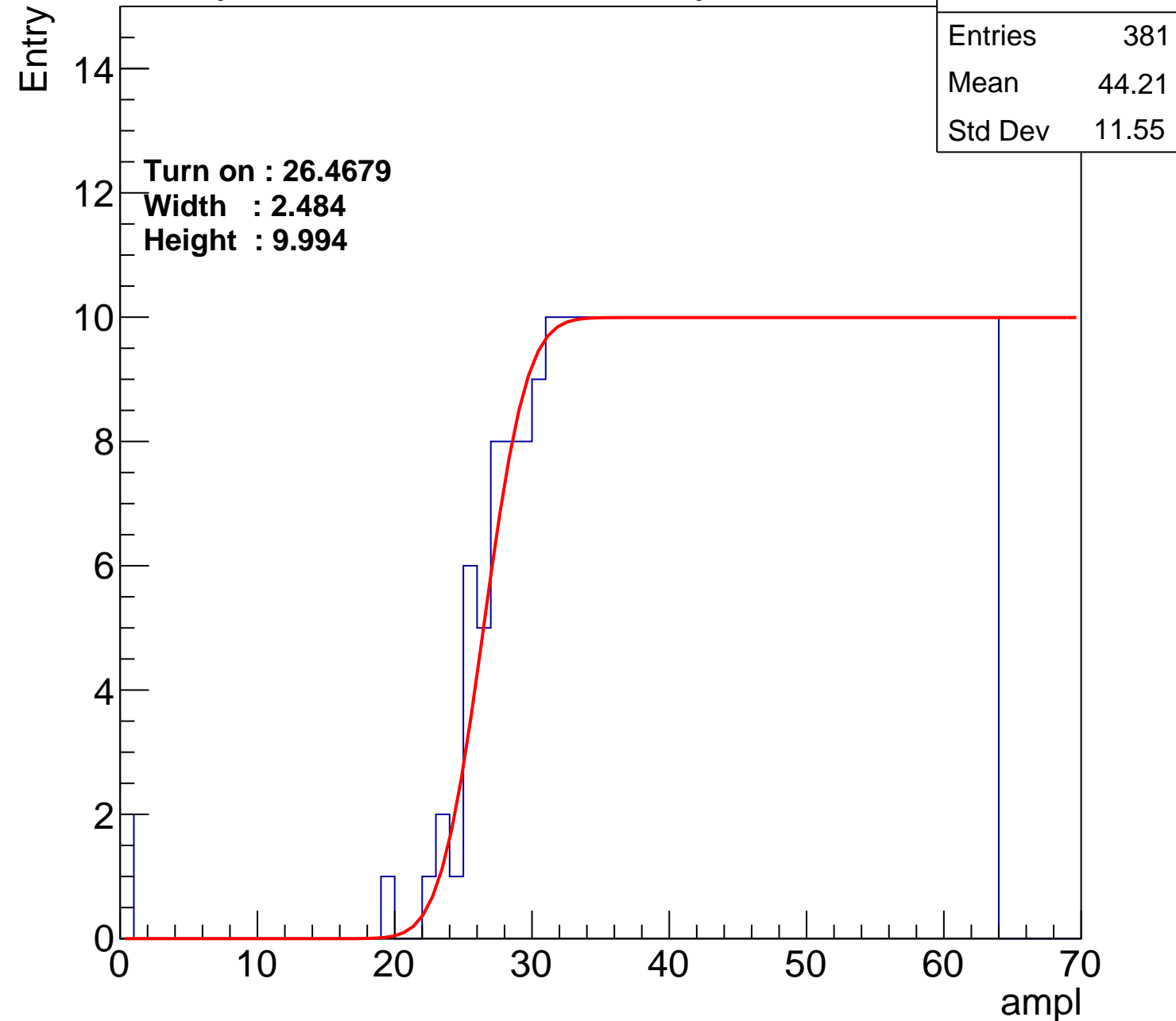
Width : 2.484

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch107

calib_packv5_042523_0143.root, FC#5, port B1

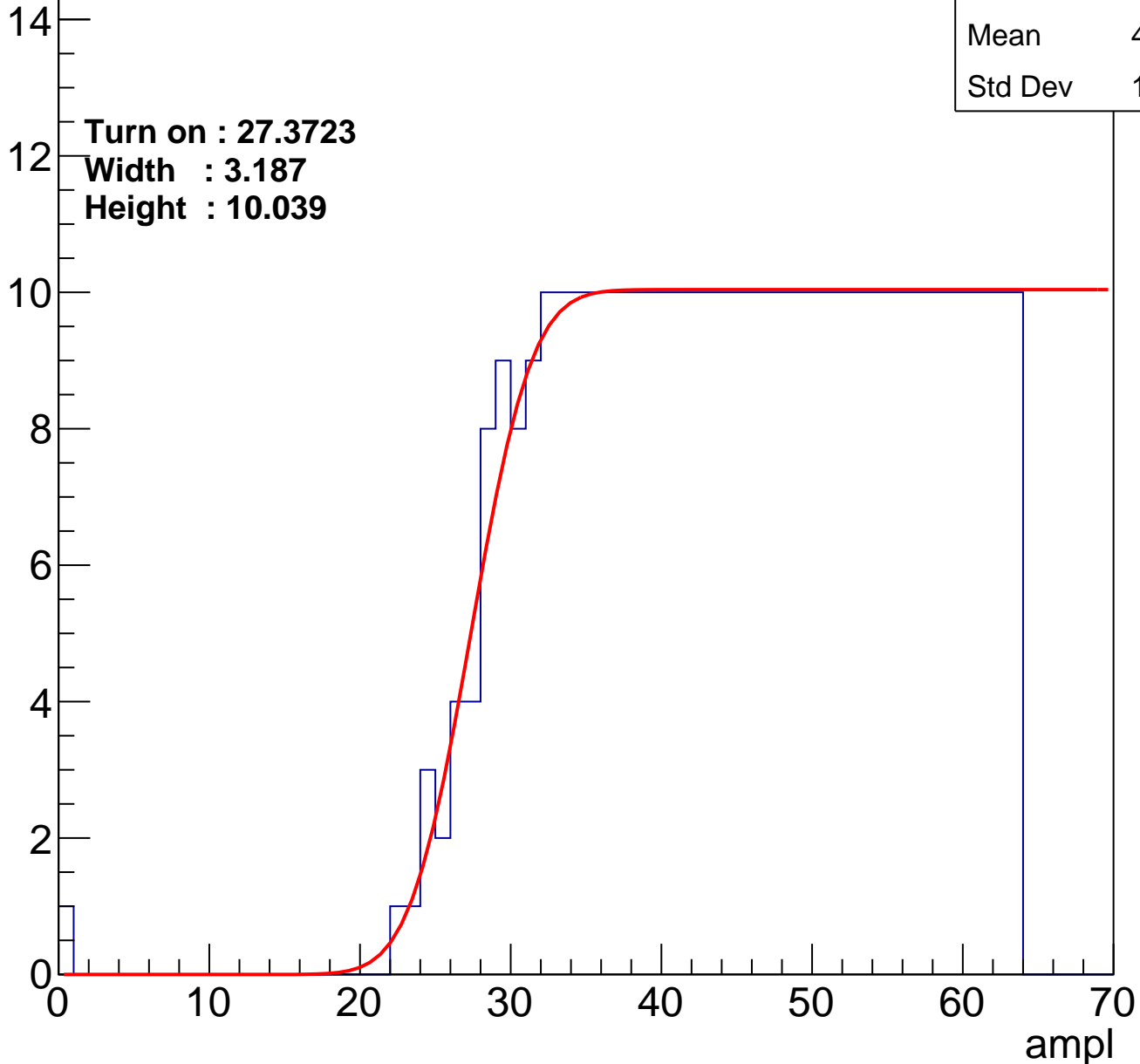
Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.3723

Width : 3.187

Height : 10.039

Entry



B0L000S, U9-ch108

calib_packv5_042523_0143.root, FC#5, port B1

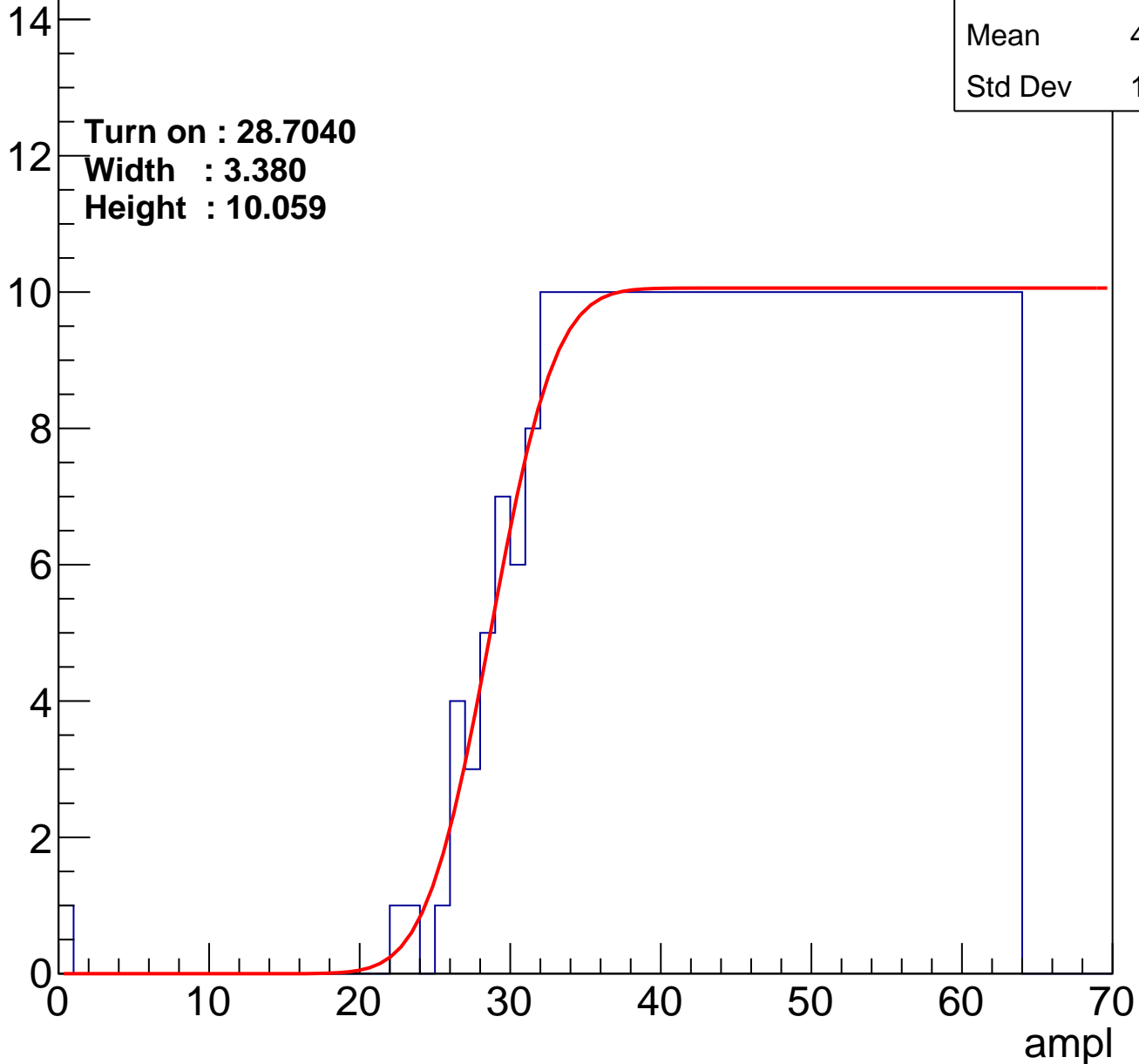
Entries	357
Mean	45.45
Std Dev	10.74

Turn on : 28.7040

Width : 3.380

Height : 10.059

Entry



B0L000S, U9-ch109

calib_packv5_042523_0143.root, FC#5, port B1

Entries	380
Mean	44.33
Std Dev	11.34

Turn on : 26.4483

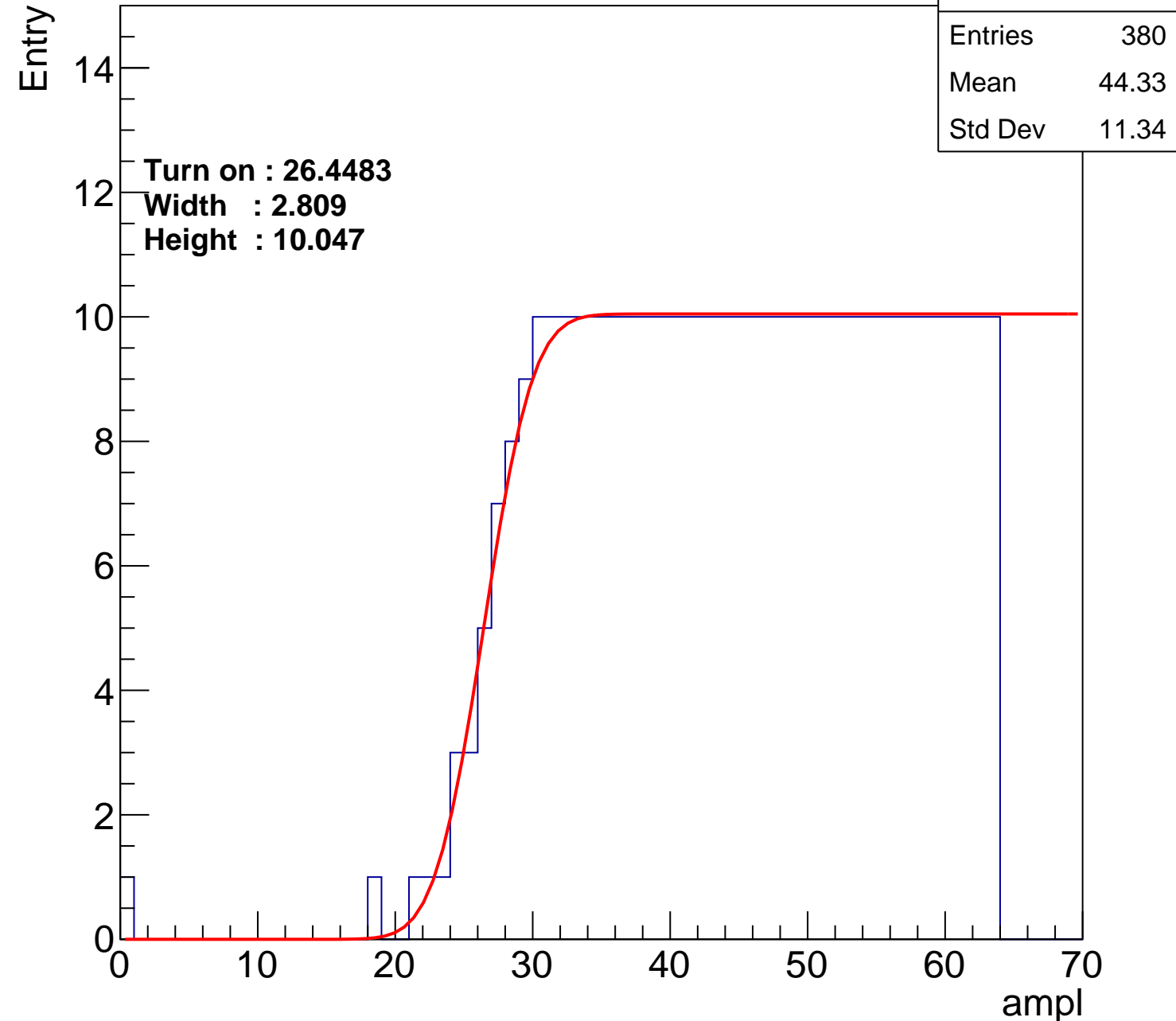
Width : 2.809

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch110

calib_packv5_042523_0143.root, FC#5, port B1

Entries	389
Mean	43.82
Std Dev	11.82

Turn on : 25.5806

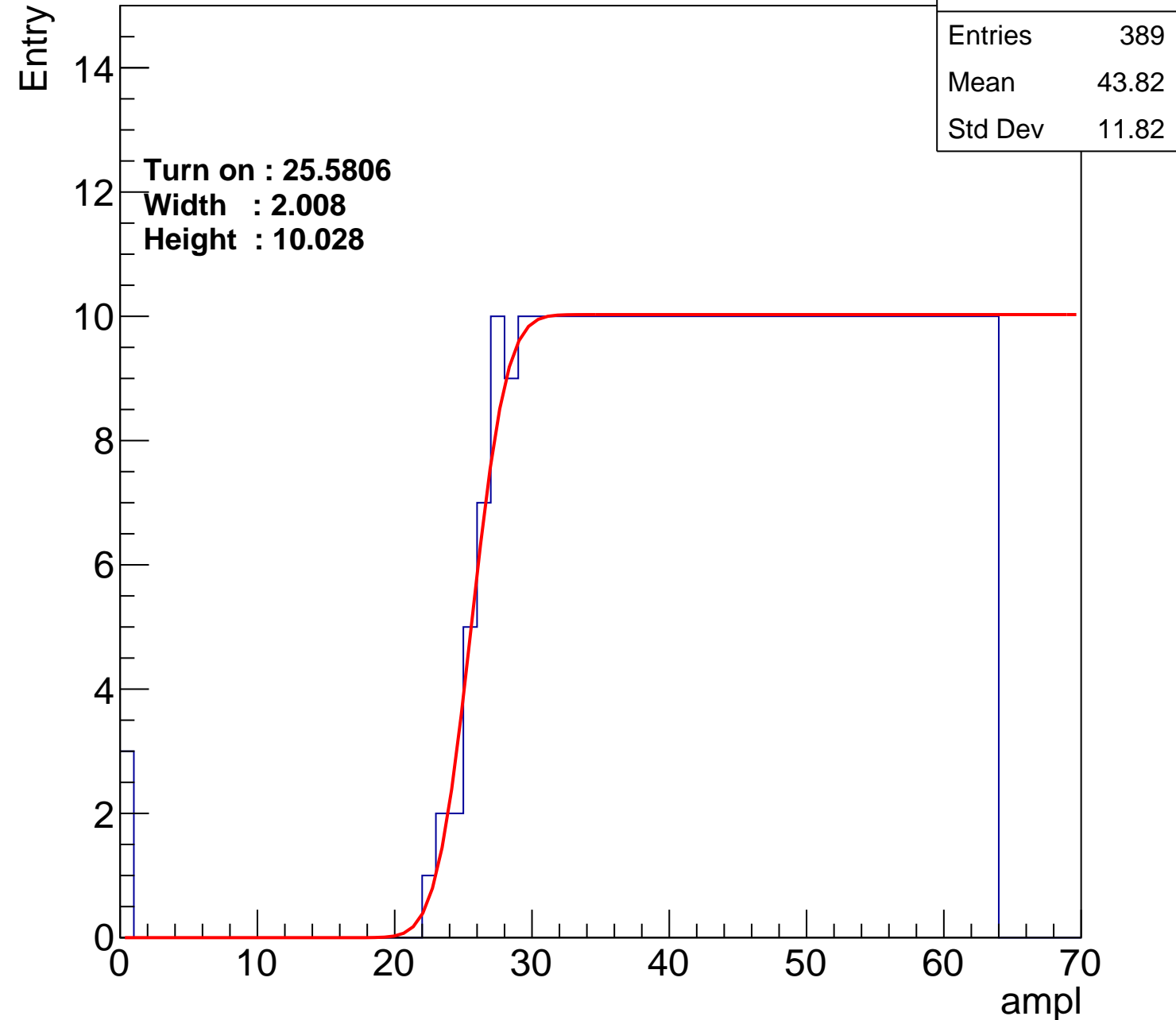
Width : 2.008

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch111

calib_packv5_042523_0143.root, FC#5, port B1

Entries	379
Mean	43.99
Std Dev	12.34

Turn on : 26.5663

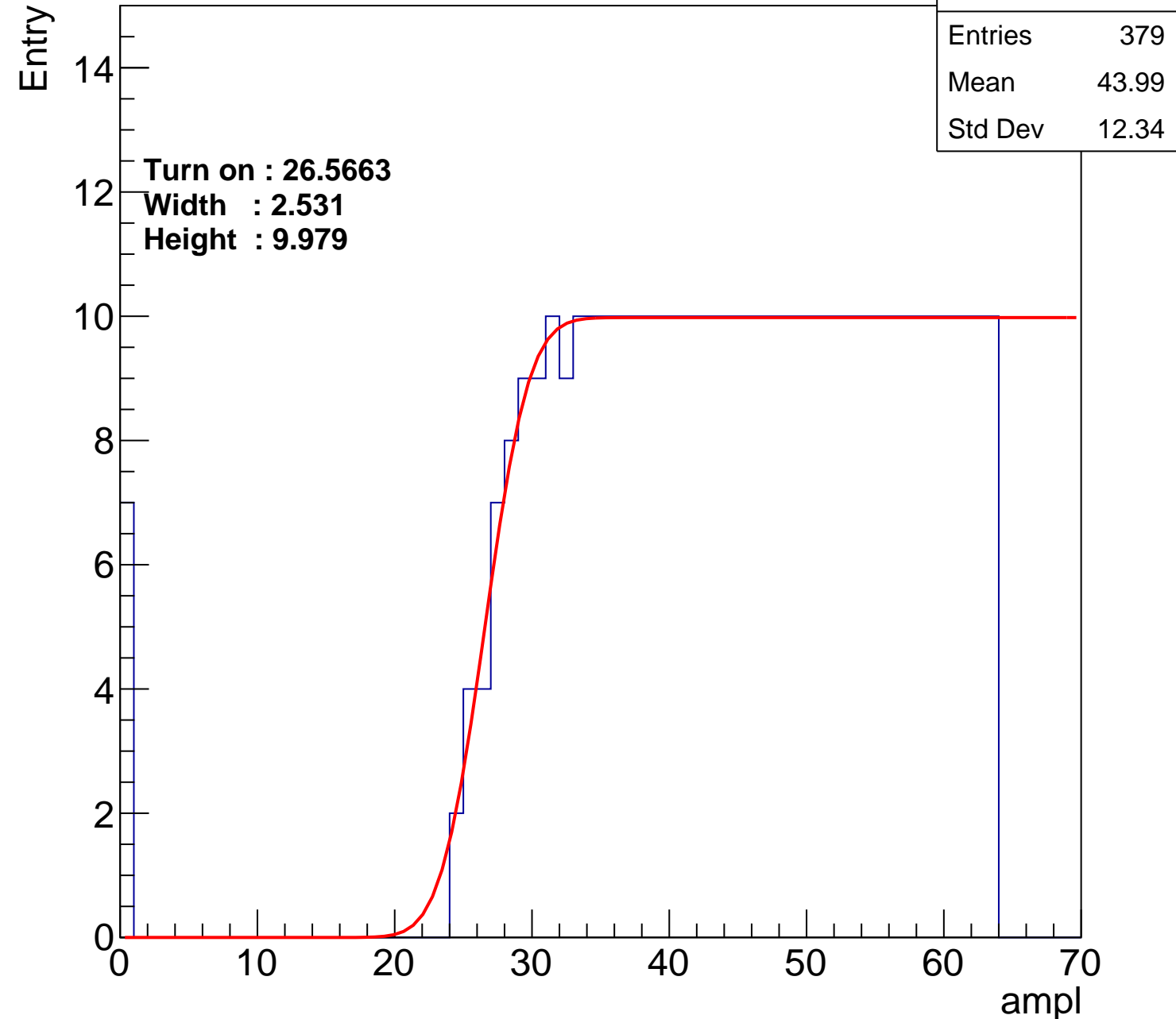
Width : 2.531

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch112

calib_packv5_042523_0143.root, FC#5, port B1

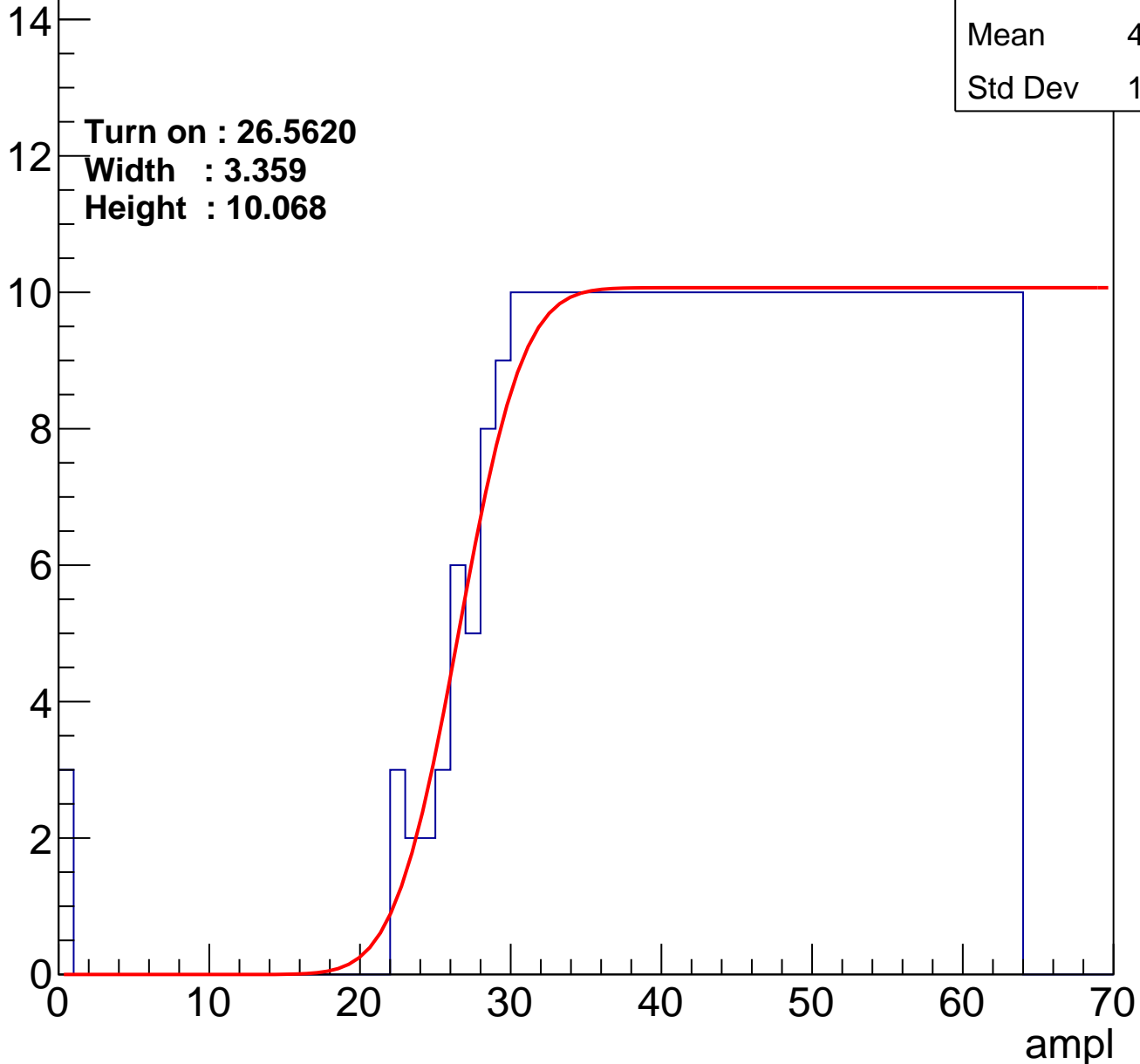
Entries	381
Mean	44.15
Std Dev	11.72

Turn on : 26.5620

Width : 3.359

Height : 10.068

Entry



B0L000S, U9-ch113

calib_packv5_042523_0143.root, FC#5, port B1

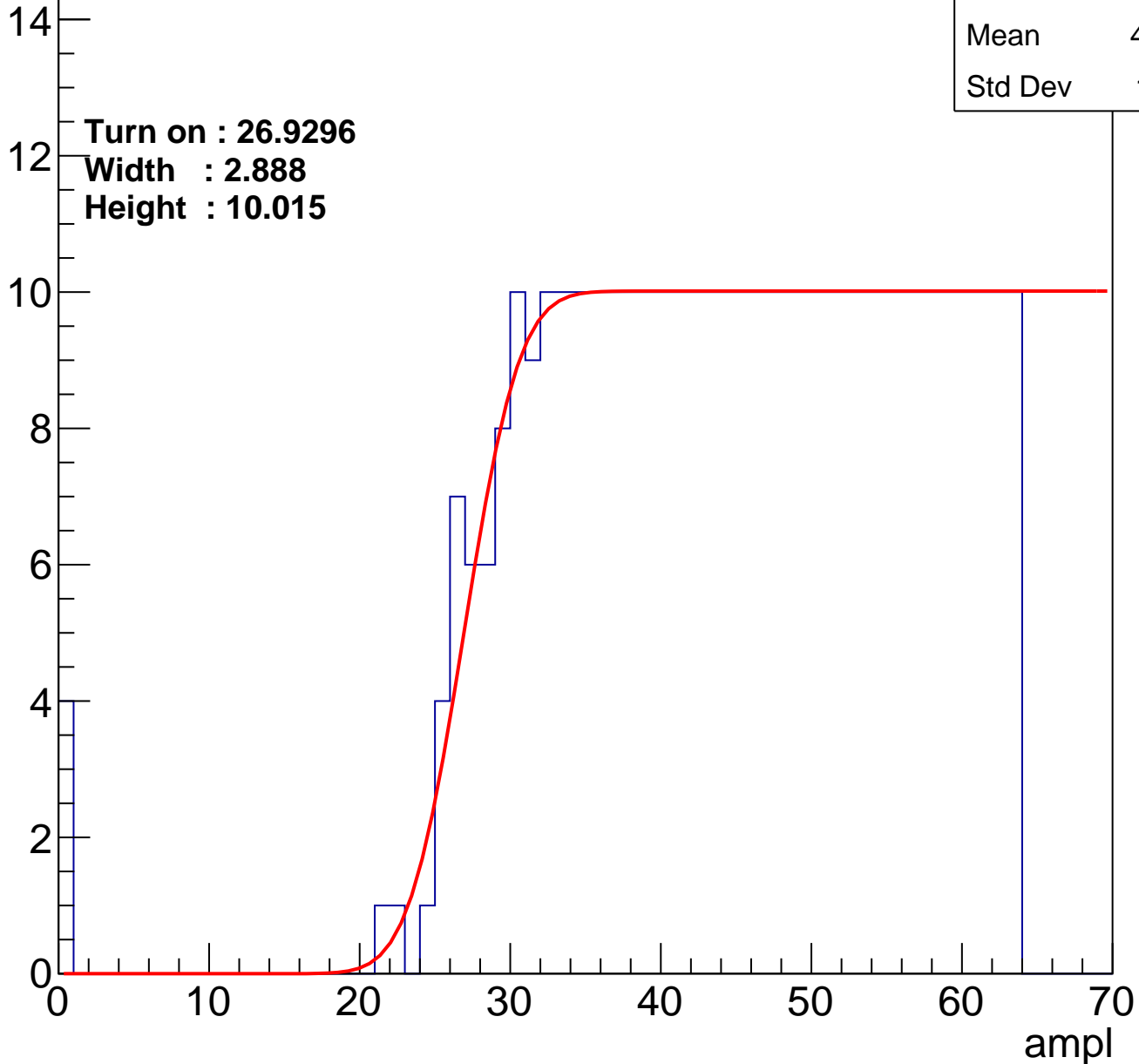
Entries	377
Mean	44.27
Std Dev	11.81

Turn on : 26.9296

Width : 2.888

Height : 10.015

Entry



B0L000S, U9-ch114

calib_packv5_042523_0143.root, FC#5, port B1

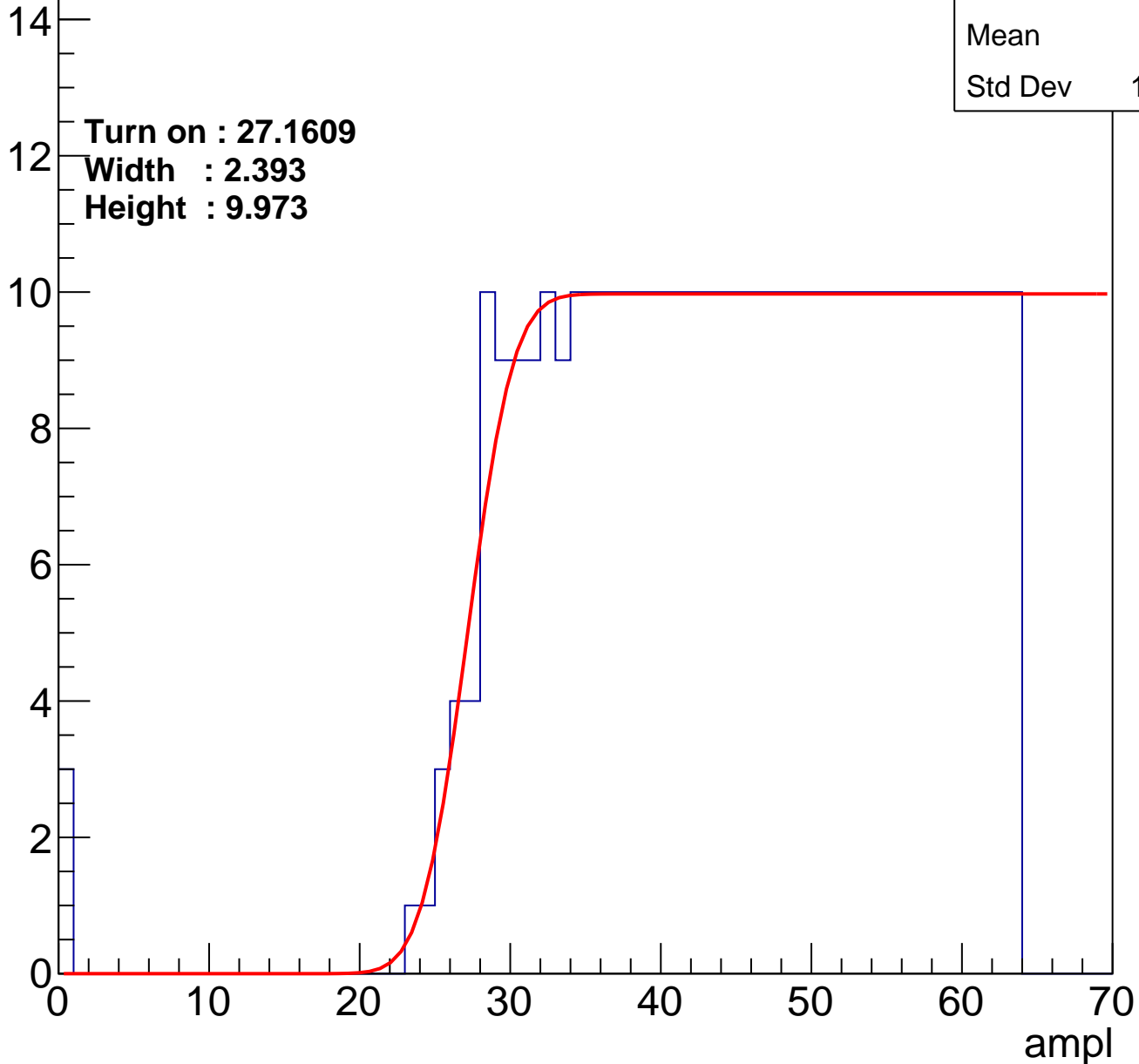
Entries	372
Mean	44.6
Std Dev	11.48

Turn on : 27.1609

Width : 2.393

Height : 9.973

Entry



B0L000S, U9-ch115

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.75
Std Dev	11.59

Turn on : 27.4977

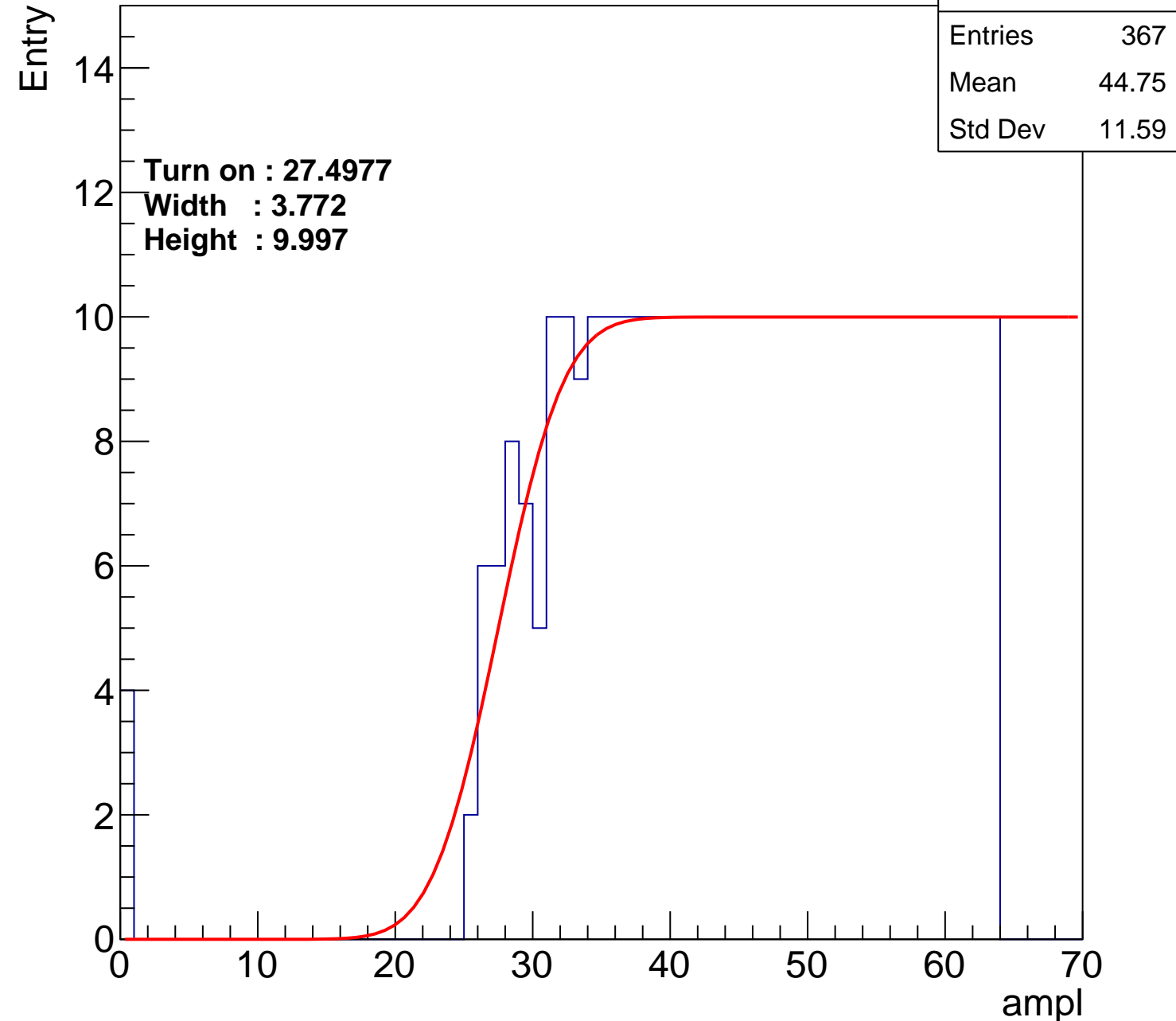
Width : 3.772

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch116

calib_packv5_042523_0143.root, FC#5, port B1

Entries	363
Mean	45.04
Std Dev	11.26

Turn on : 27.8114

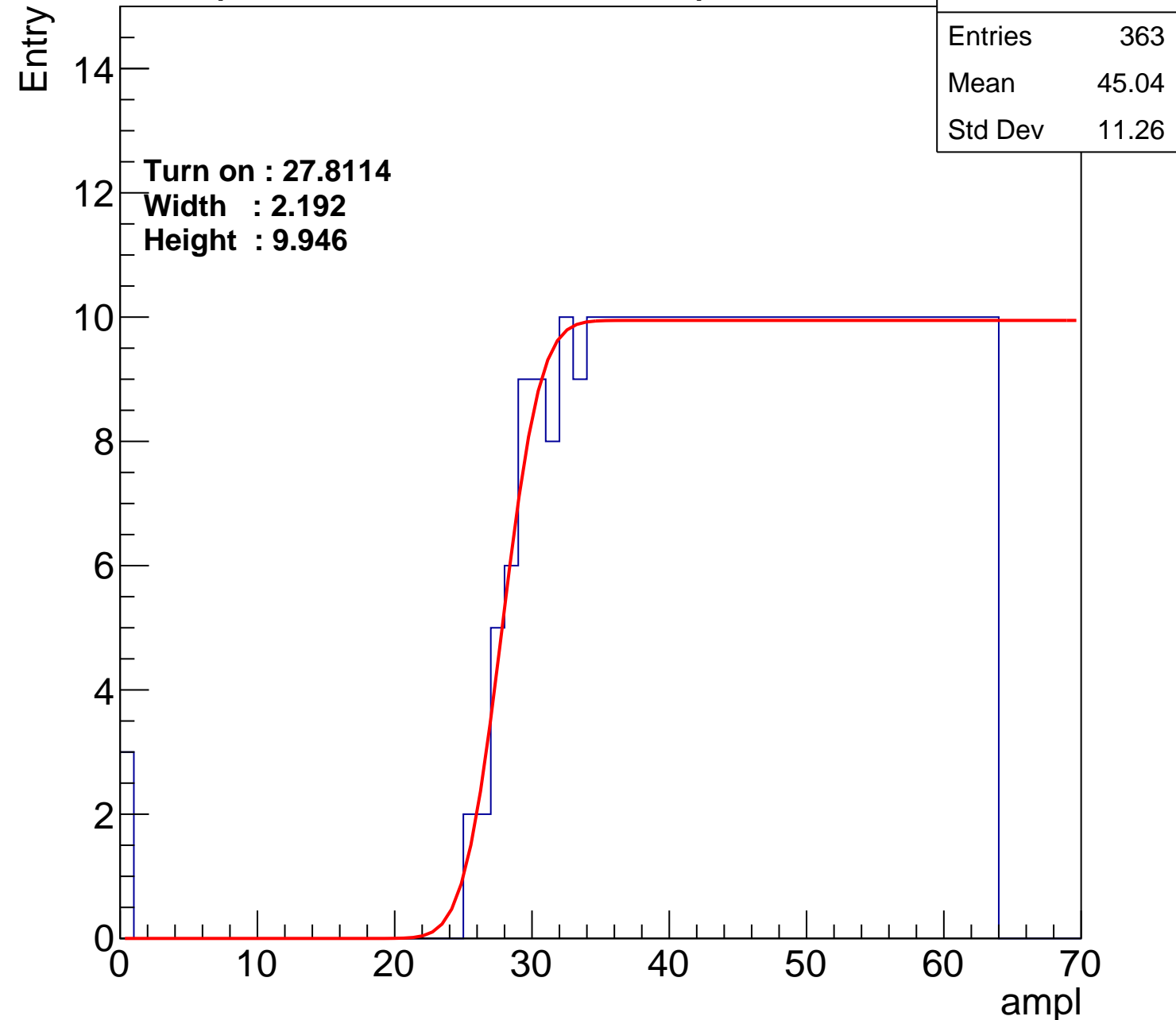
Width : 2.192

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch117

calib_packv5_042523_0143.root, FC#5, port B1

Entries	349
Mean	45.78
Std Dev	10.74

Turn on : 29.4148

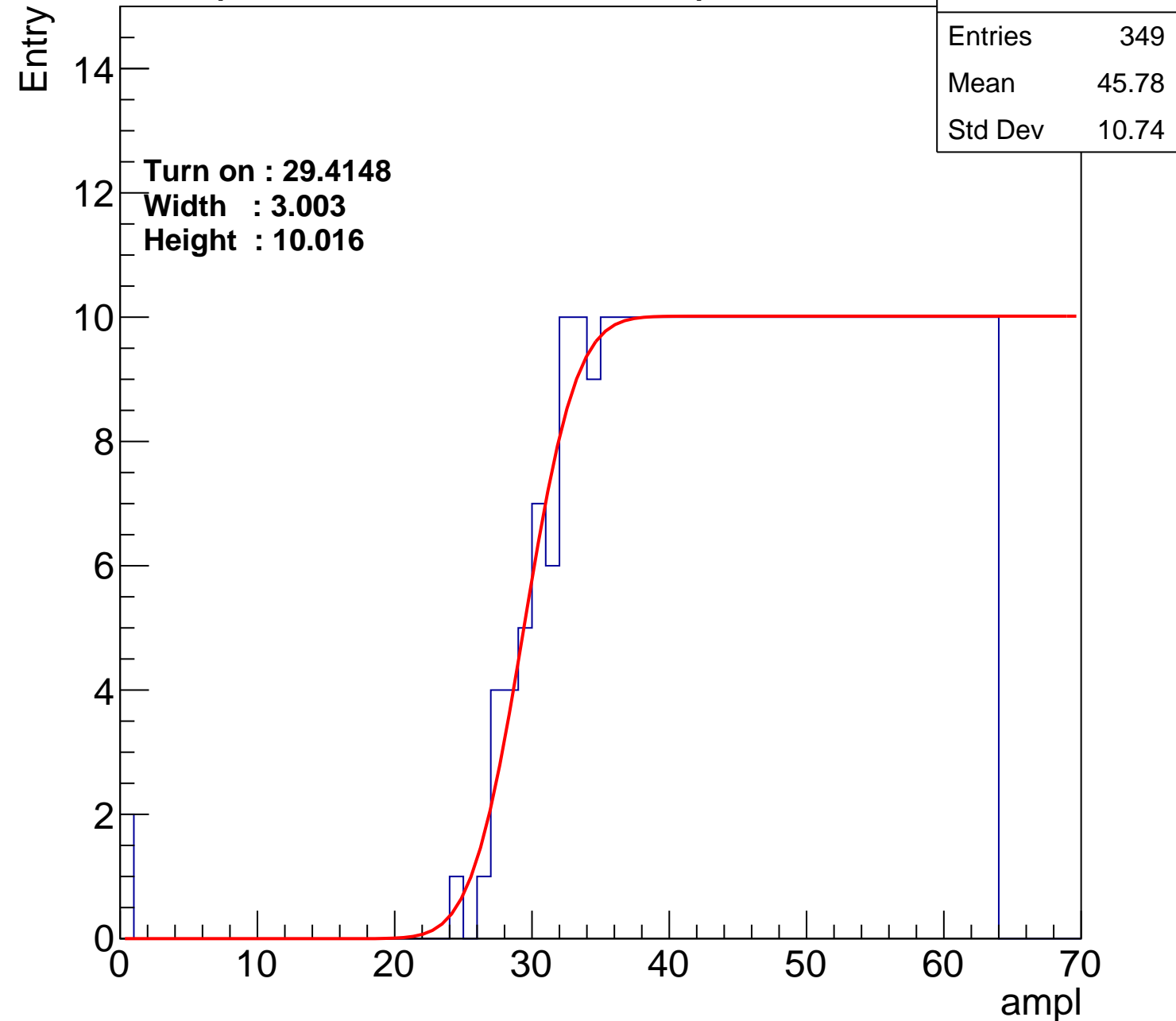
Width : 3.003

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch118

calib_packv5_042523_0143.root, FC#5, port B1

Entries	366
Mean	44.67
Std Dev	11.86

Turn on : 28.0044

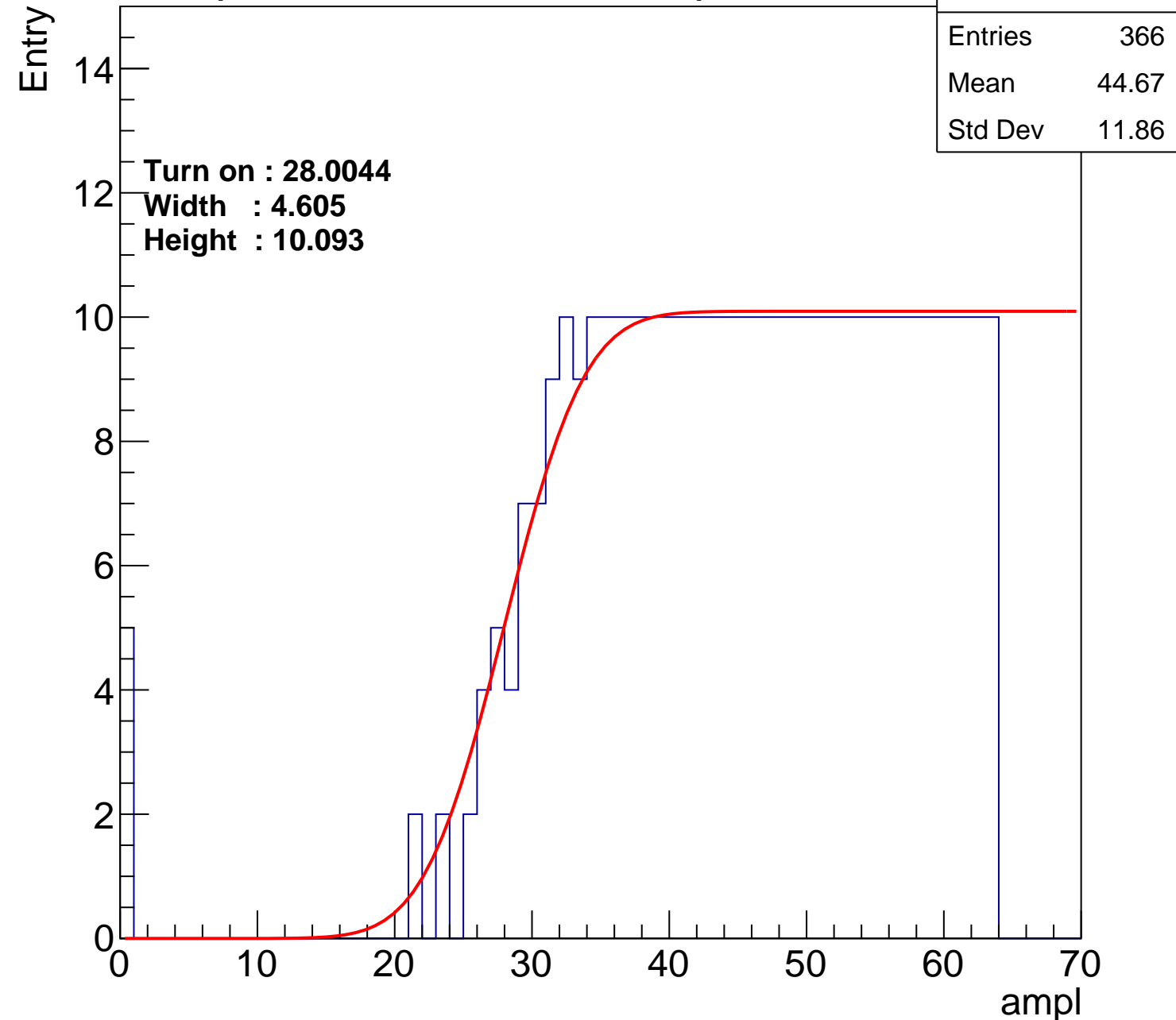
Width : 4.605

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch119

calib_packv5_042523_0143.root, FC#5, port B1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.5462

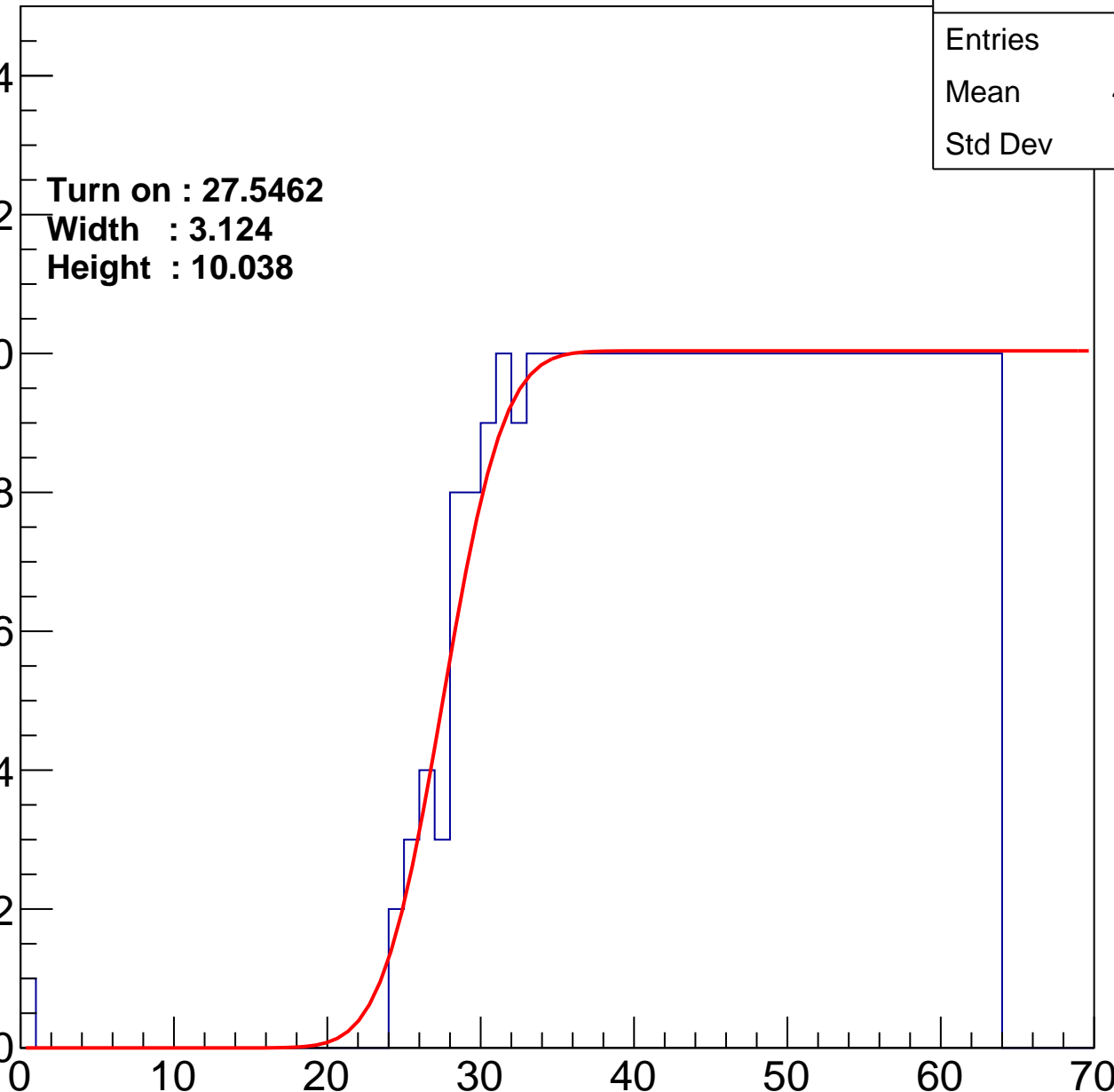
Width : 3.124

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch120

calib_packv5_042523_0143.root, FC#5, port B1

Entries	361
Mean	45.3
Std Dev	10.77

Turn on : 27.0607

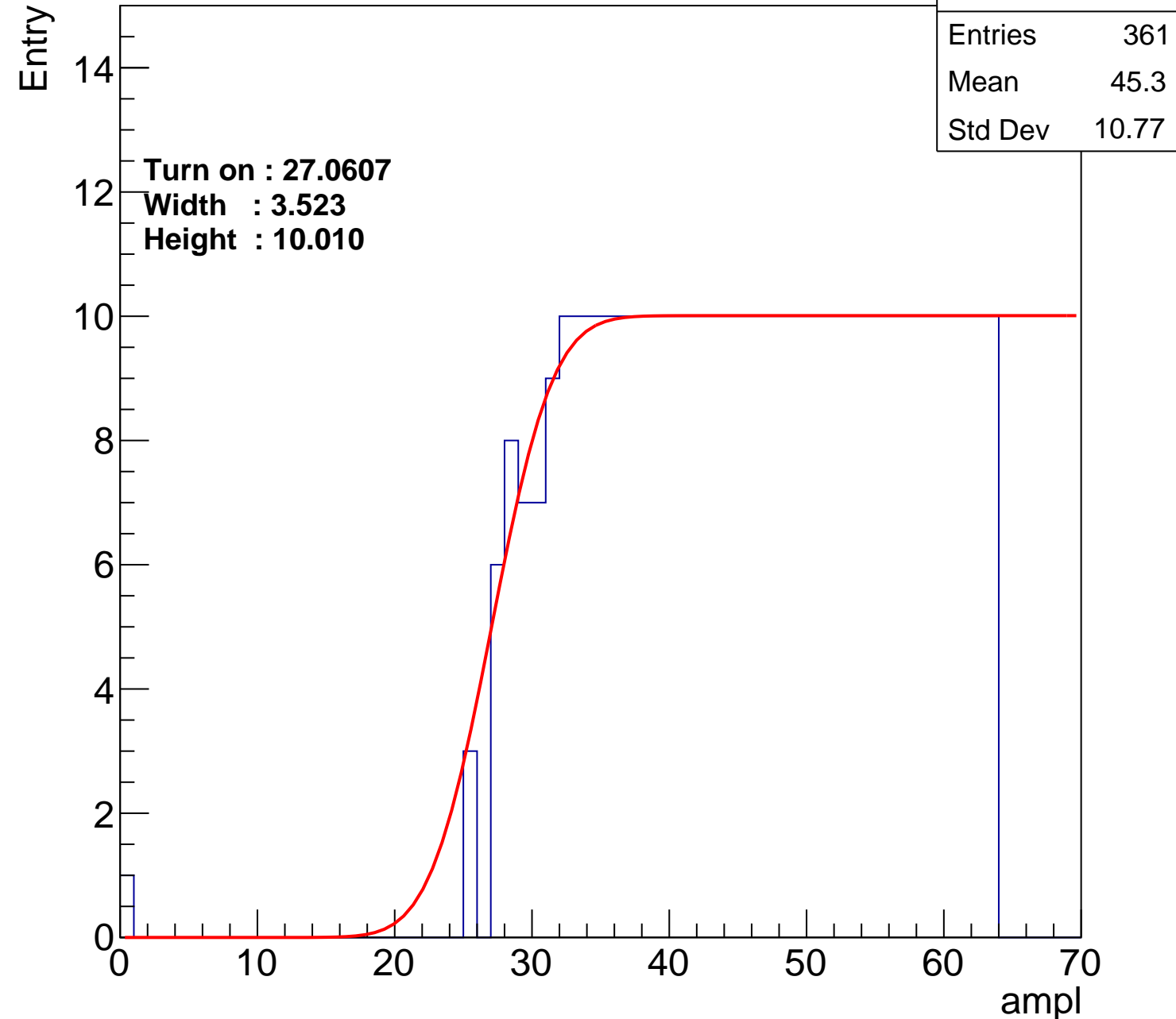
Width : 3.523

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch121

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 28.1469

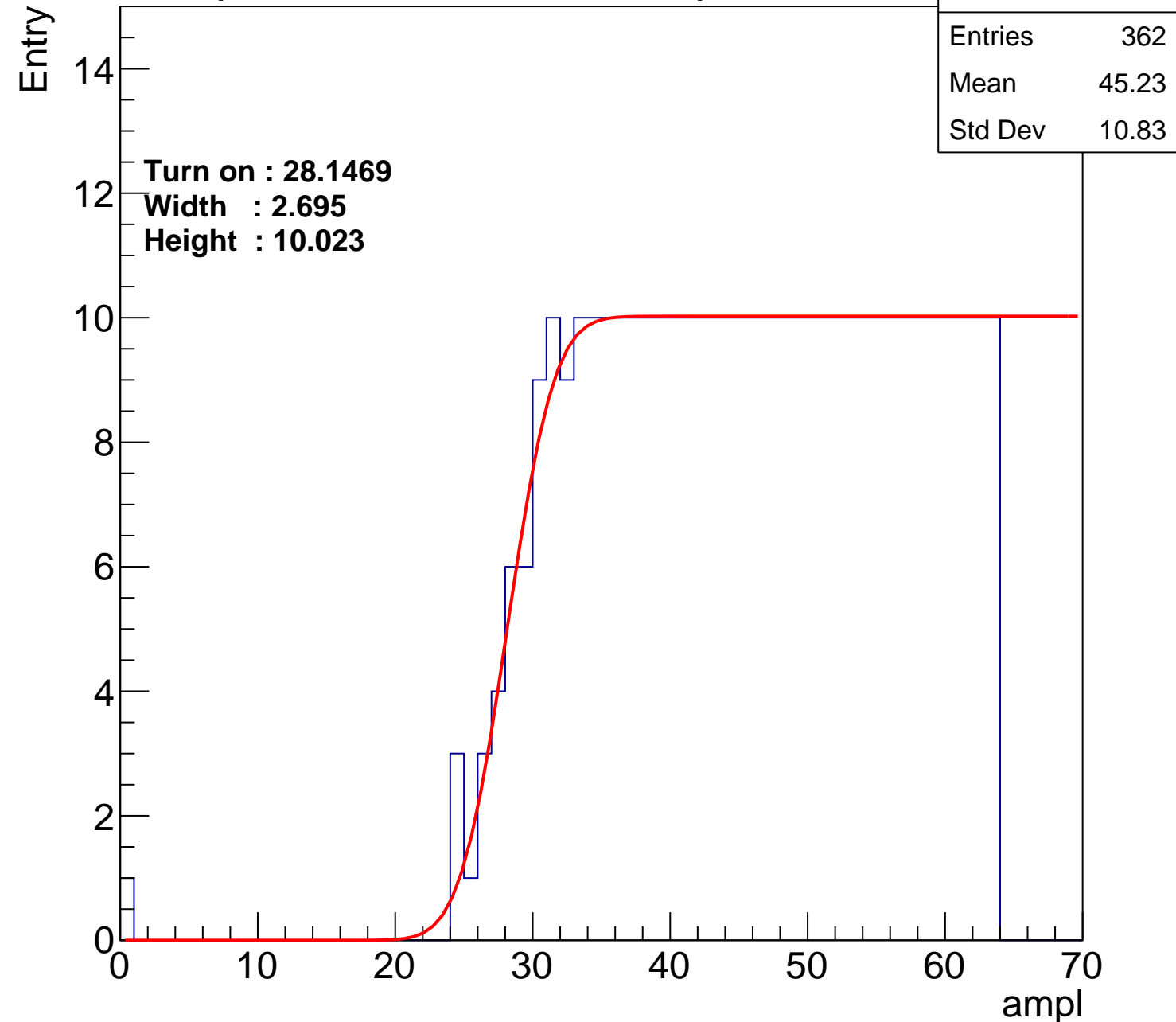
Width : 2.695

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch122

calib_packv5_042523_0143.root, FC#5, port B1

Entries	365
---------	-----

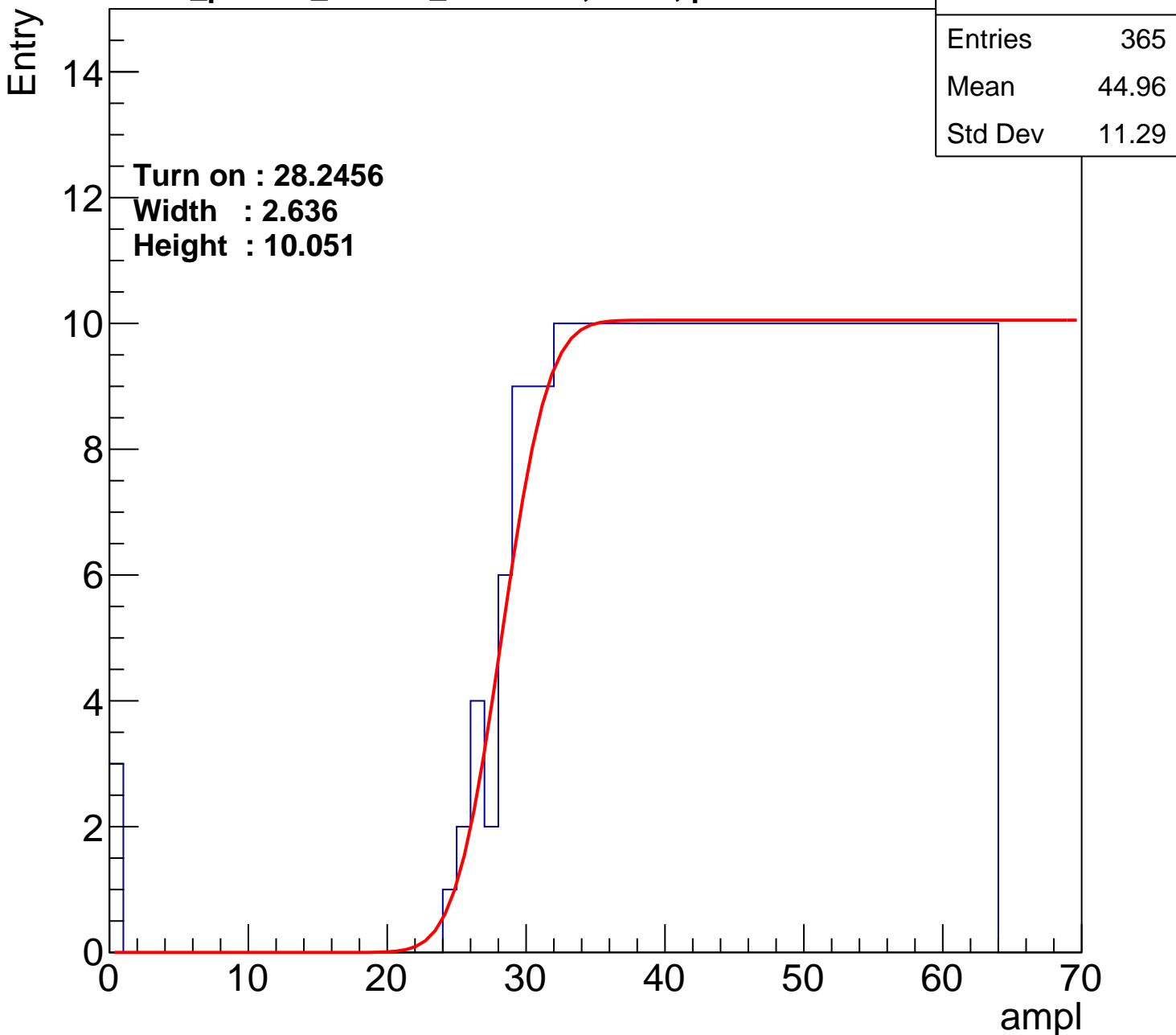
Mean	44.96
------	-------

Std Dev	11.29
---------	-------

Turn on : 28.2456

Width : 2.636

Height : 10.051



B0L000S, U9-ch123

calib_packv5_042523_0143.root, FC#5, port B1

Entries	362
Mean	45.2
Std Dev	10.89

Turn on : 27.9873

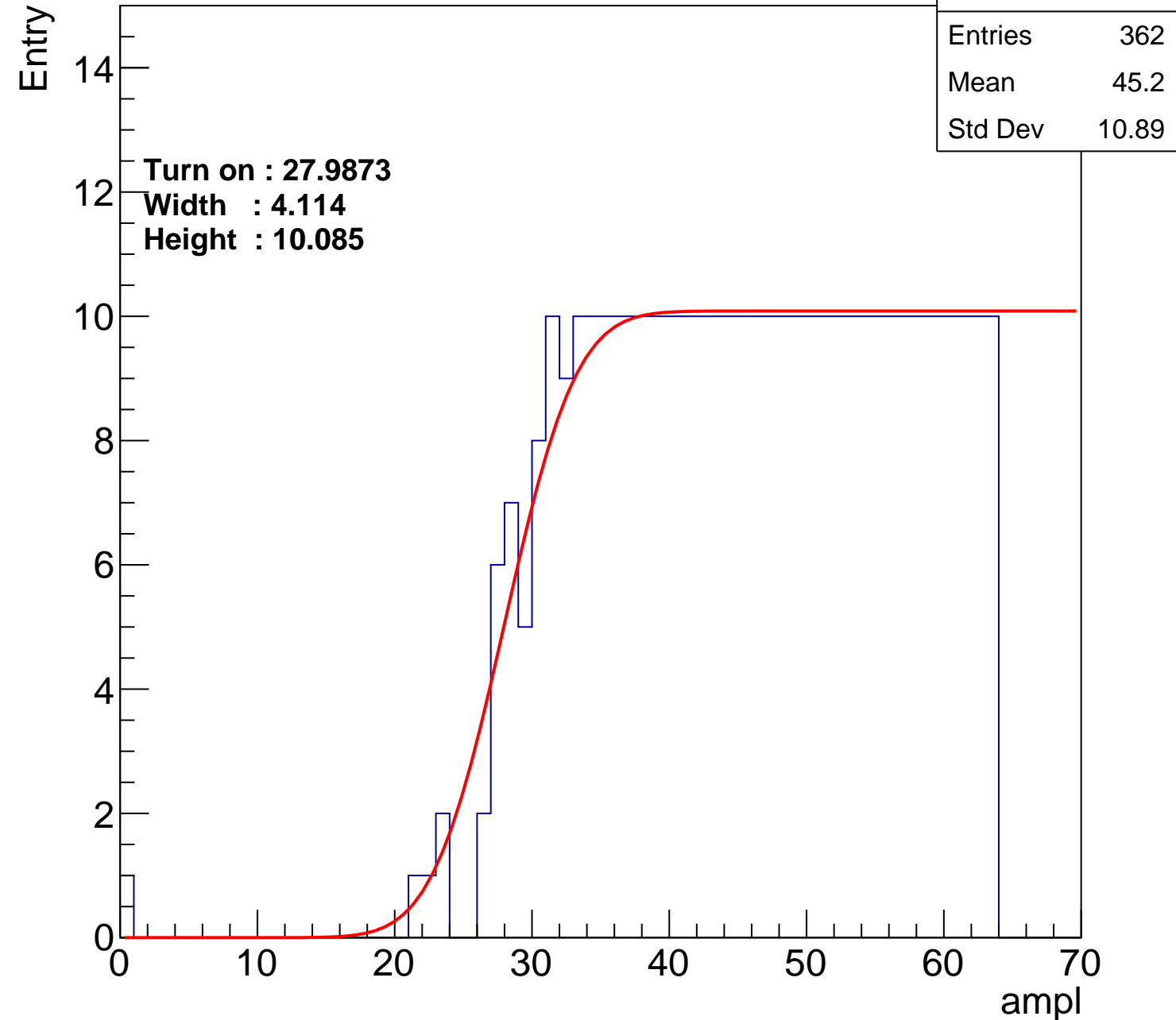
Width : 4.114

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch124

calib_packv5_042523_0143.root, FC#5, port B1

Entries	356
Mean	45.15
Std Dev	11.74

Turn on : 29.3380

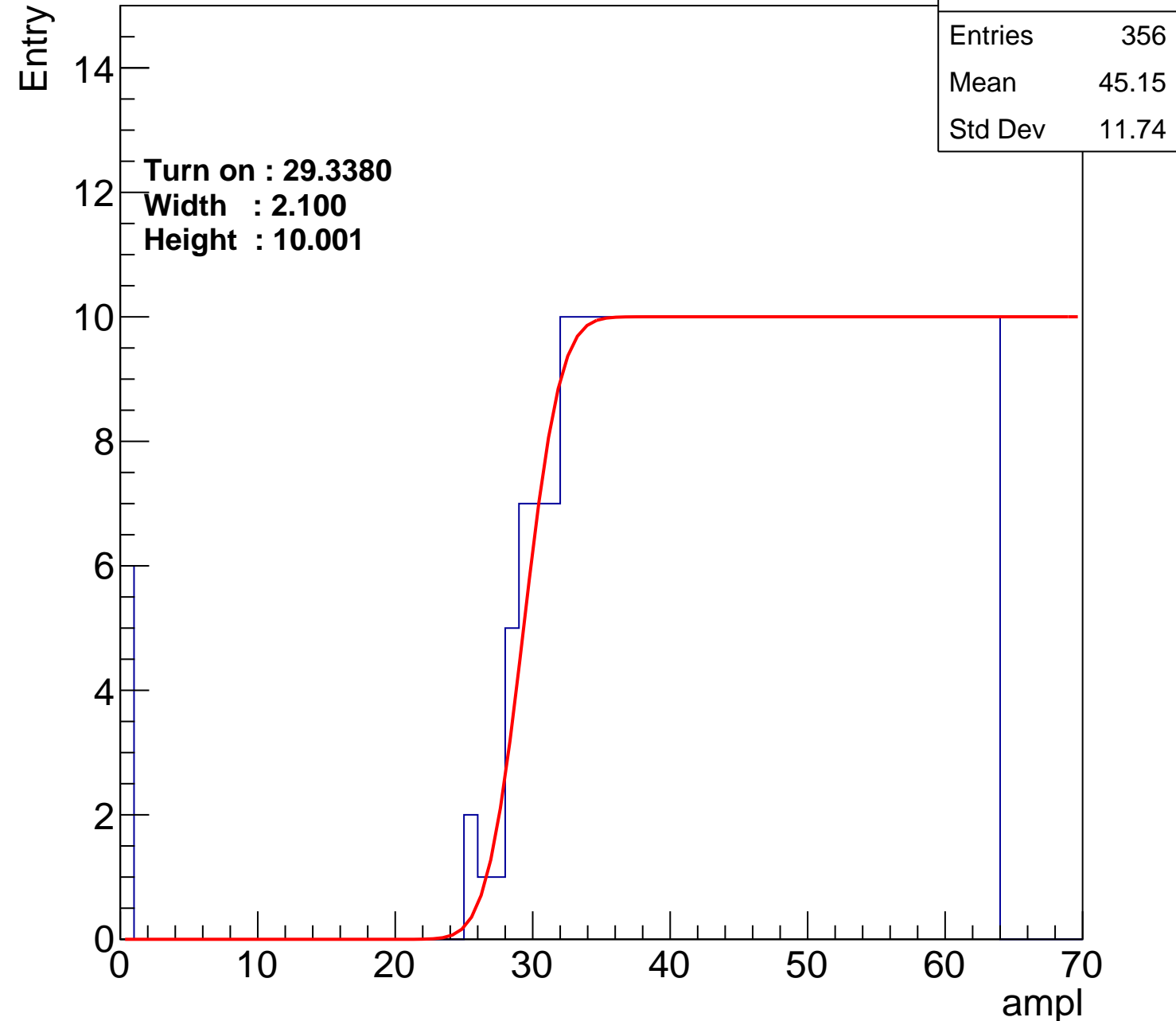
Width : 2.100

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch125

calib_packv5_042523_0143.root, FC#5, port B1

Entries	355
Mean	45.43
Std Dev	11.07

Turn on : 28.9494

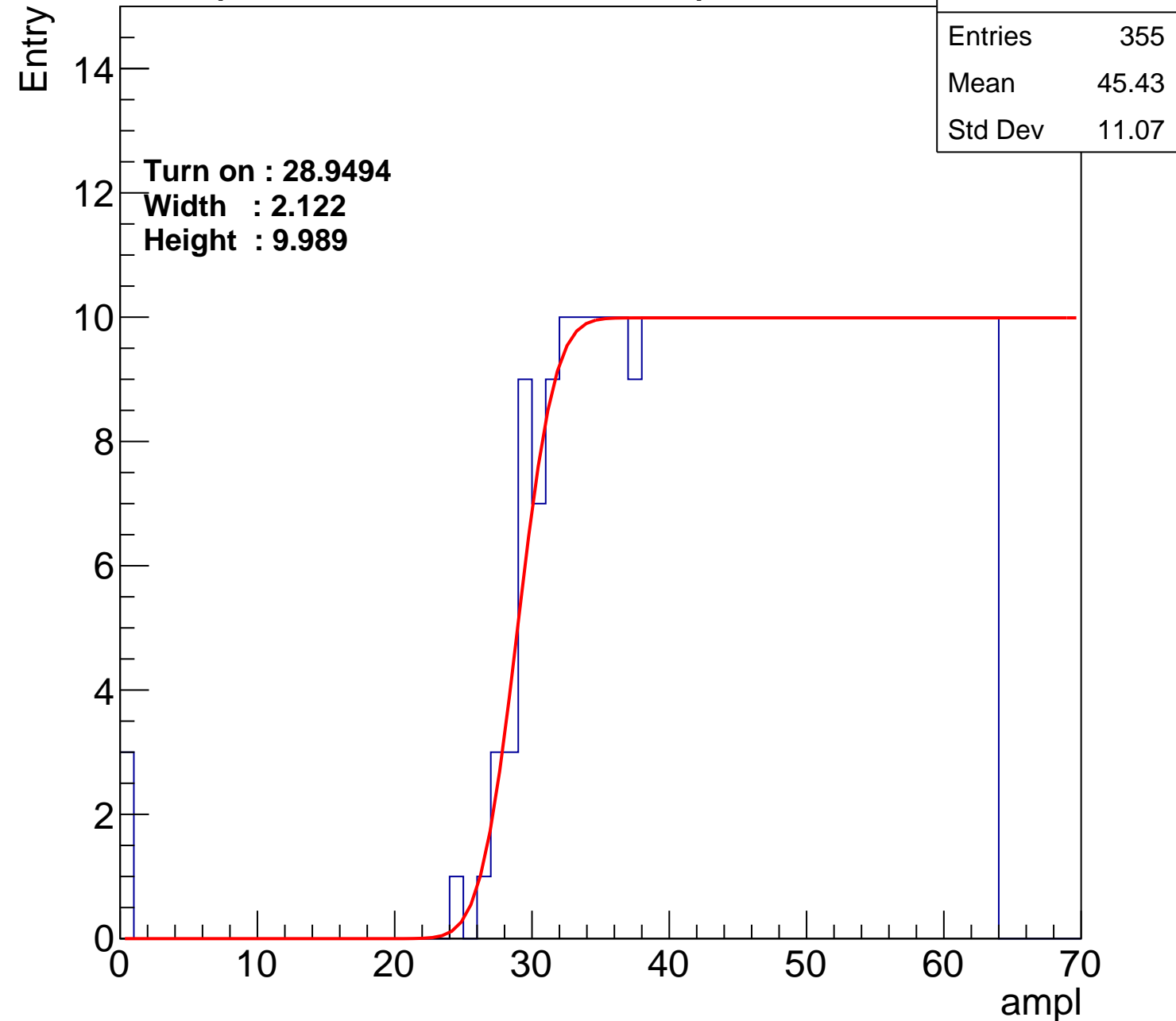
Width : 2.122

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch126

calib_packv5_042523_0143.root, FC#5, port B1

Entries	387
Mean	43.9
Std Dev	11.79

Turn on : 25.6133

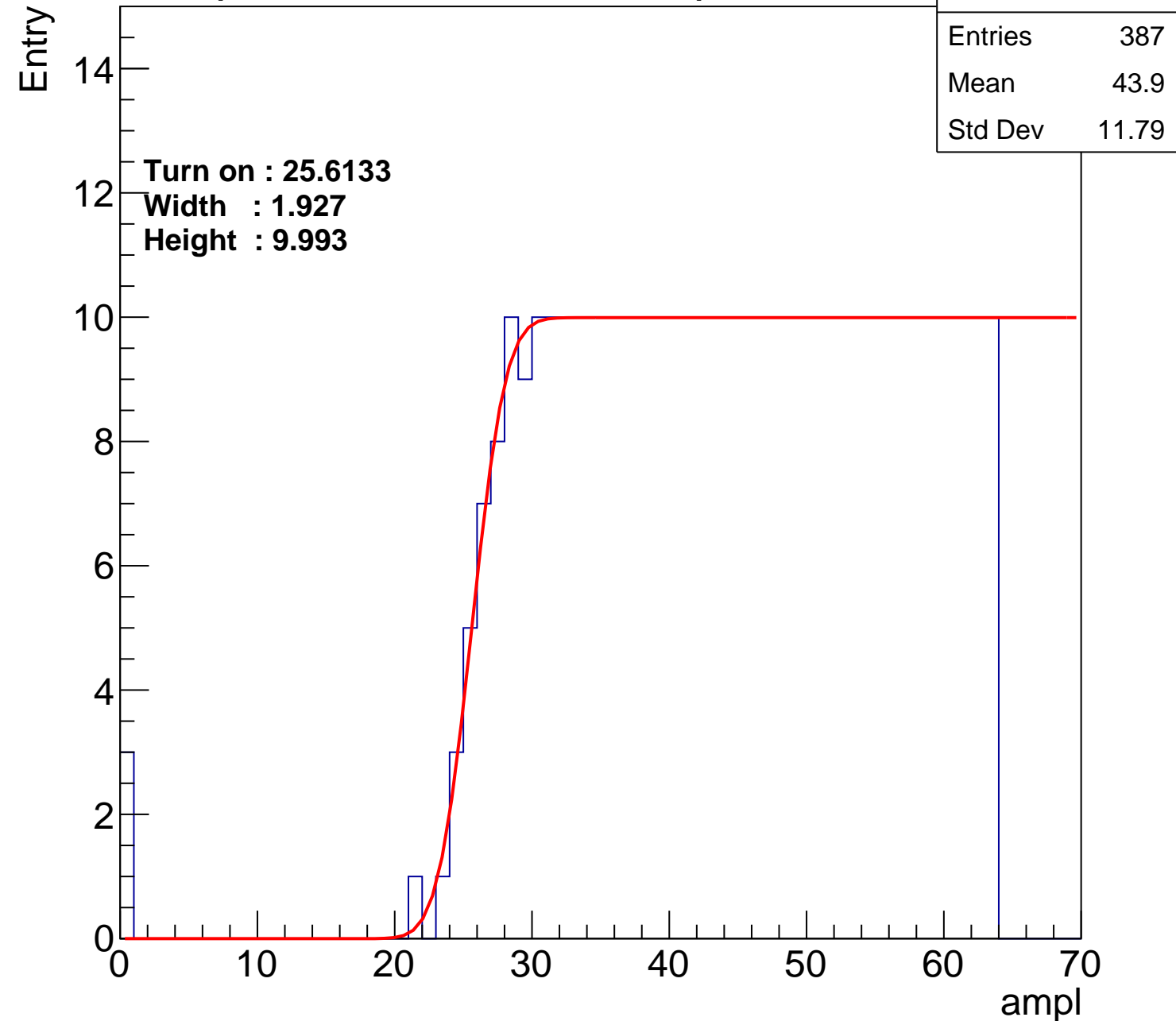
Width : 1.927

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.23
Std Dev	11.04

Turn on : 28.4475

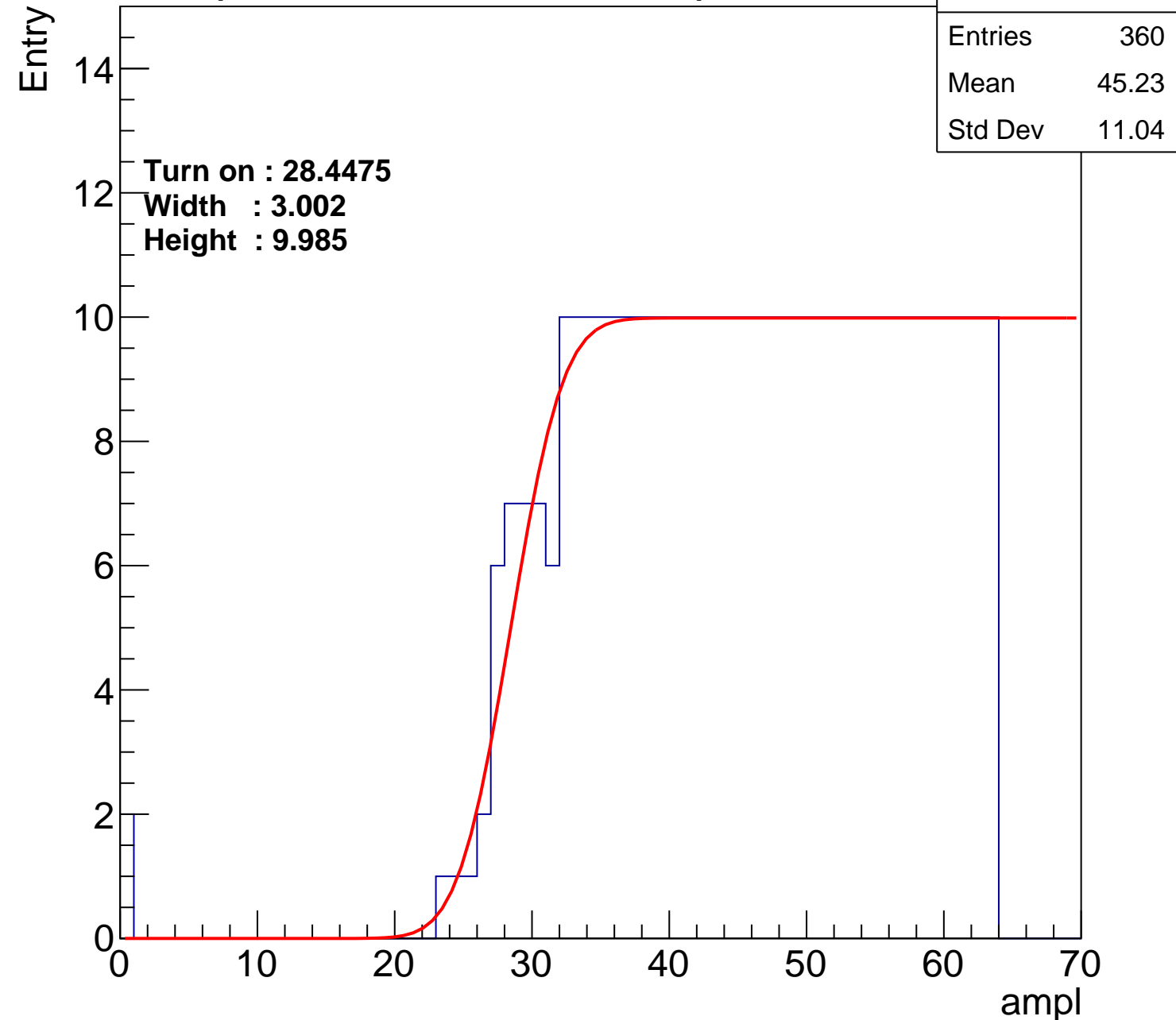
Width : 3.002

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L000S, U9-ch127

calib_packv5_042523_0143.root, FC#5, port B1

Entries	360
Mean	45.23
Std Dev	11.04

Turn on : 28.4475

Width : 3.002

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl

