

B0L102S, U8-ch0

calib_packv5_042523_0143.root, FC#12, port B1

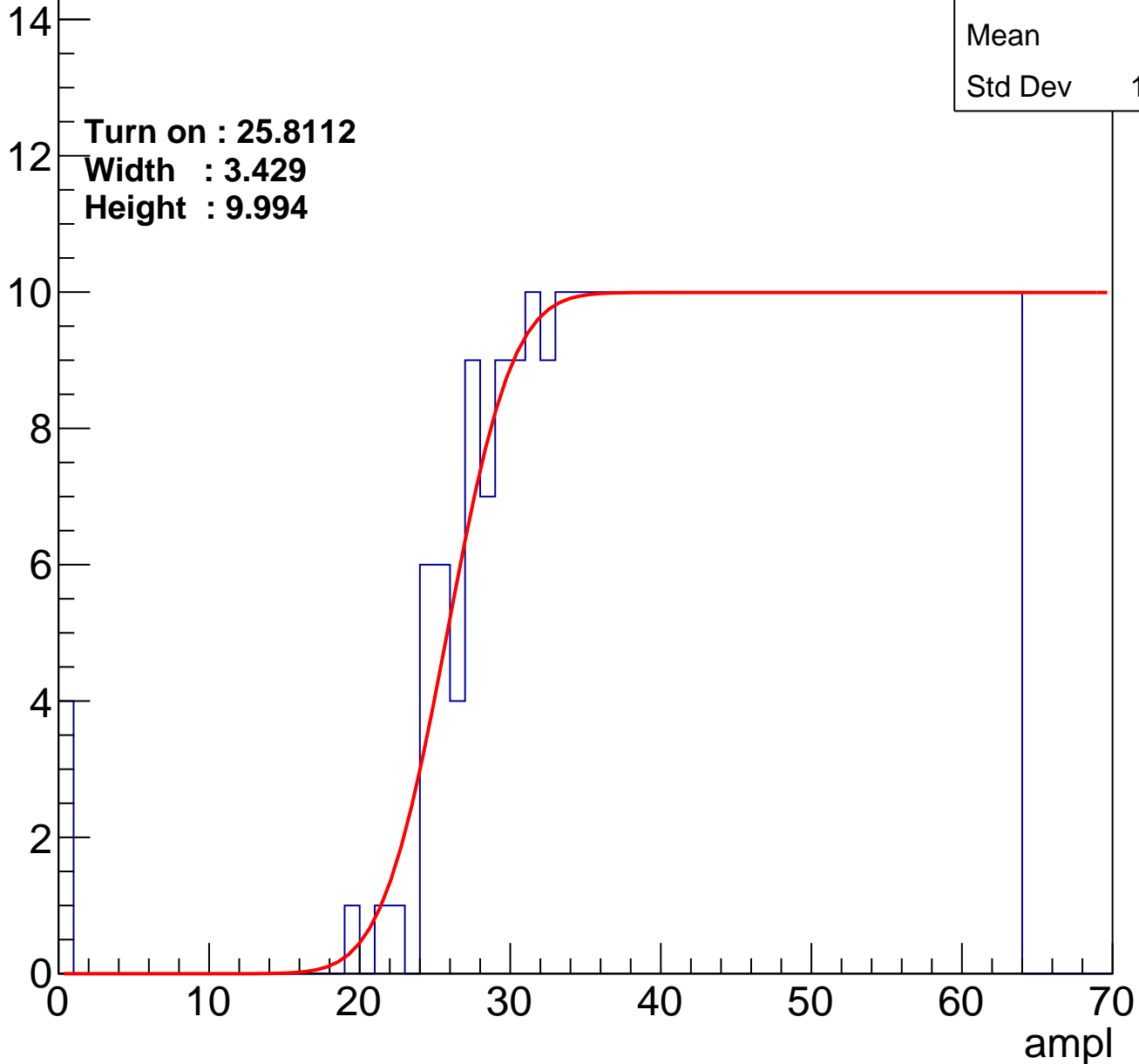
Entries	386
Mean	43.8
Std Dev	12.06

Turn on : 25.8112

Width : 3.429

Height : 9.994

Entry



B0L102S, U8-ch1

calib_packv5_042523_0143.root, FC#12, port B1

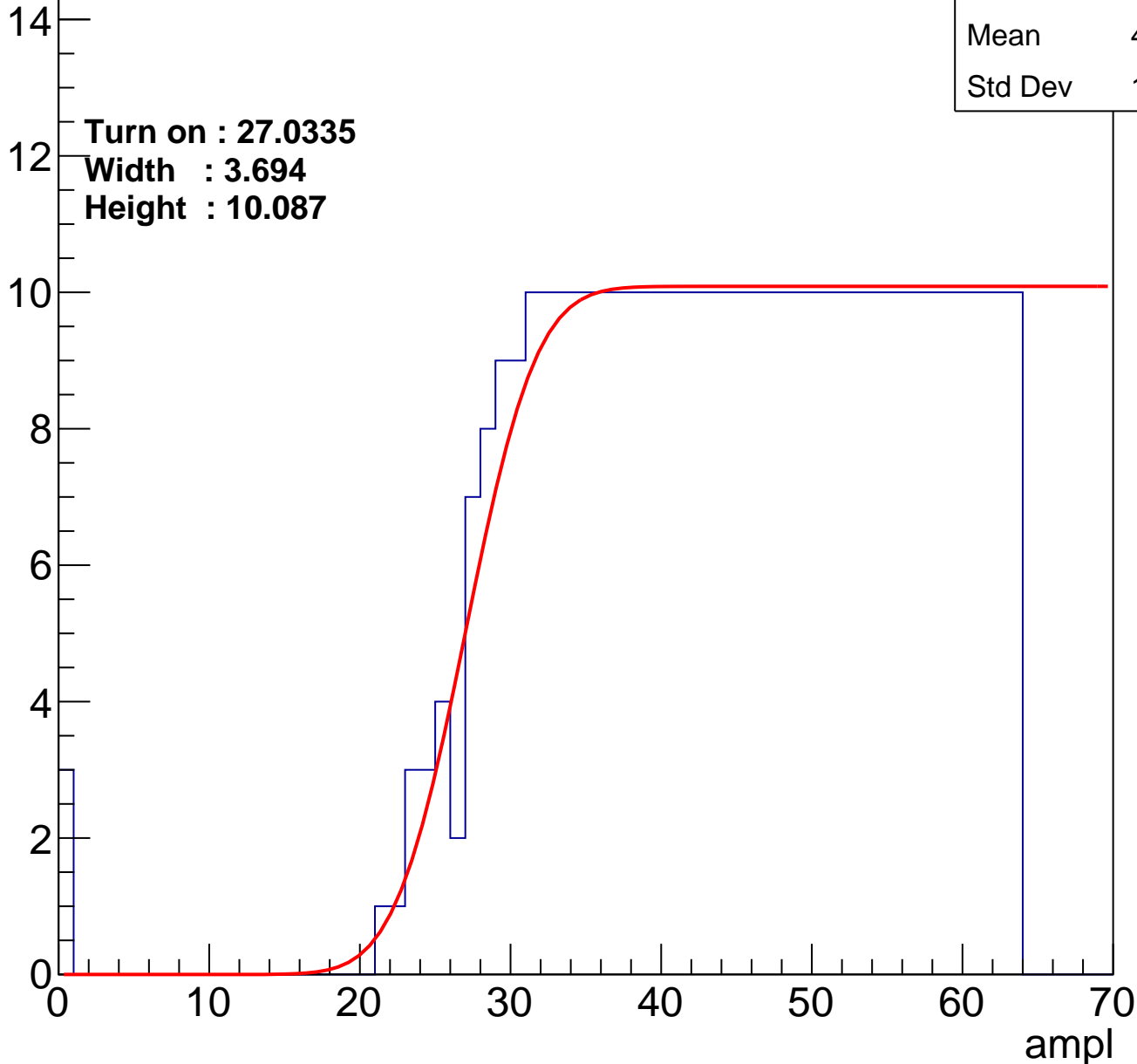
Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 27.0335

Width : 3.694

Height : 10.087

Entry



B0L102S, U8-ch2

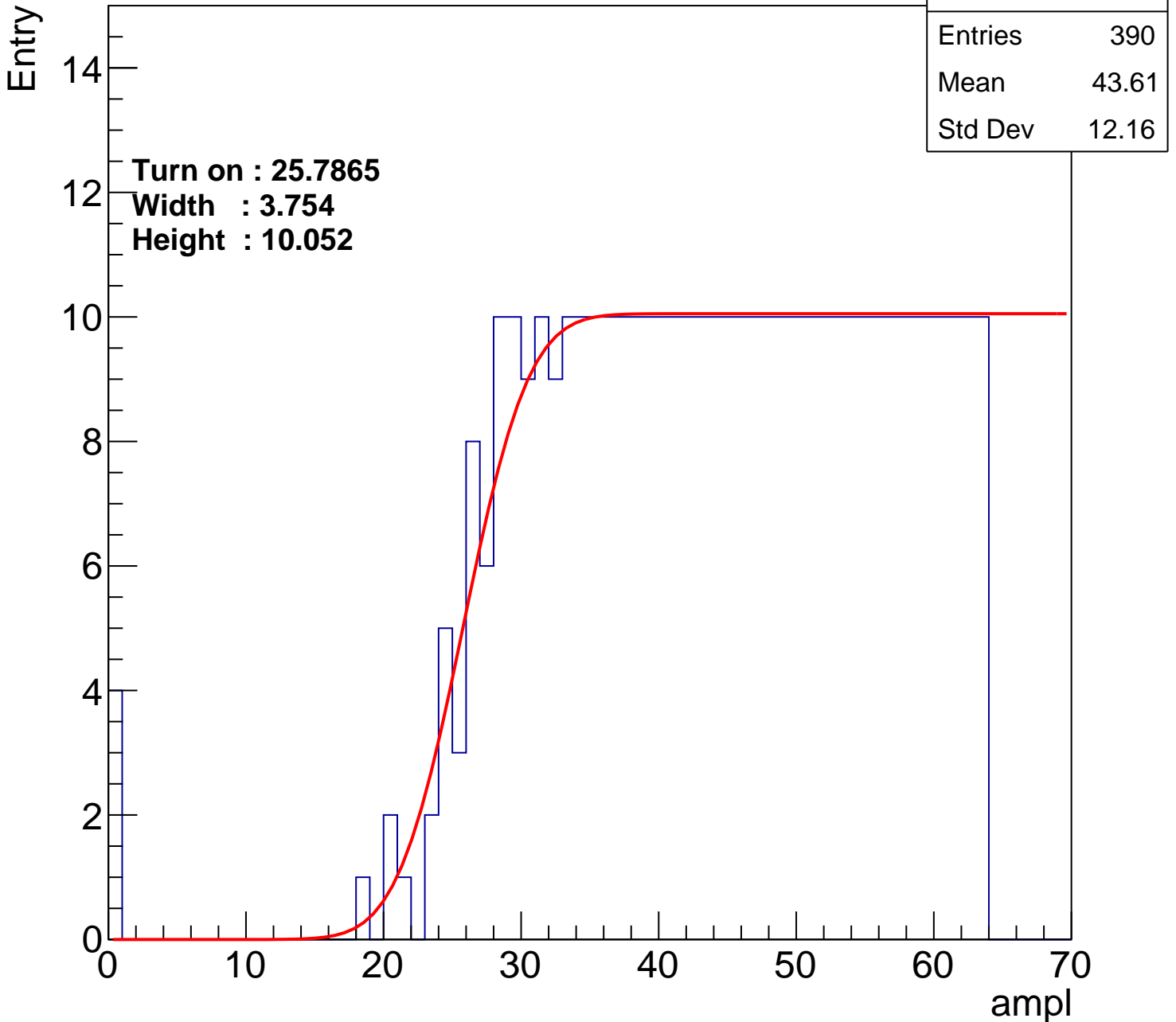
calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.61
Std Dev	12.16

Turn on : 25.7865

Width : 3.754

Height : 10.052



B0L102S, U8-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 26.0908

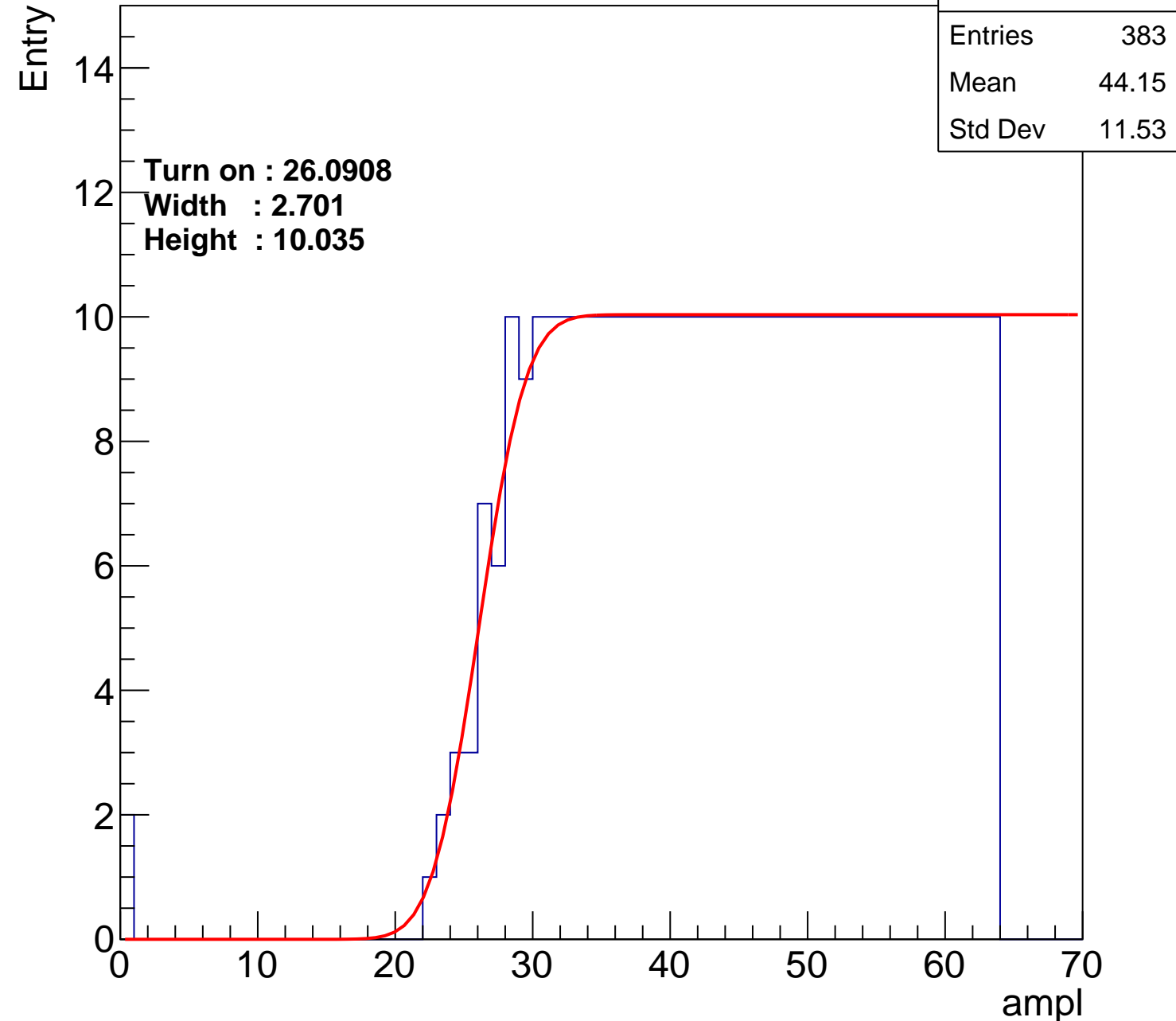
Width : 2.701

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch4

calib_packv5_042523_0143.root, FC#12, port B1

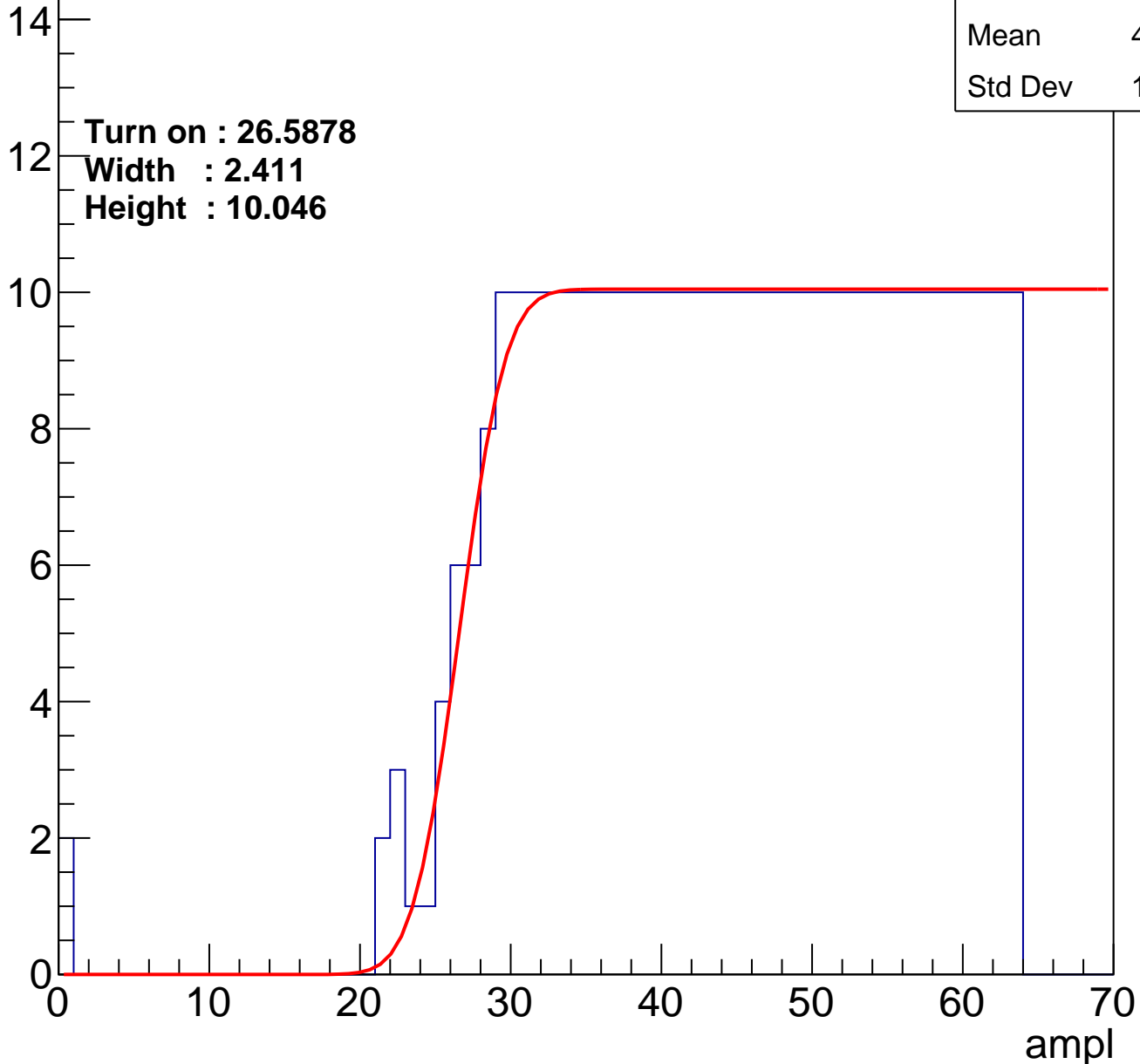
Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 26.5878

Width : 2.411

Height : 10.046

Entry



B0L102S, U8-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 26.0717

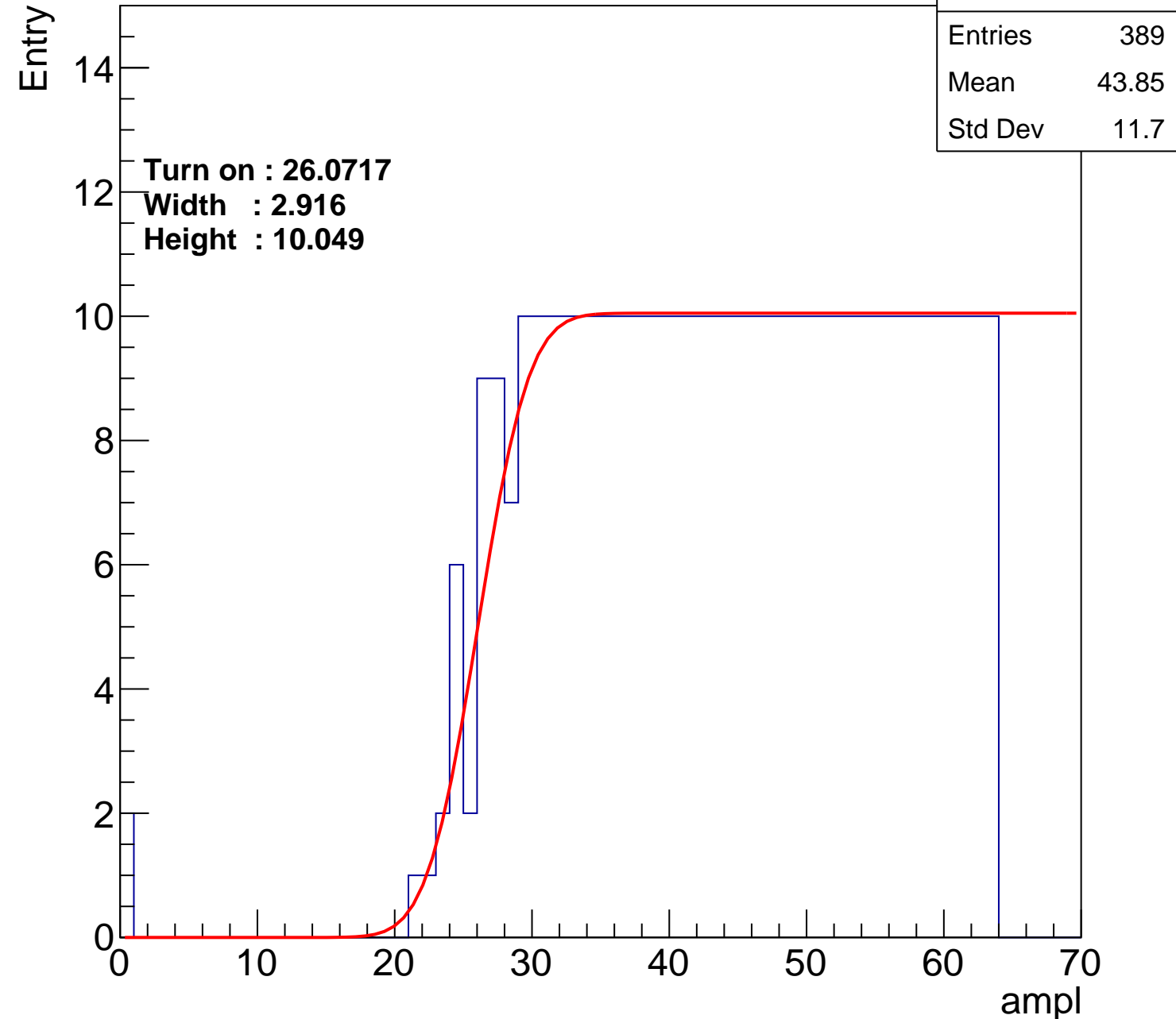
Width : 2.916

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch6

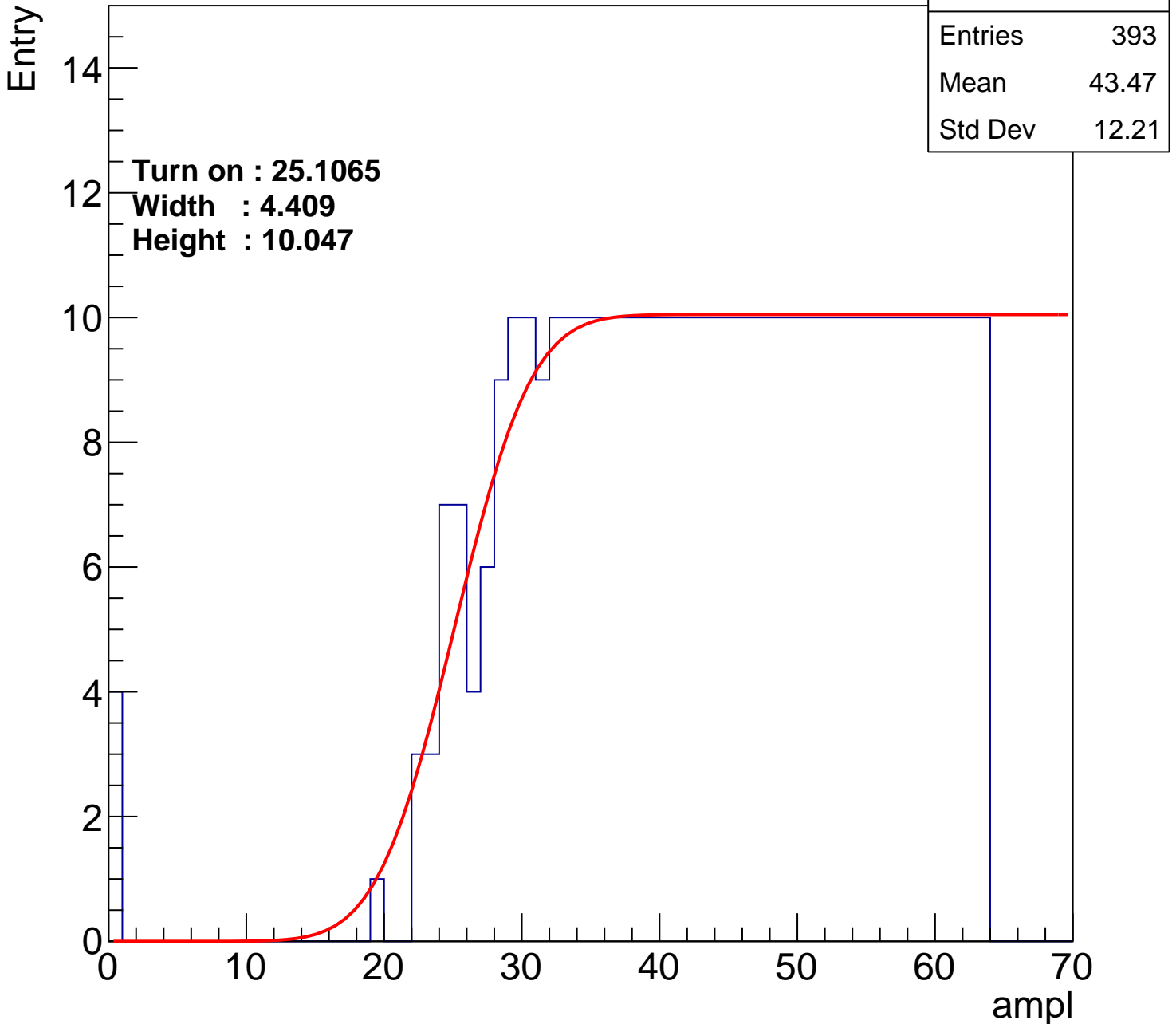
calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.47
Std Dev	12.21

Turn on : 25.1065

Width : 4.409

Height : 10.047



B0L102S, U8-ch7

calib_packv5_042523_0143.root, FC#12, port B1

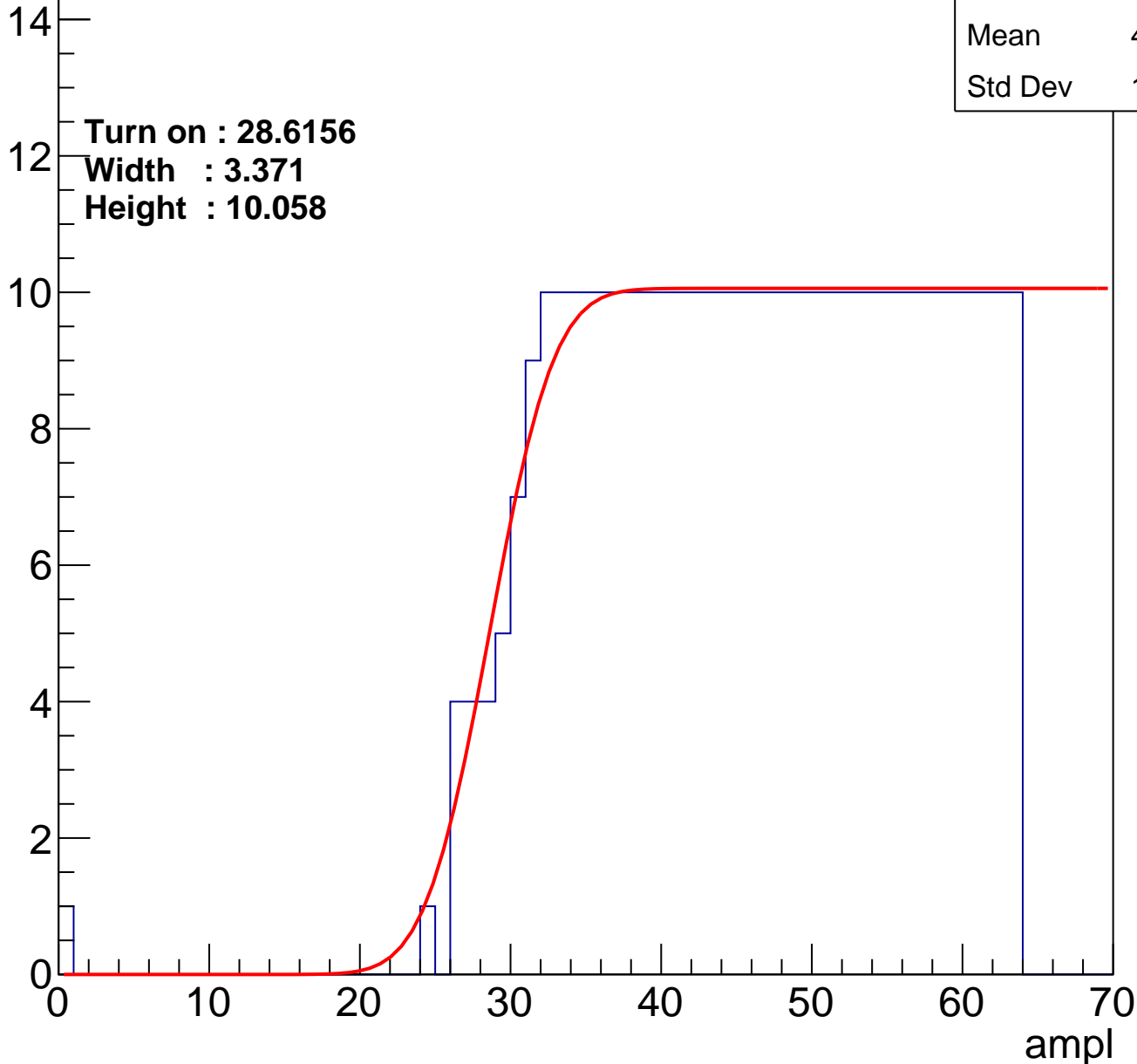
Entry

Entries	355
Mean	45.58
Std Dev	10.63

Turn on : 28.6156

Width : 3.371

Height : 10.058



B0L102S, U8-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.4
Std Dev	11.38

Turn on : 27.5145

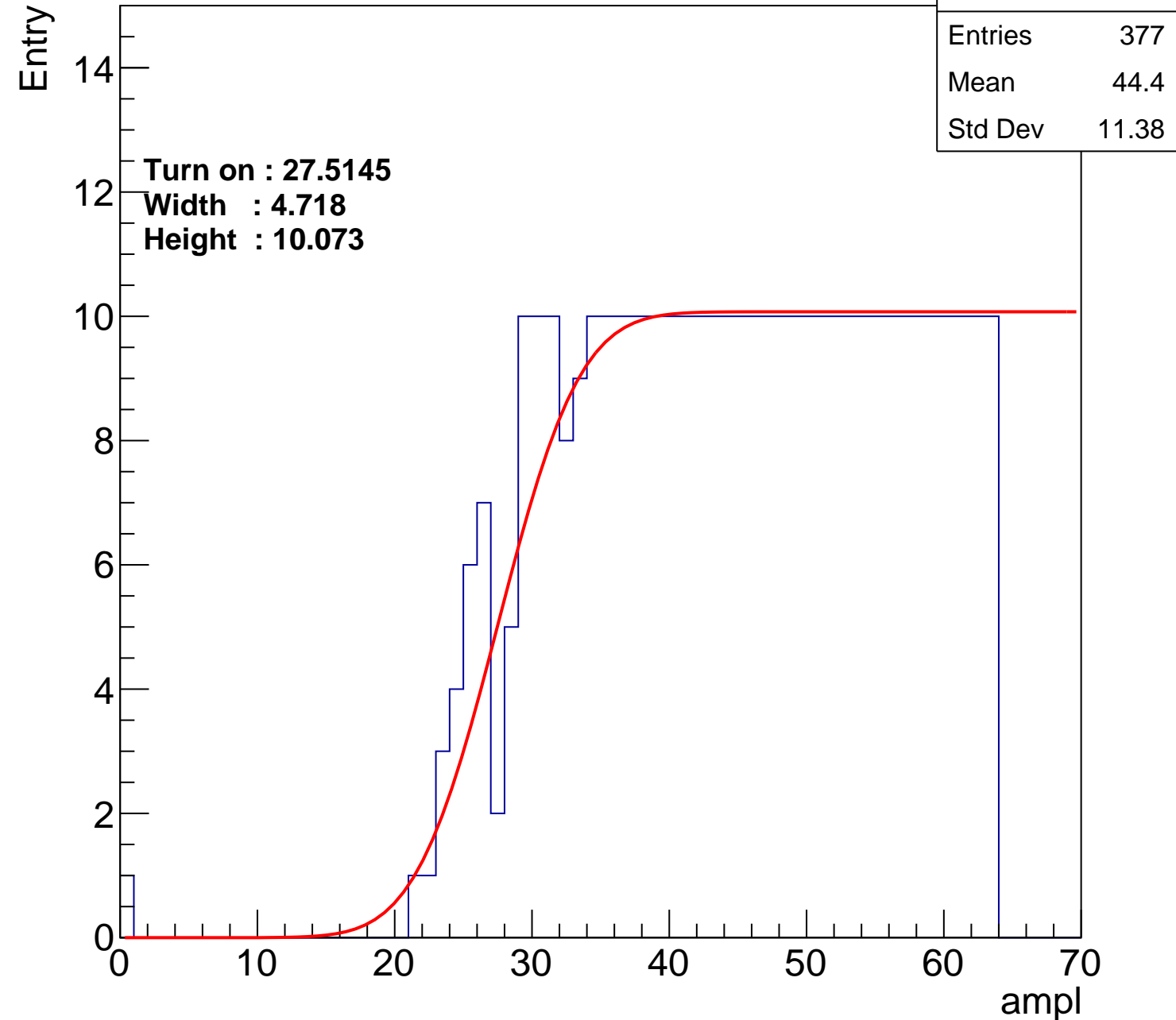
Width : 4.718

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch9

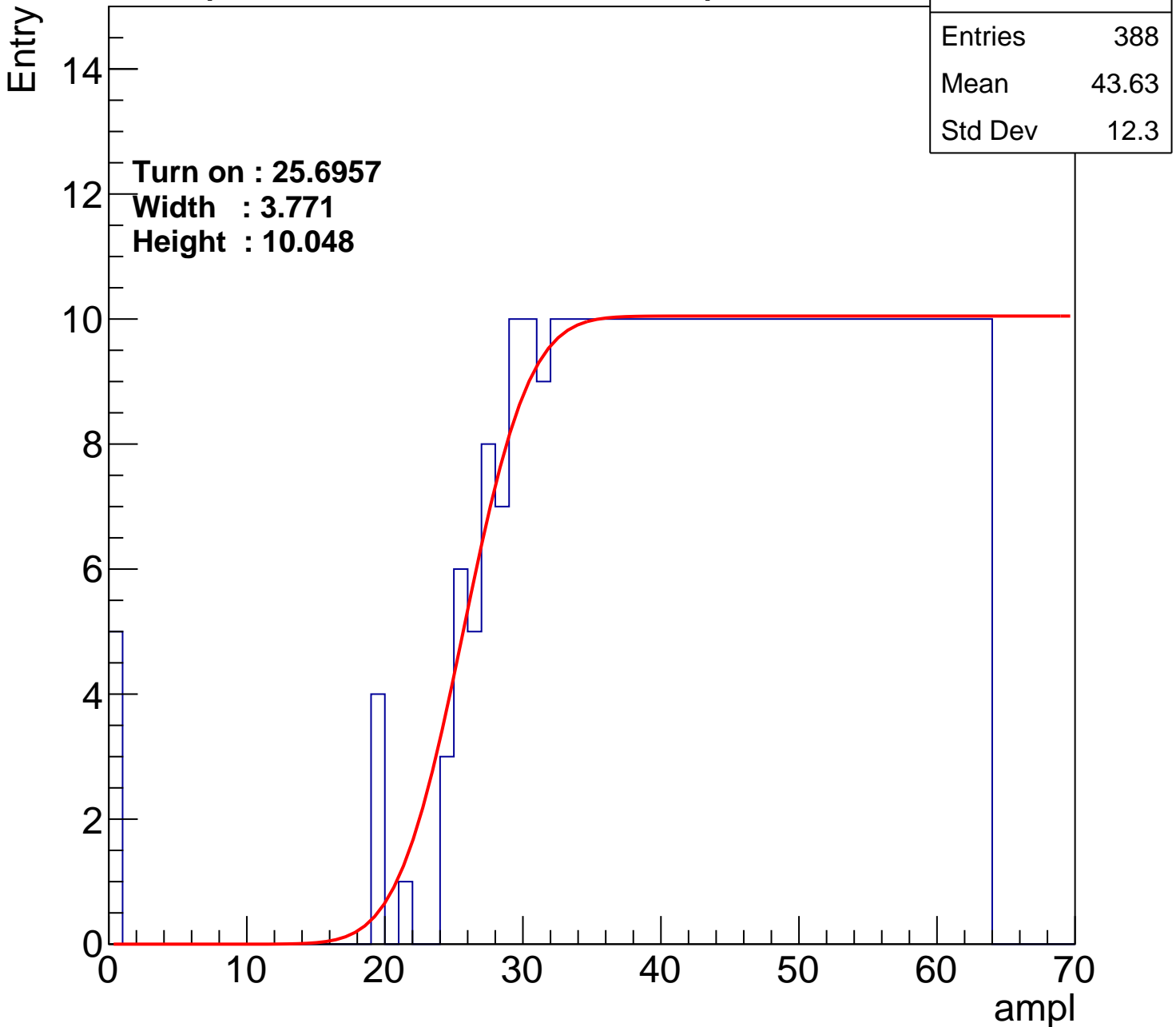
calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.63
Std Dev	12.3

Turn on : 25.6957

Width : 3.771

Height : 10.048



B0L102S, U8-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.79
Std Dev	11.99

Turn on : 26.1243

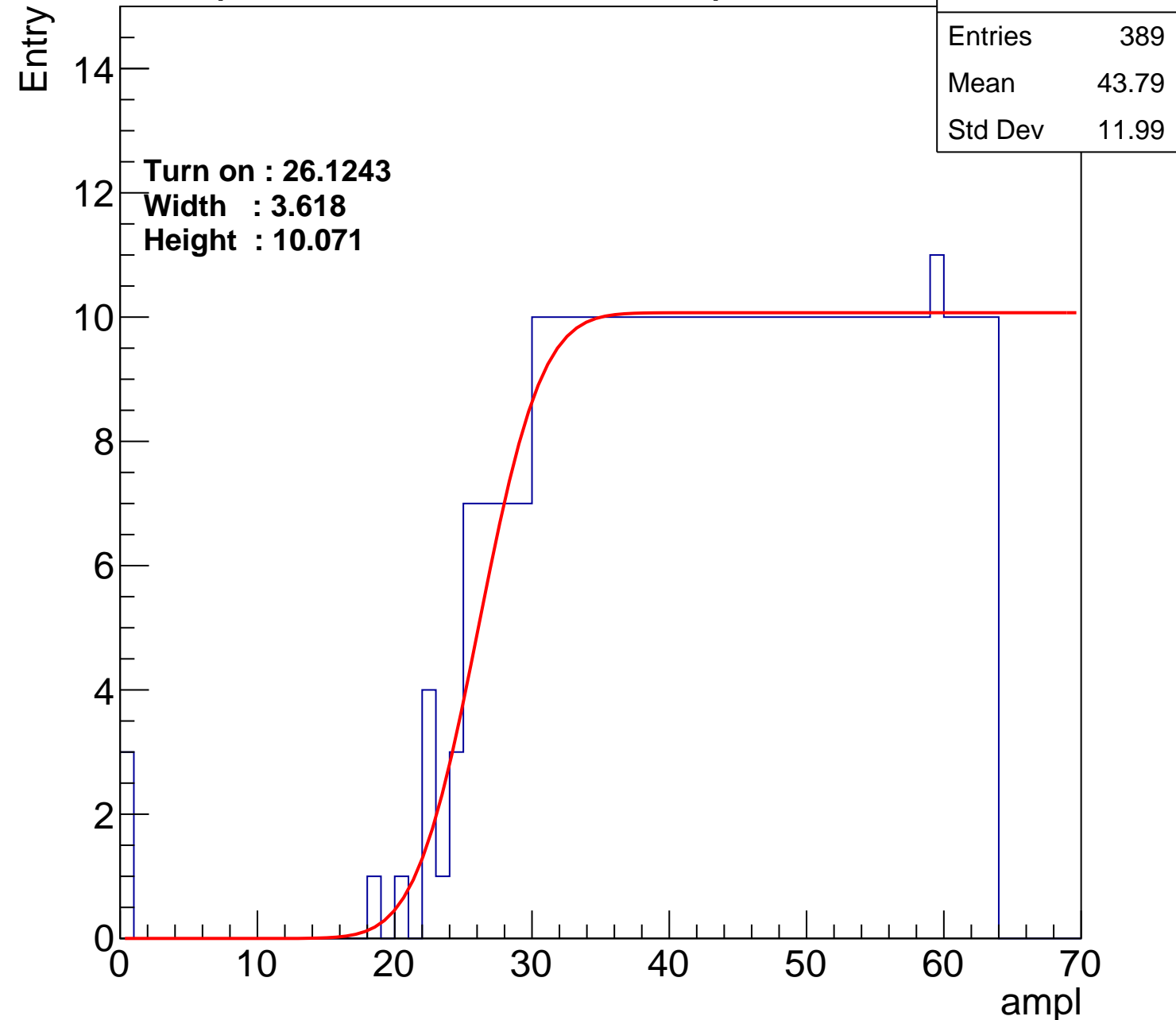
Width : 3.618

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch11

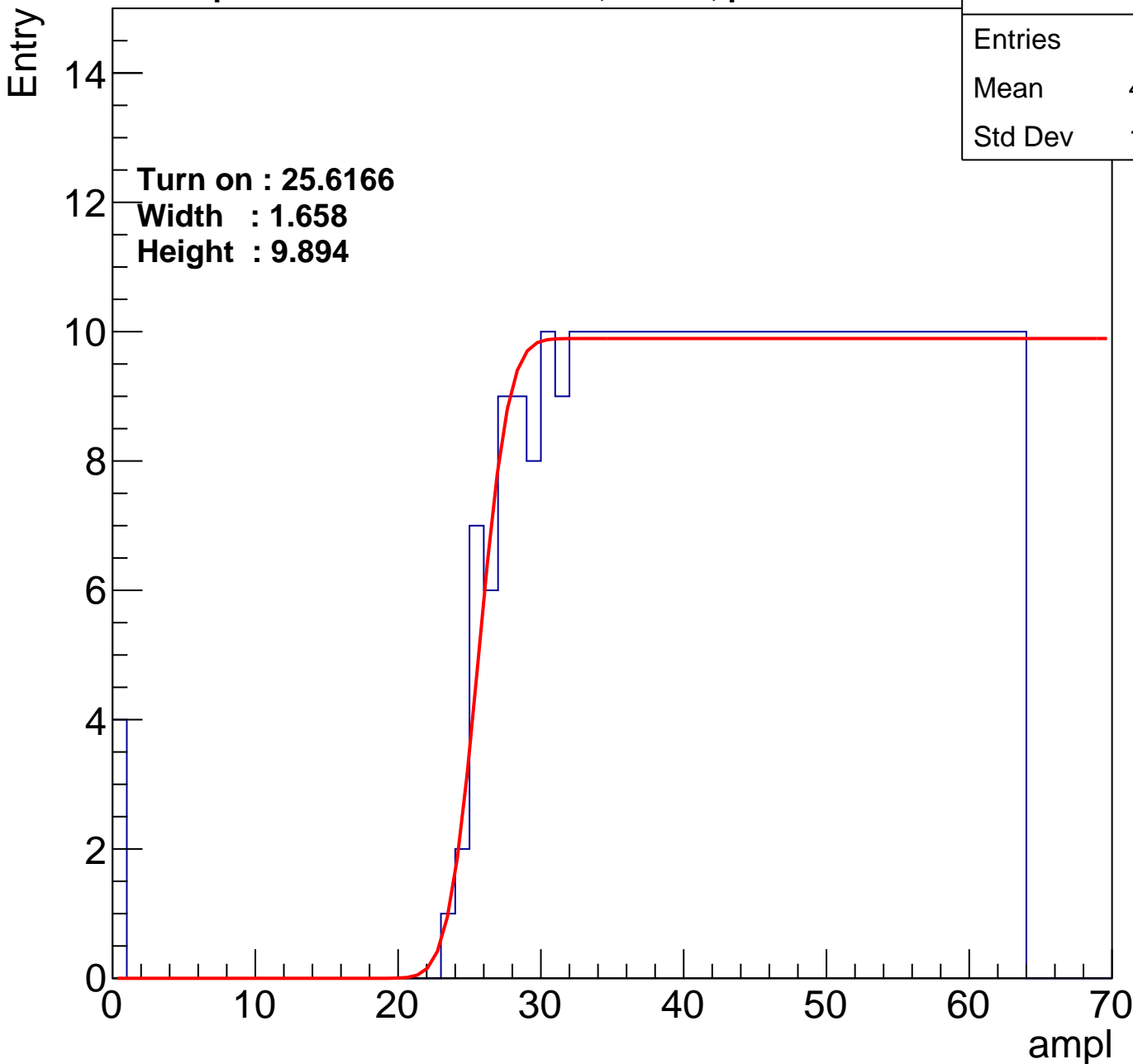
calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.92
Std Dev	11.93

Turn on : 25.6166

Width : 1.658

Height : 9.894



B0L102S, U8-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	43.06
Std Dev	12.28

Turn on : 24.0015

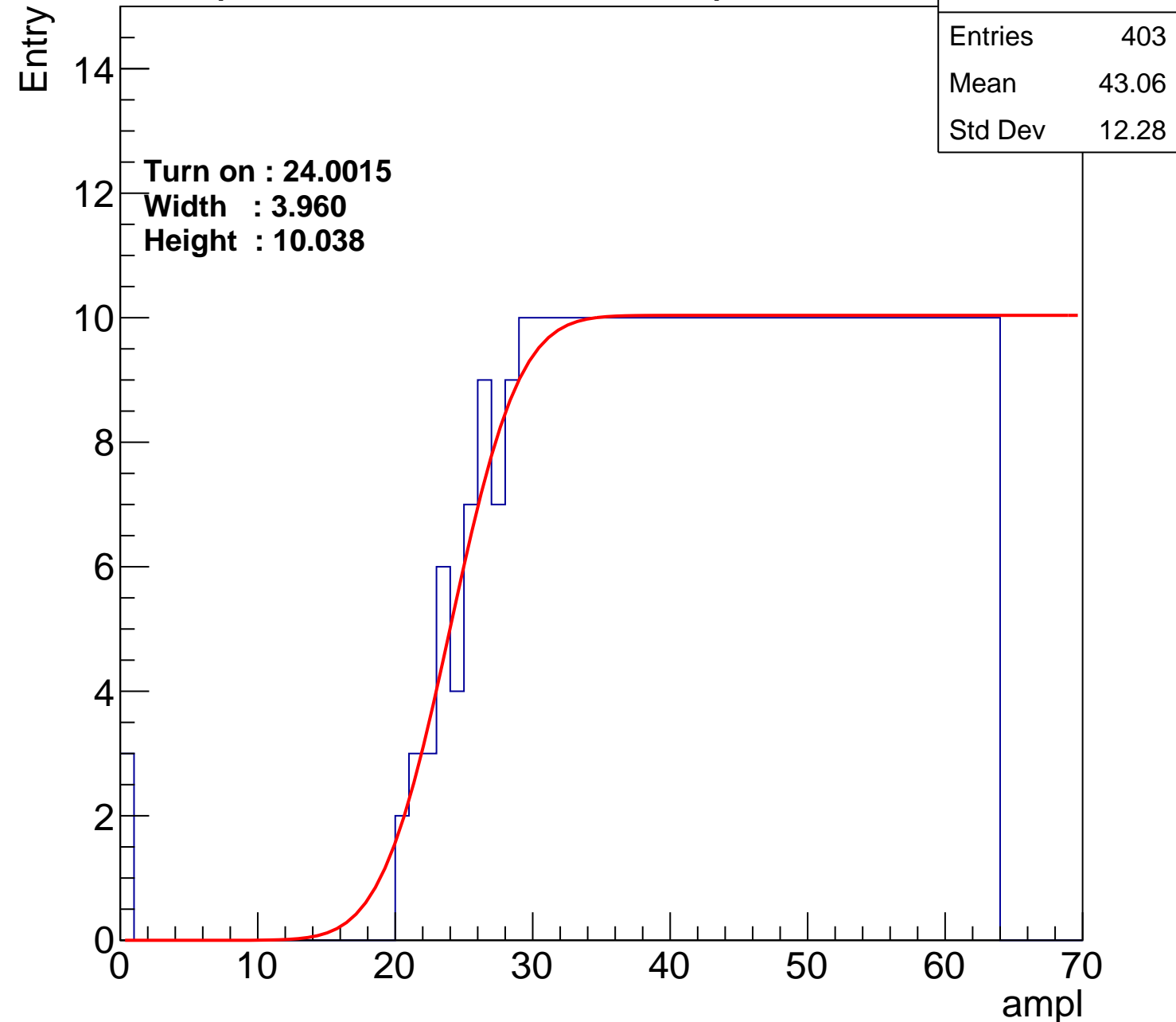
Width : 3.960

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.57
Std Dev	11.54

Turn on : 27.6971

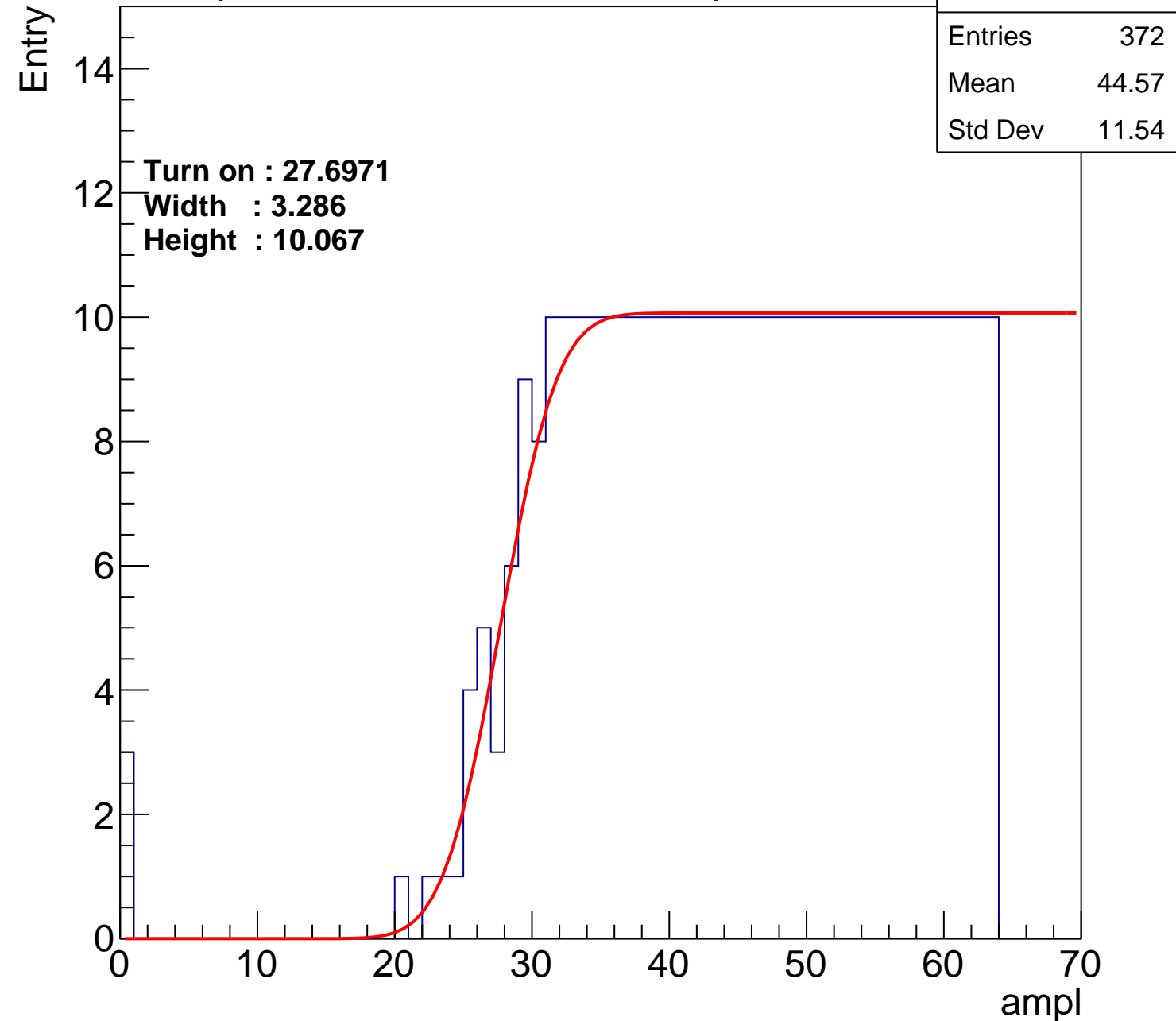
Width : 3.286

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.39
Std Dev	12.35

Turn on : 25.4352

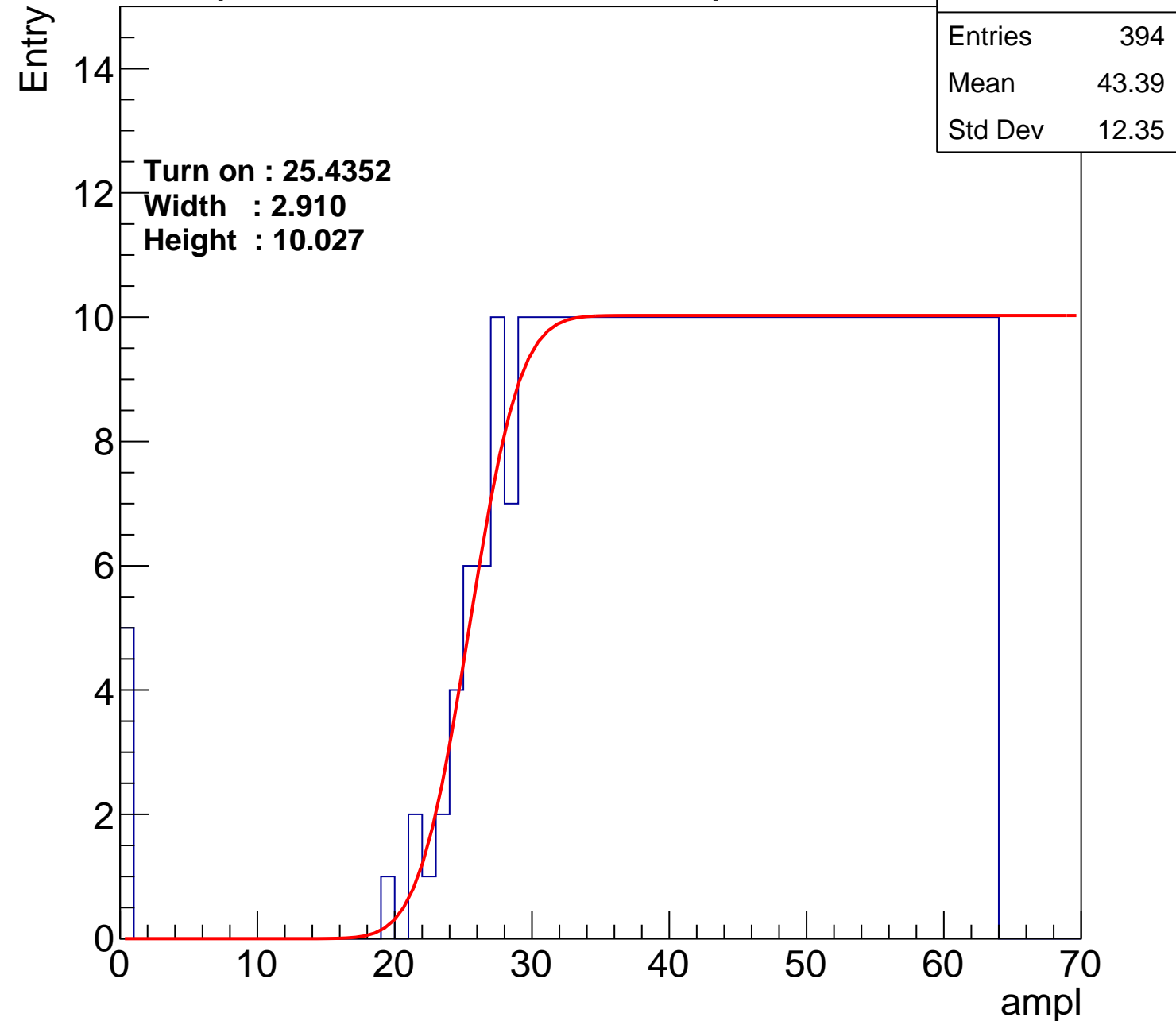
Width : 2.910

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 25.4967

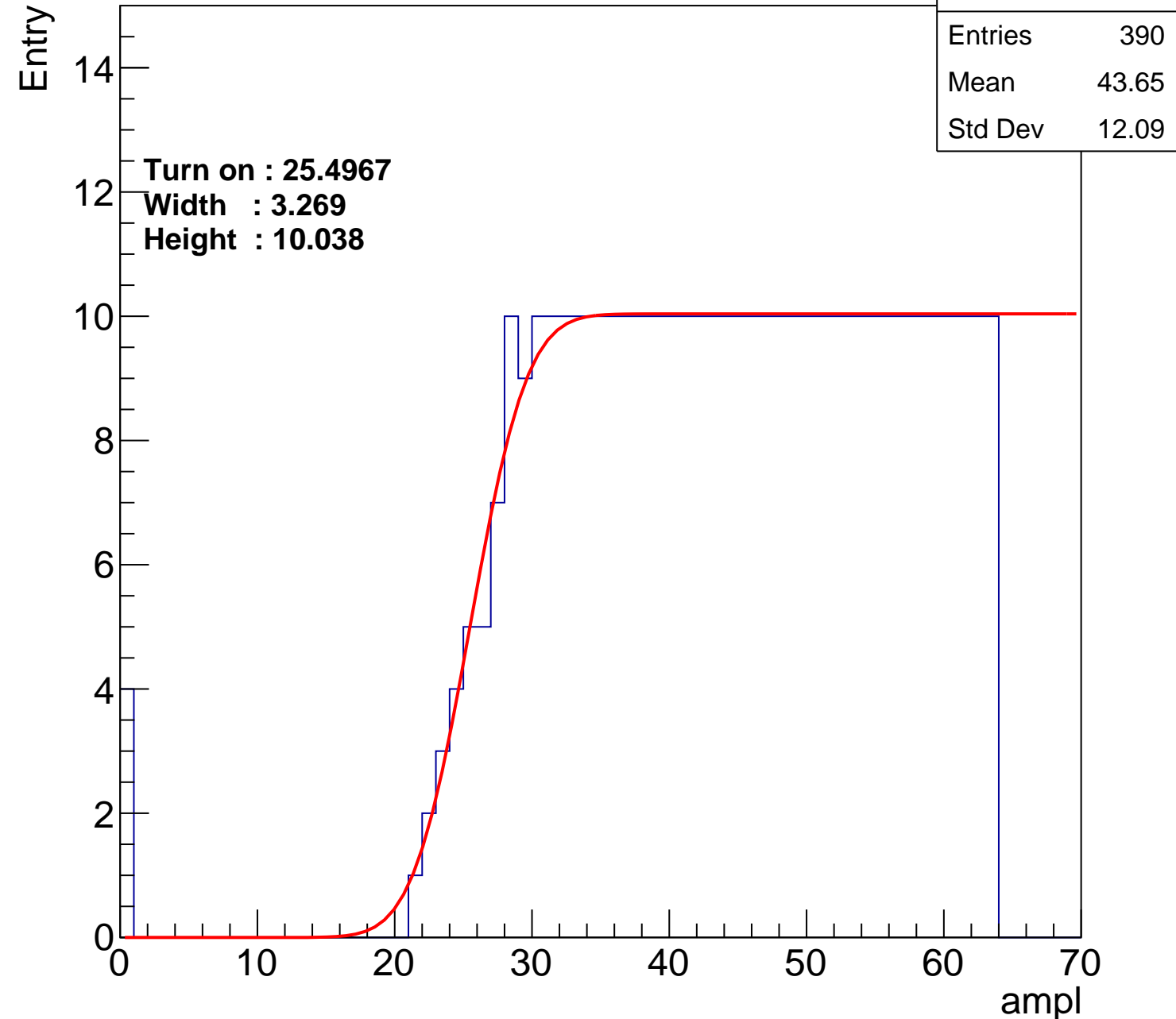
Width : 3.269

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.57
Std Dev	12.12

Turn on : 25.2682

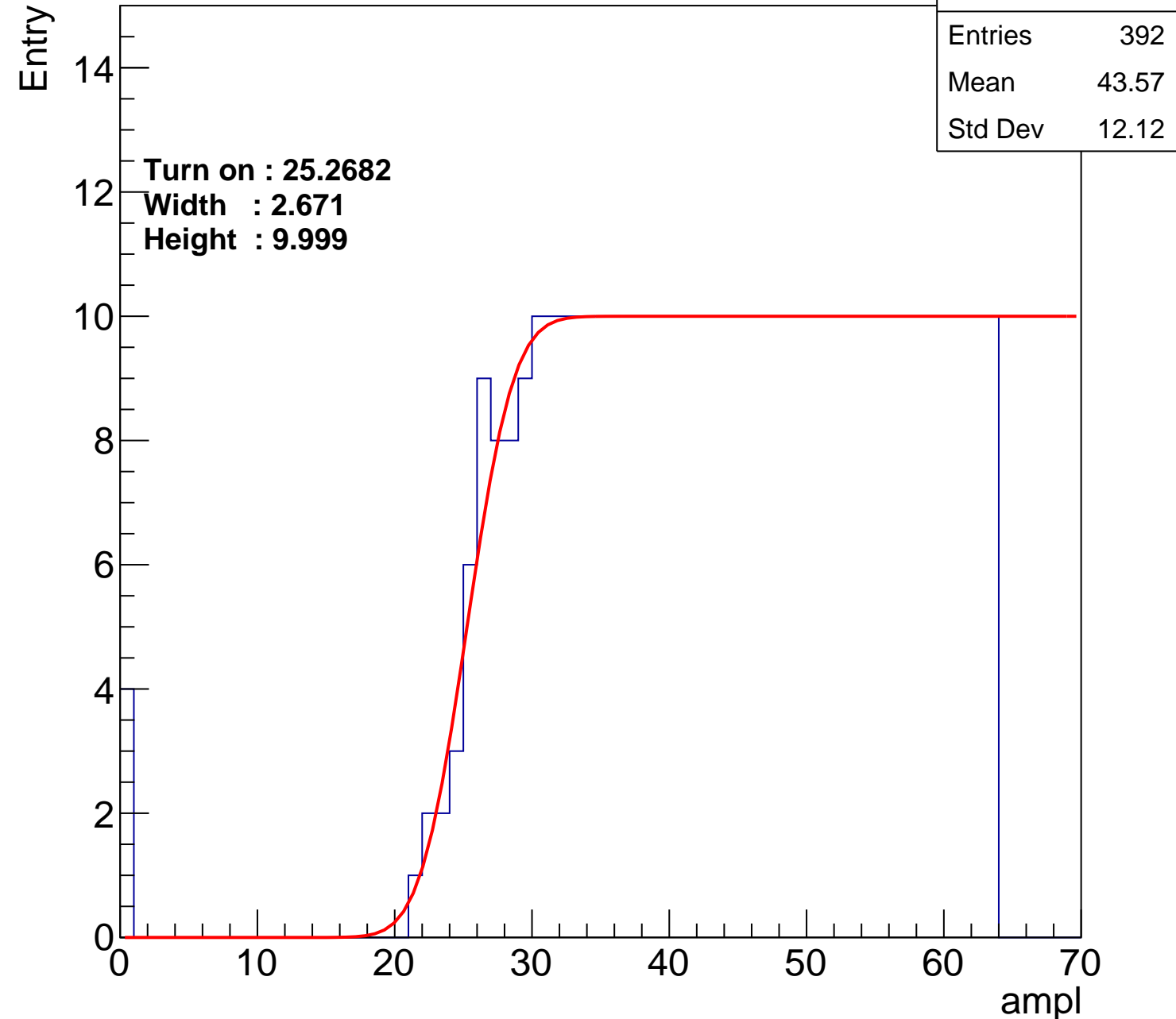
Width : 2.671

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.7329

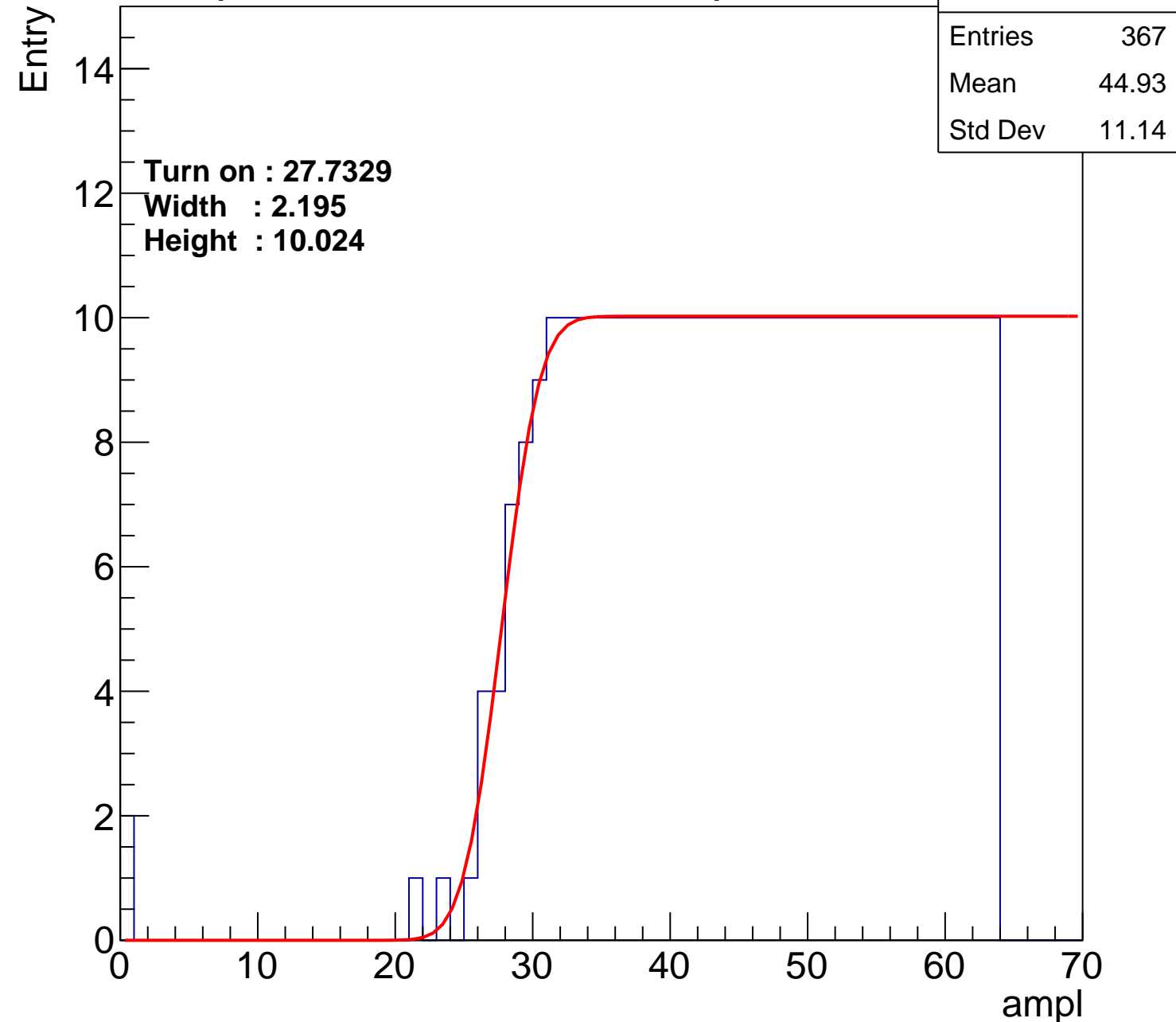
Width : 2.195

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch18

calib_packv5_042523_0143.root, FC#12, port B1

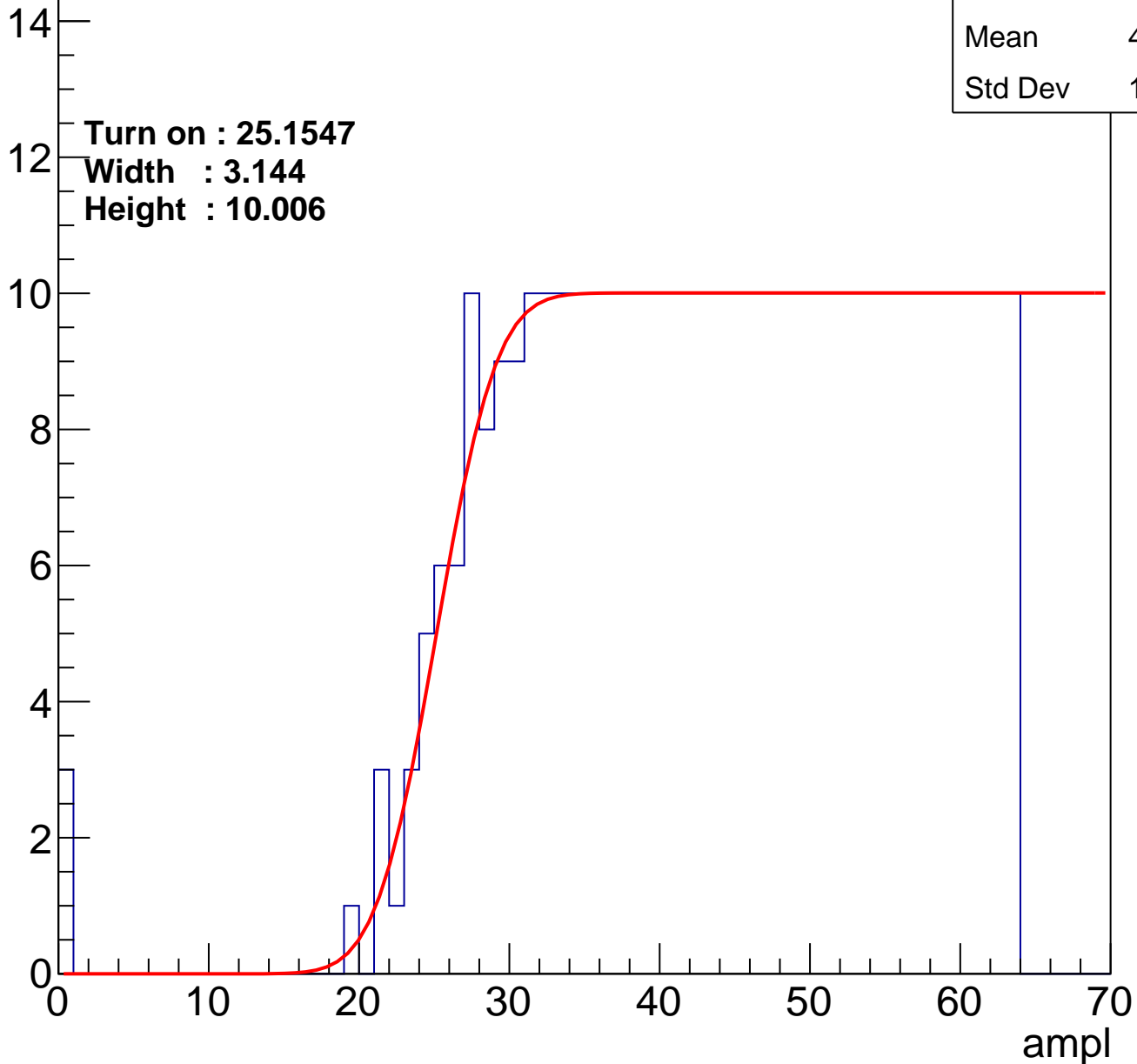
Entries	394
Mean	43.49
Std Dev	12.08

Turn on : 25.1547

Width : 3.144

Height : 10.006

Entry



B0L102S, U8-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.55
Std Dev	11.42

Turn on : 27.2393

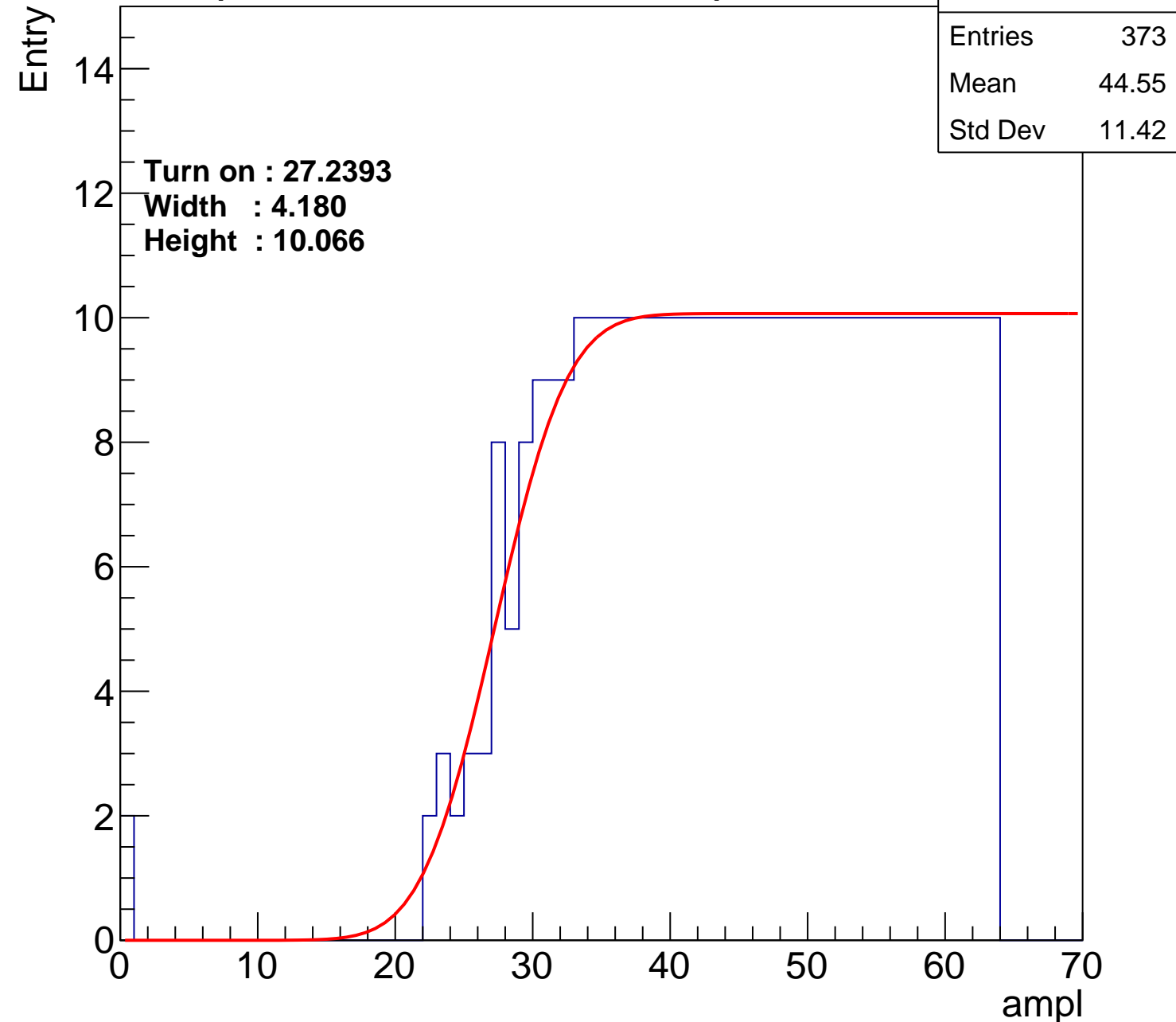
Width : 4.180

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 27.0579

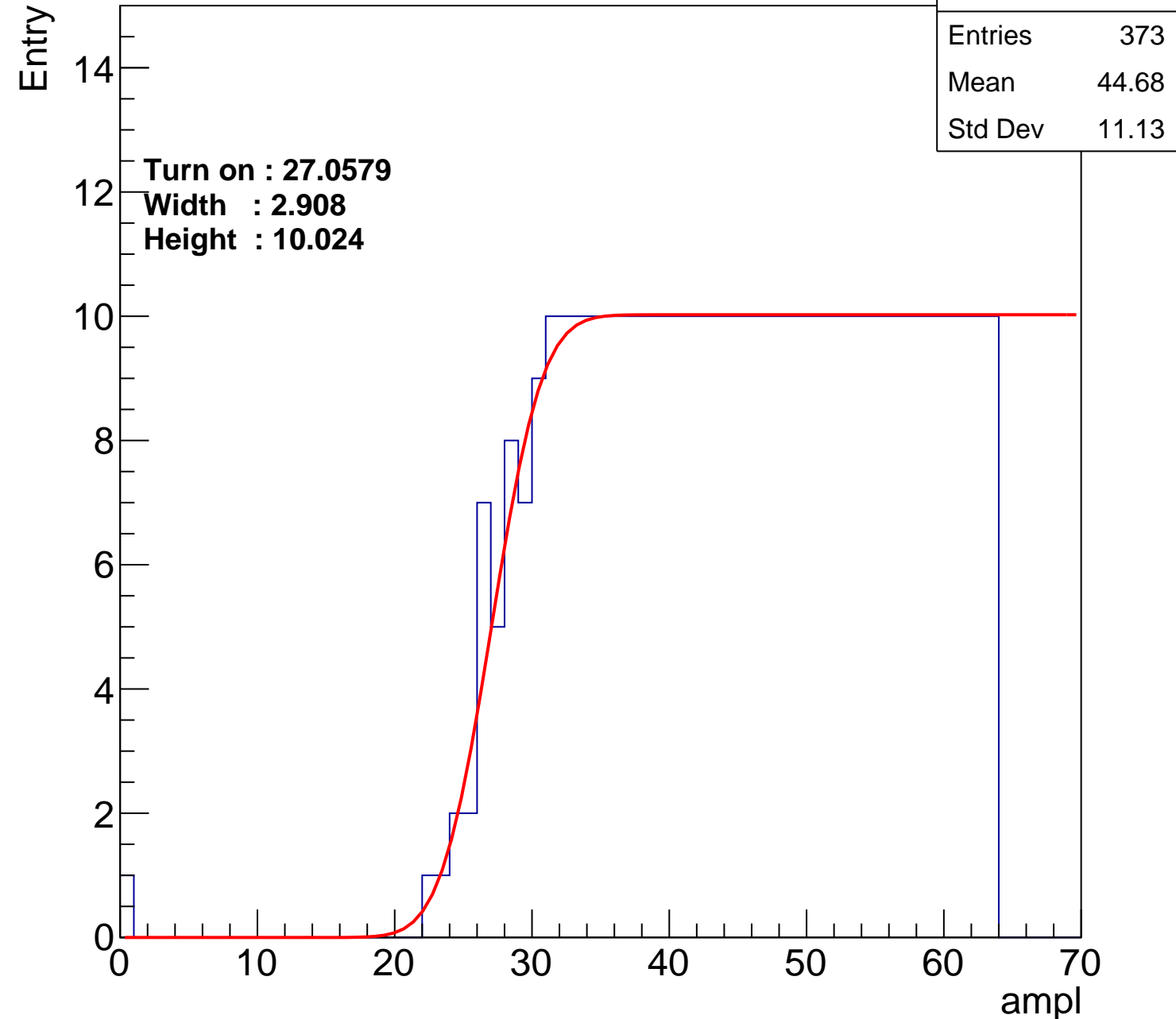
Width : 2.908

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch21

calib_packv5_042523_0143.root, FC#12, port B1

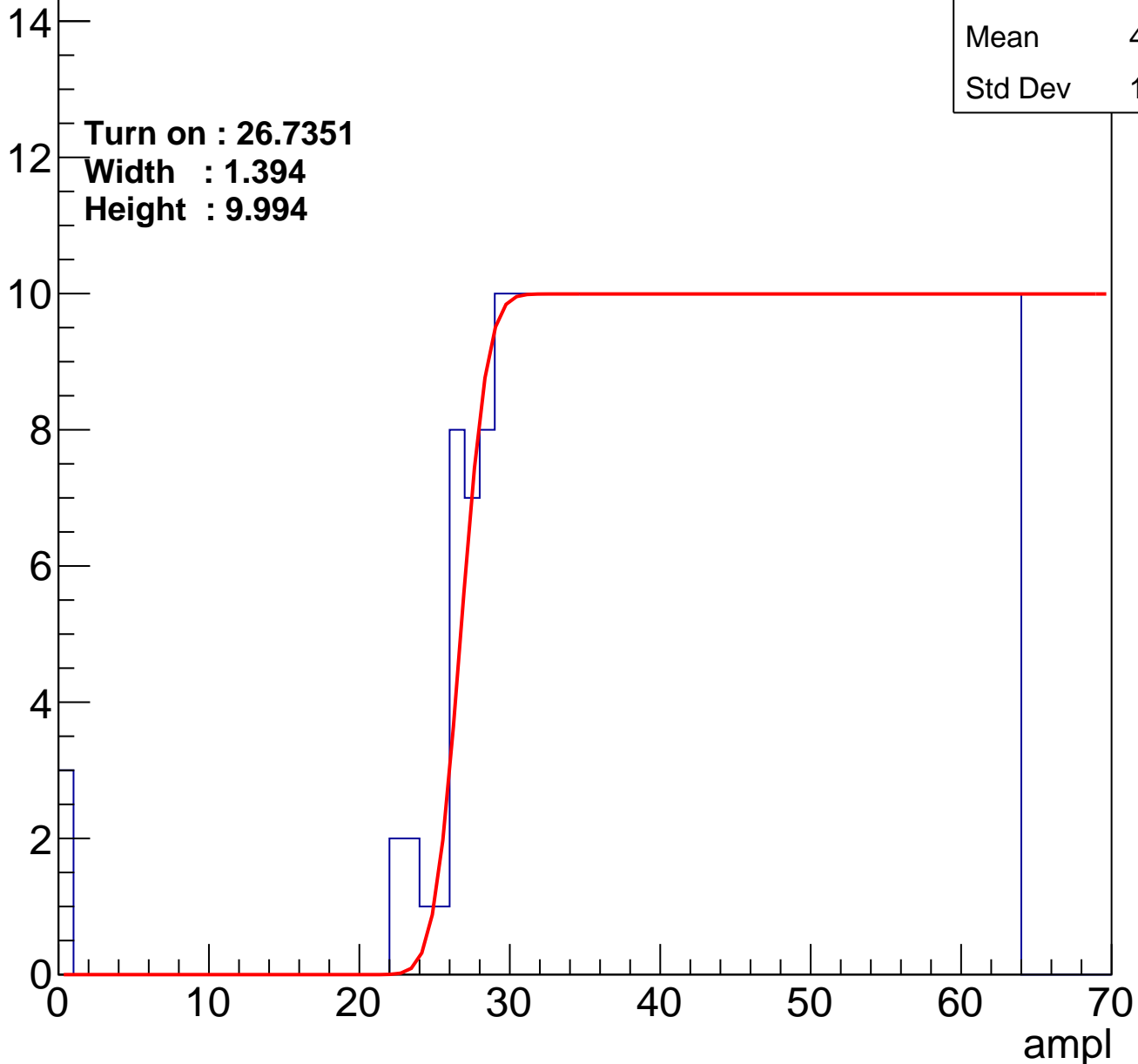
Entries	382
Mean	44.14
Std Dev	11.69

Turn on : 26.7351

Width : 1.394

Height : 9.994

Entry



B0L102S, U8-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.86
Std Dev	10.99

Turn on : 26.8384

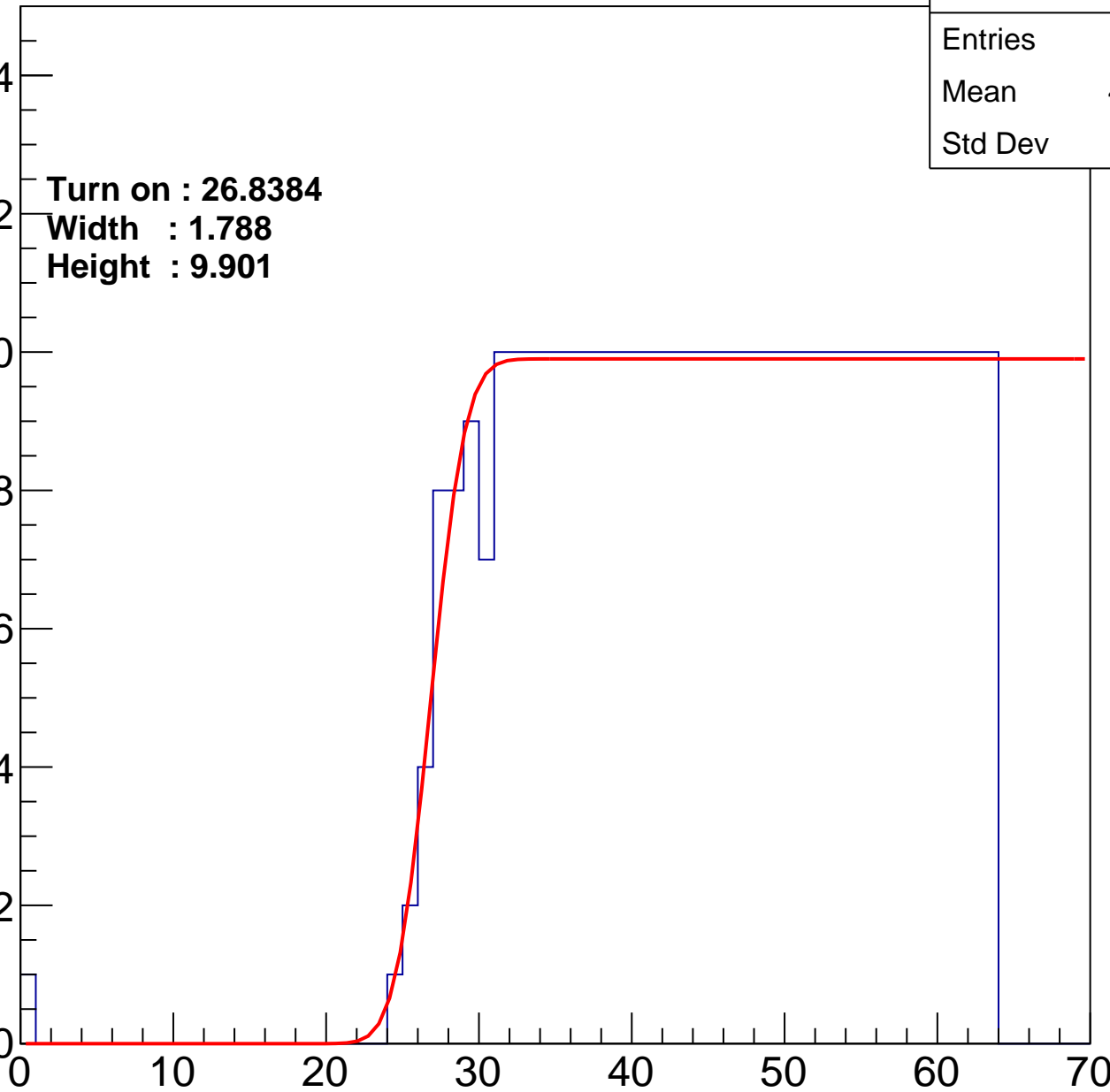
Width : 1.788

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	354
Mean	45.63
Std Dev	10.6

Turn on : 29.2841

Width : 2.440

Height : 10.029

Entry

14

12

10

8

6

4

2

0

0

10

20

30

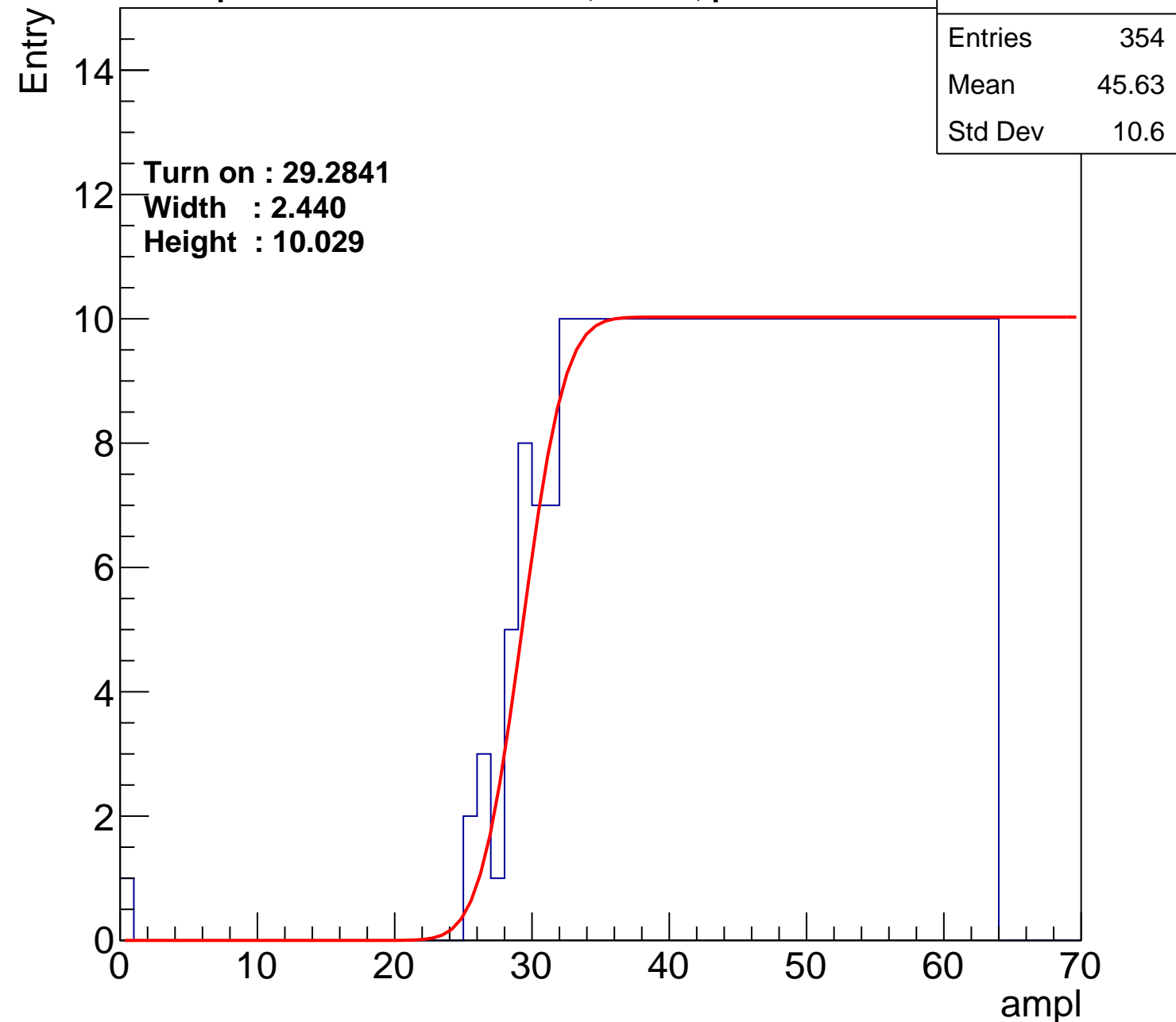
40

50

60

70

ampl



B0L102S, U8-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 27.2733

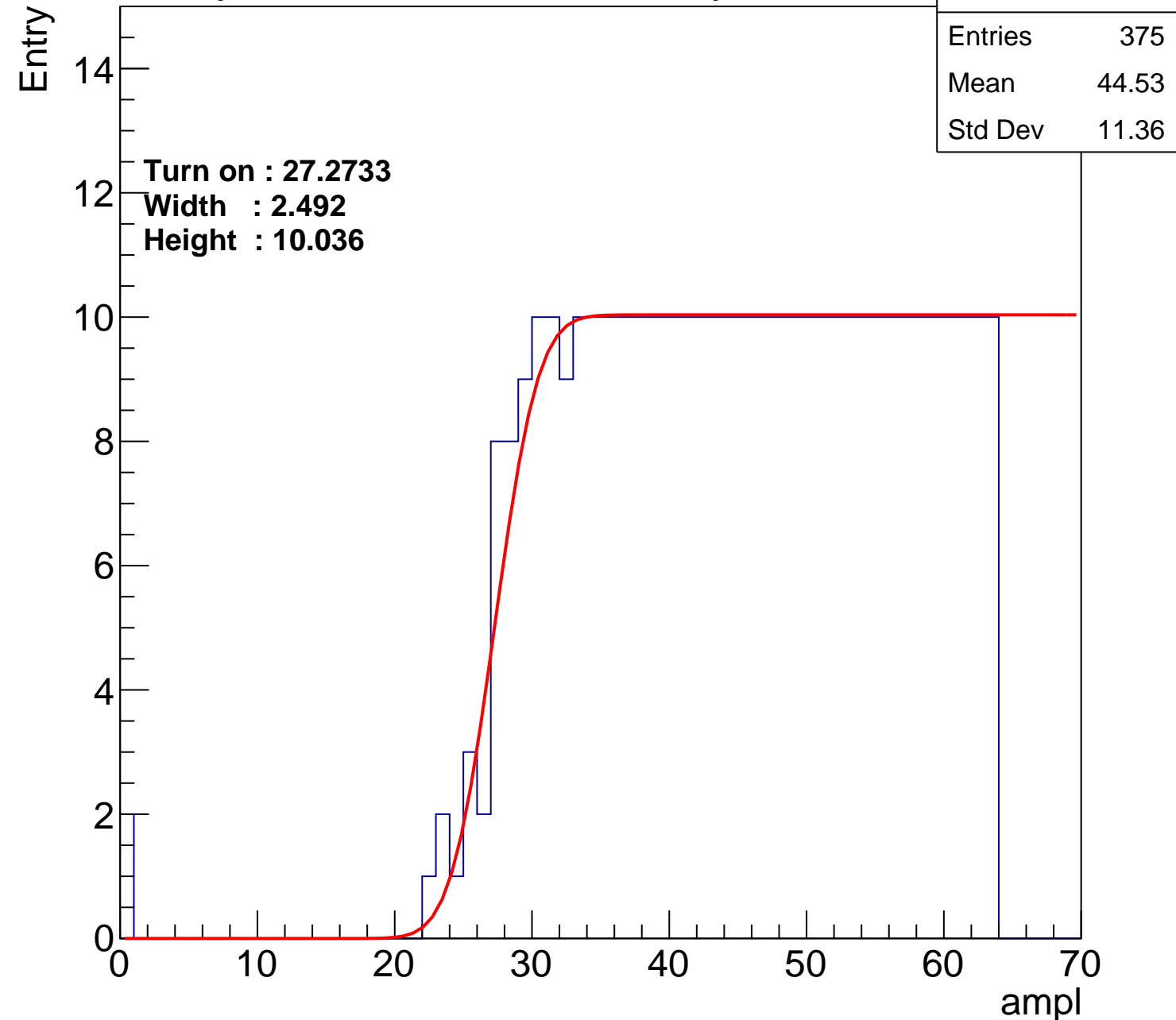
Width : 2.492

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.72
Std Dev	11.45

Turn on : 28.4507

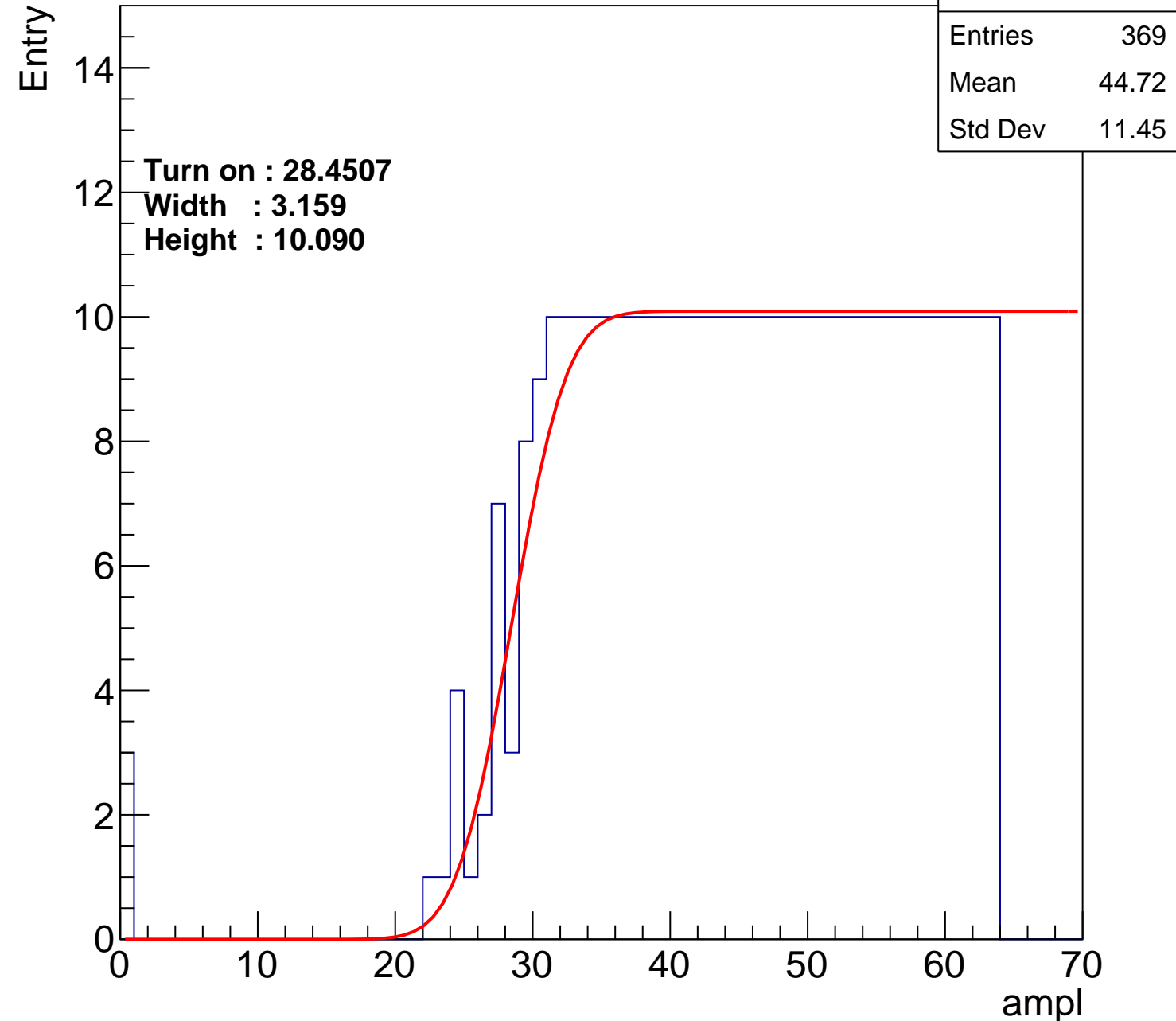
Width : 3.159

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch26

calib_packv5_042523_0143.root, FC#12, port B1

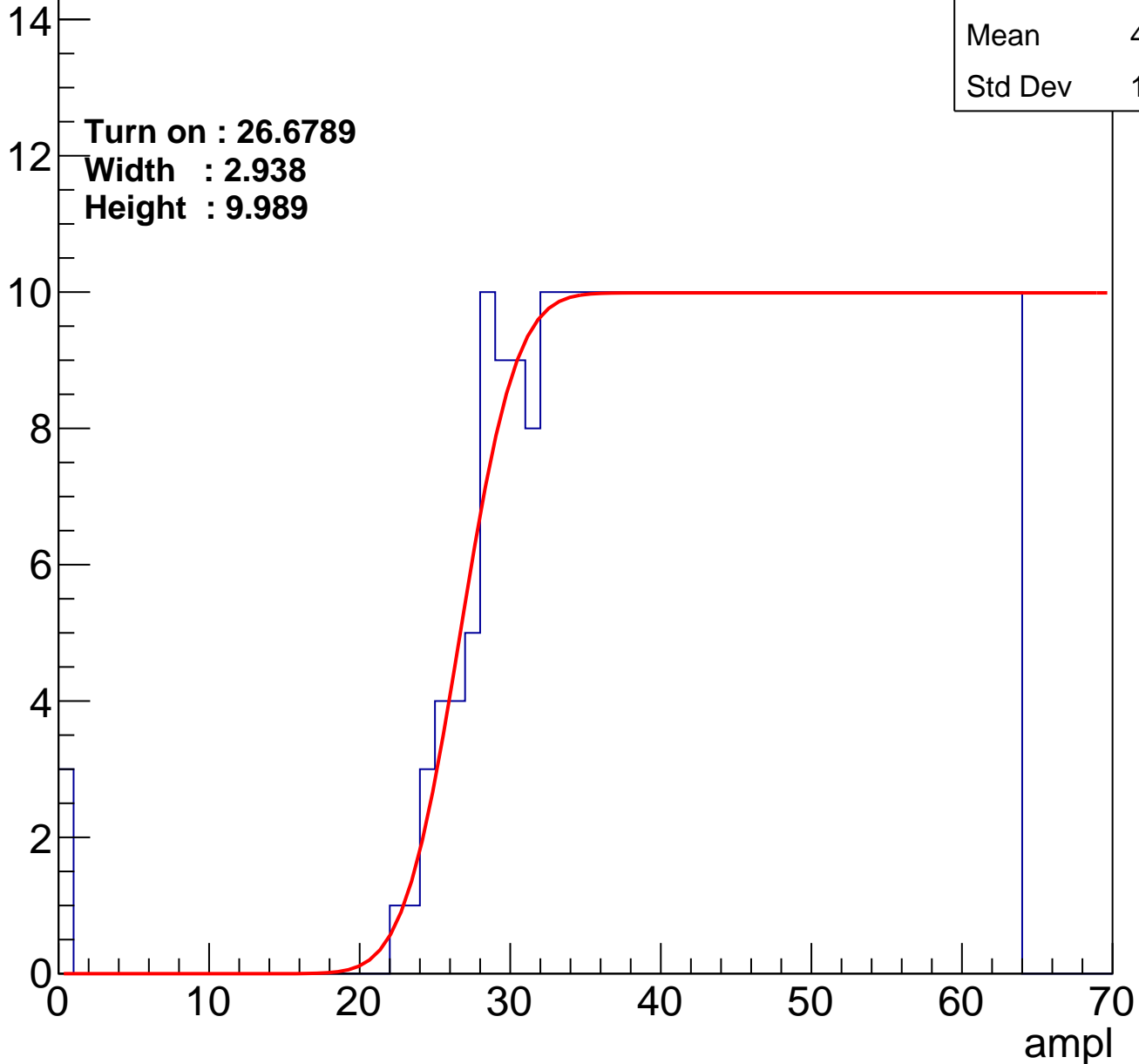
Entries	377
Mean	44.34
Std Dev	11.63

Turn on : 26.6789

Width : 2.938

Height : 9.989

Entry



B0L102S, U8-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.95
Std Dev	11.1

Turn on : 27.8956

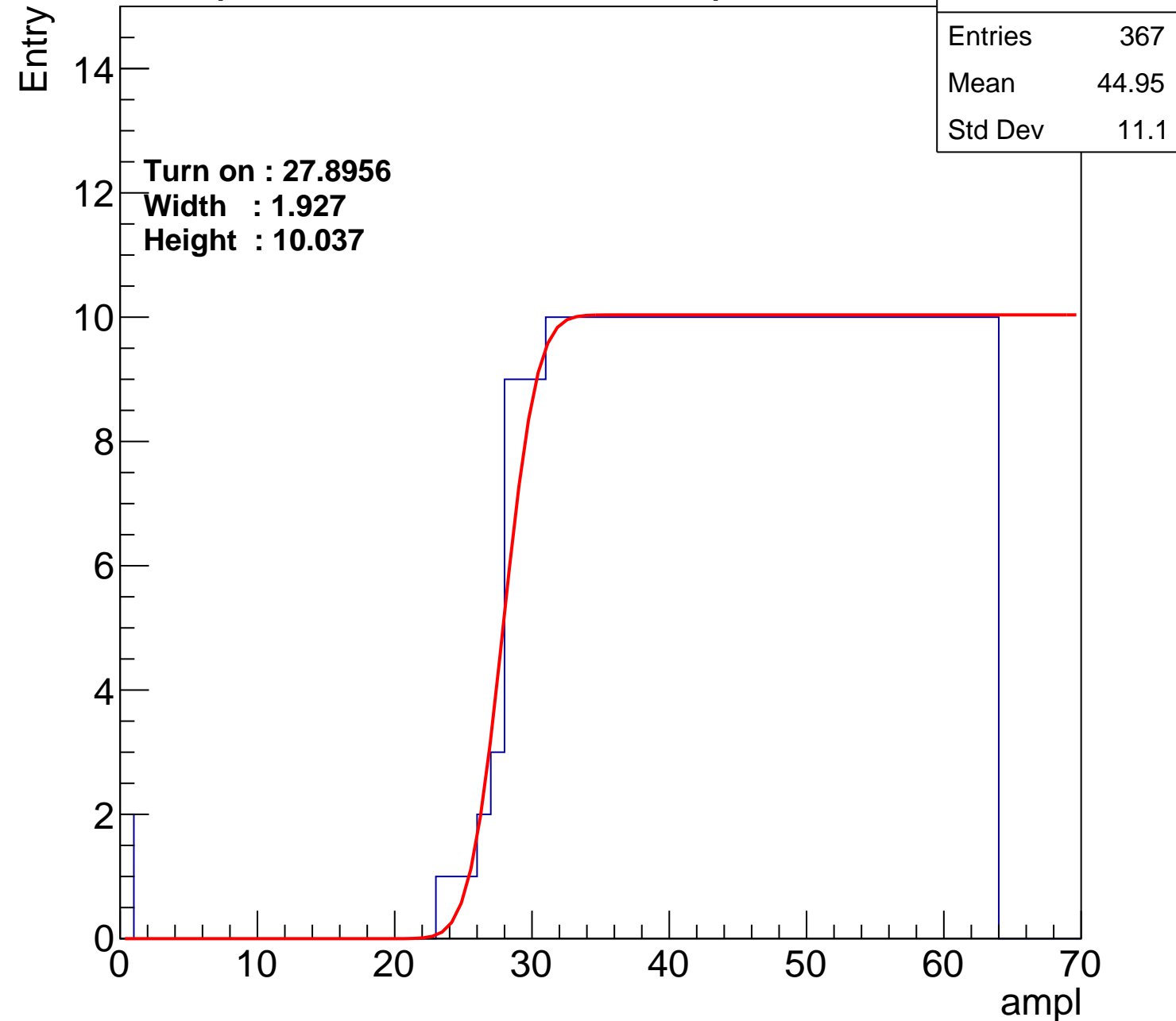
Width : 1.927

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 26.8075

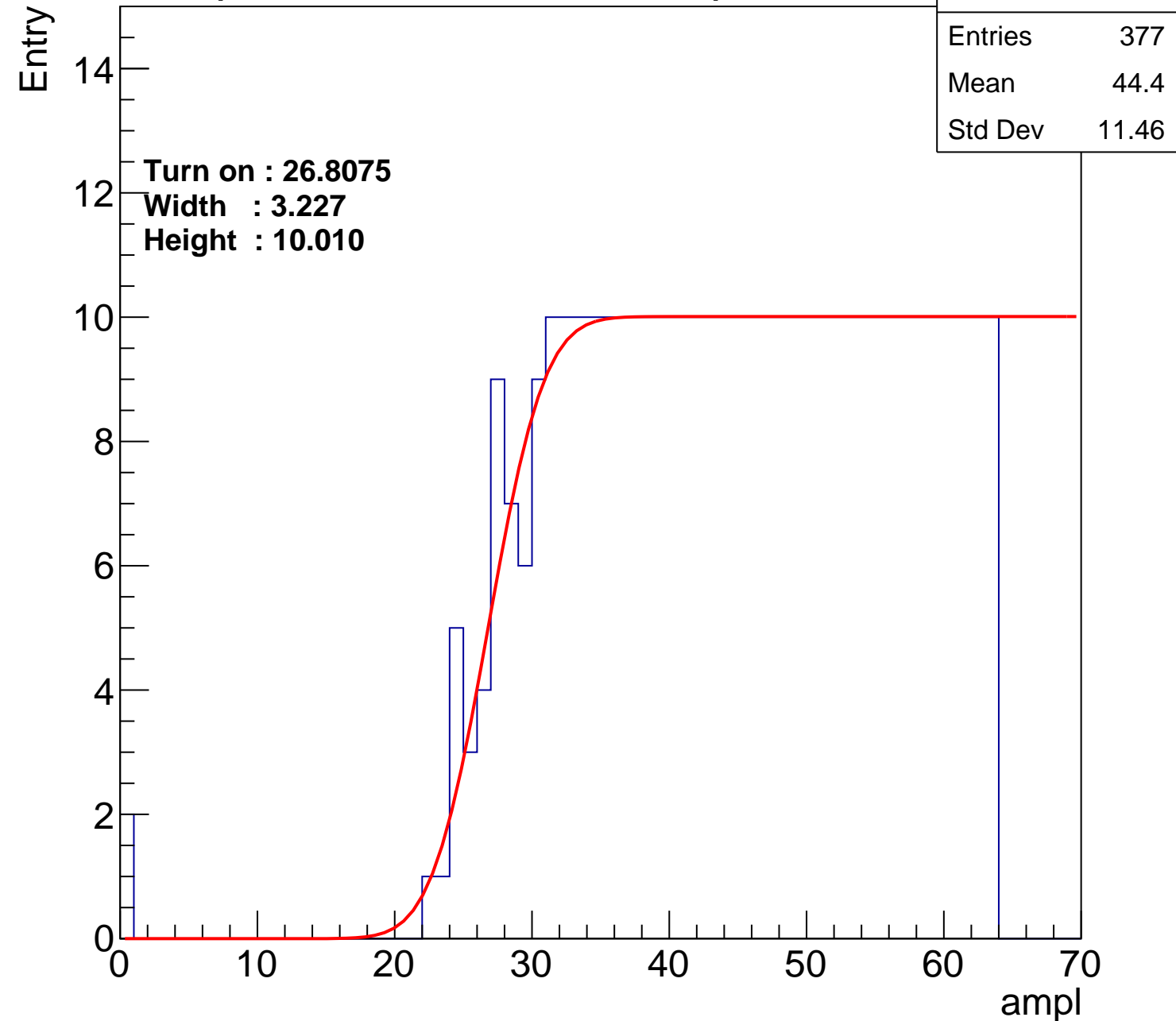
Width : 3.227

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.52
Std Dev	11.22

Turn on : 26.3445

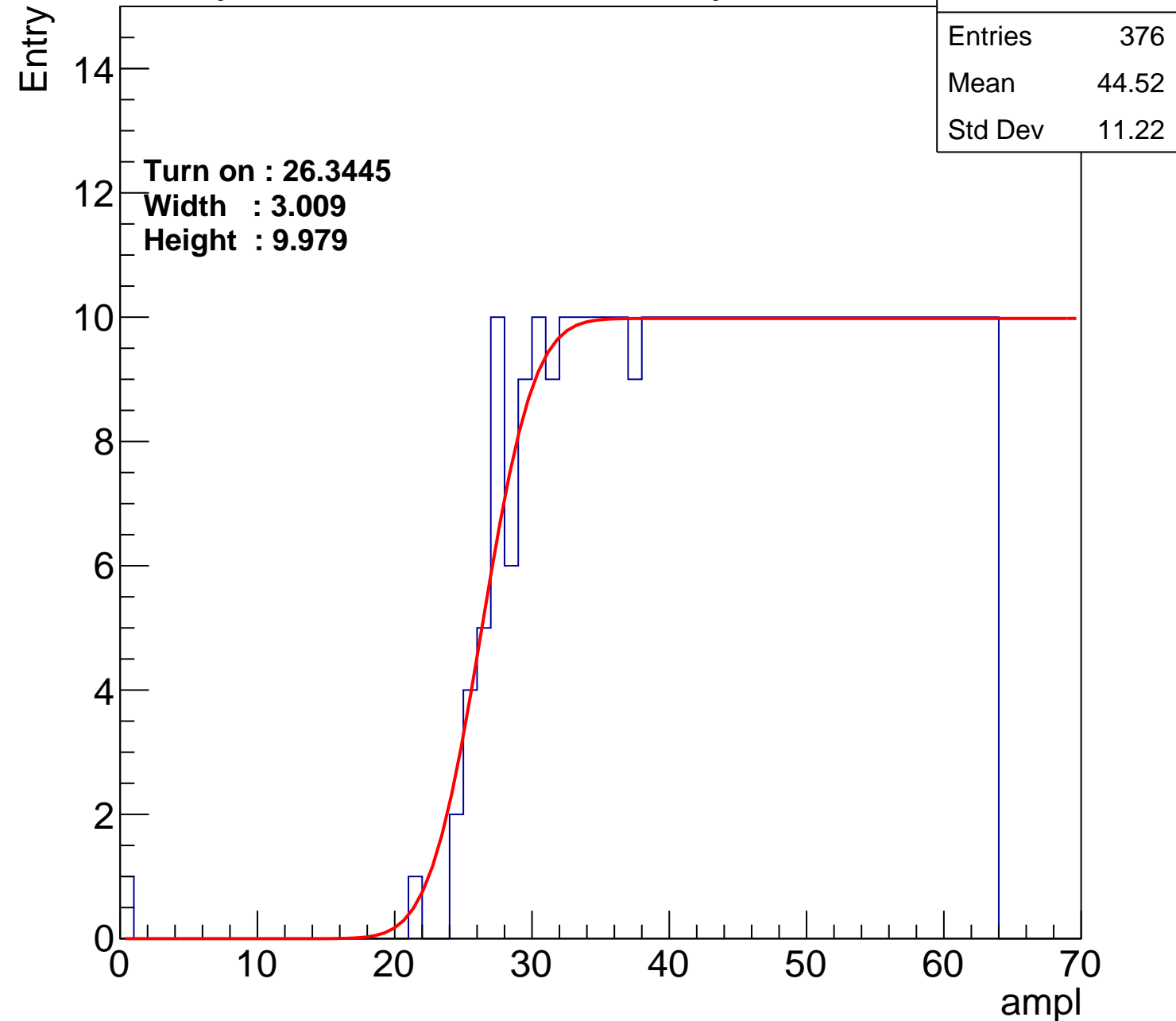
Width : 3.009

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.12
Std Dev	11.64

Turn on : 26.1773

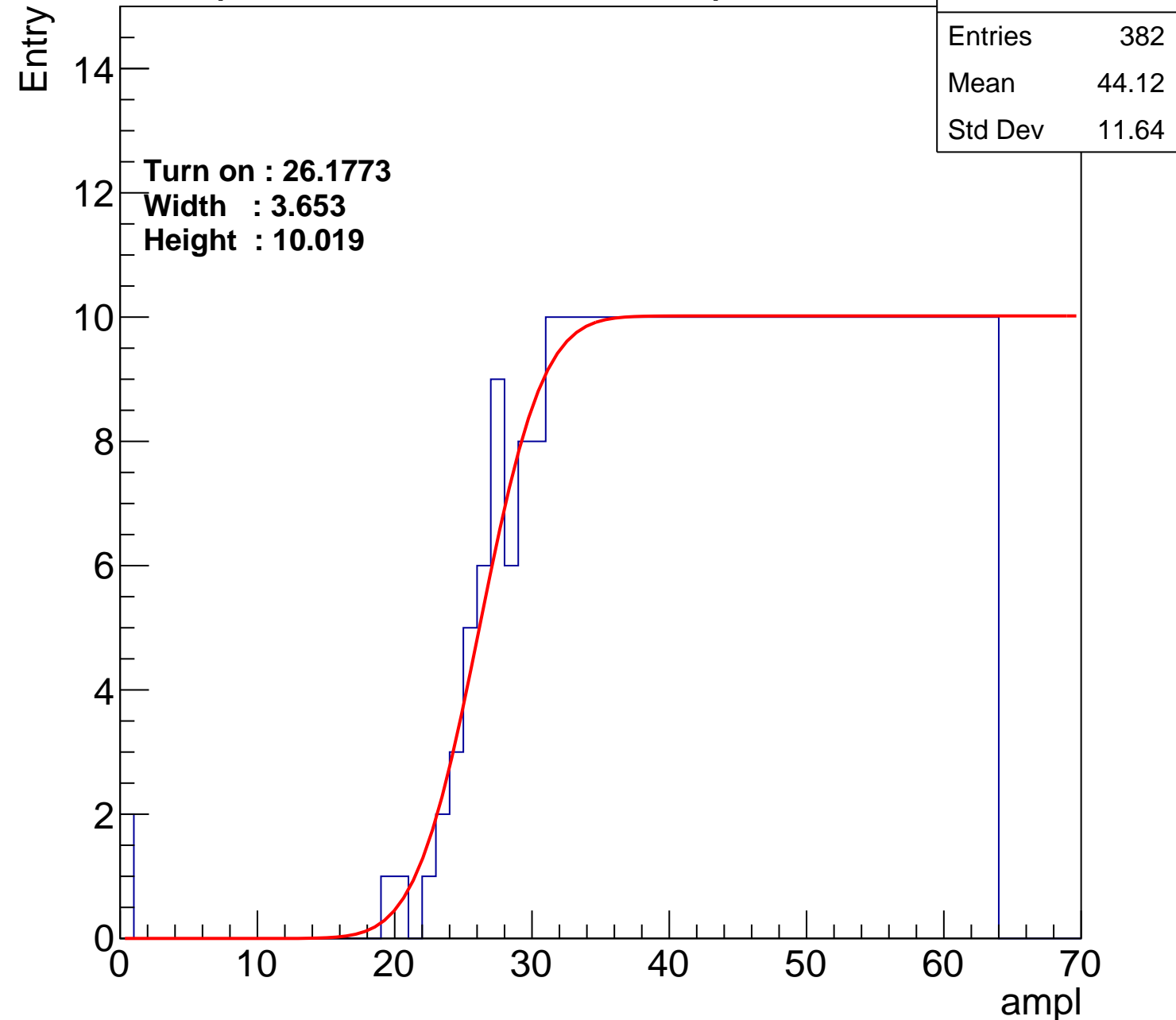
Width : 3.653

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 26.3209

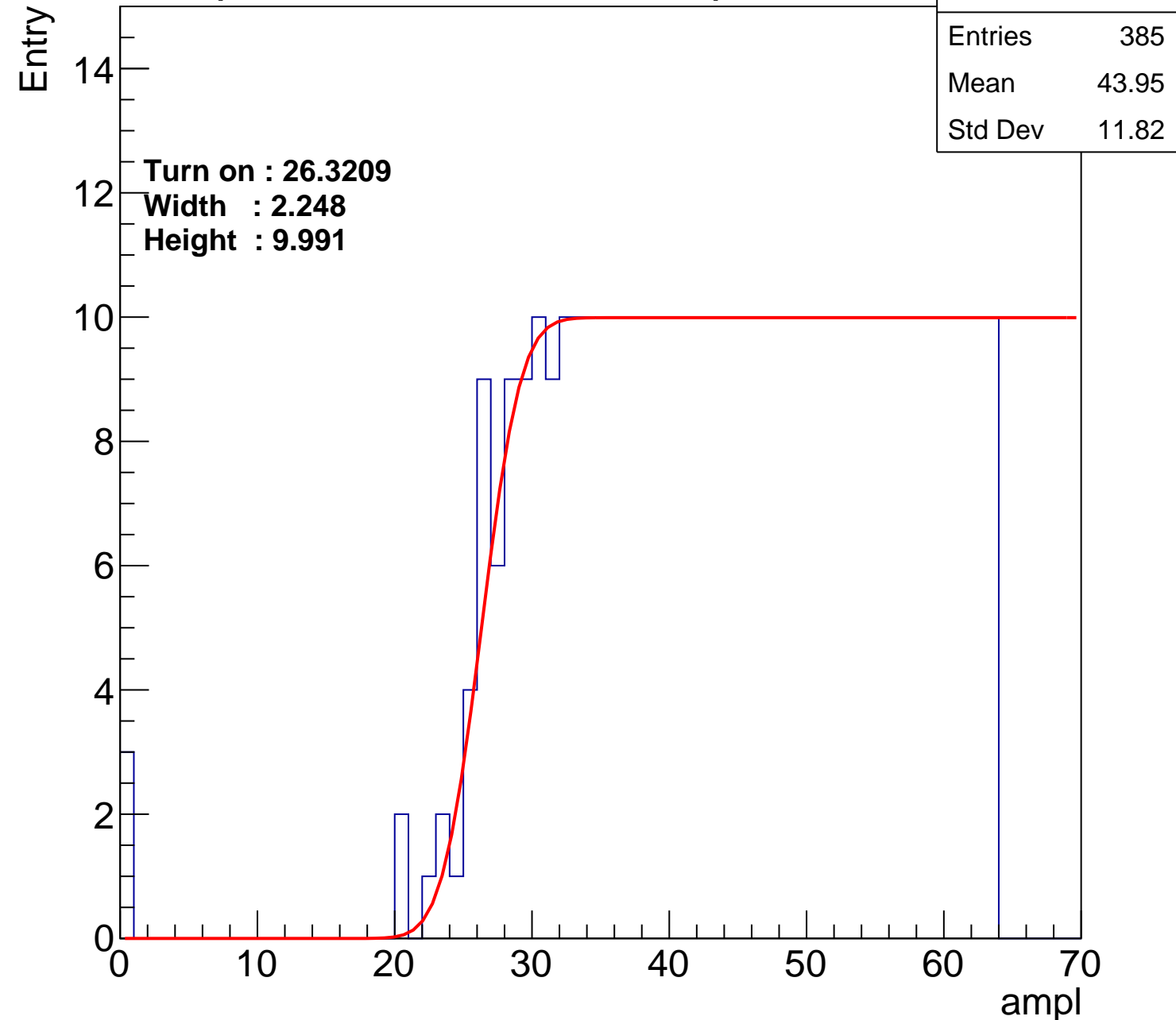
Width : 2.248

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.15
Std Dev	11.77

Turn on : 26.4399

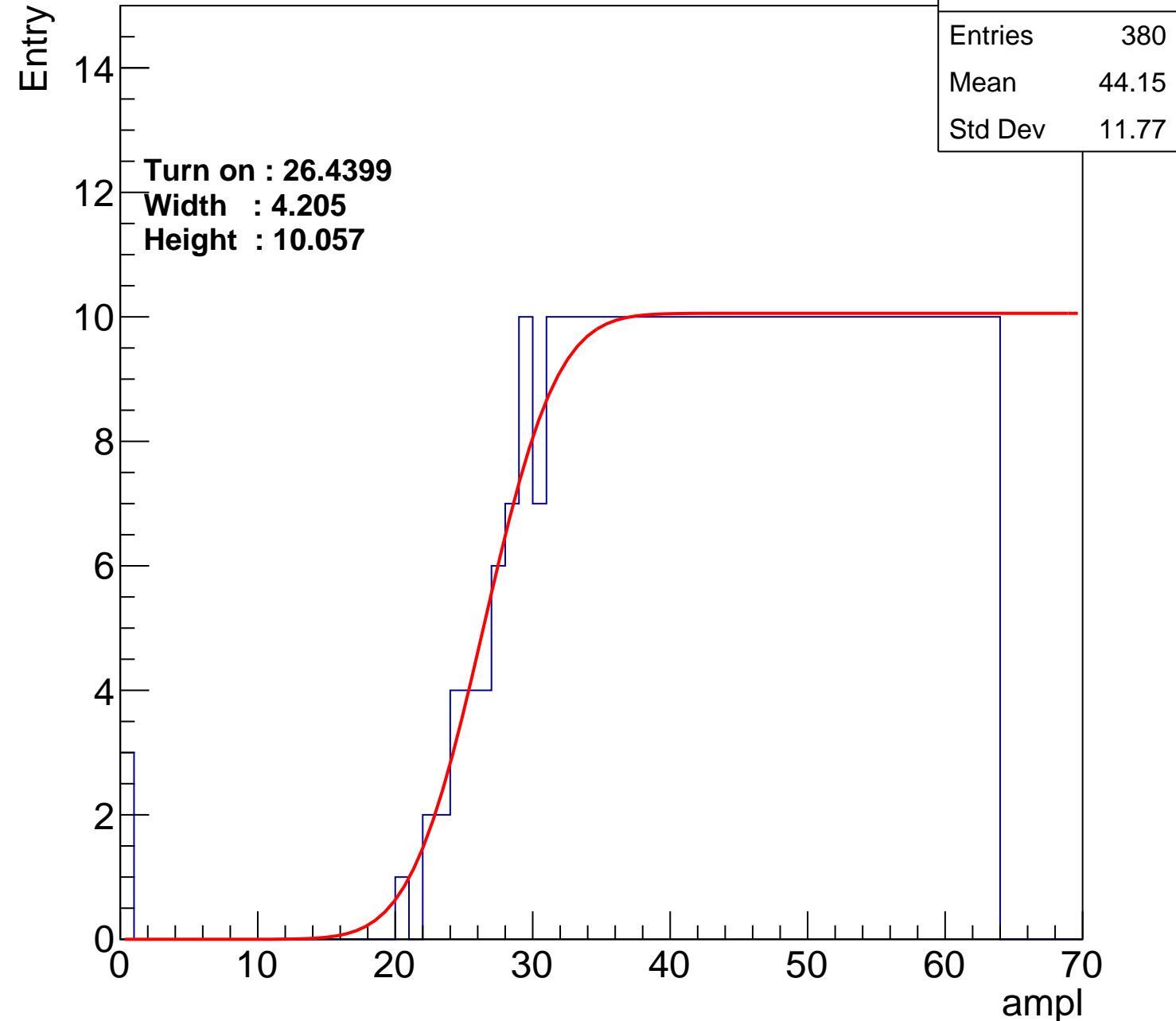
Width : 4.205

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.87
Std Dev	11.89

Turn on : 25.9665

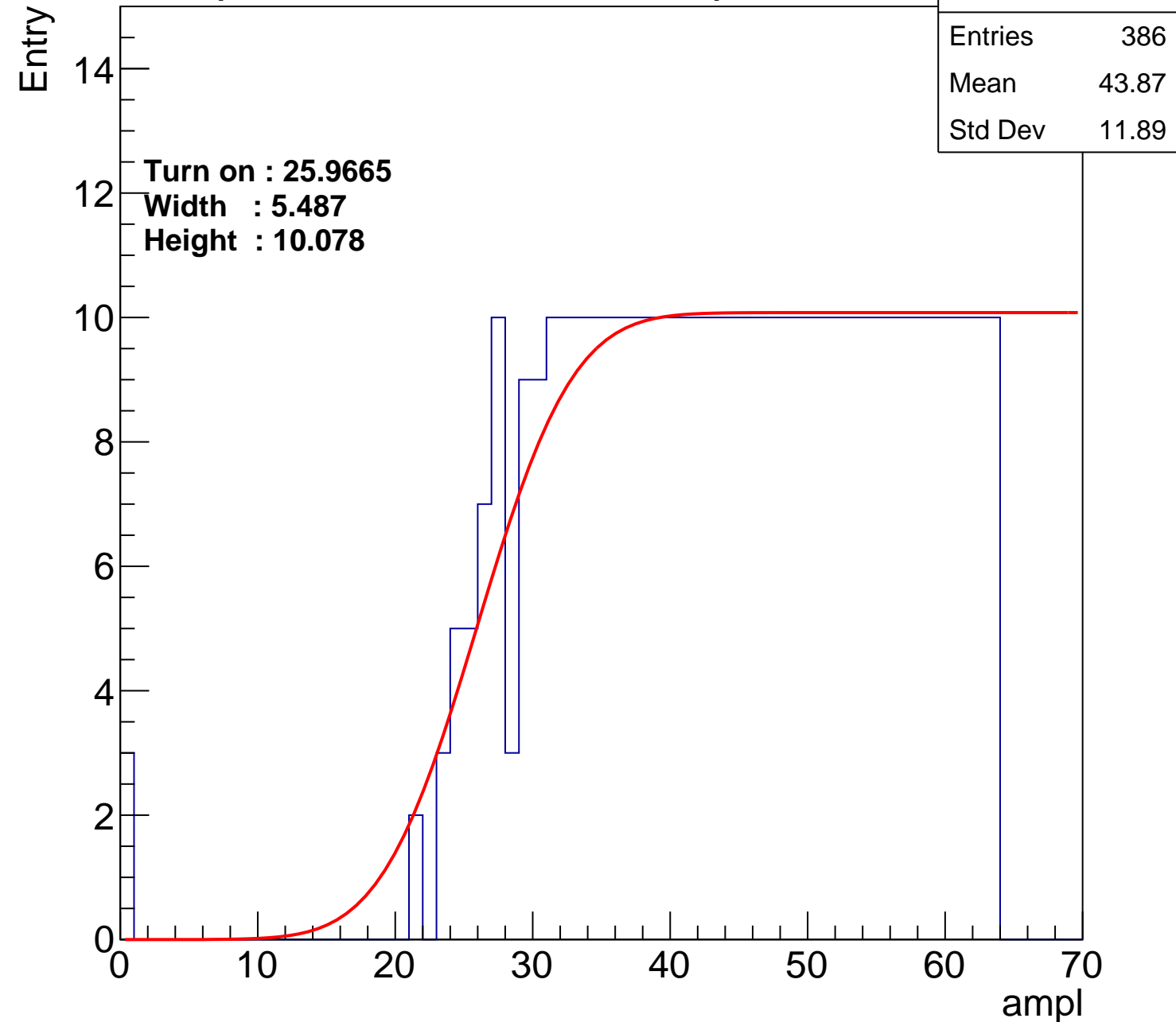
Width : 5.487

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.56
Std Dev	12.02

Turn on : 25.5432

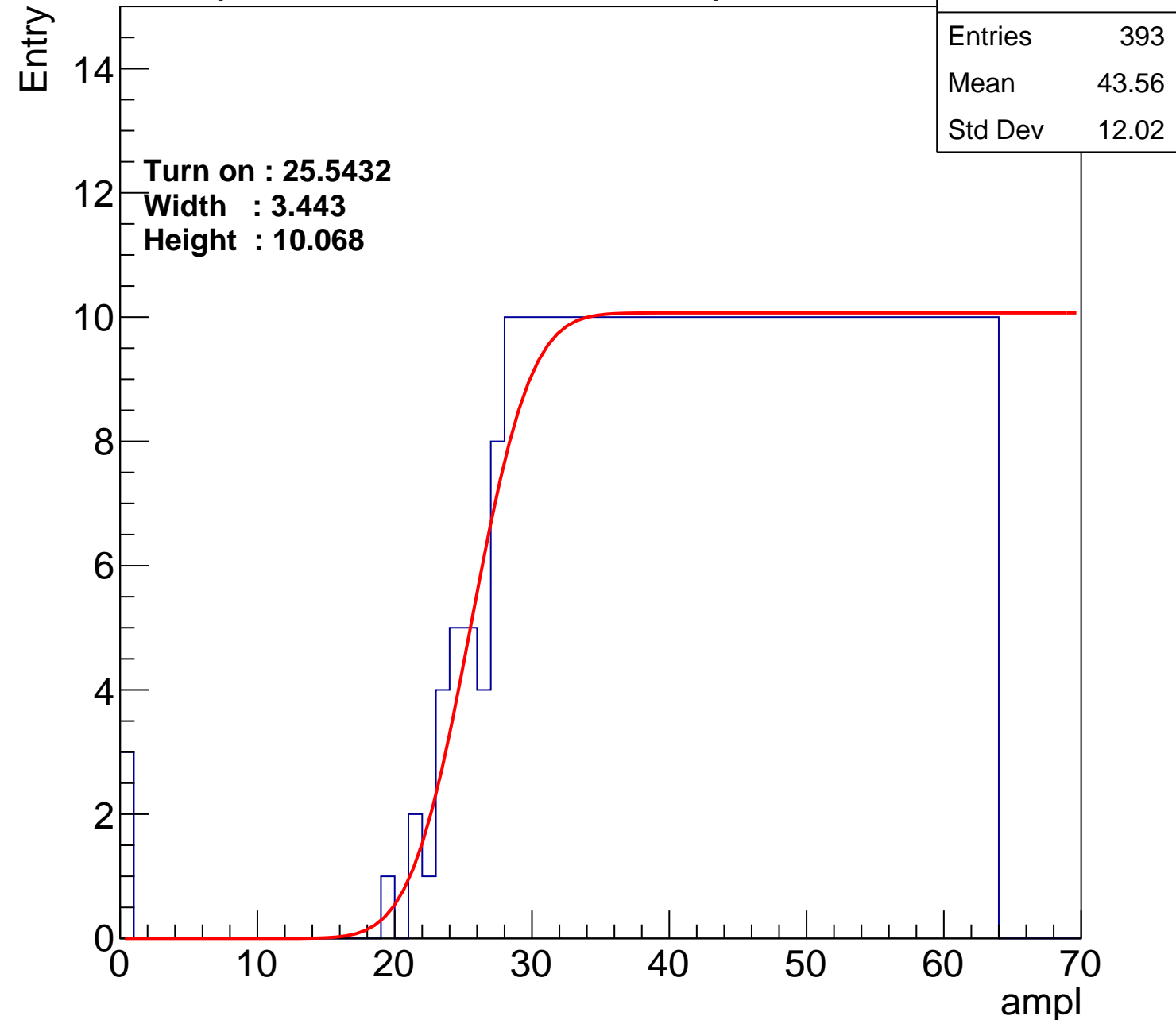
Width : 3.443

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.79
Std Dev	11.53

Turn on : 27.8503

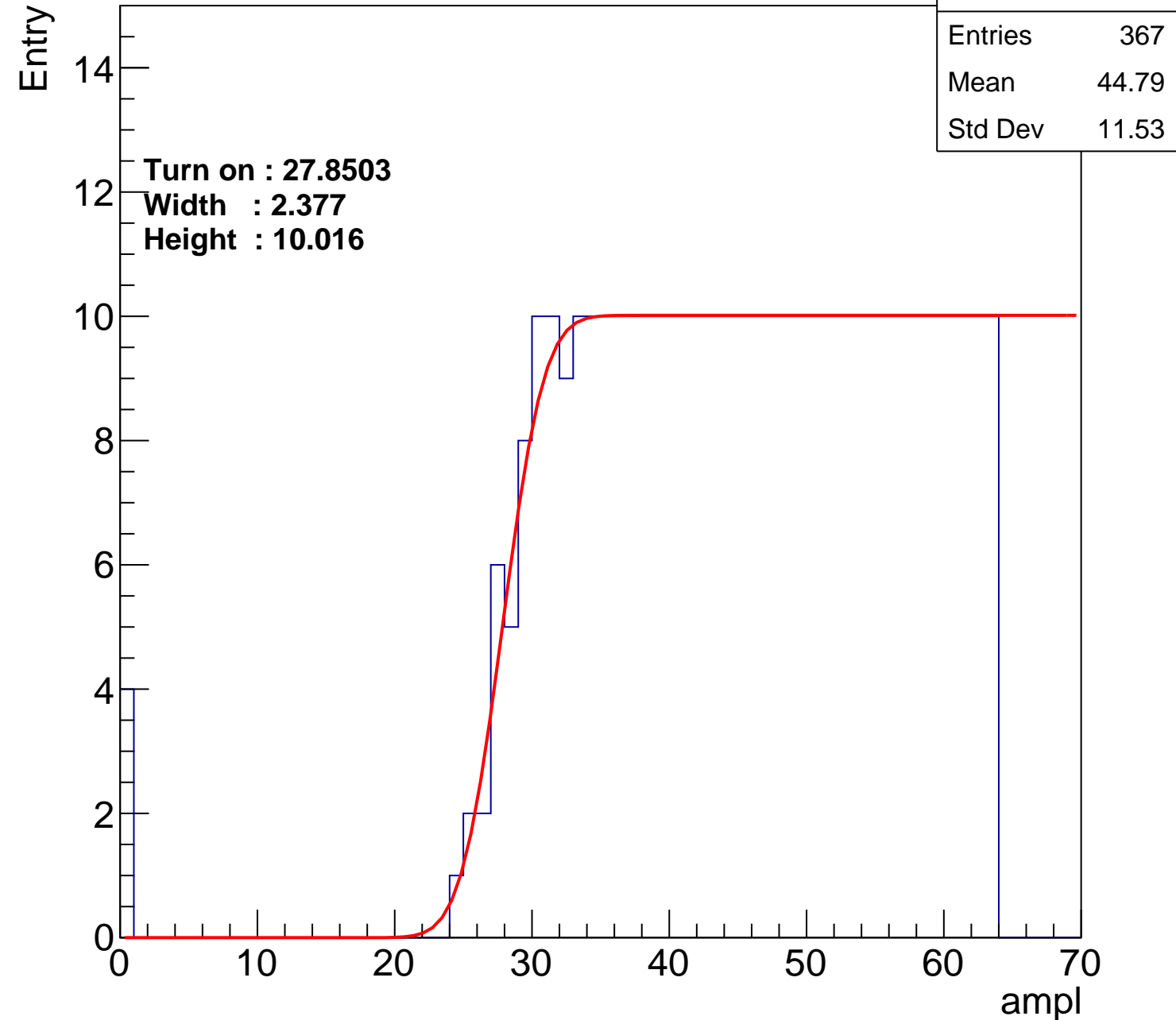
Width : 2.377

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.2
Std Dev	11.7

Turn on : 26.5586

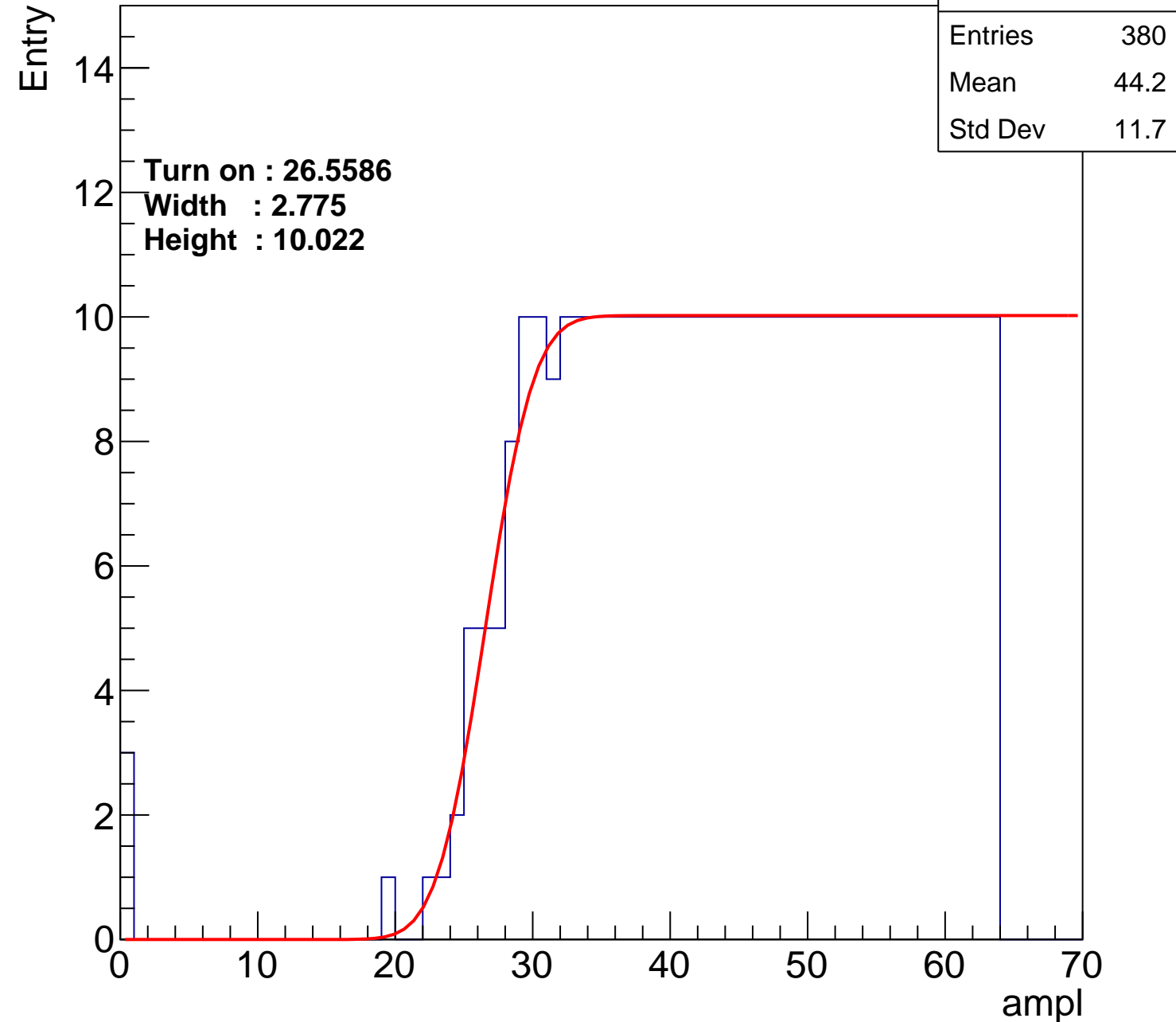
Width : 2.775

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.58
Std Dev	12.05

Turn on : 25.5399

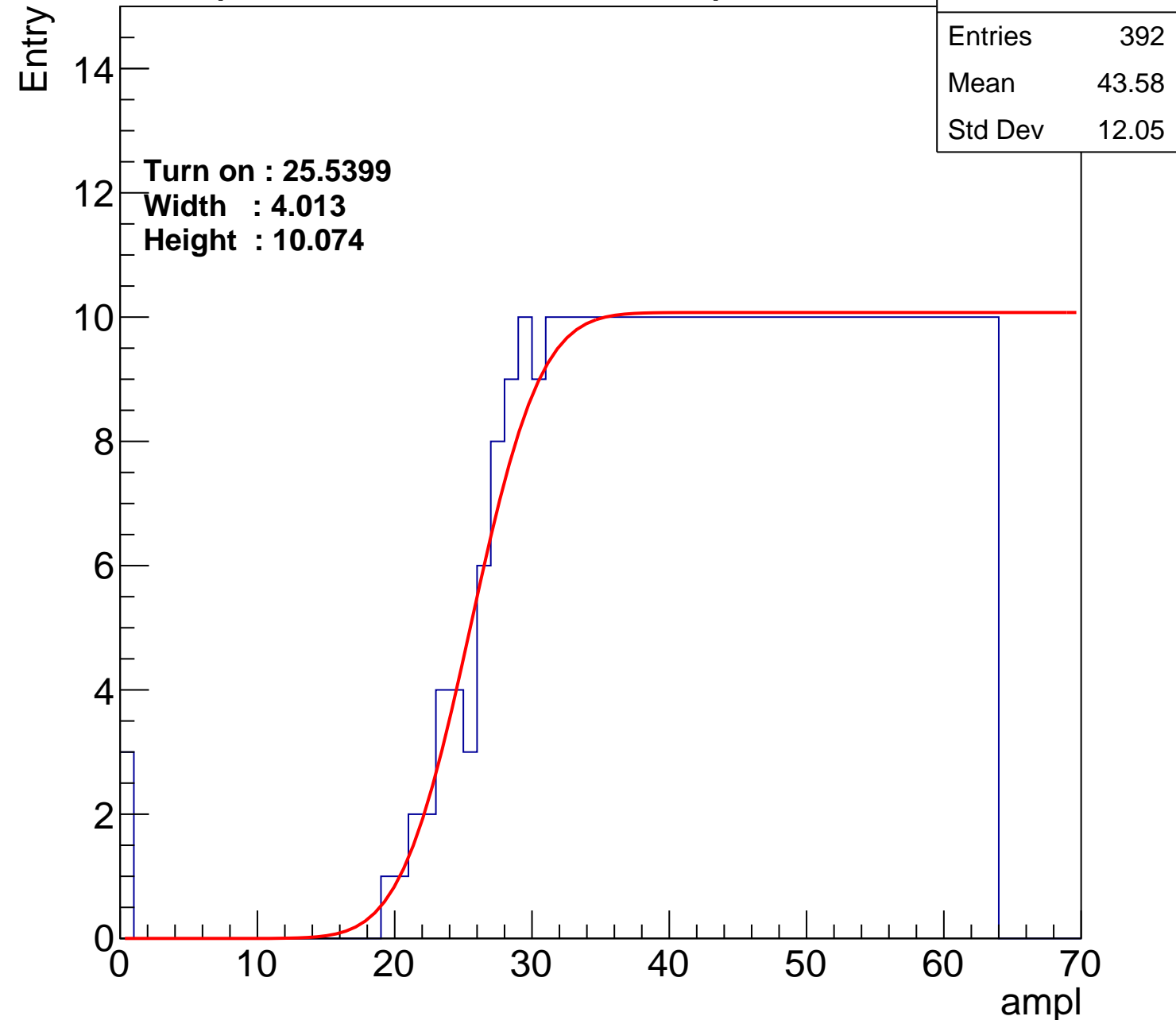
Width : 4.013

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	373
Mean	44.63
Std Dev	11.22

Turn on : 26.6589

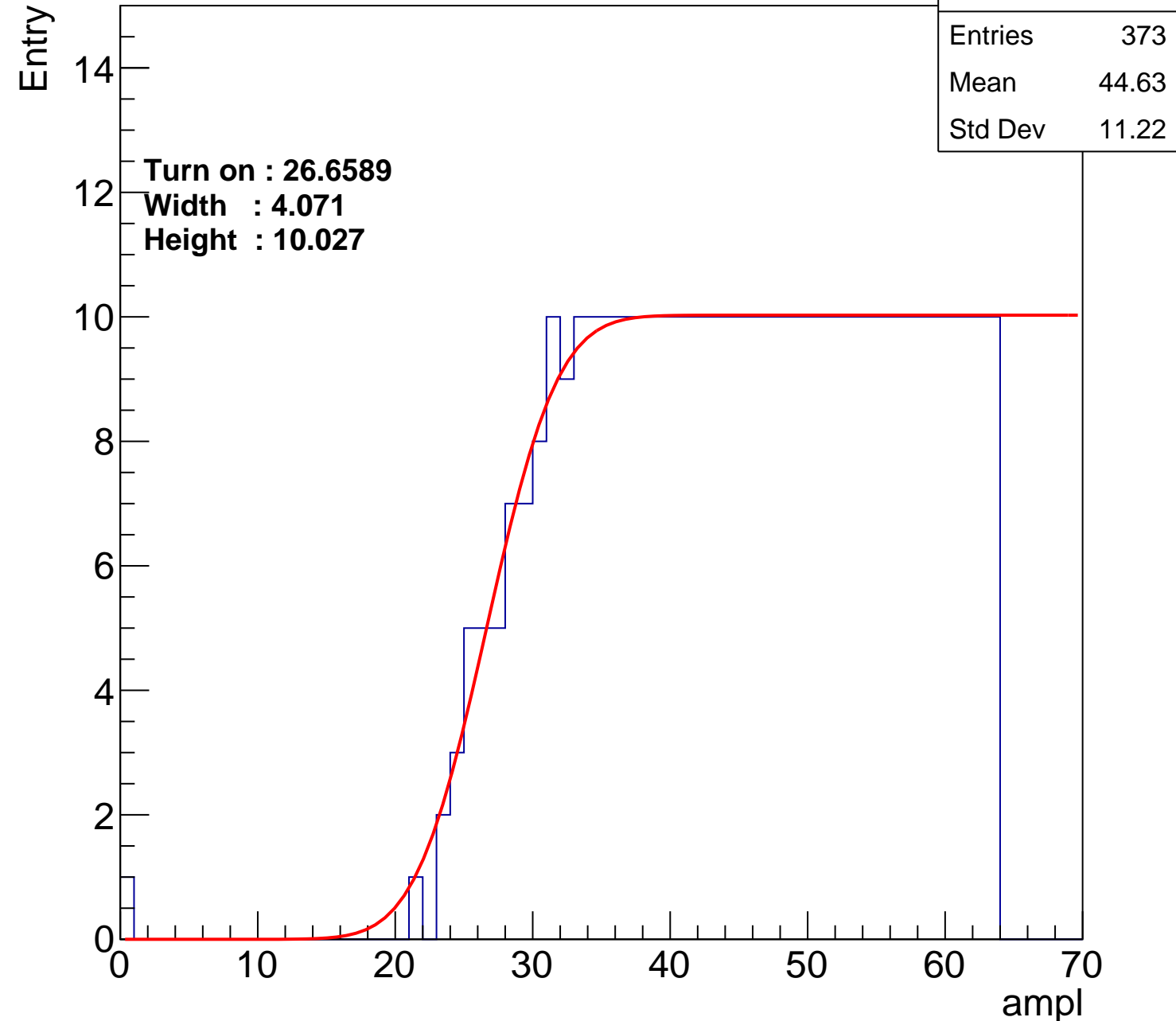
Width : 4.071

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.08
Std Dev	11.59

Turn on : 25.7586

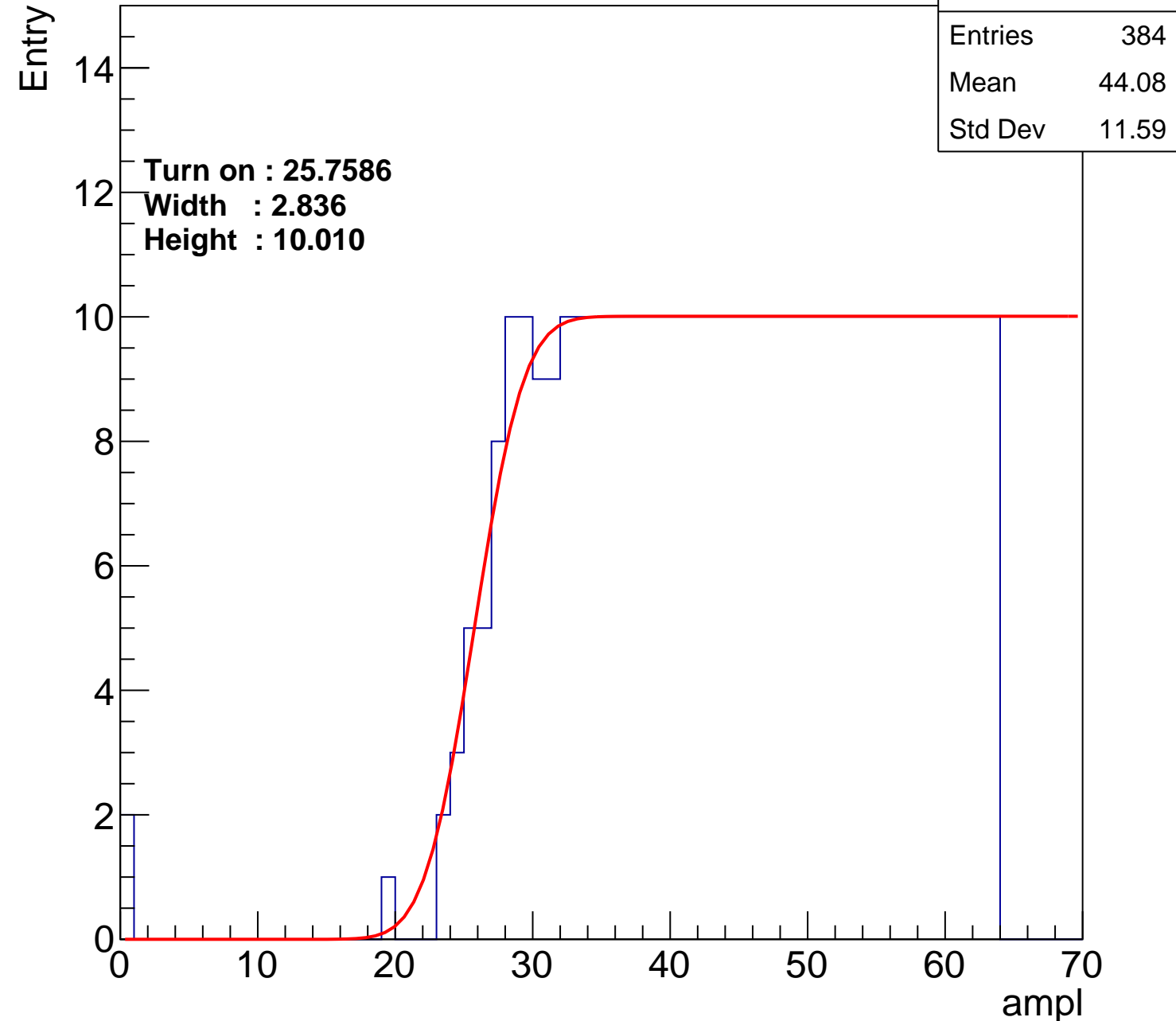
Width : 2.836

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.84
Std Dev	11.64

Turn on : 25.7727

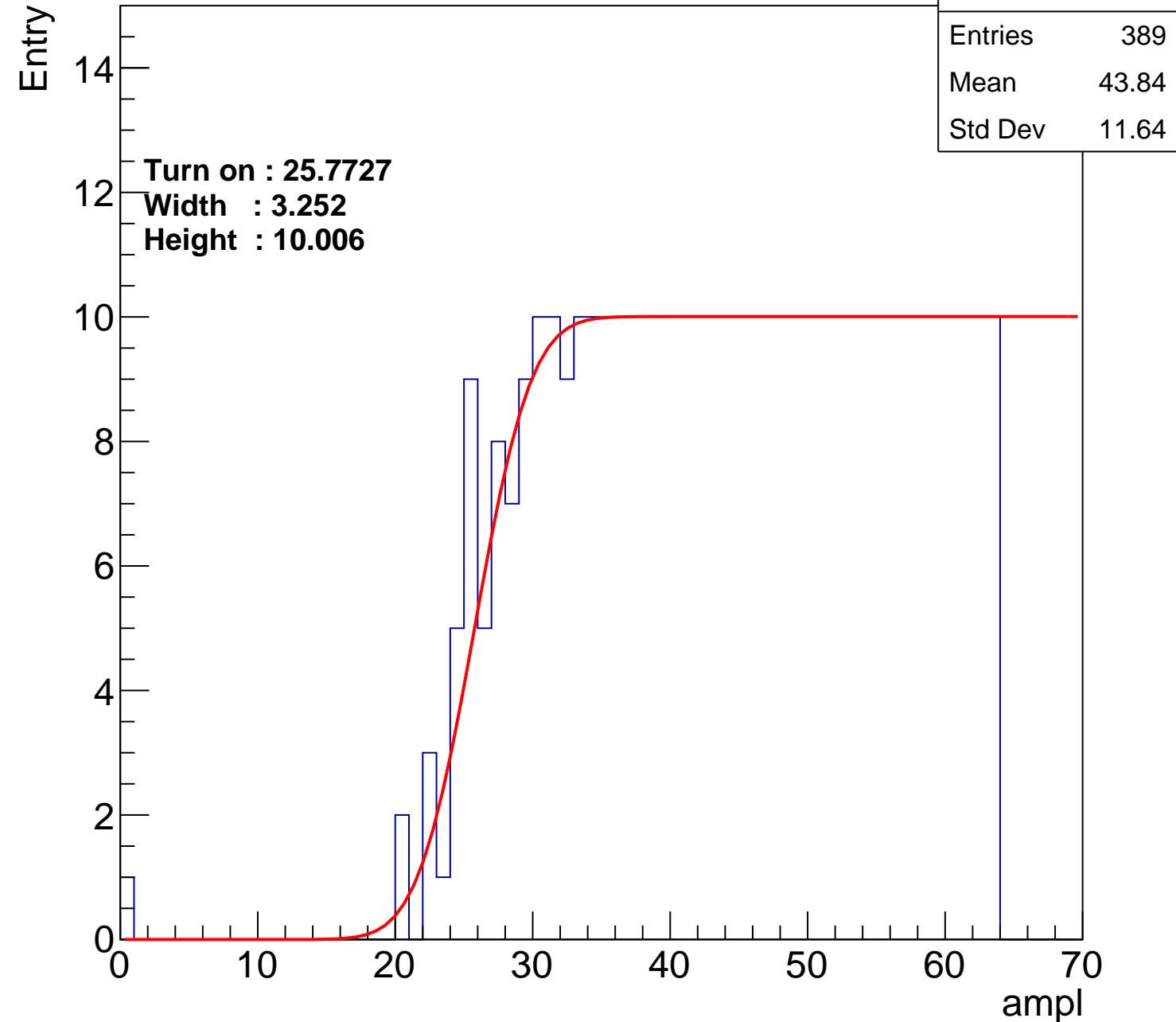
Width : 3.252

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.17
Std Dev	11.81

Turn on : 26.3445

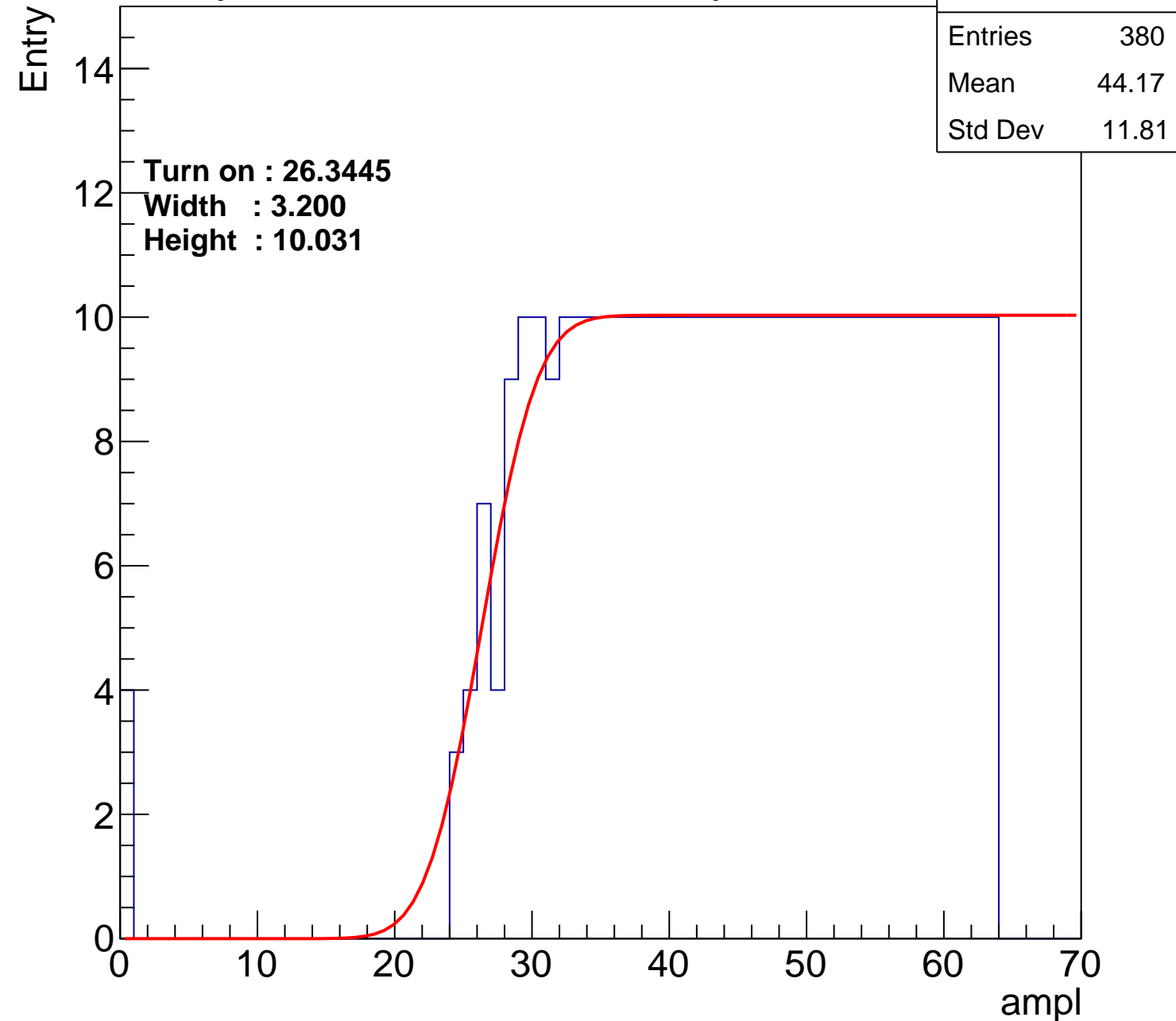
Width : 3.200

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.45
Std Dev	11.85

Turn on : 24.4215

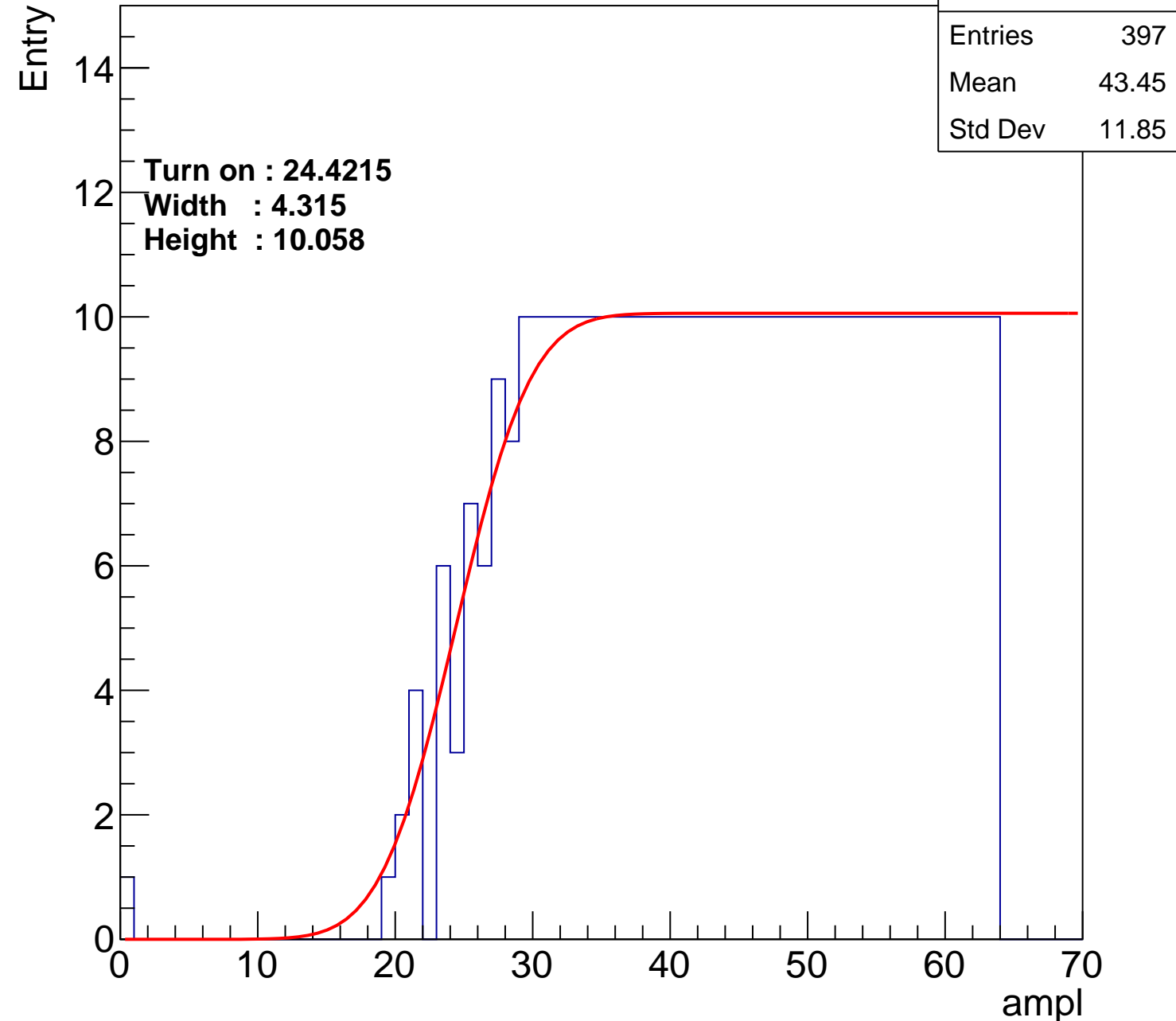
Width : 4.315

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.82
Std Dev	11.86

Turn on : 26.0974

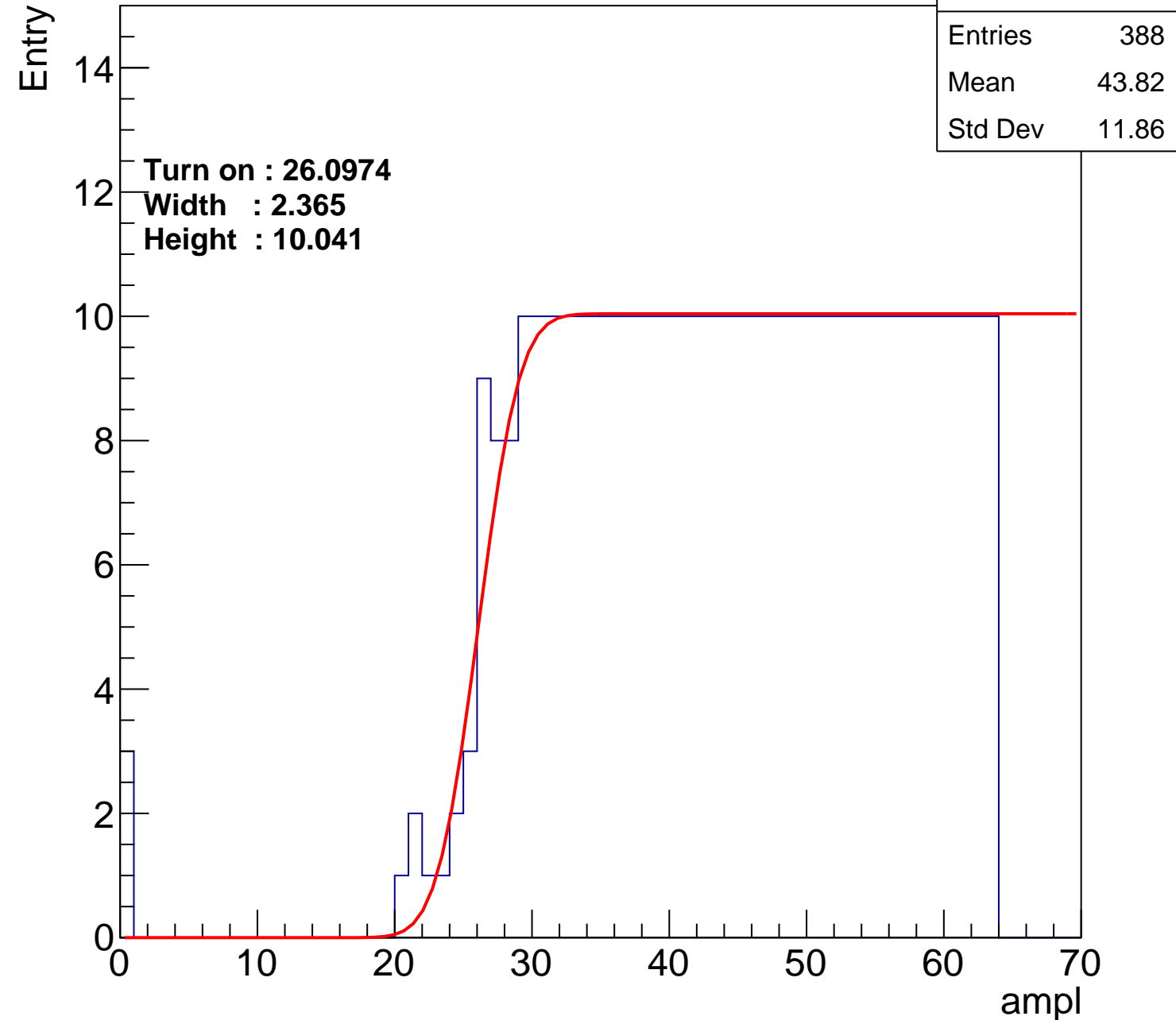
Width : 2.365

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 26.3893

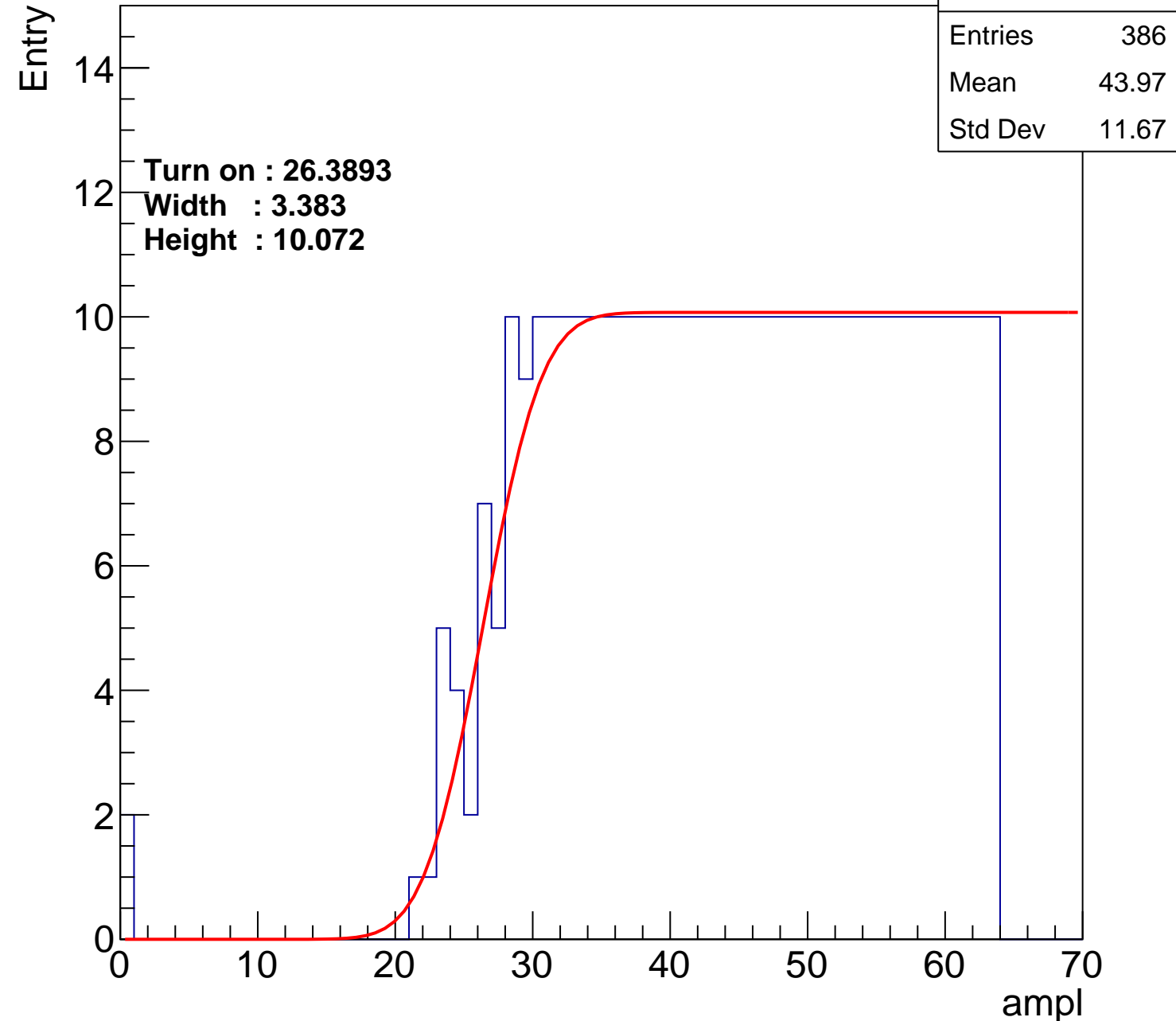
Width : 3.383

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.64
Std Dev	11.88

Turn on : 25.2397

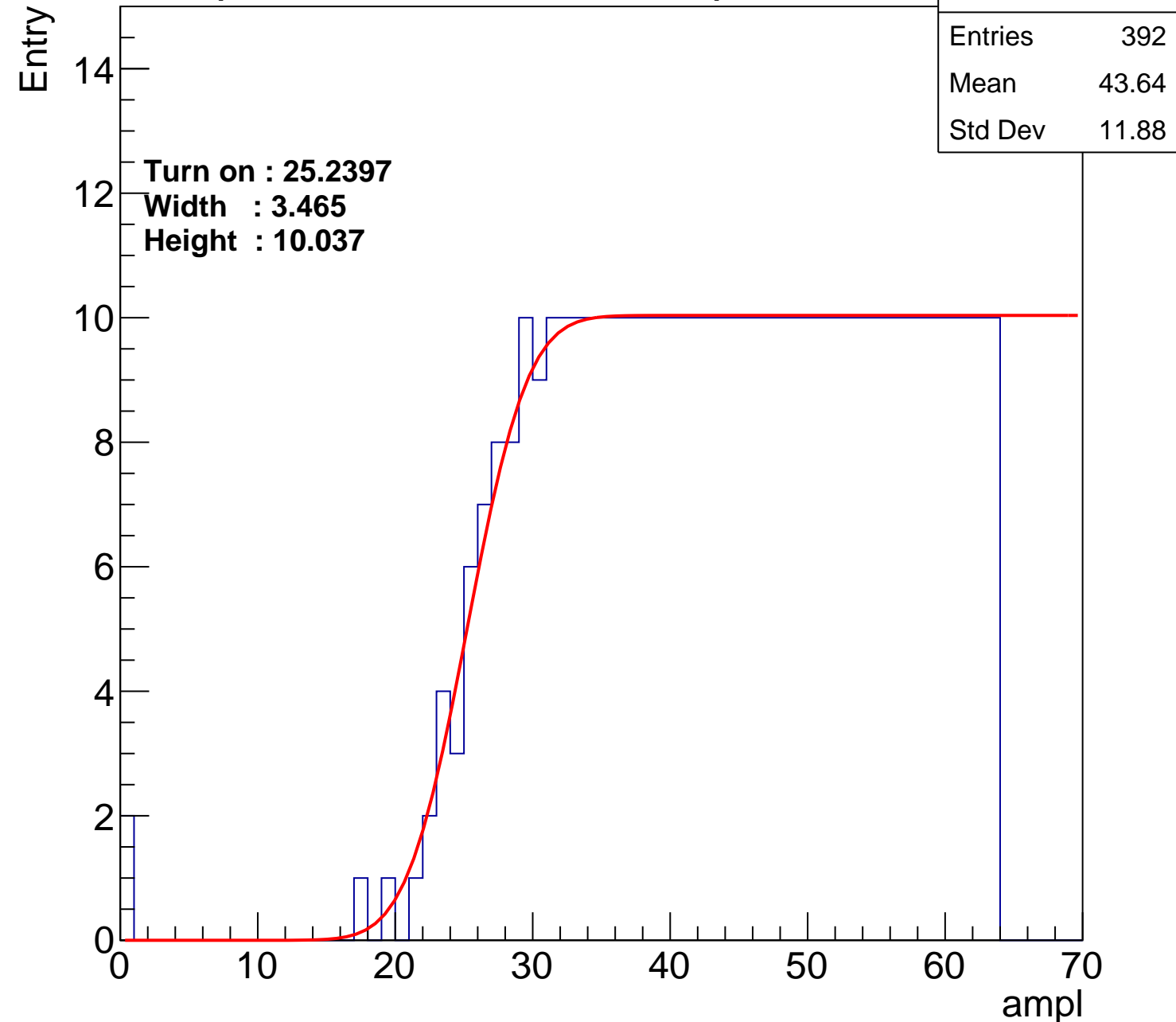
Width : 3.465

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.89
Std Dev	12.39

Turn on : 23.7774

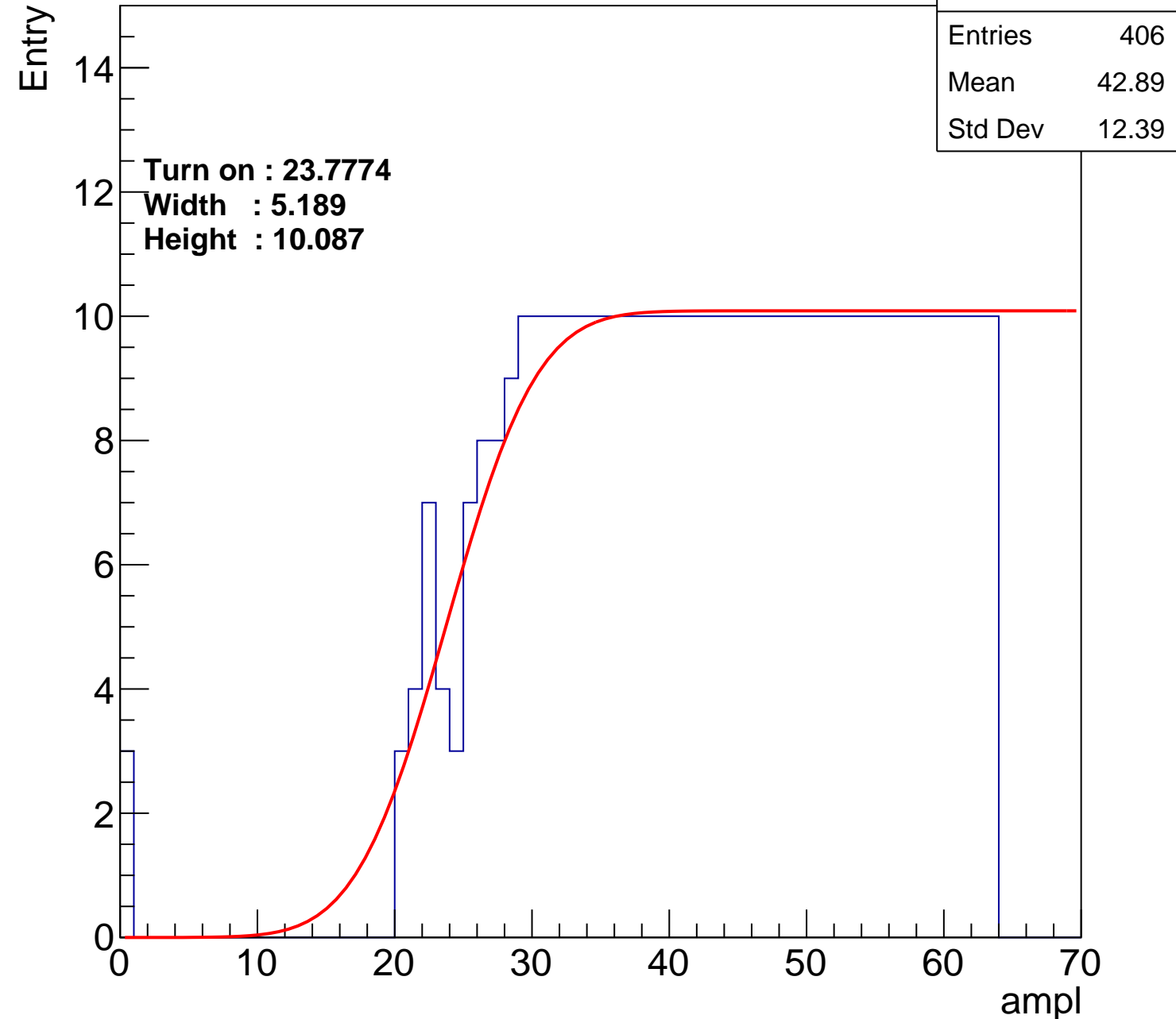
Width : 5.189

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.9
Std Dev	11.19

Turn on : 27.4912

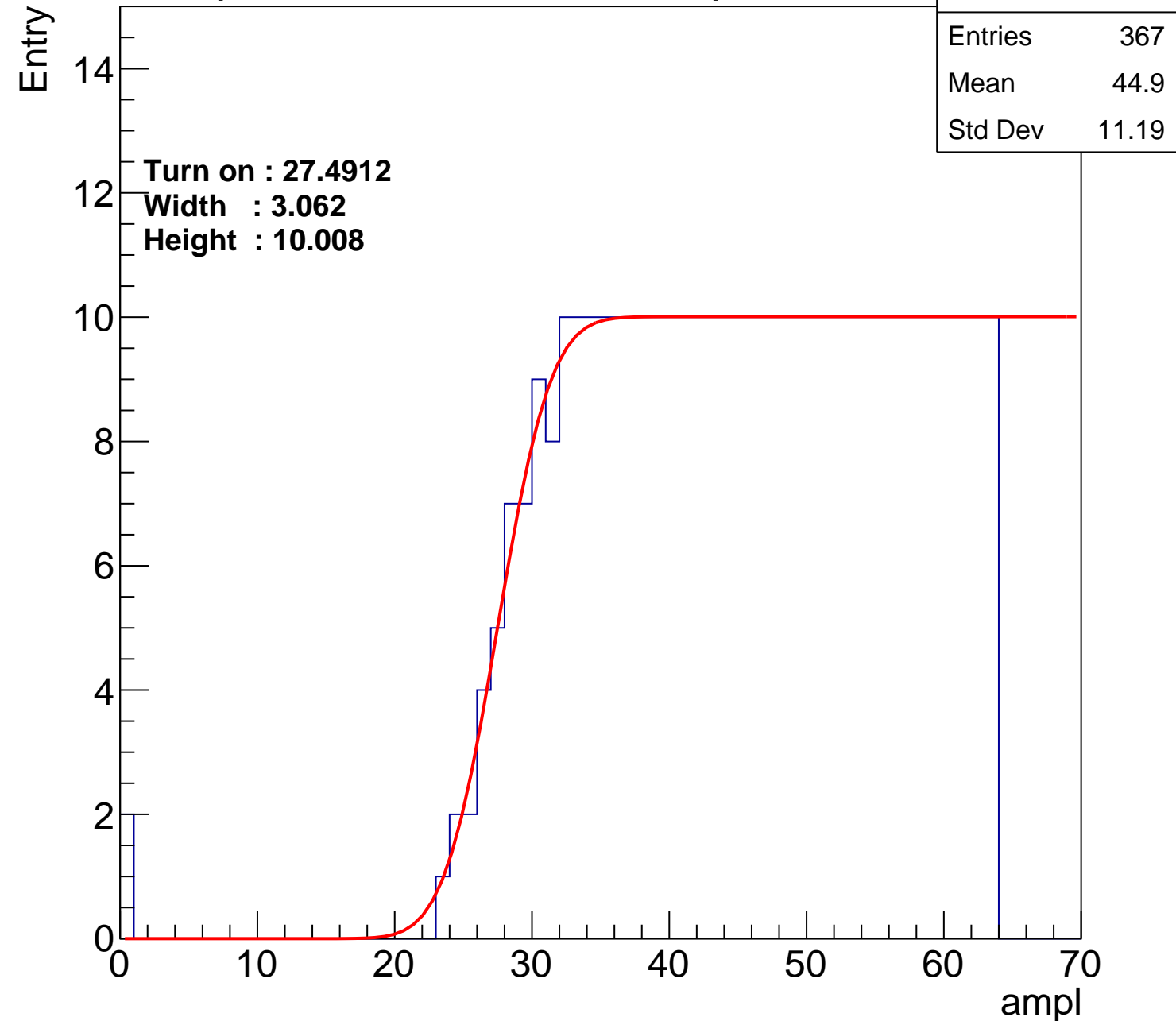
Width : 3.062

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.53
Std Dev	12.18

Turn on : 25.0935

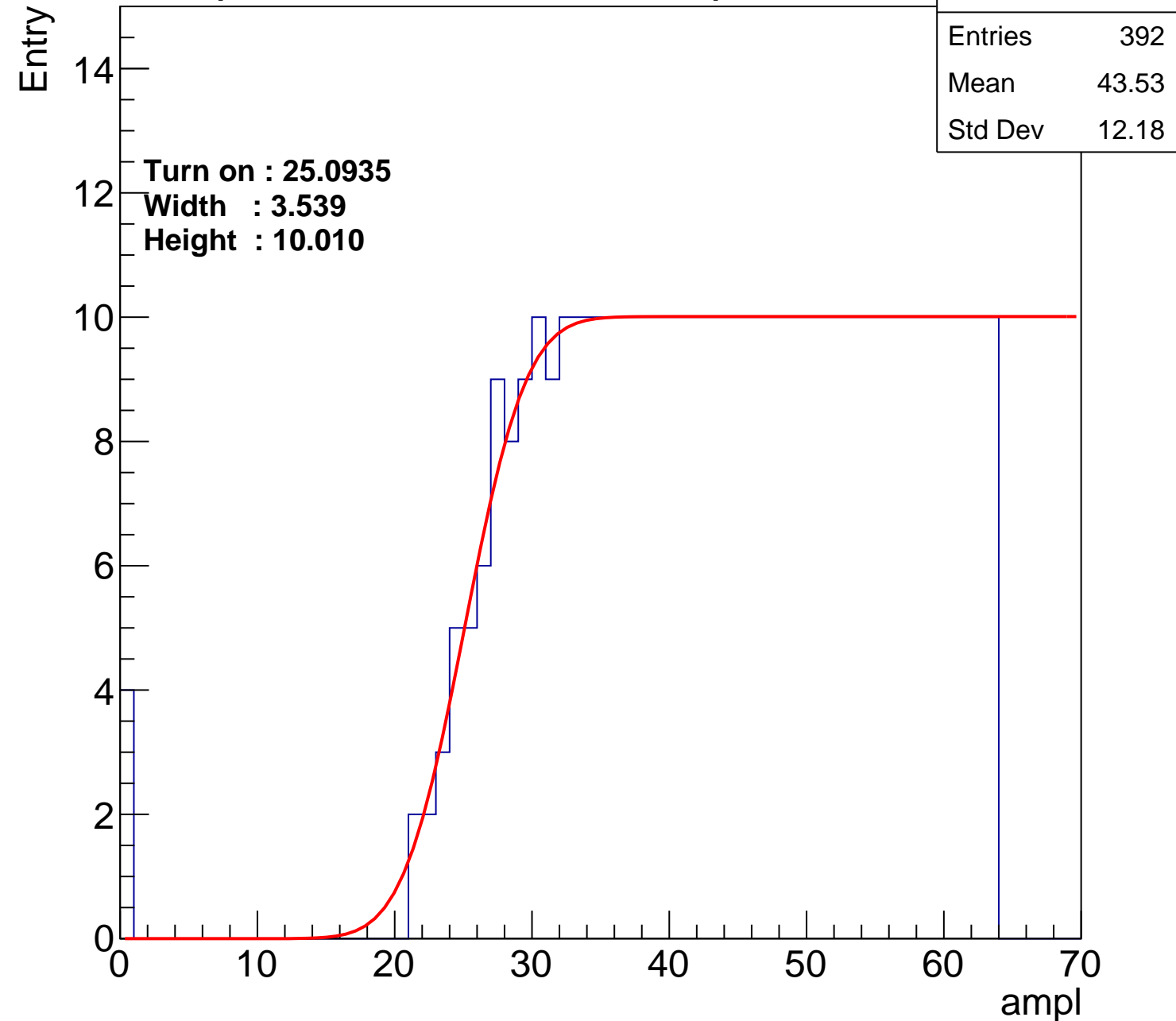
Width : 3.539

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.88
Std Dev	11.66

Turn on : 25.8152

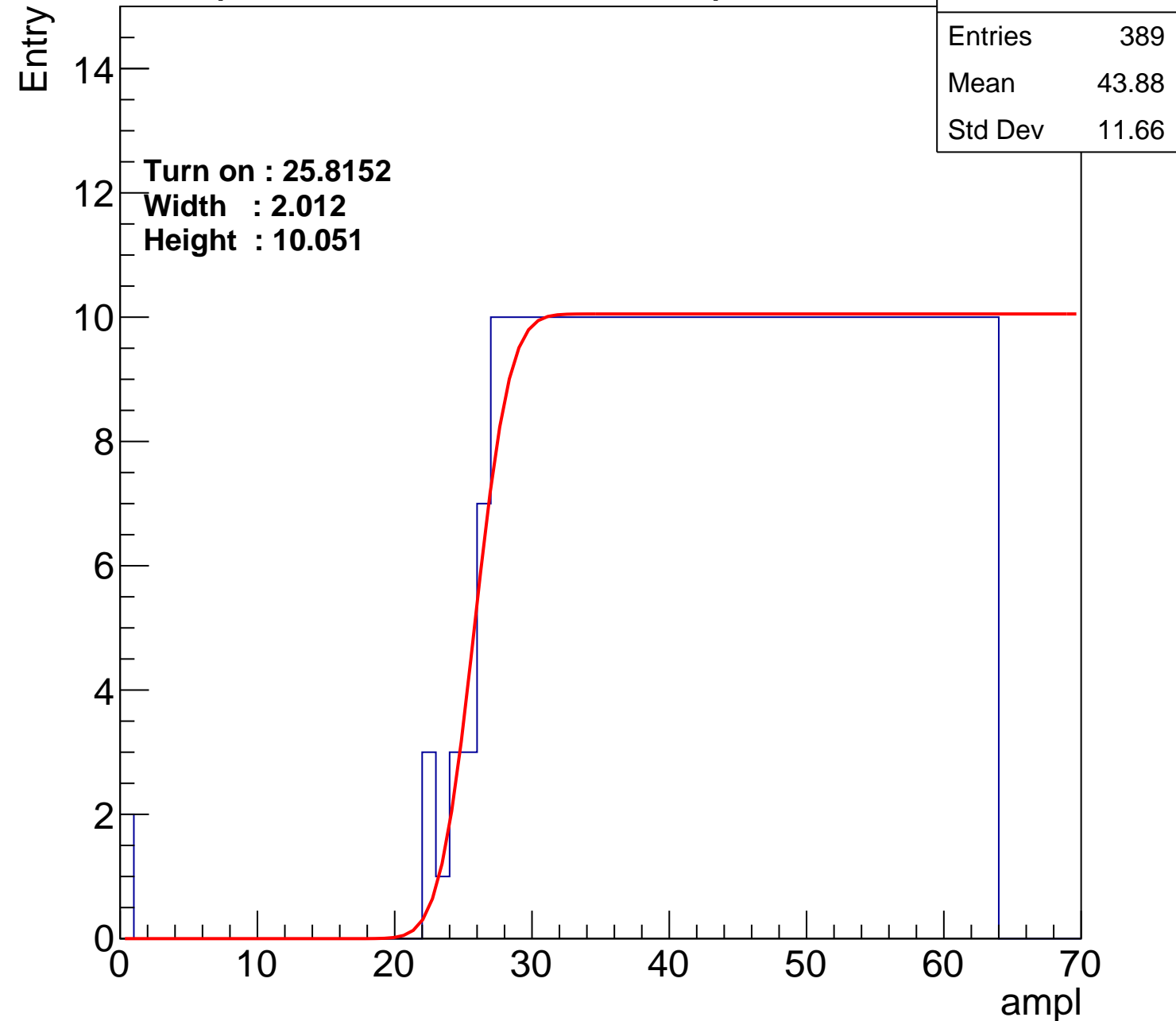
Width : 2.012

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.1565

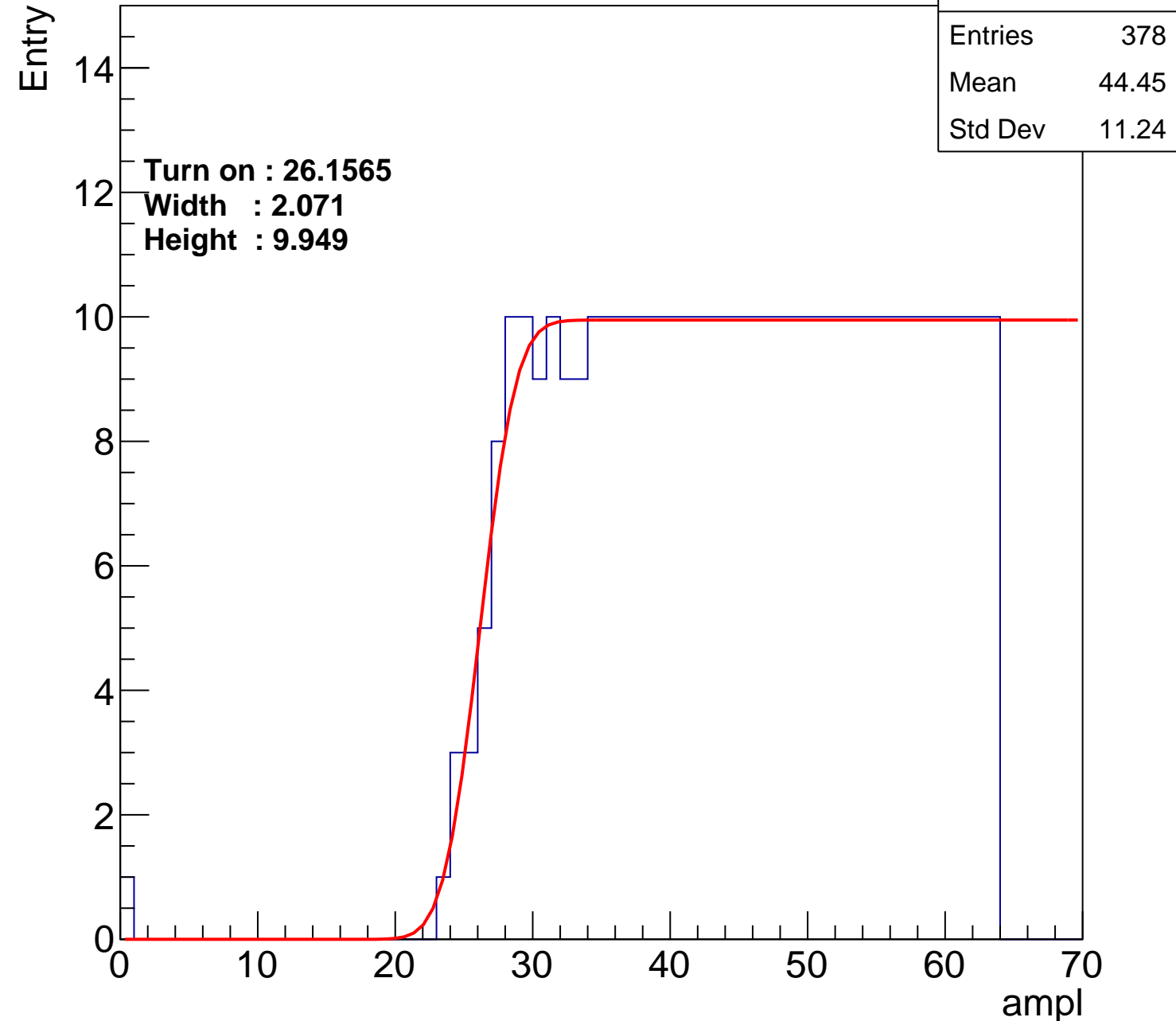
Width : 2.071

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.27
Std Dev	11.46

Turn on : 26.8572

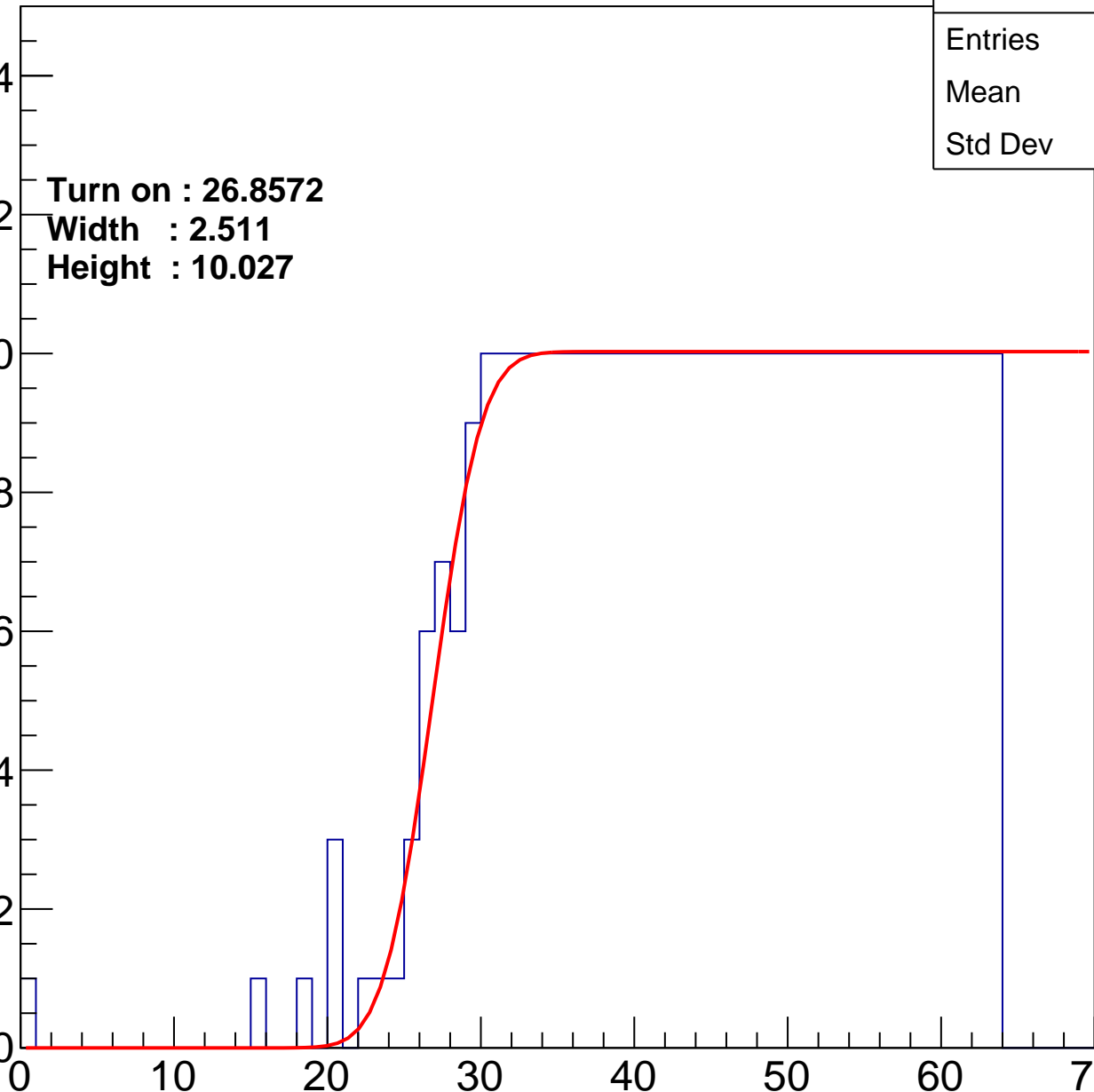
Width : 2.511

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.11
Std Dev	11.47

Turn on : 25.9355

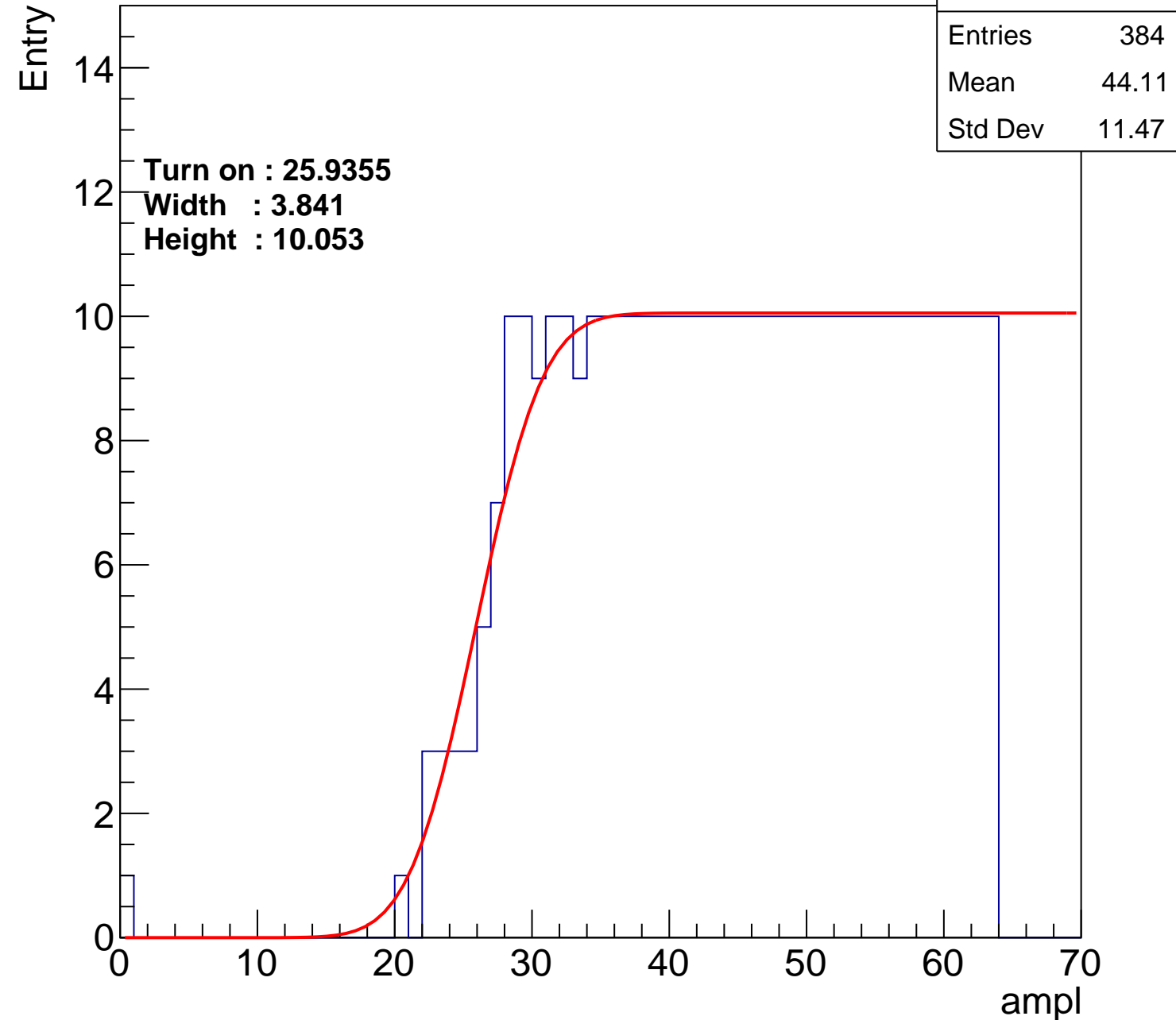
Width : 3.841

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.31
Std Dev	11.79

Turn on : 26.9271

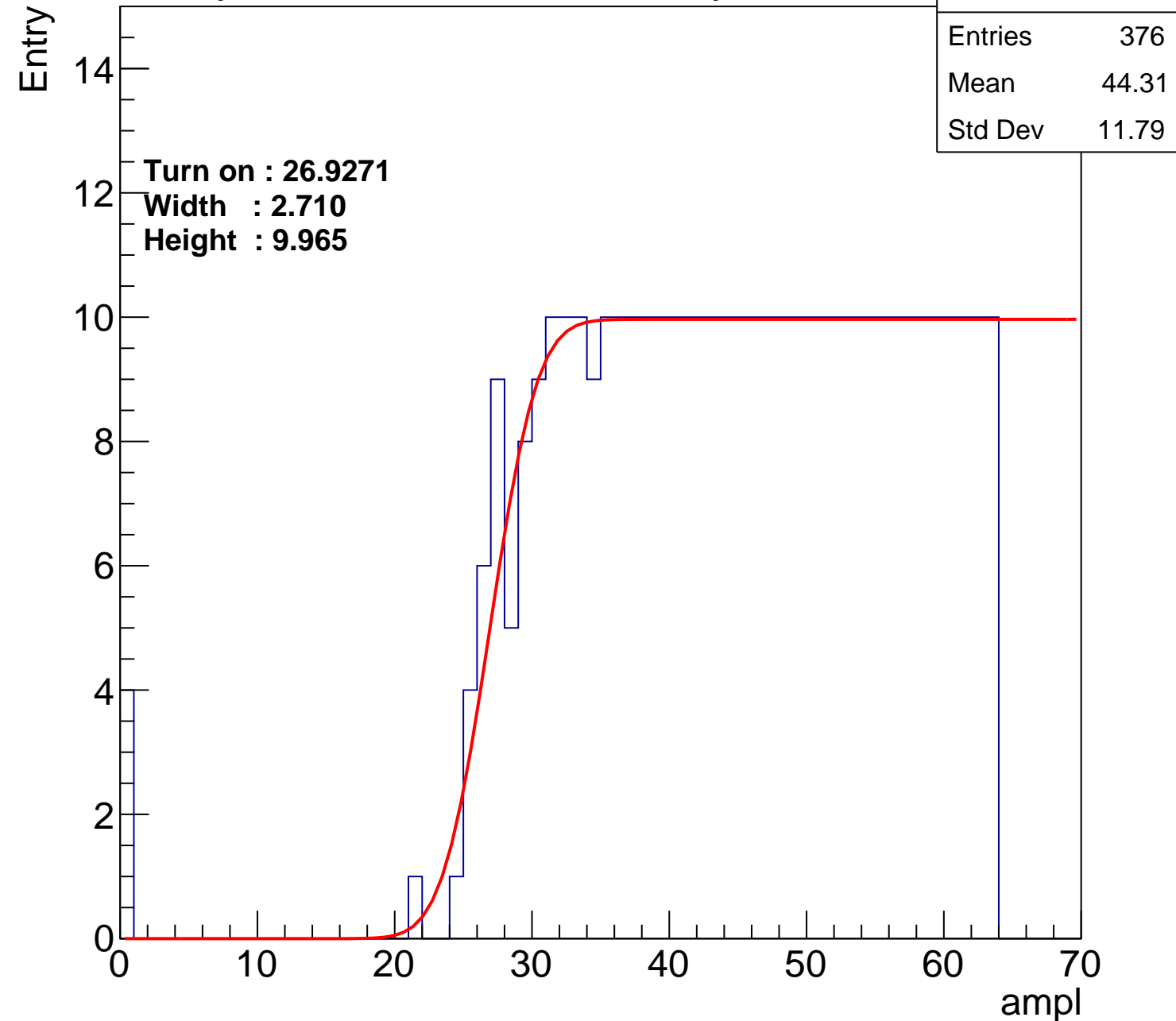
Width : 2.710

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.19
Std Dev	12.06

Turn on : 24.1188

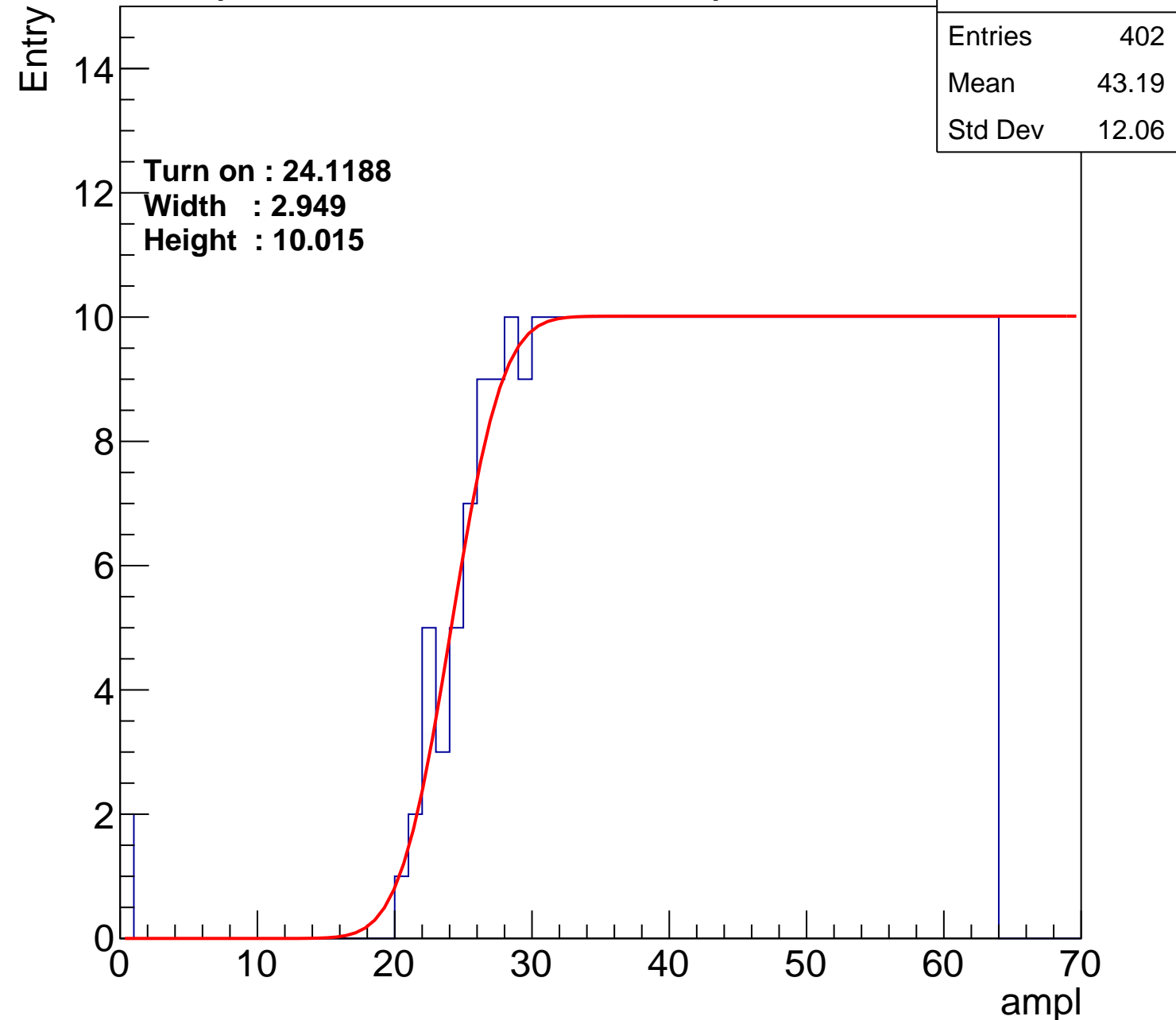
Width : 2.949

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.46
Std Dev	11.53

Turn on : 26.5455

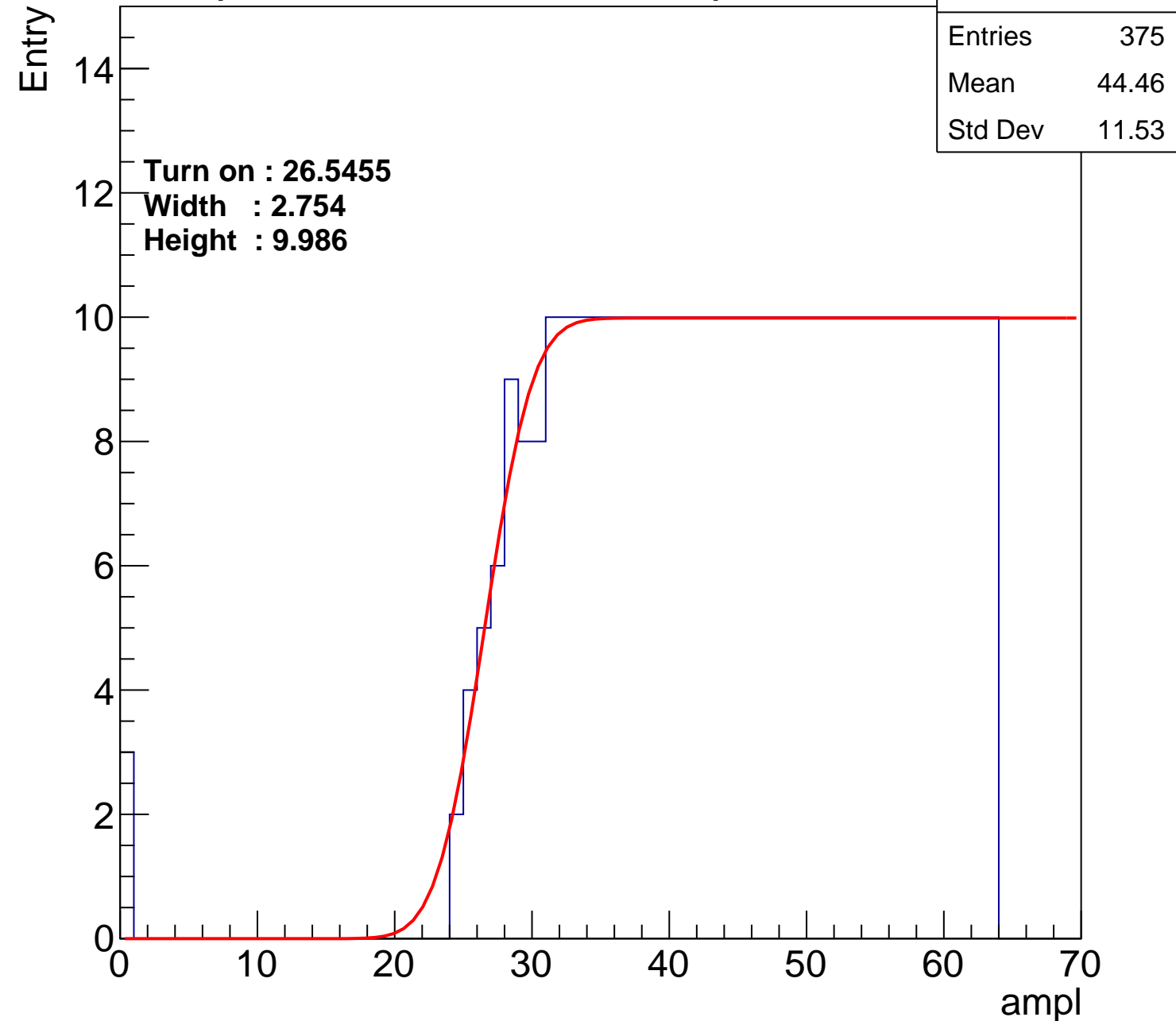
Width : 2.754

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.75
Std Dev	11.69

Turn on : 25.4448

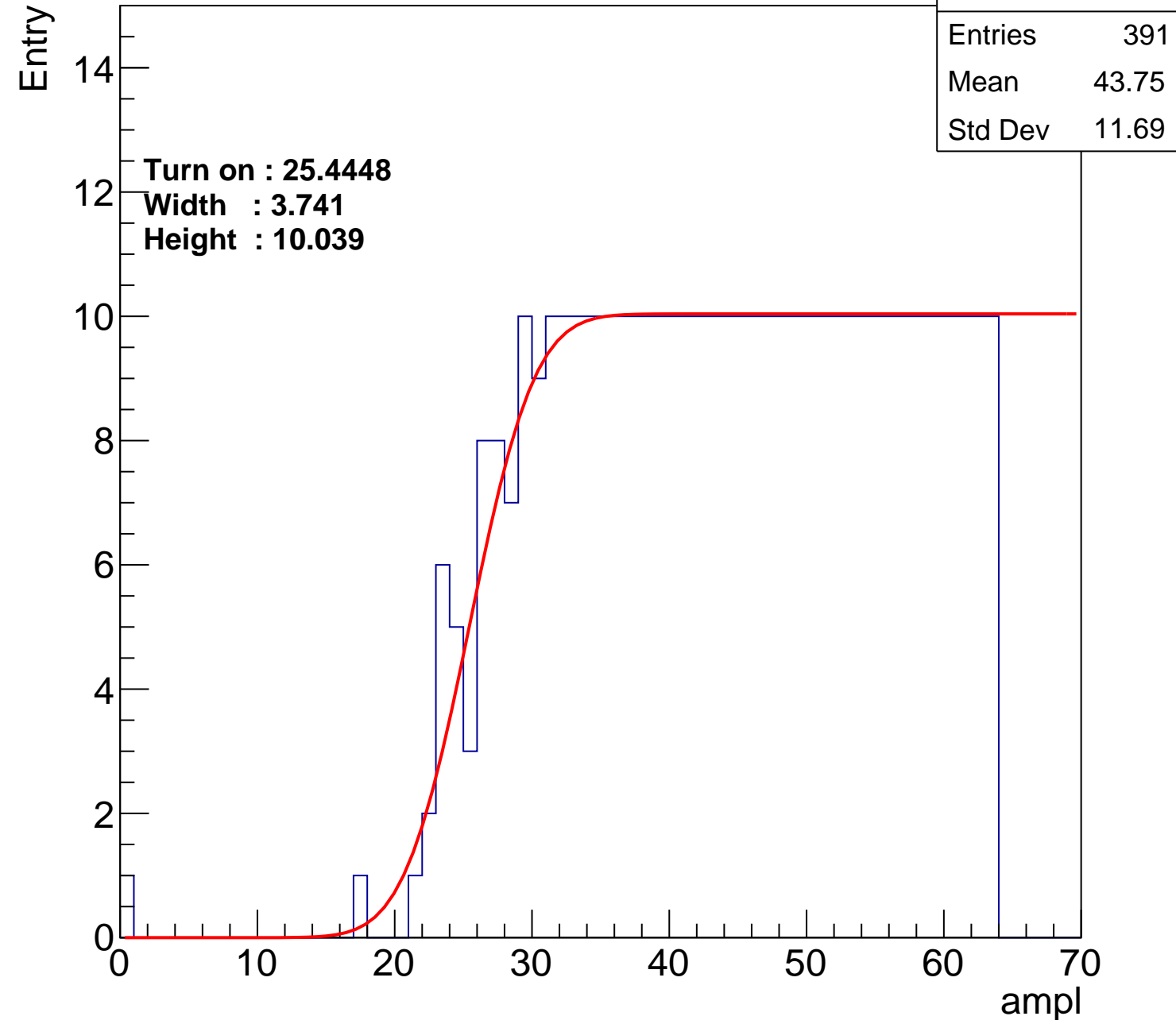
Width : 3.741

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.49
Std Dev	11.37

Turn on : 26.9195

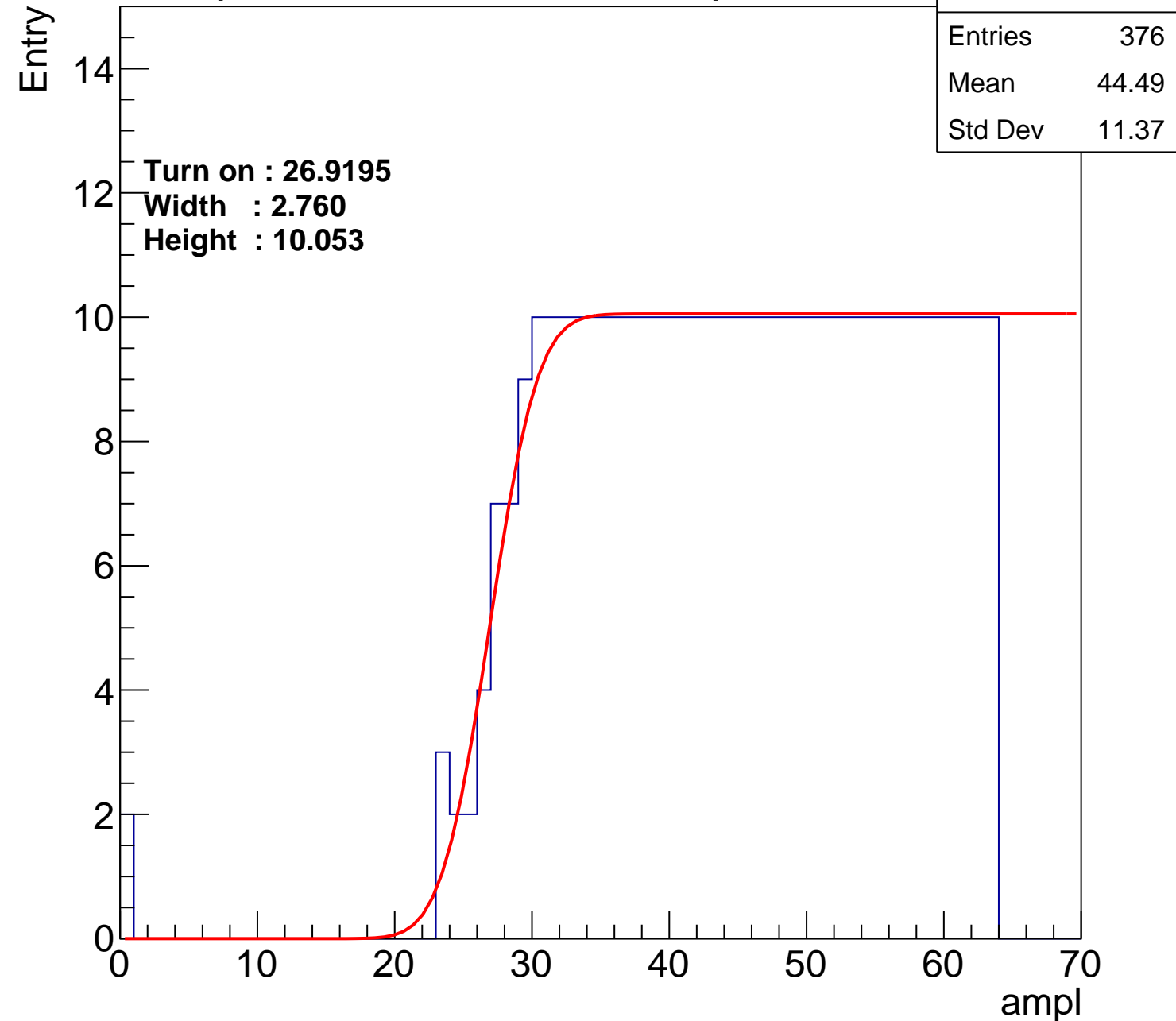
Width : 2.760

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.39
Std Dev	11.96

Turn on : 24.8159

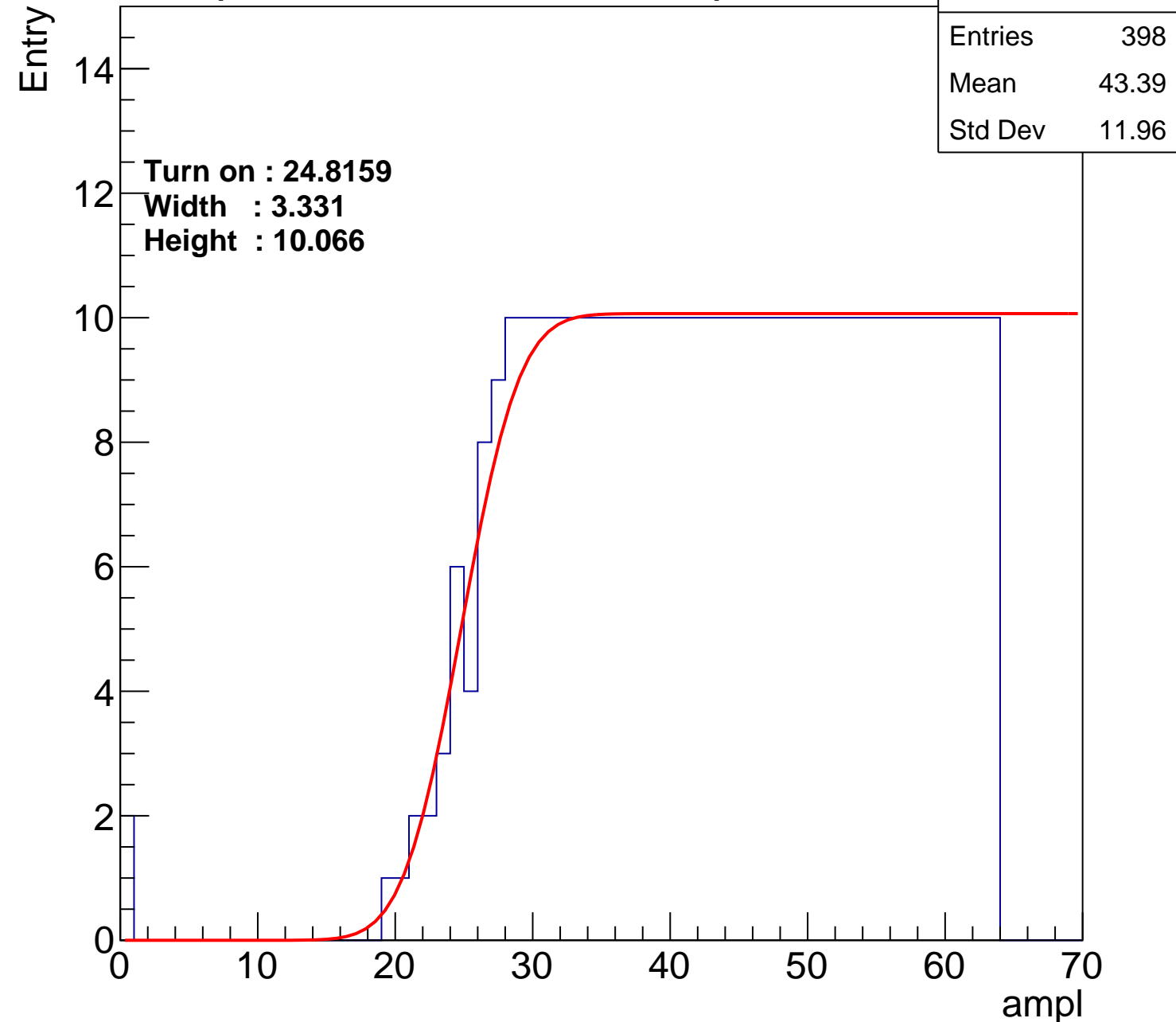
Width : 3.331

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	413
Mean	42.63
Std Dev	12.37

Turn on : 22.5893

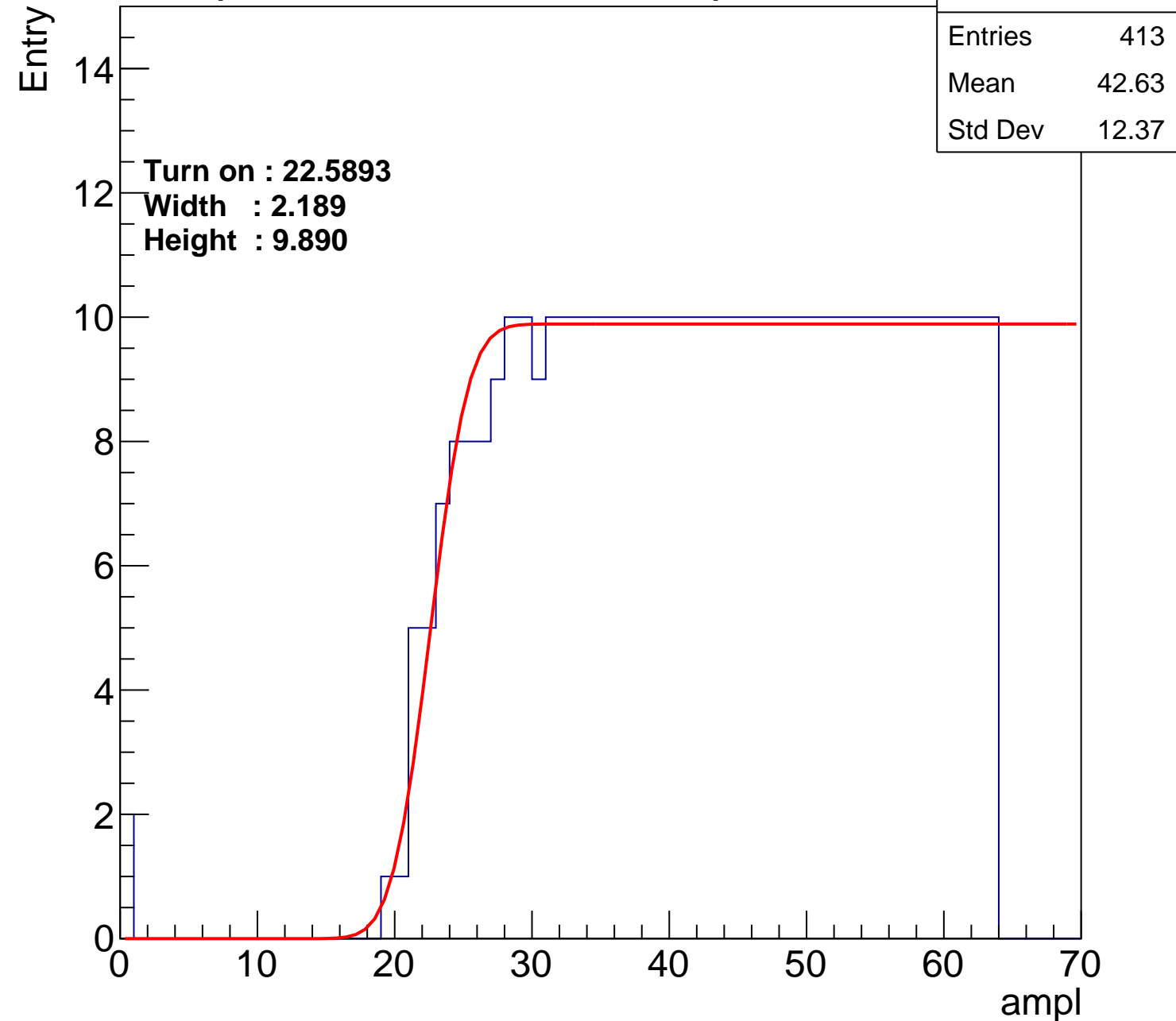
Width : 2.189

Height : 9.890

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.98
Std Dev	12.36

Turn on : 23.7617

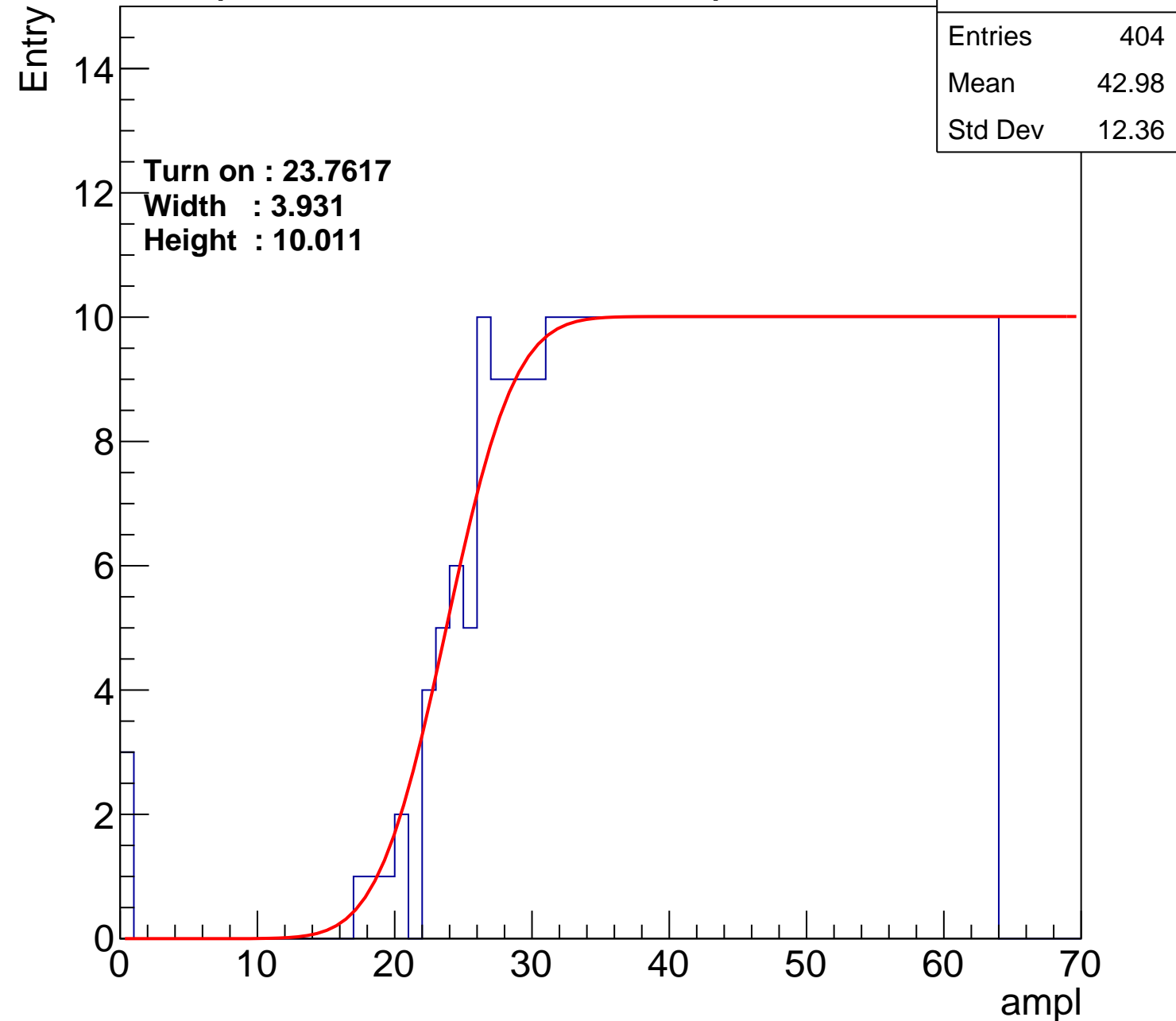
Width : 3.931

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch61

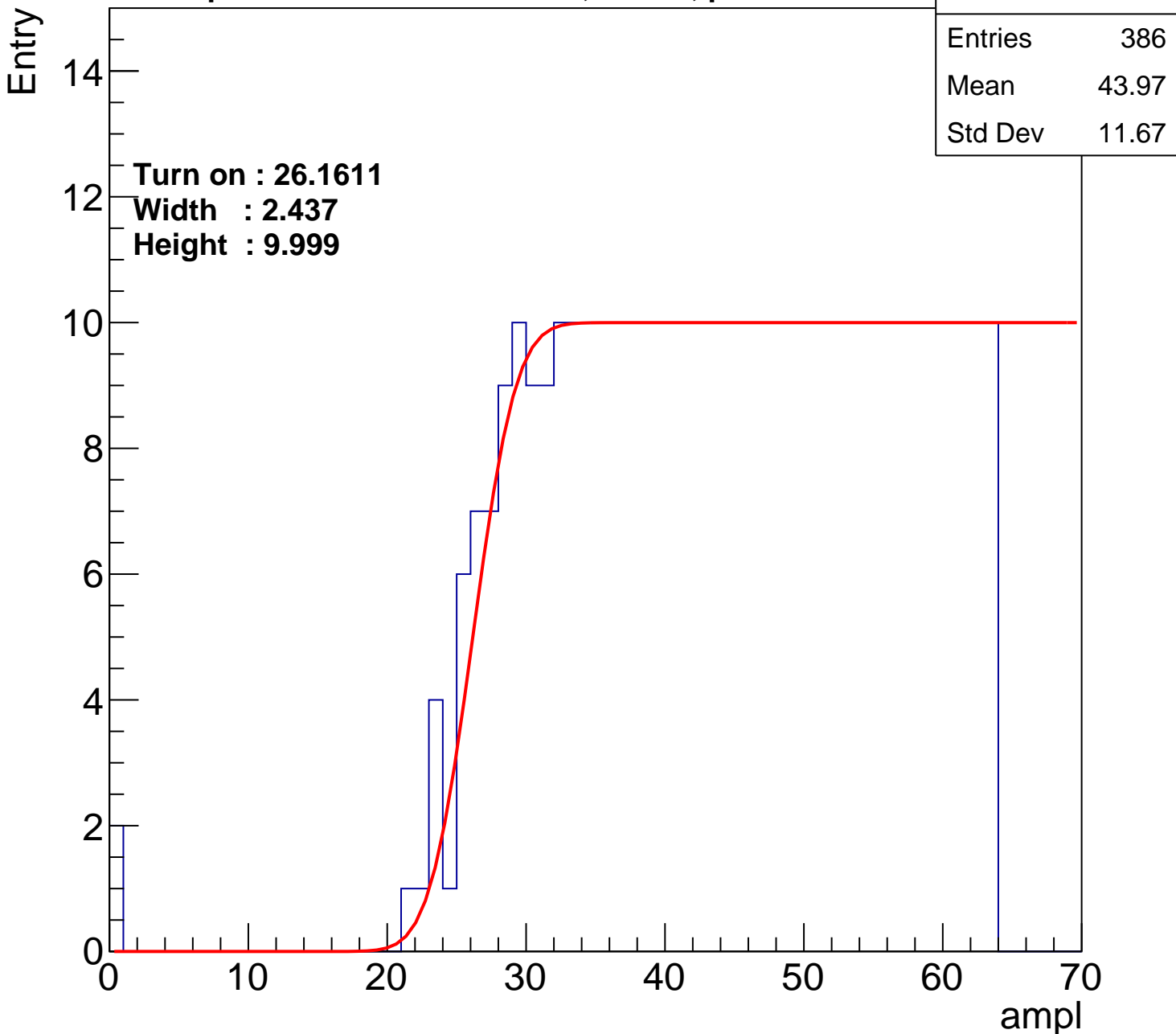
calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 26.1611

Width : 2.437

Height : 9.999



B0L102S, U8-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.07
Std Dev	11.65

Turn on : 27.1885

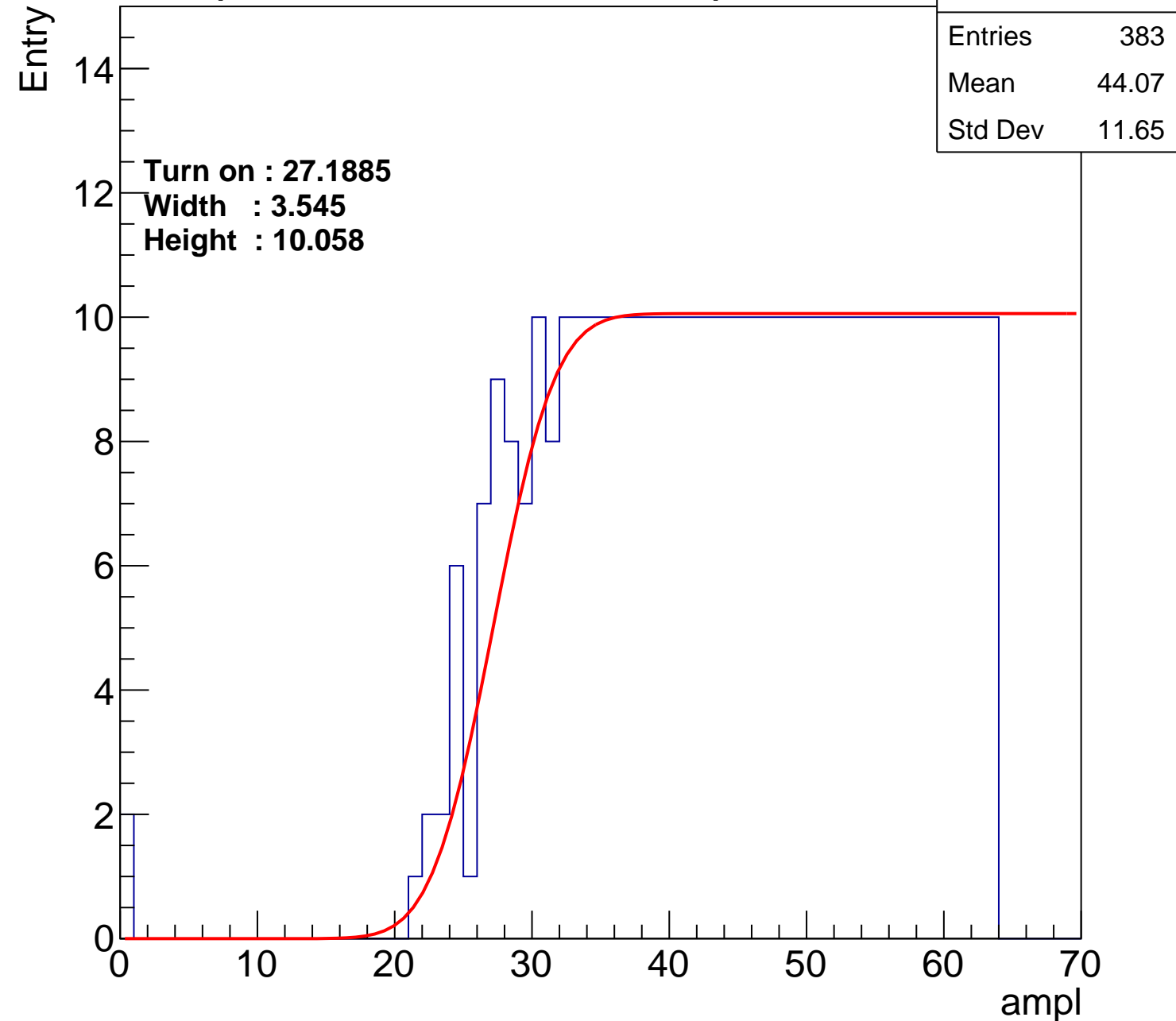
Width : 3.545

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.58
Std Dev	11.98

Turn on : 25.0899

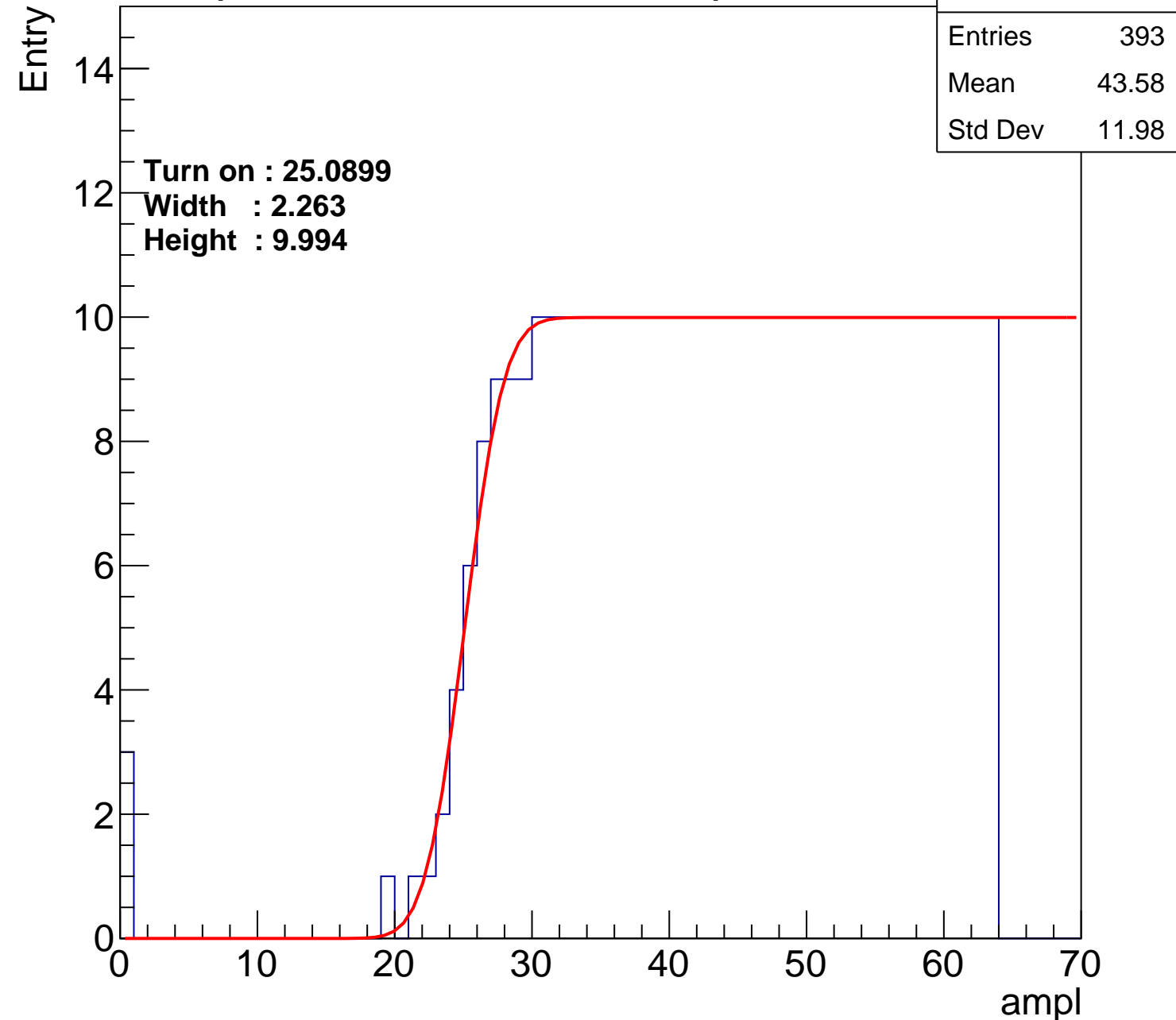
Width : 2.263

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.53
Std Dev	11.9

Turn on : 24.9368

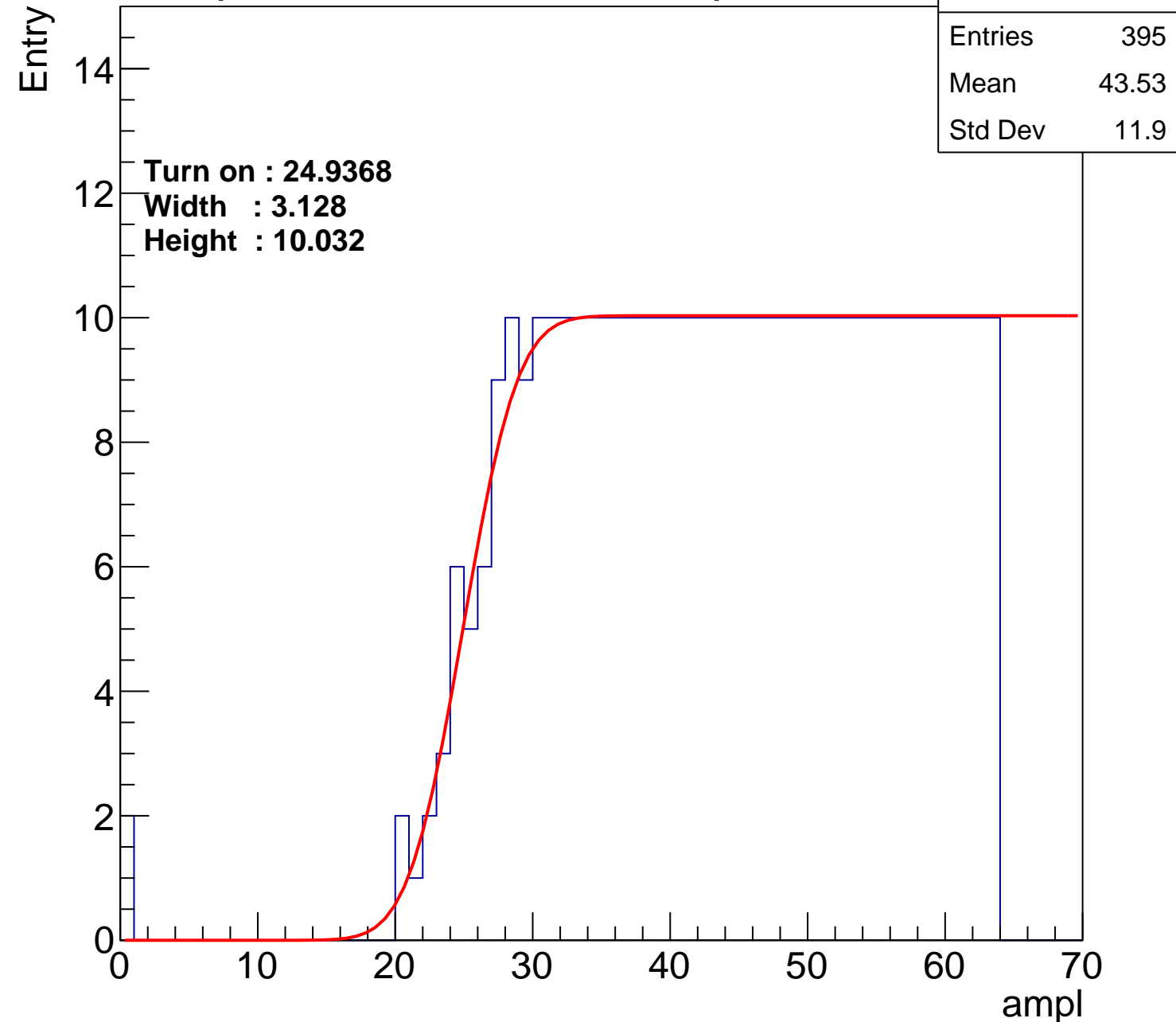
Width : 3.128

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch65

calib_packv5_042523_0143.root, FC#12, port B1

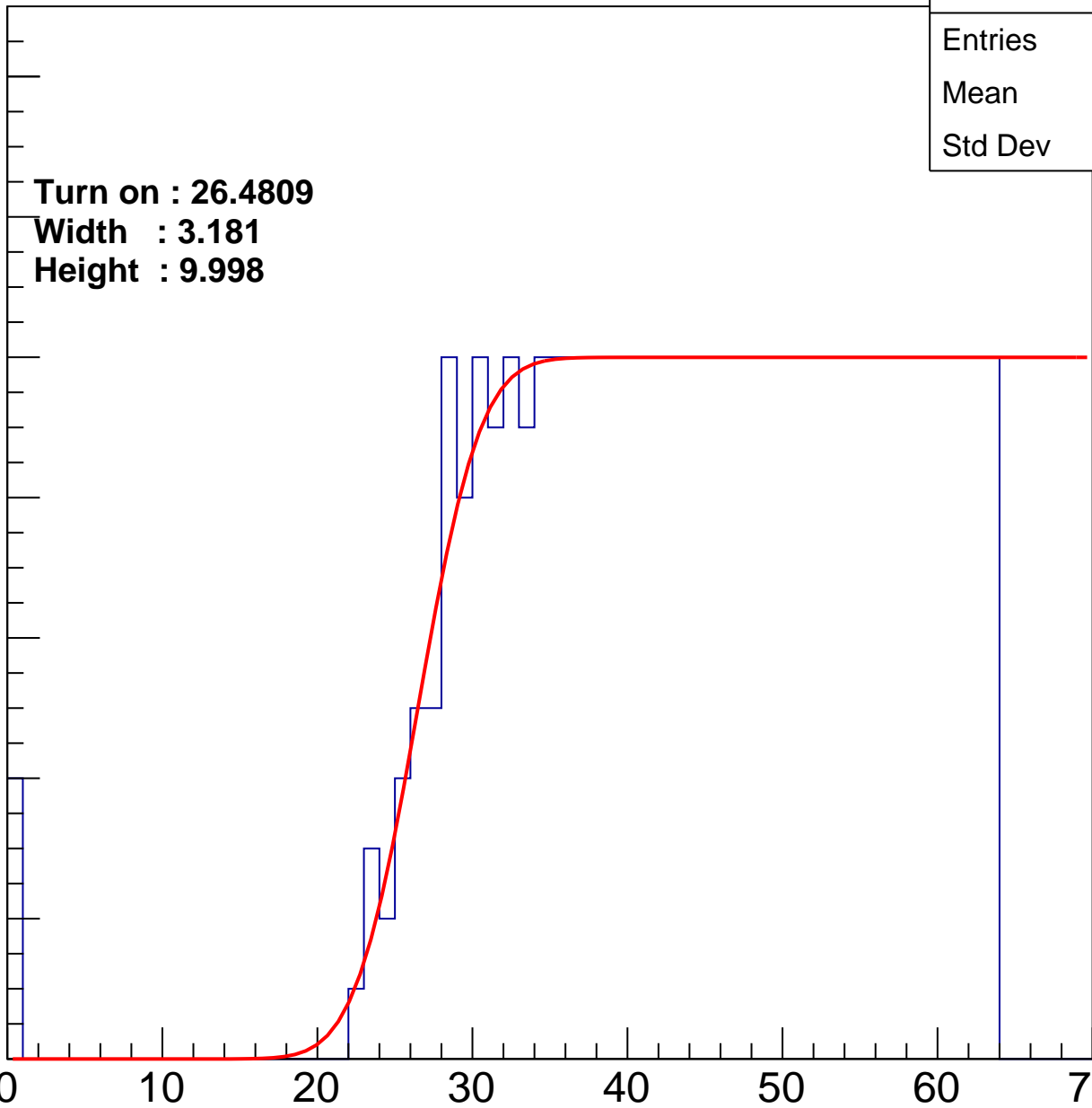
Entry

14
12
10
8
6
4
2
0

Turn on : 26.4809
Width : 3.181
Height : 9.998

Entries	380
Mean	44.11
Std Dev	11.9

ampl



B0L102S, U8-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.06
Std Dev	11.49

Turn on : 25.5383

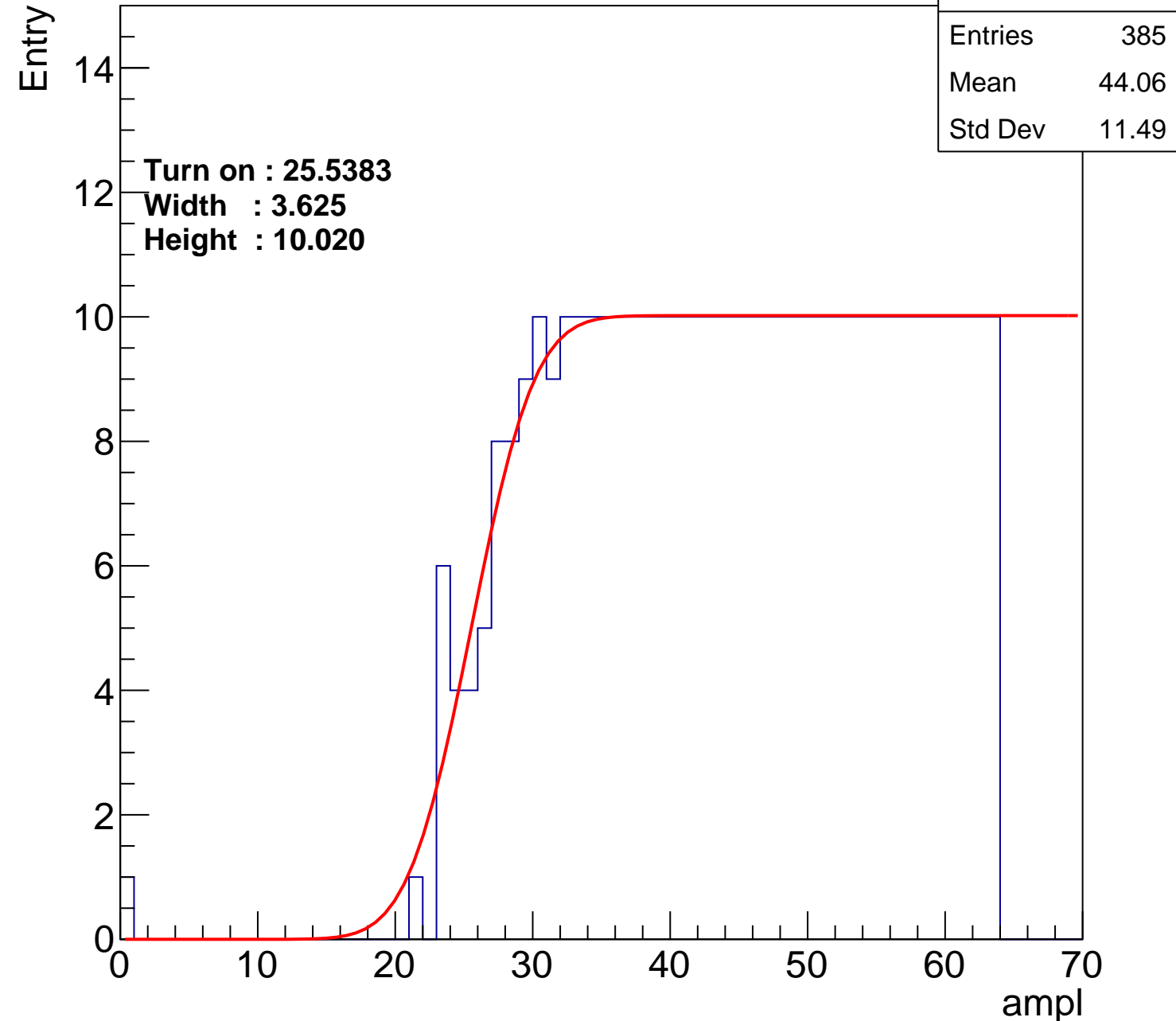
Width : 3.625

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.17
Std Dev	11.86

Turn on : 27.2589

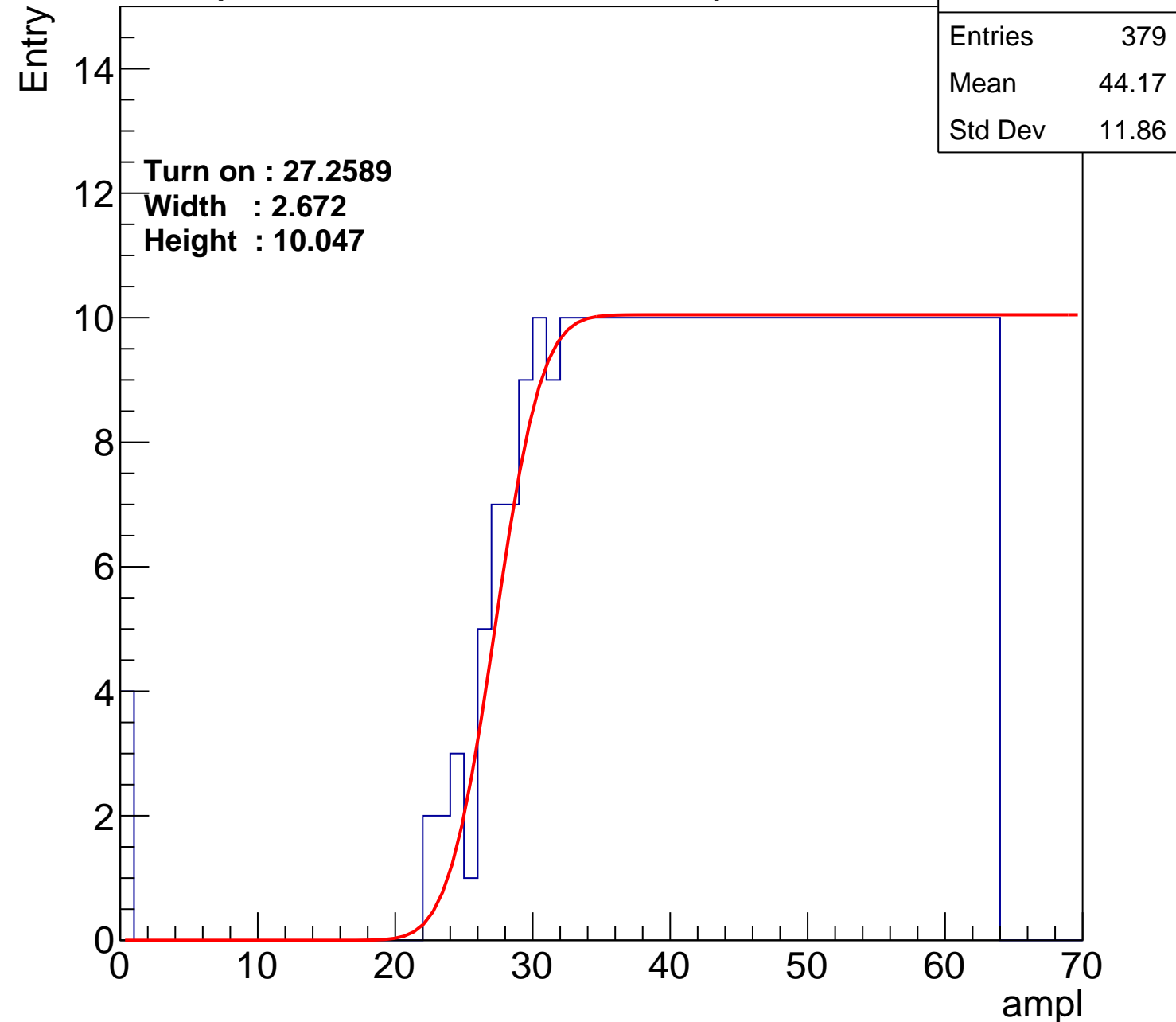
Width : 2.672

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.64
Std Dev	11.71

Turn on : 24.7226

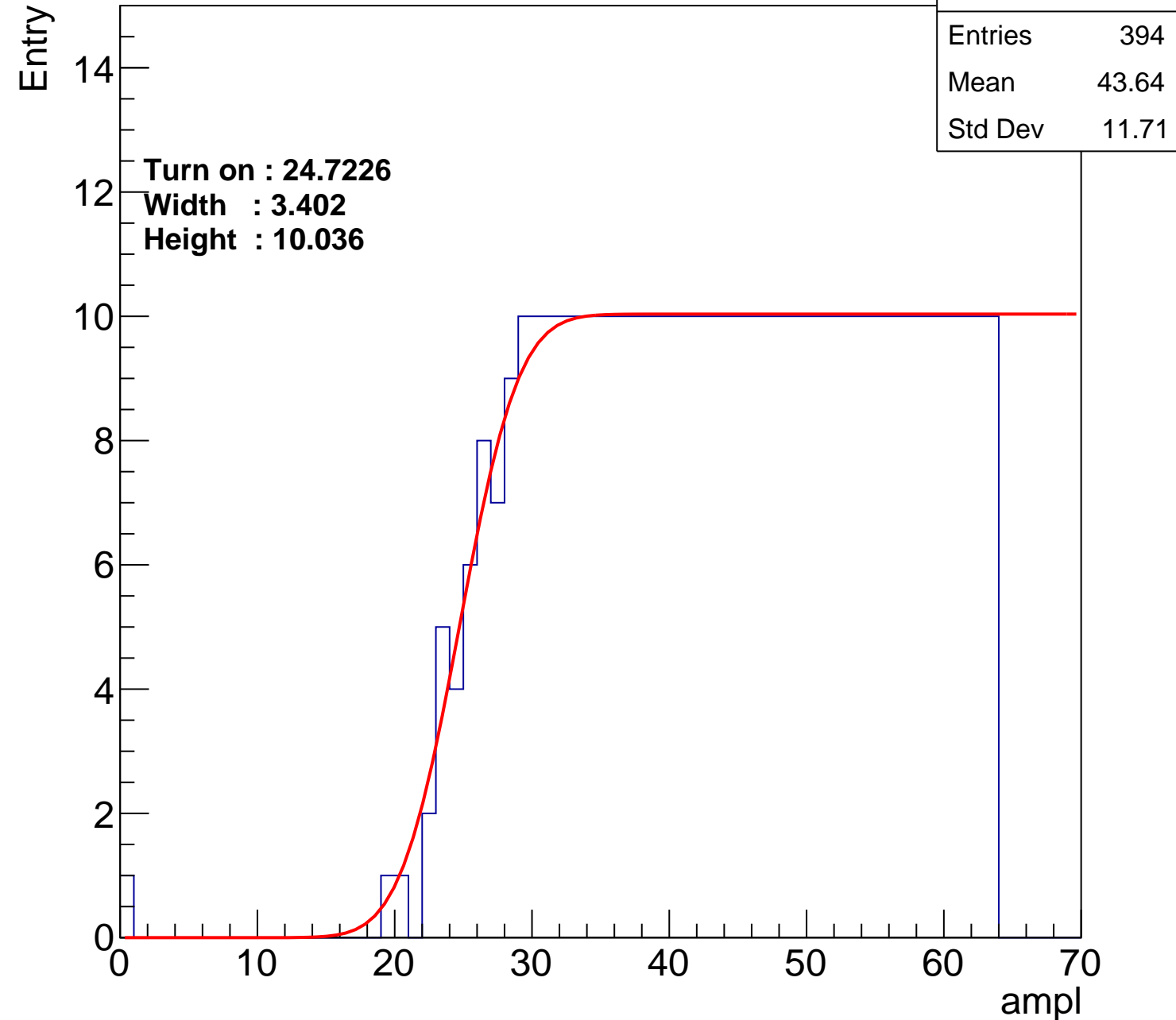
Width : 3.402

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.74
Std Dev	12.17

Turn on : 25.9420

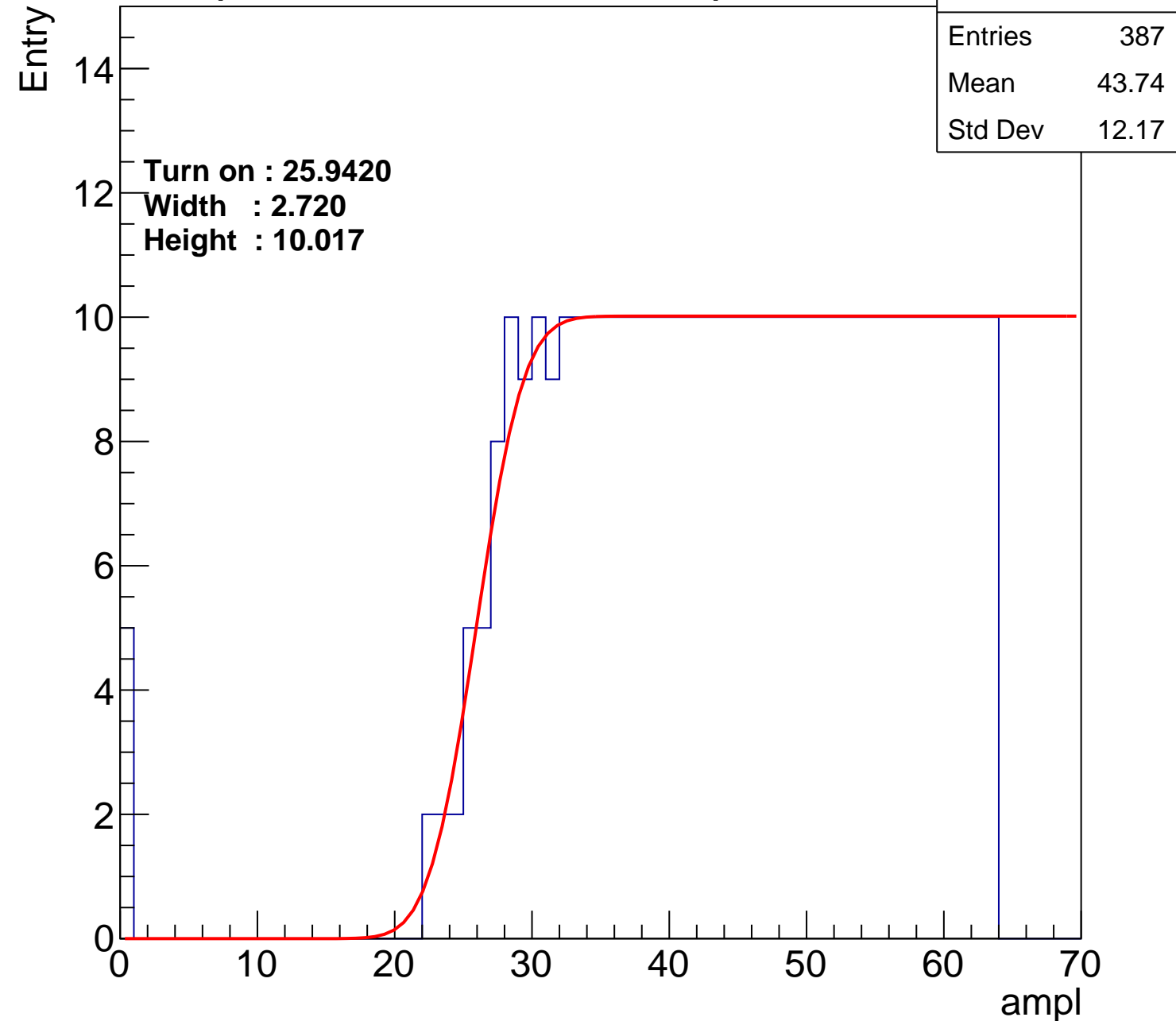
Width : 2.720

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.77
Std Dev	11.9

Turn on : 26.0378

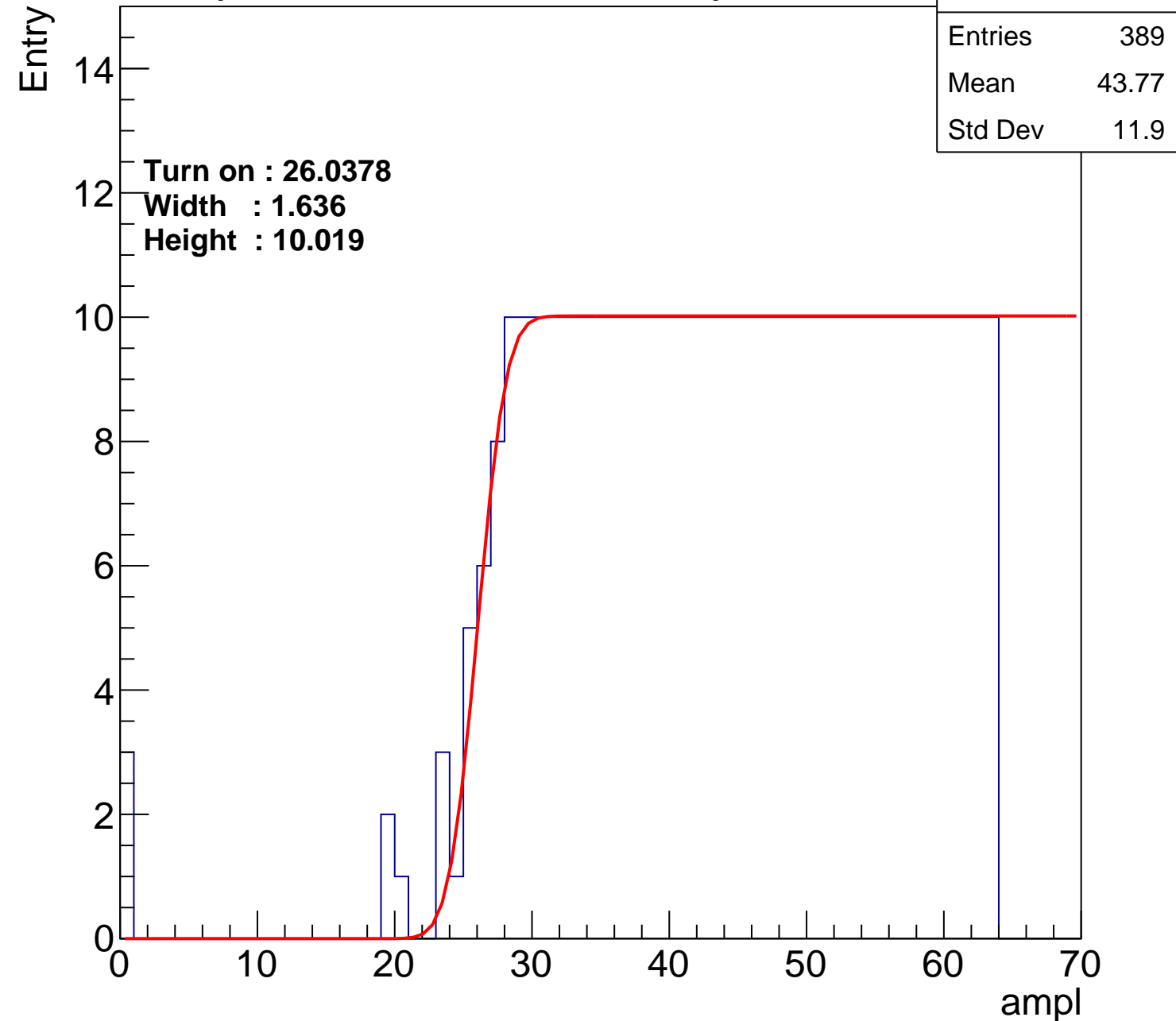
Width : 1.636

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	42.88
Std Dev	12.57

Turn on : 24.1431

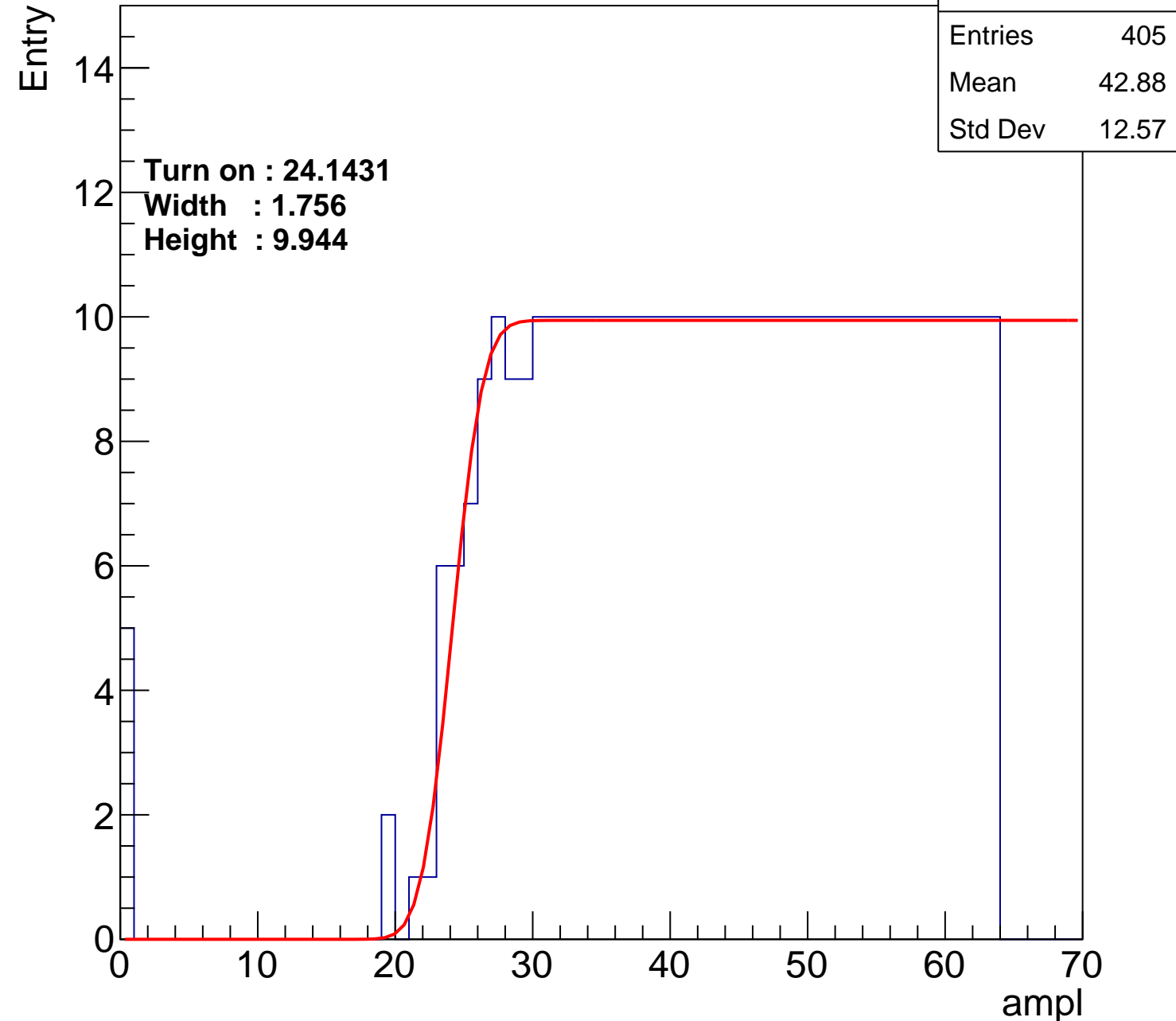
Width : 1.756

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.72
Std Dev	11.5

Turn on : 27.8345

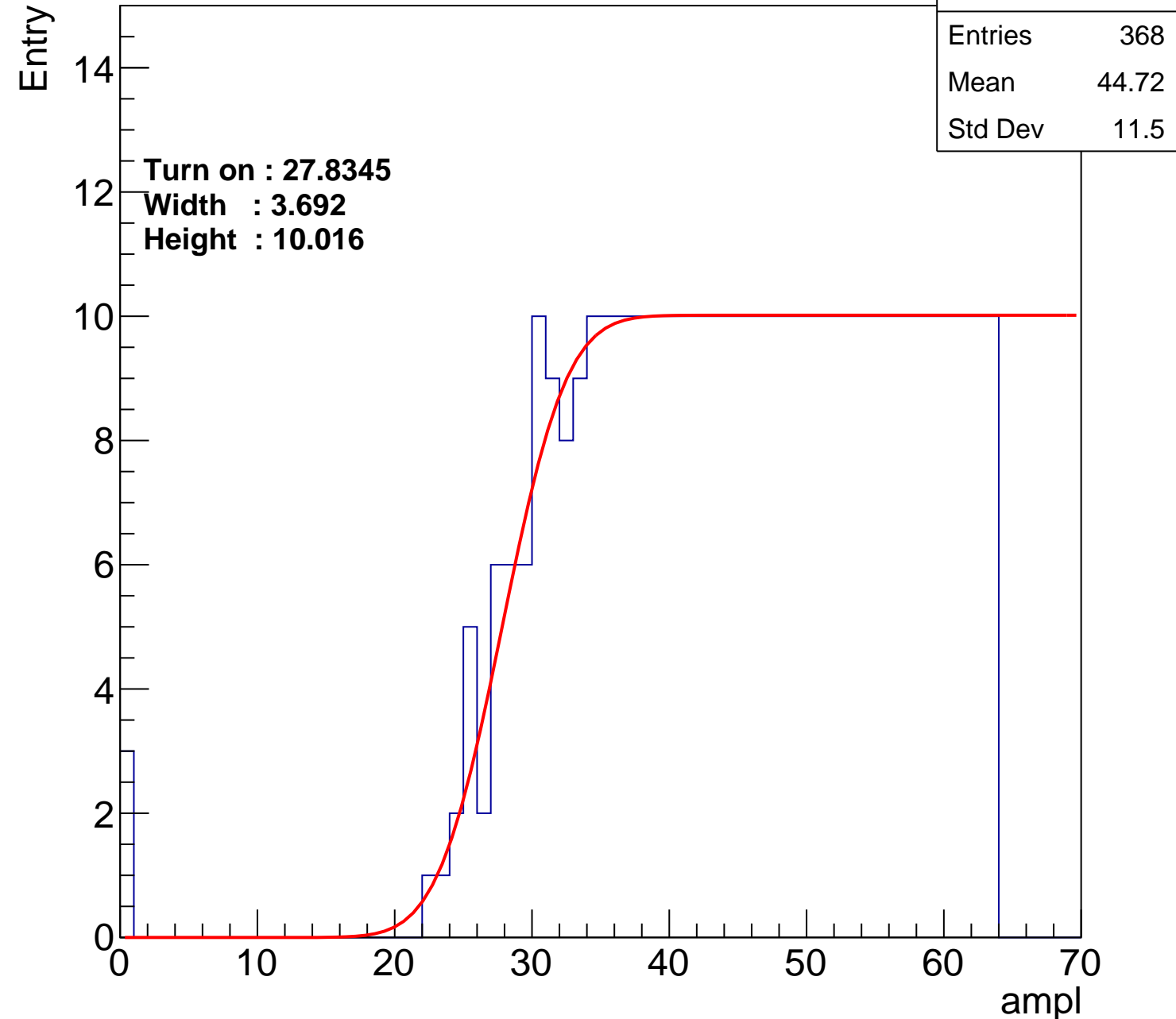
Width : 3.692

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.38
Std Dev	11.61

Turn on : 27.0084

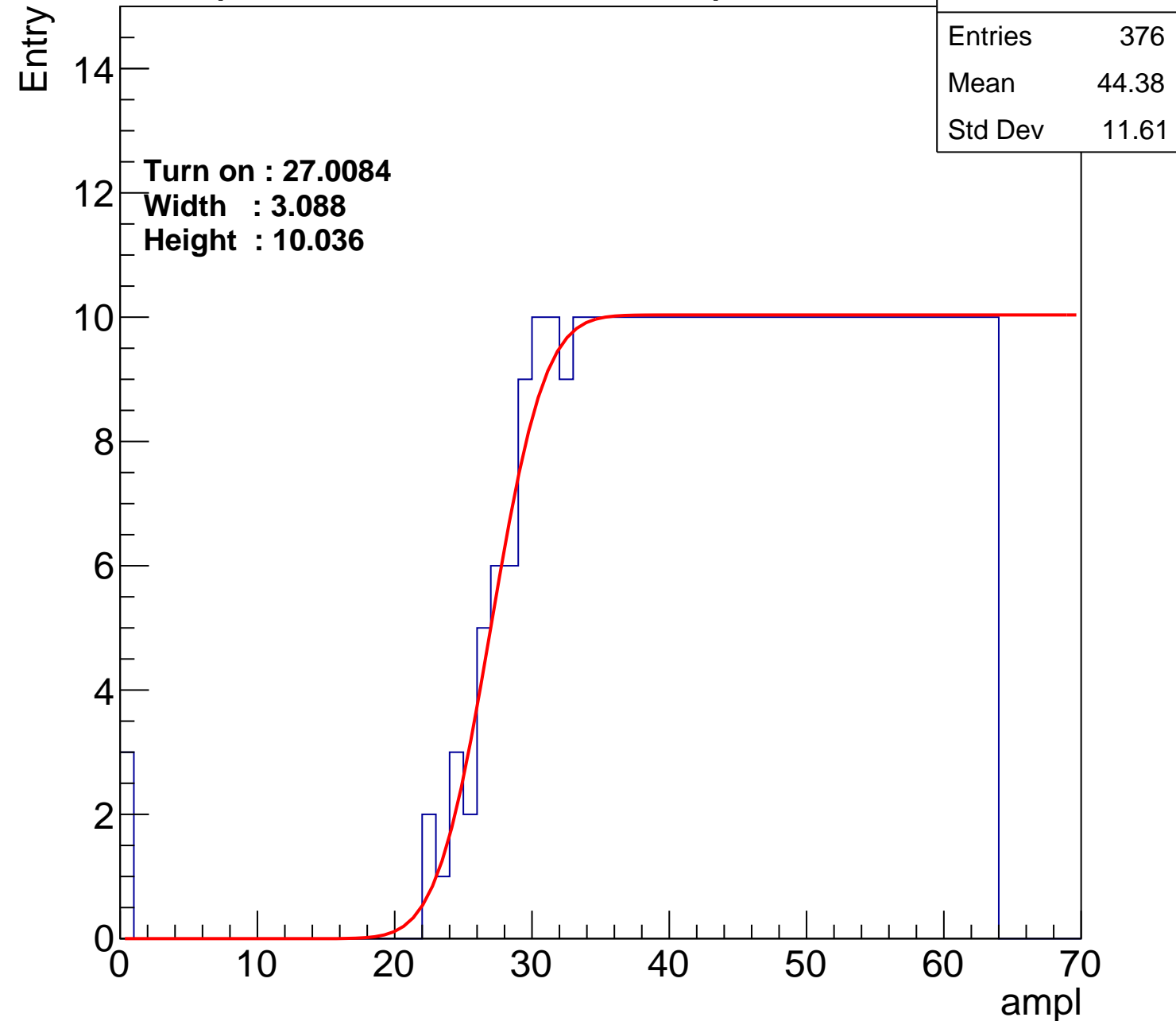
Width : 3.088

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 27.8789

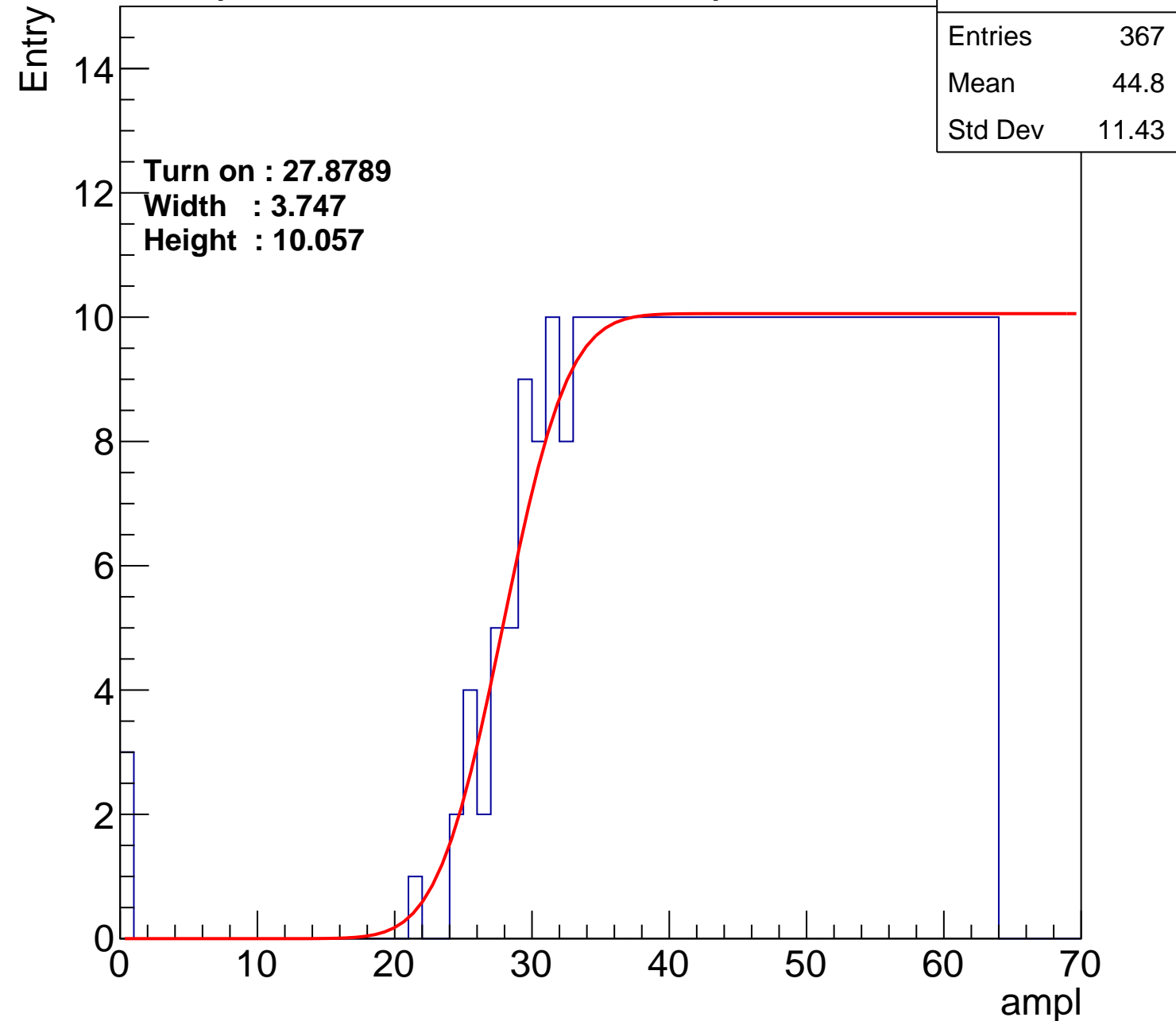
Width : 3.747

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.48
Std Dev	12.2

Turn on : 25.5005

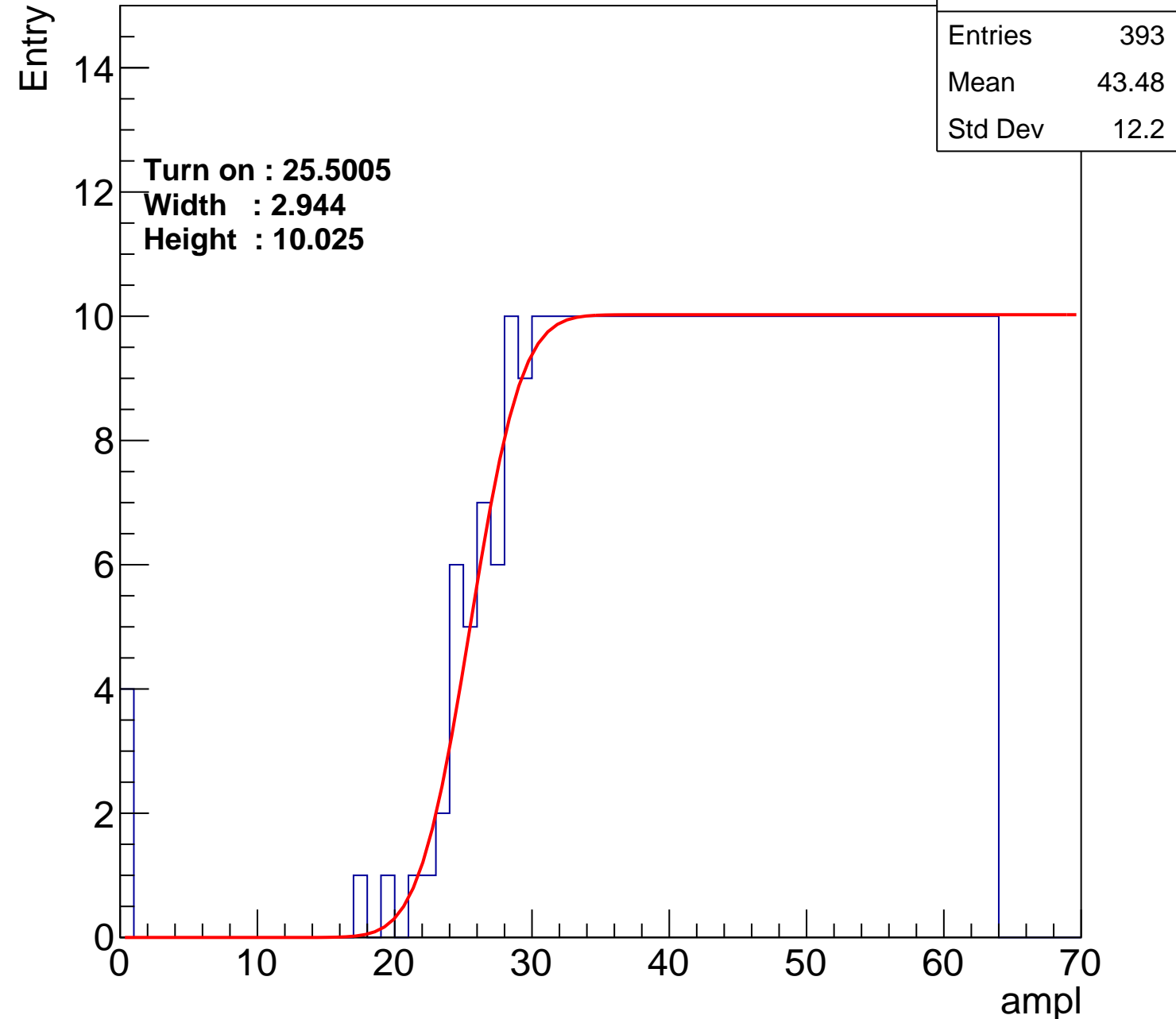
Width : 2.944

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.78
Std Dev	12.26

Turn on : 22.9099

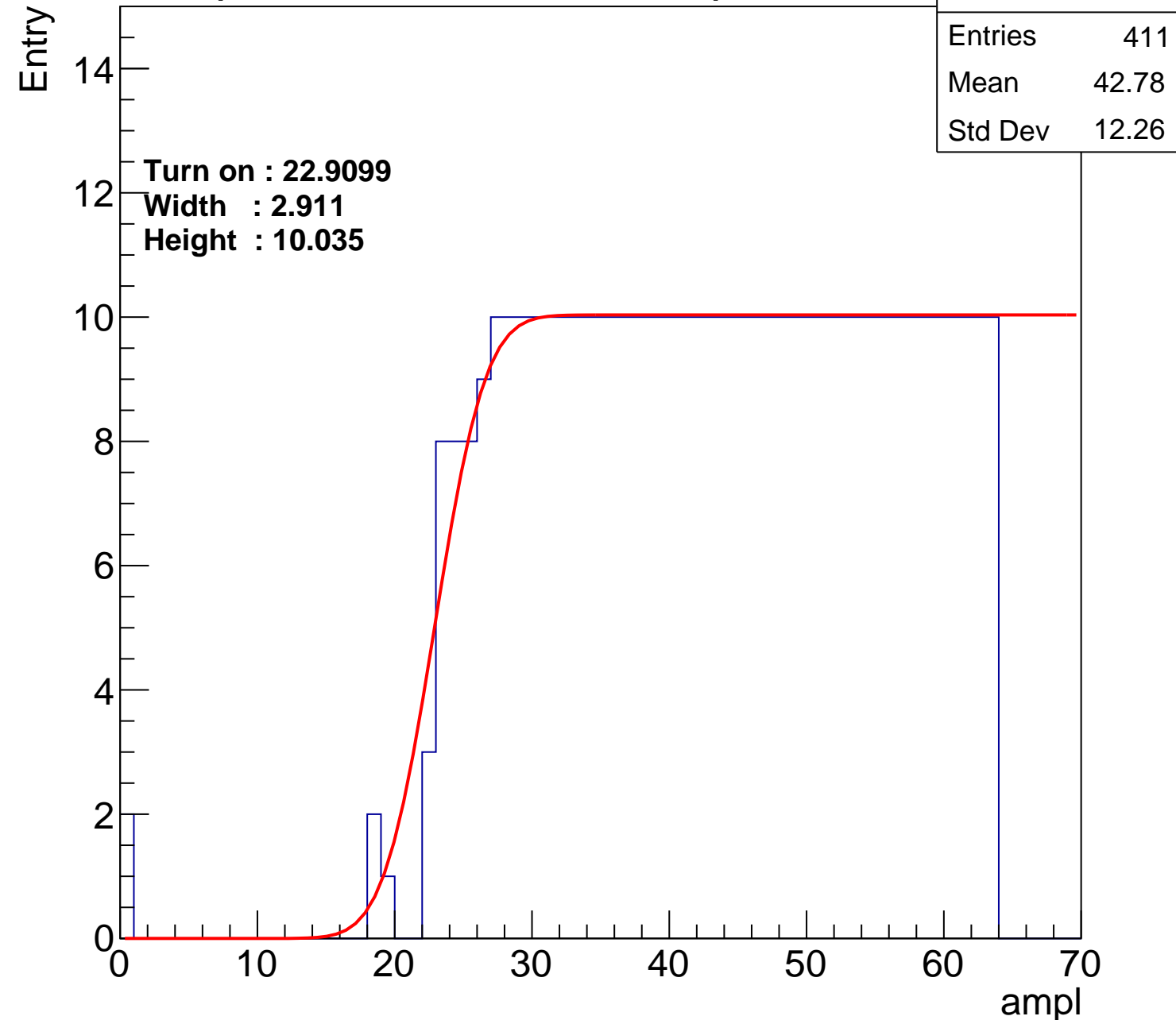
Width : 2.911

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.64
Std Dev	11.75

Turn on : 24.7163

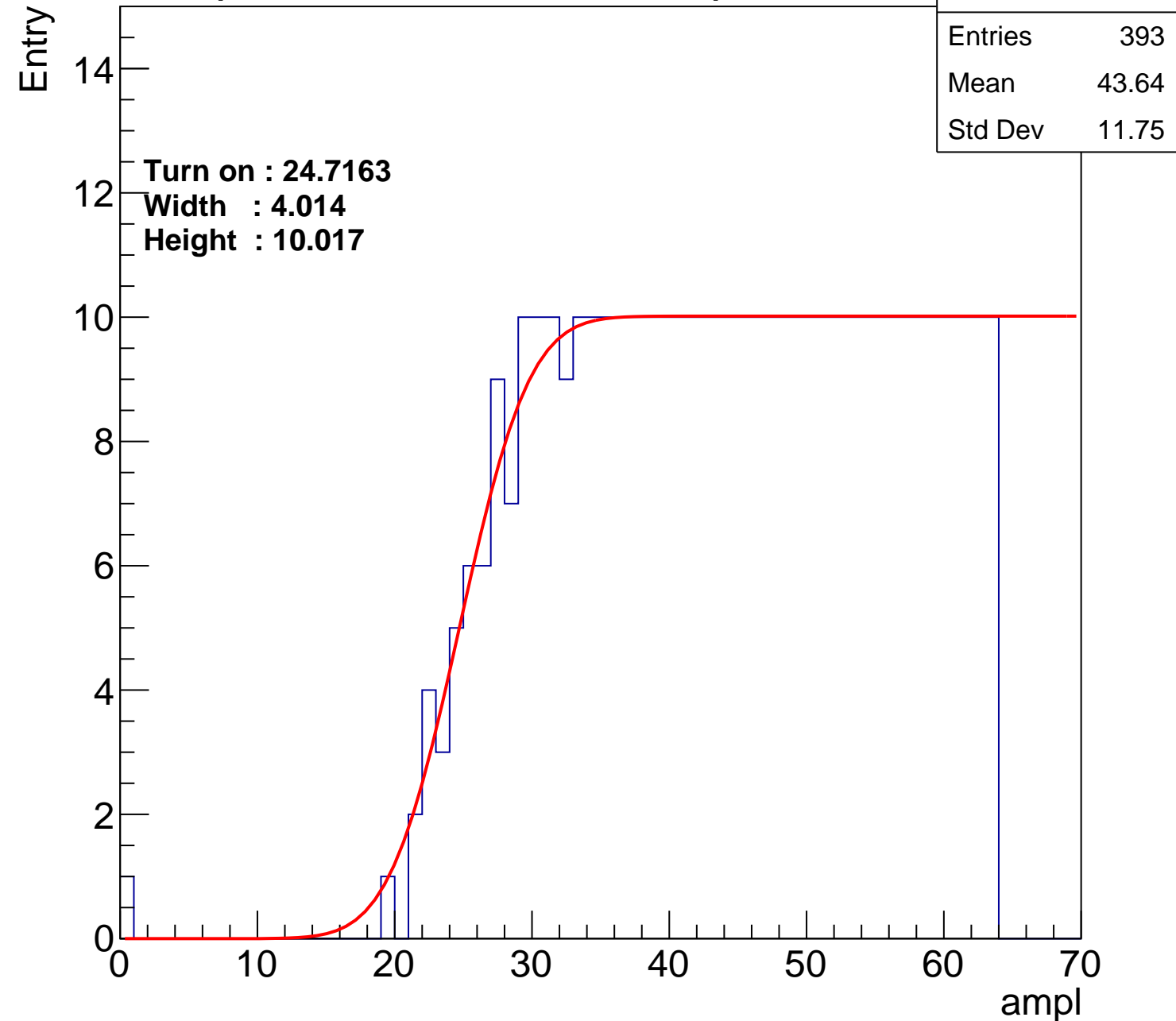
Width : 4.014

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.38
Std Dev	12.2

Turn on : 24.7430

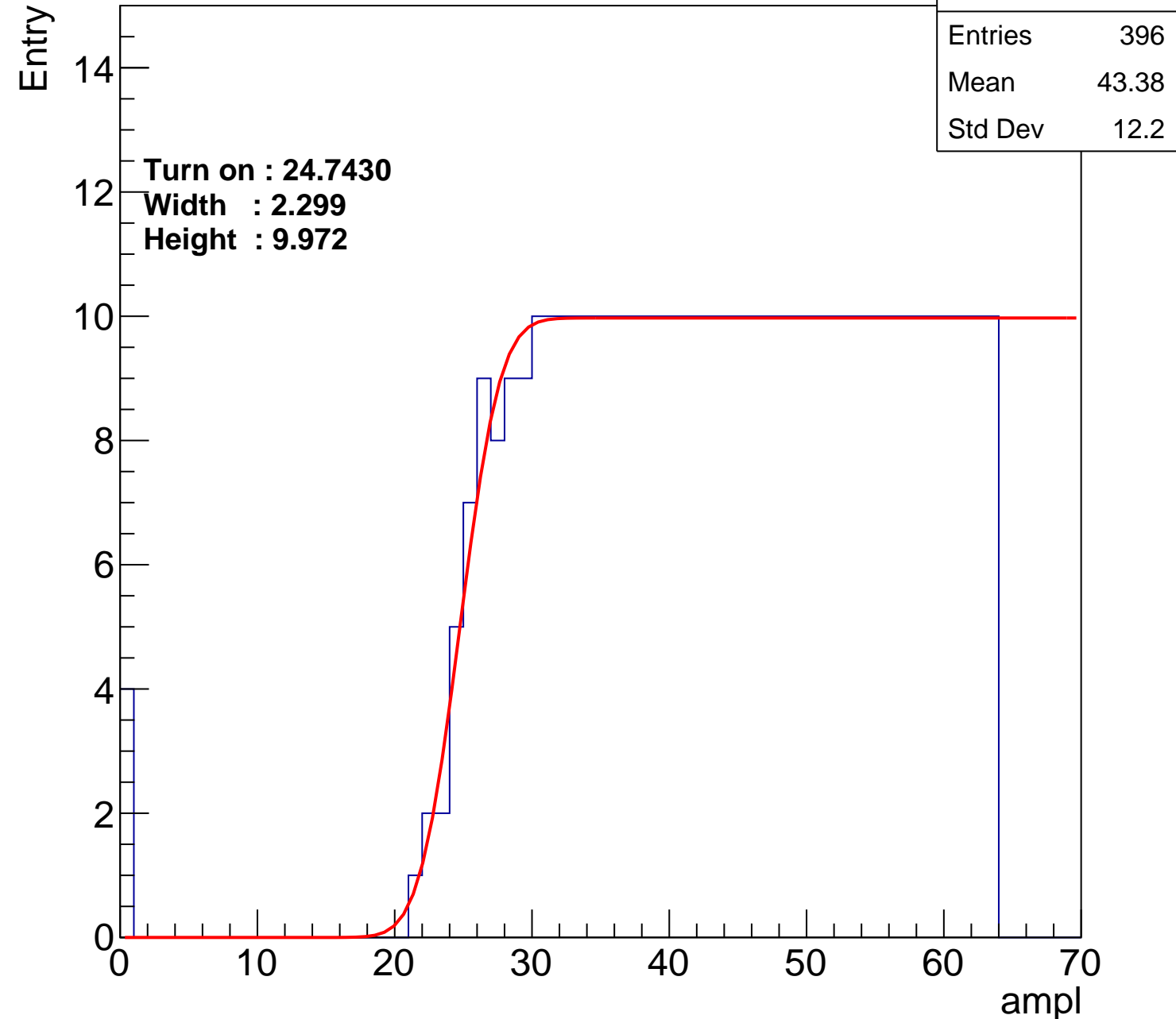
Width : 2.299

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.83
Std Dev	11.42

Turn on : 27.7199

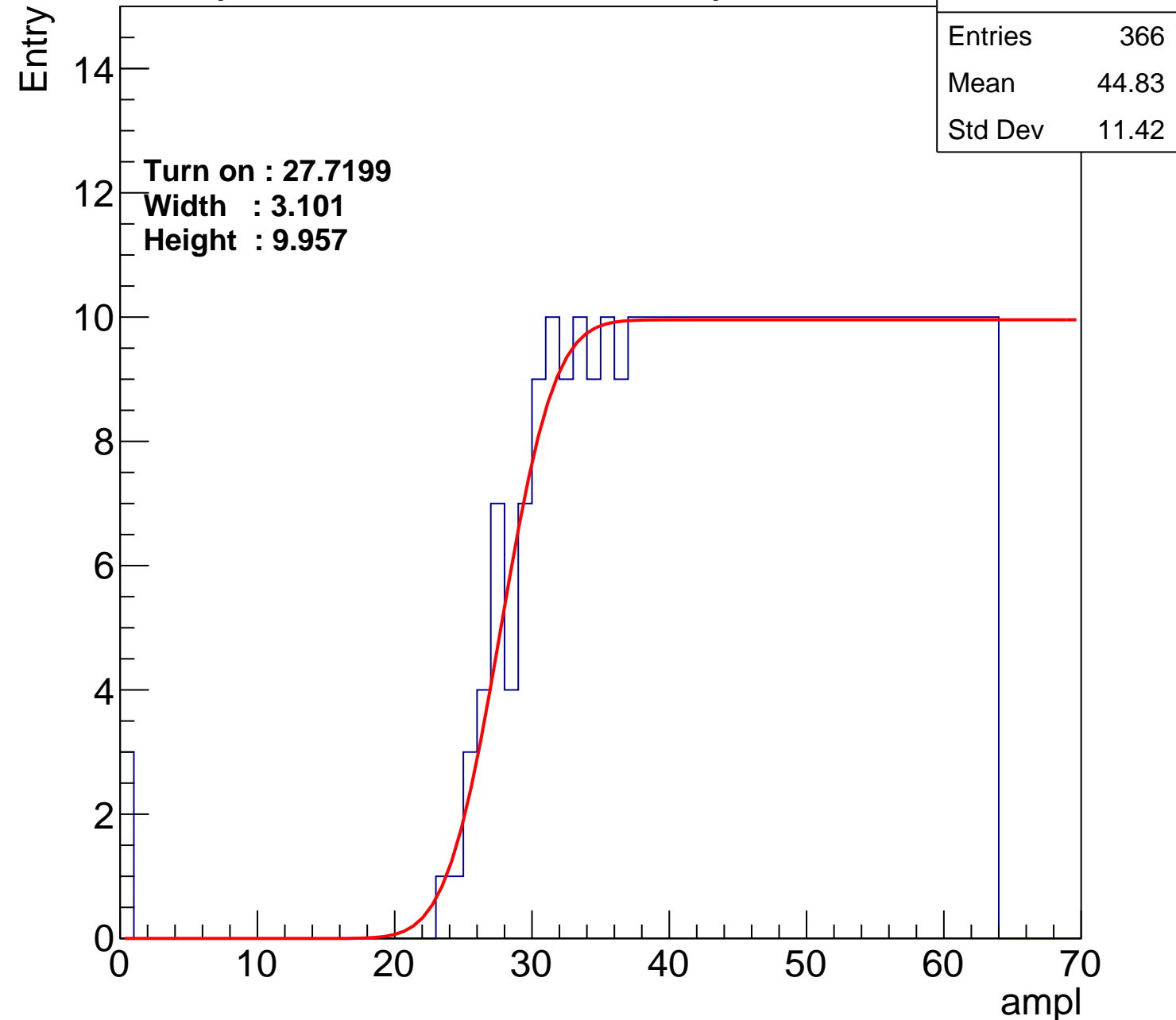
Width : 3.101

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.87
Std Dev	11.66

Turn on : 26.2110

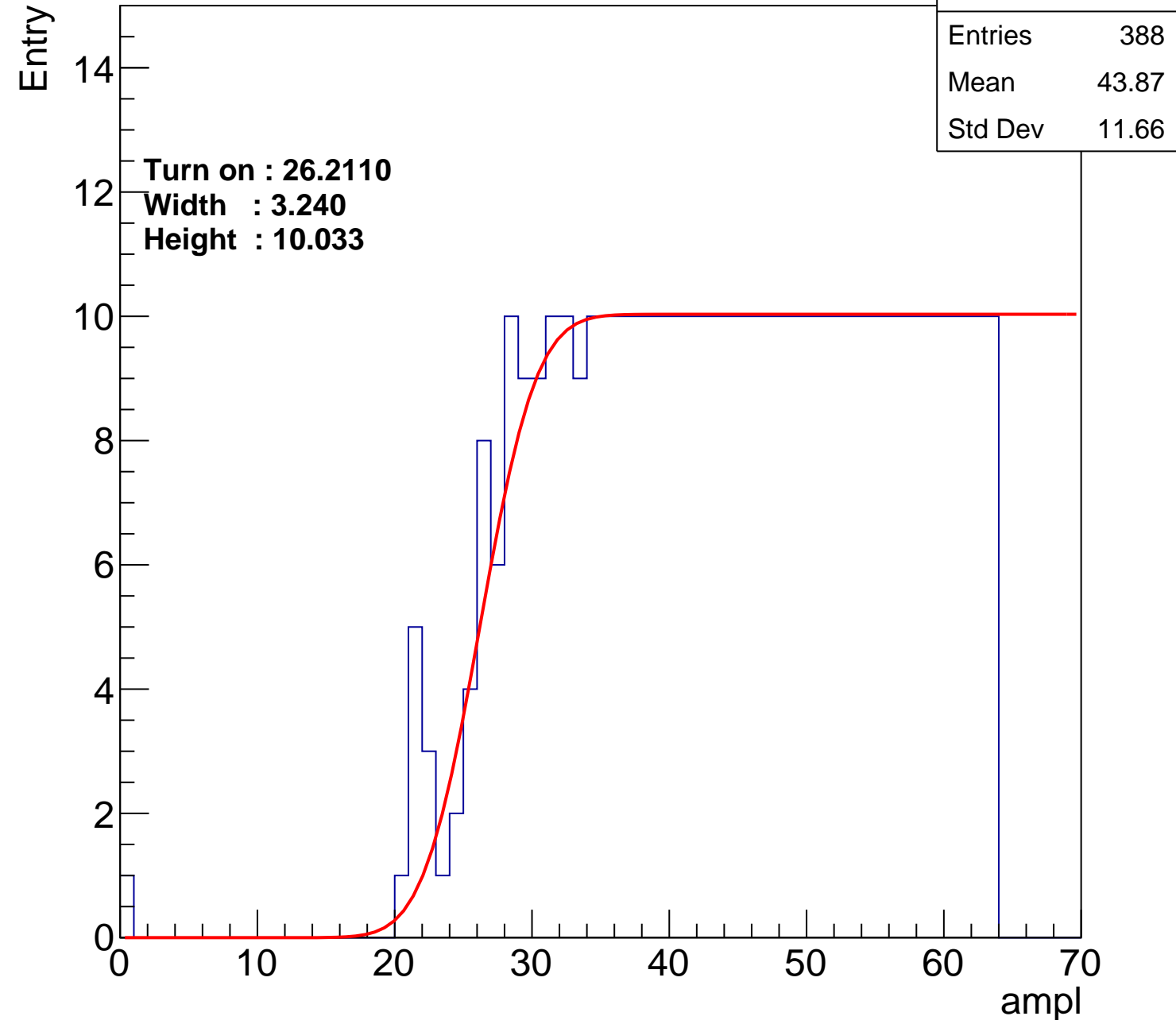
Width : 3.240

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch81

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.36
Std Dev	12.16

Turn on : 24.6540

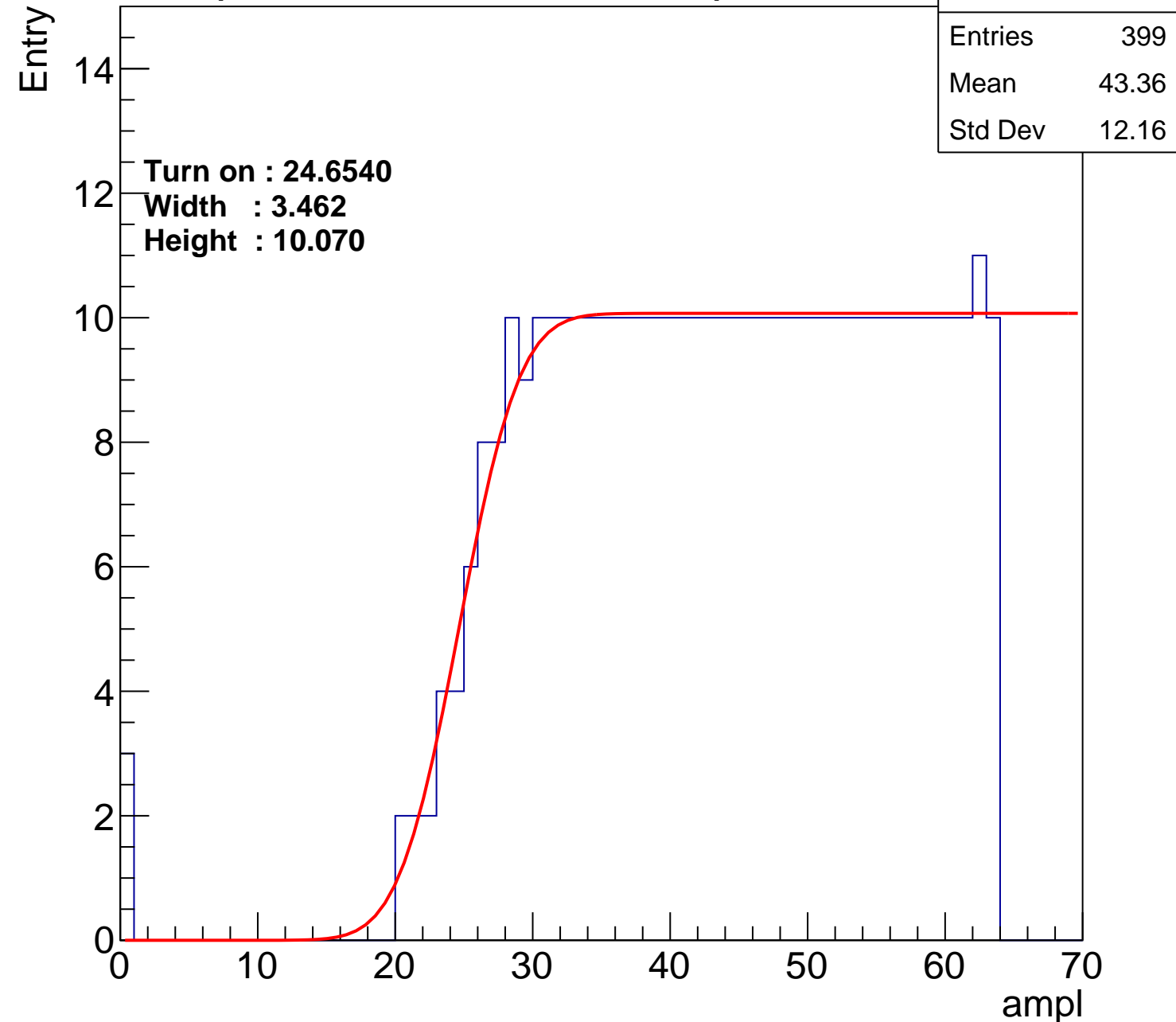
Width : 3.462

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.85
Std Dev	11.81

Turn on : 26.2485

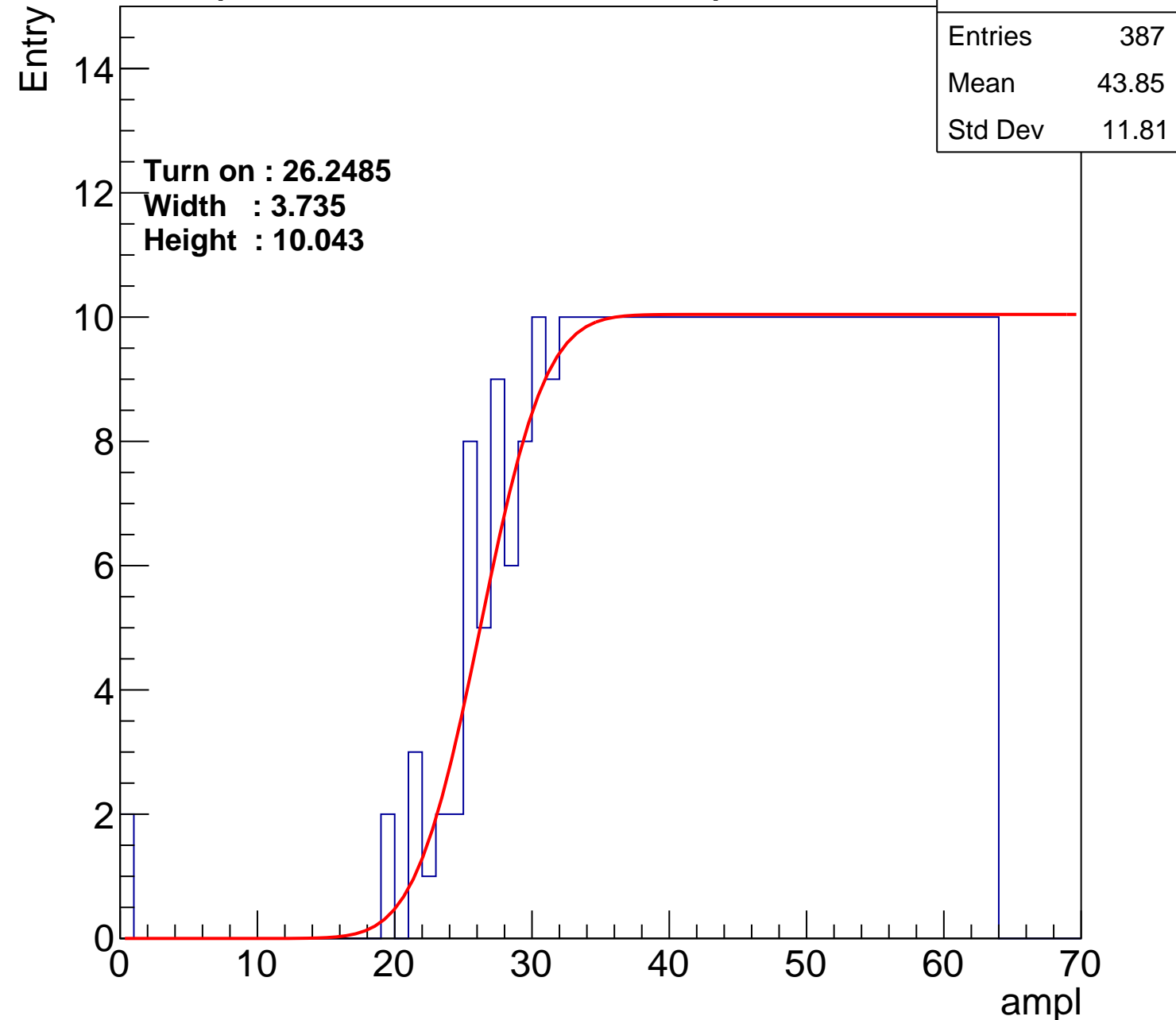
Width : 3.735

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.8
Std Dev	12.13

Turn on : 25.8669

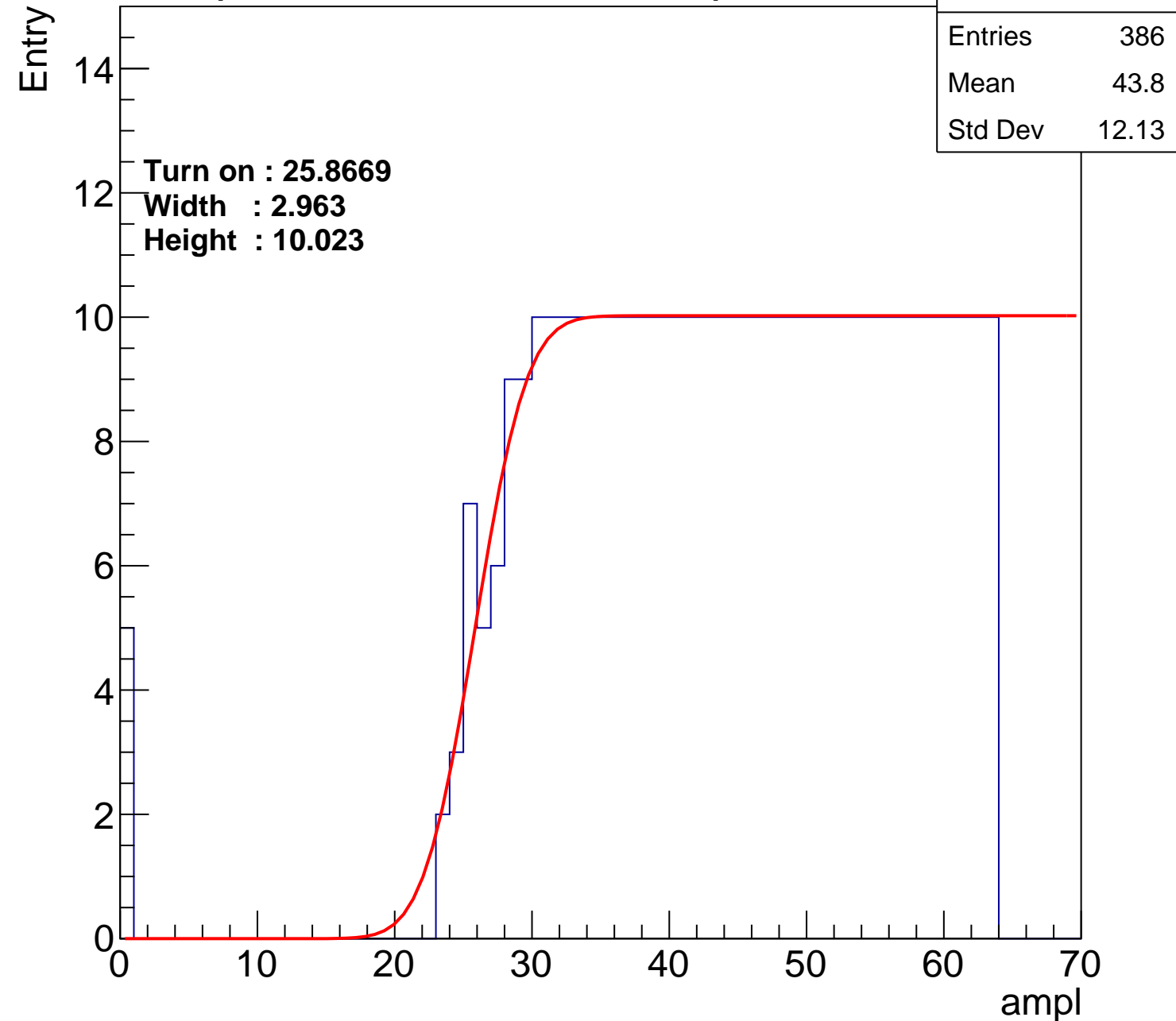
Width : 2.963

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.99
Std Dev	11.8

Turn on : 25.9880

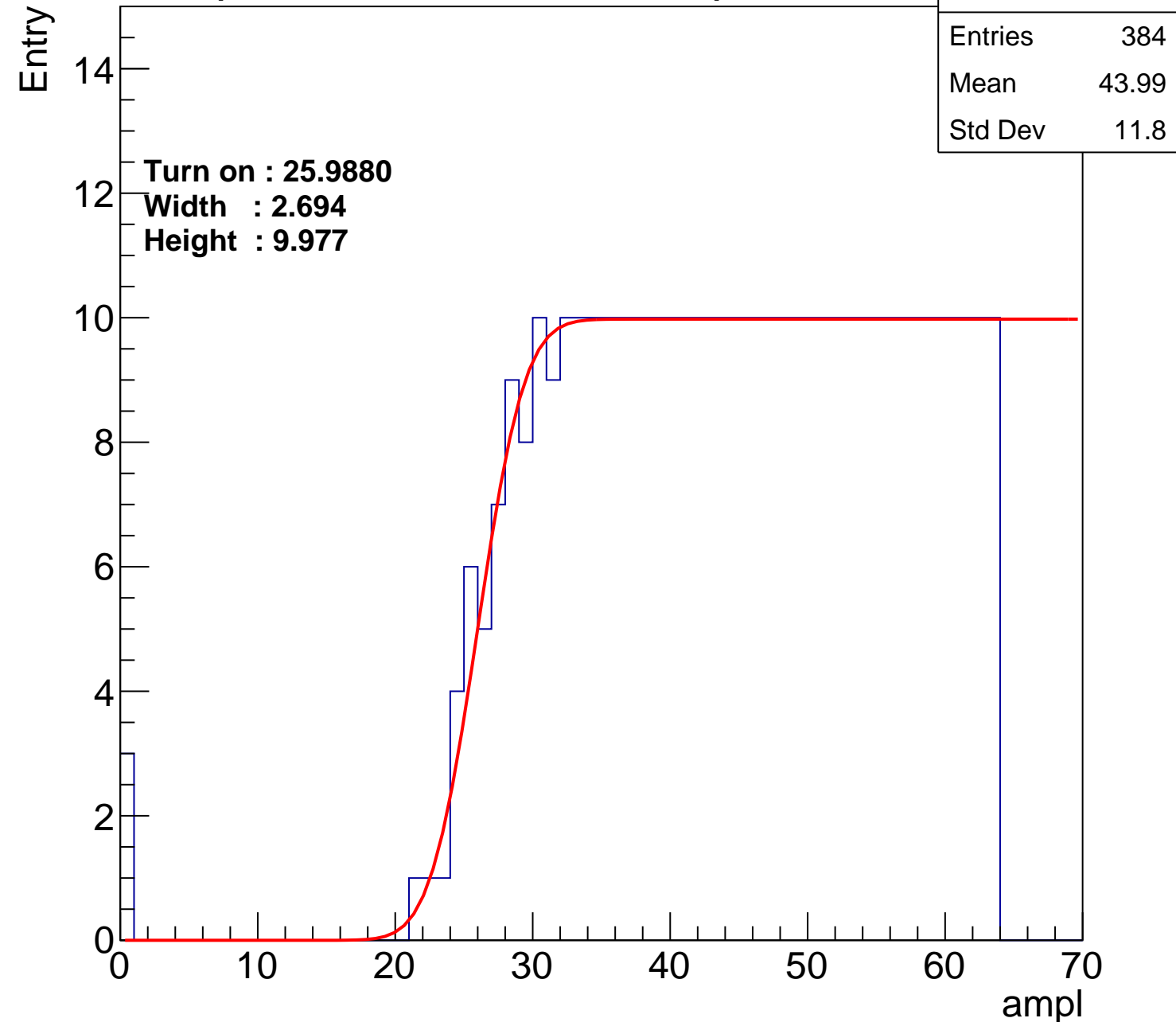
Width : 2.694

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.3
Std Dev	10.97

Turn on : 28.3348

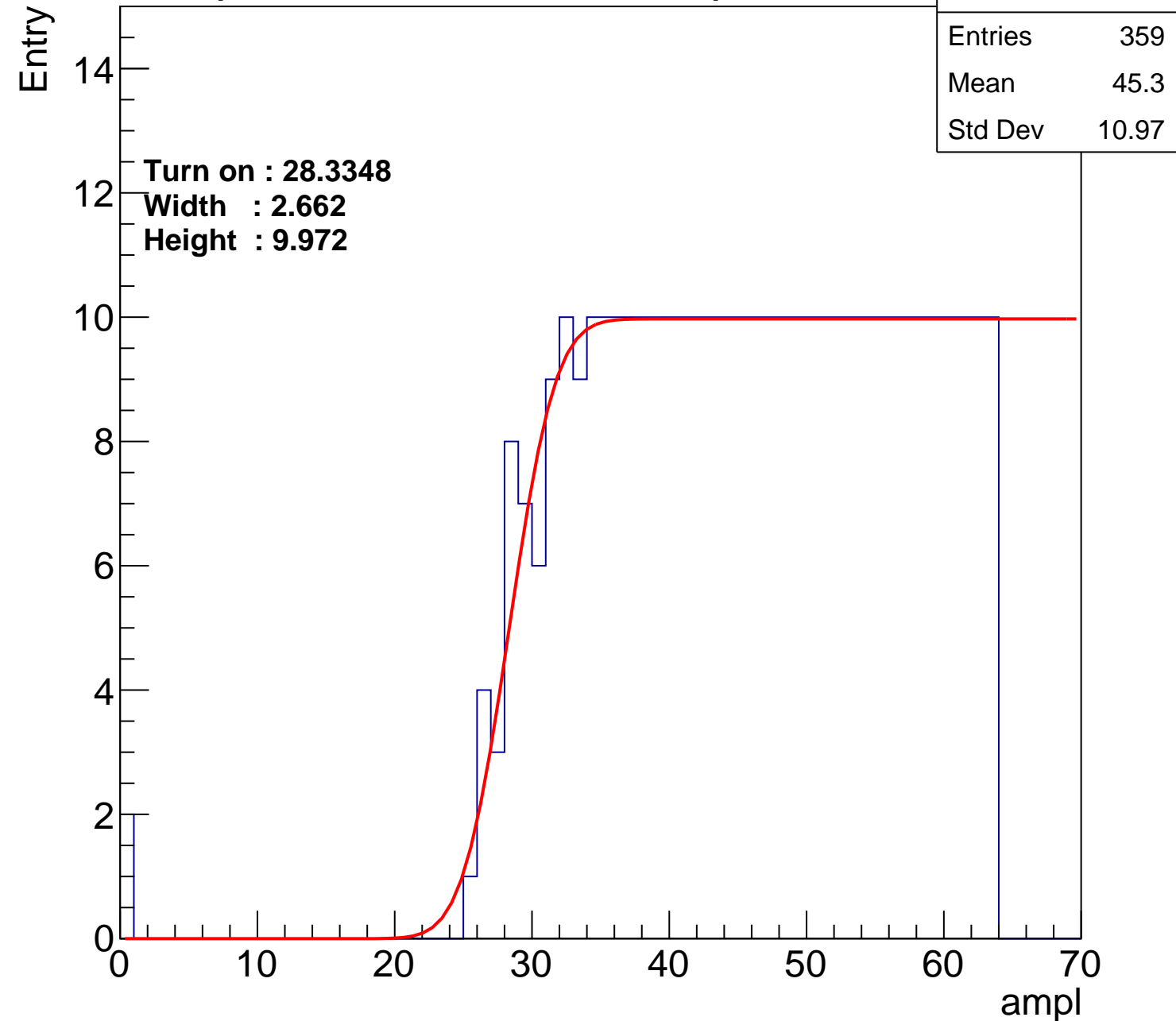
Width : 2.662

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch86

calib_packv5_042523_0143.root, FC#12, port B1

Entries	405
Mean	43.03
Std Dev	12.16

Turn on : 23.7739

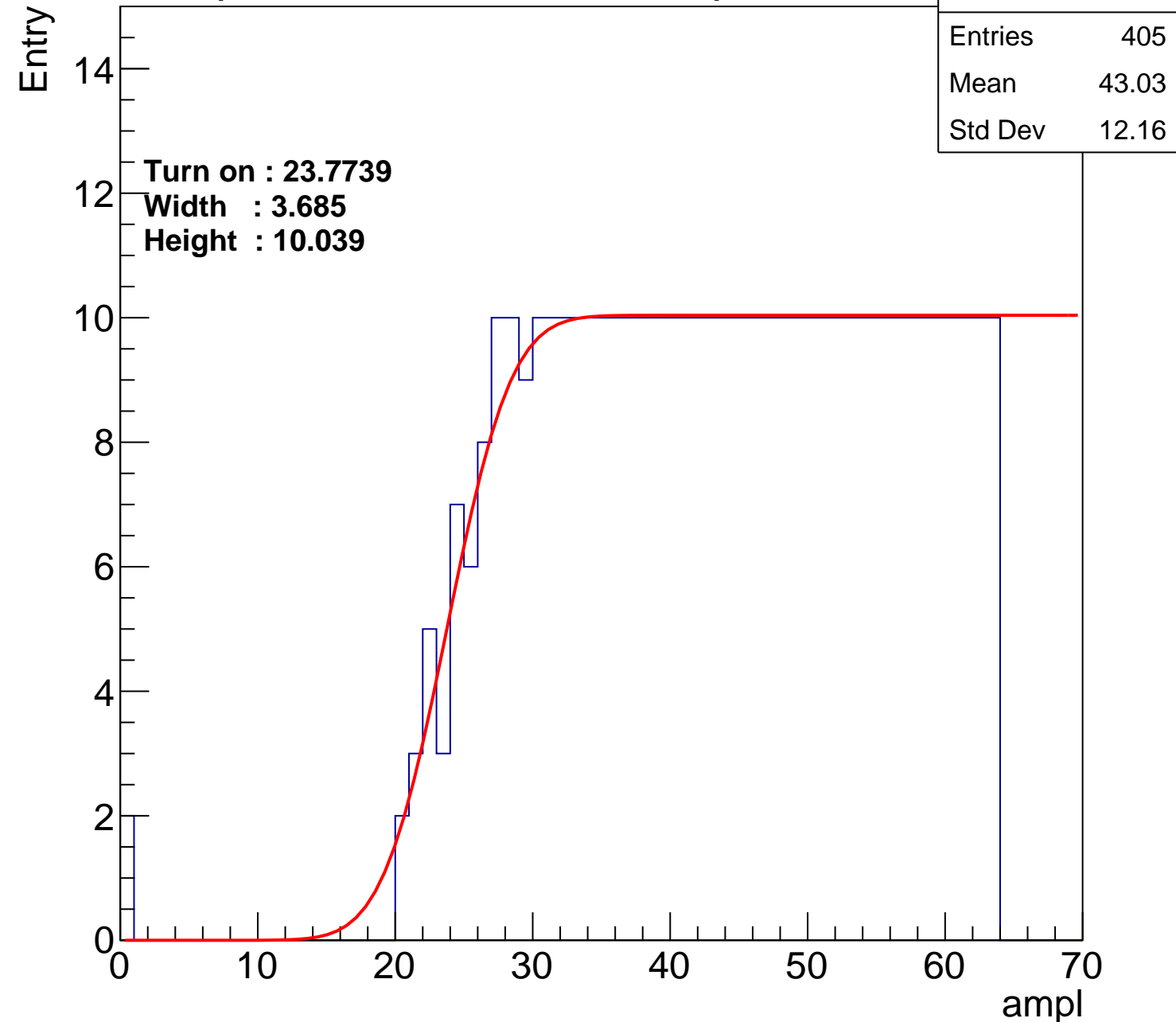
Width : 3.685

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 26.8831

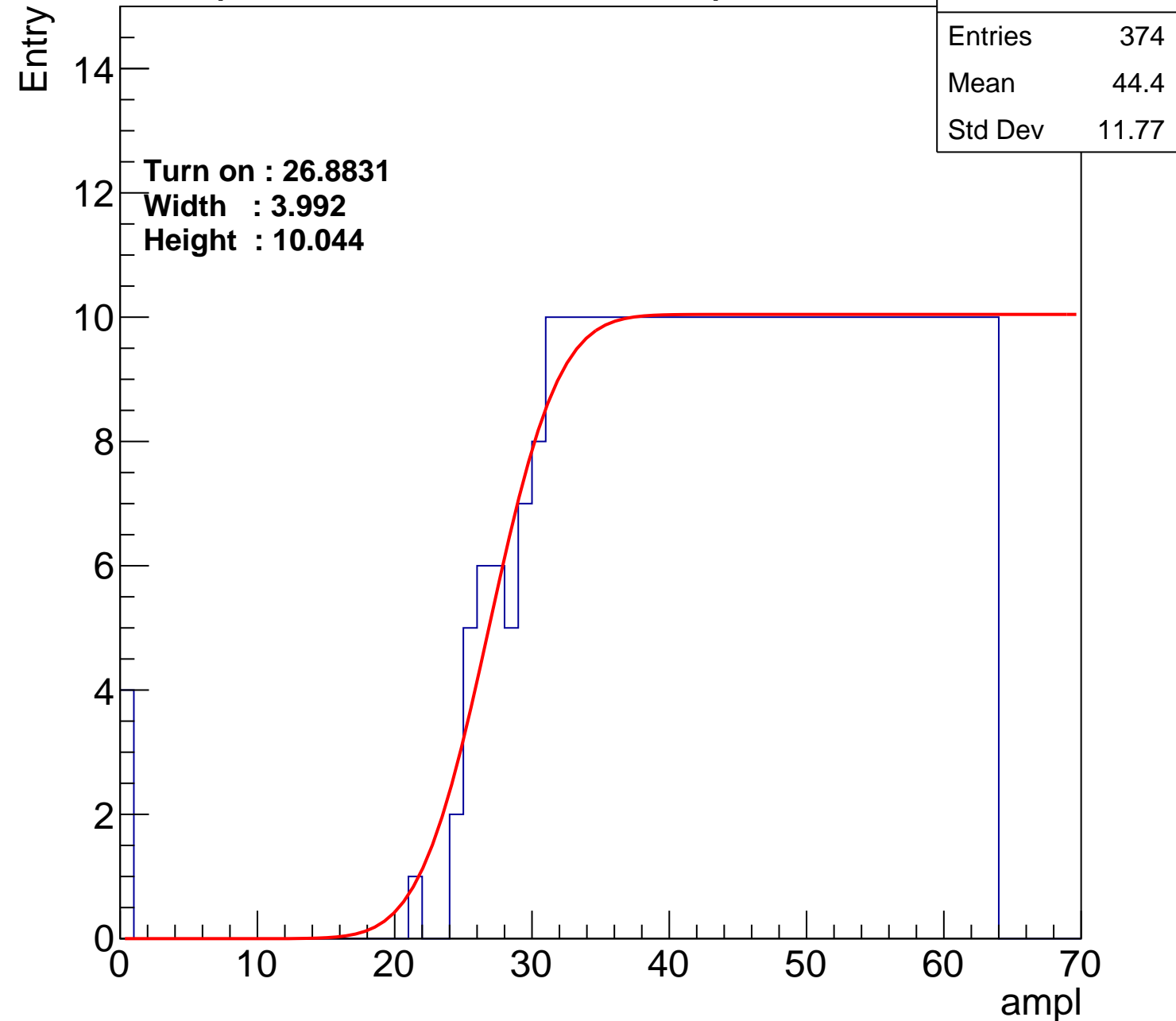
Width : 3.992

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch88

calib_packv5_042523_0143.root, FC#12, port B1

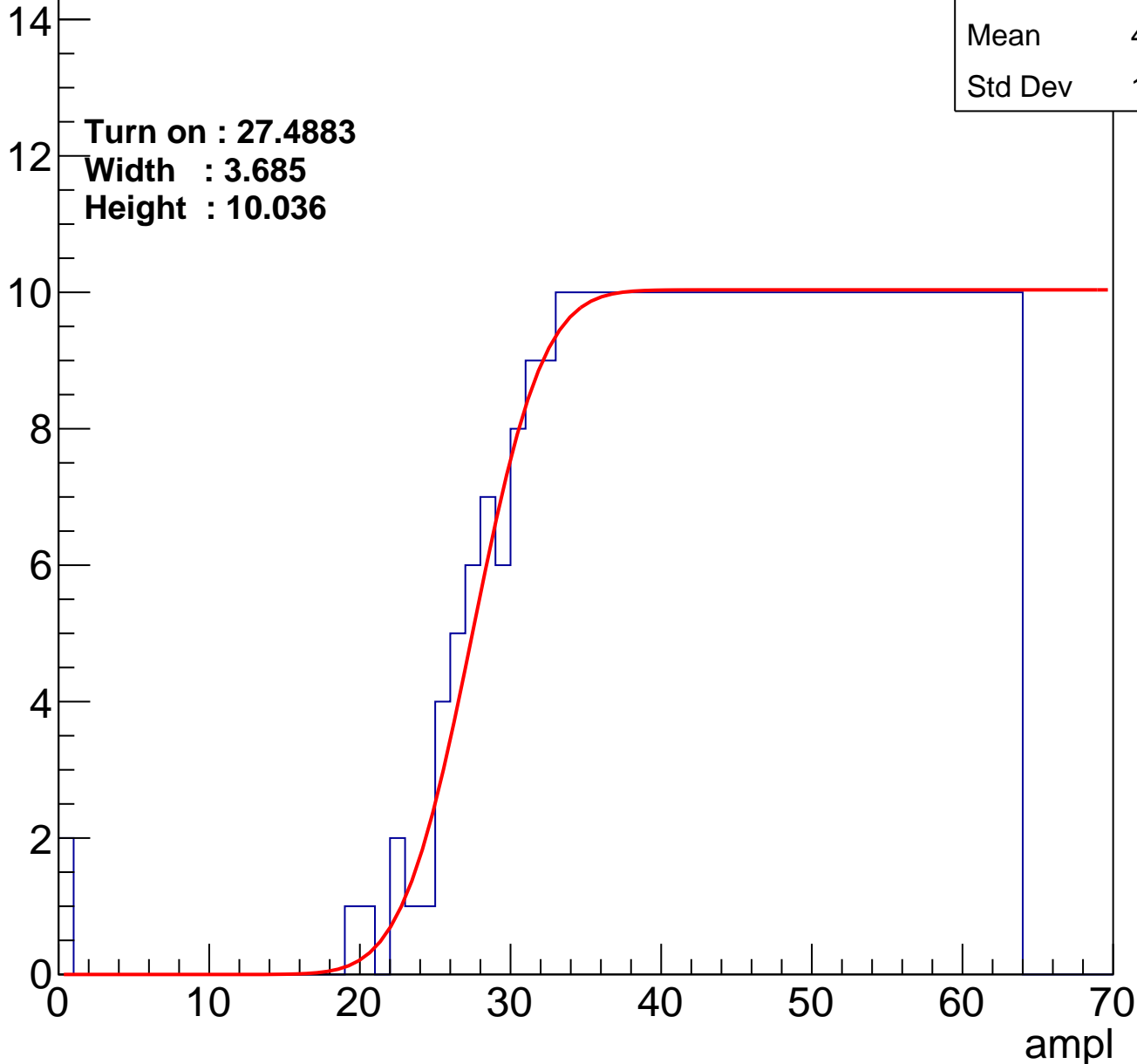
Entries	372
Mean	44.57
Std Dev	11.46

Turn on : 27.4883

Width : 3.685

Height : 10.036

Entry



B0L102S, U8-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.08
Std Dev	12.35

Turn on : 24.8619

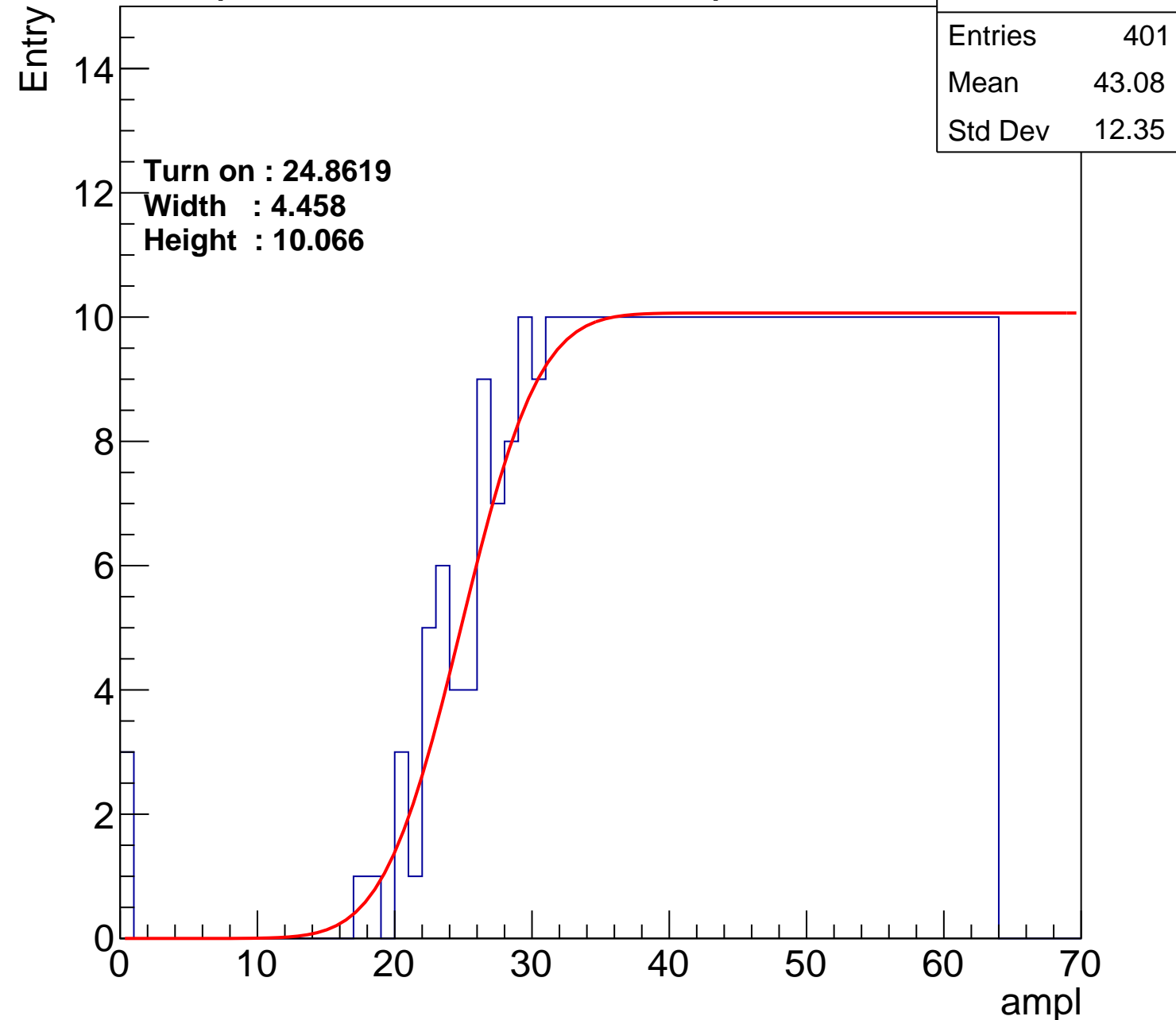
Width : 4.458

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.5
Std Dev	12.12

Turn on : 24.9811

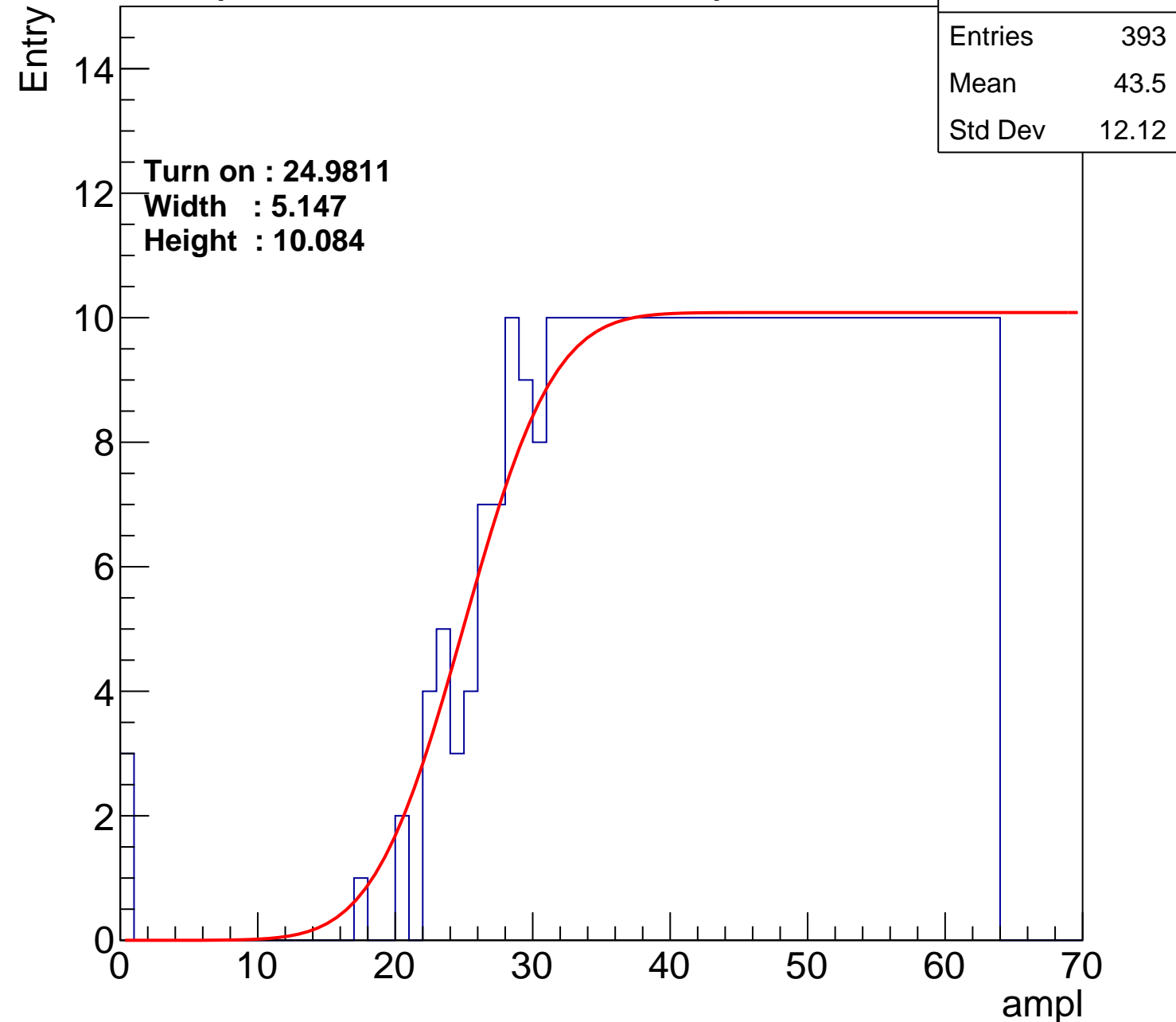
Width : 5.147

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.8
Std Dev	12.02

Turn on : 25.2370

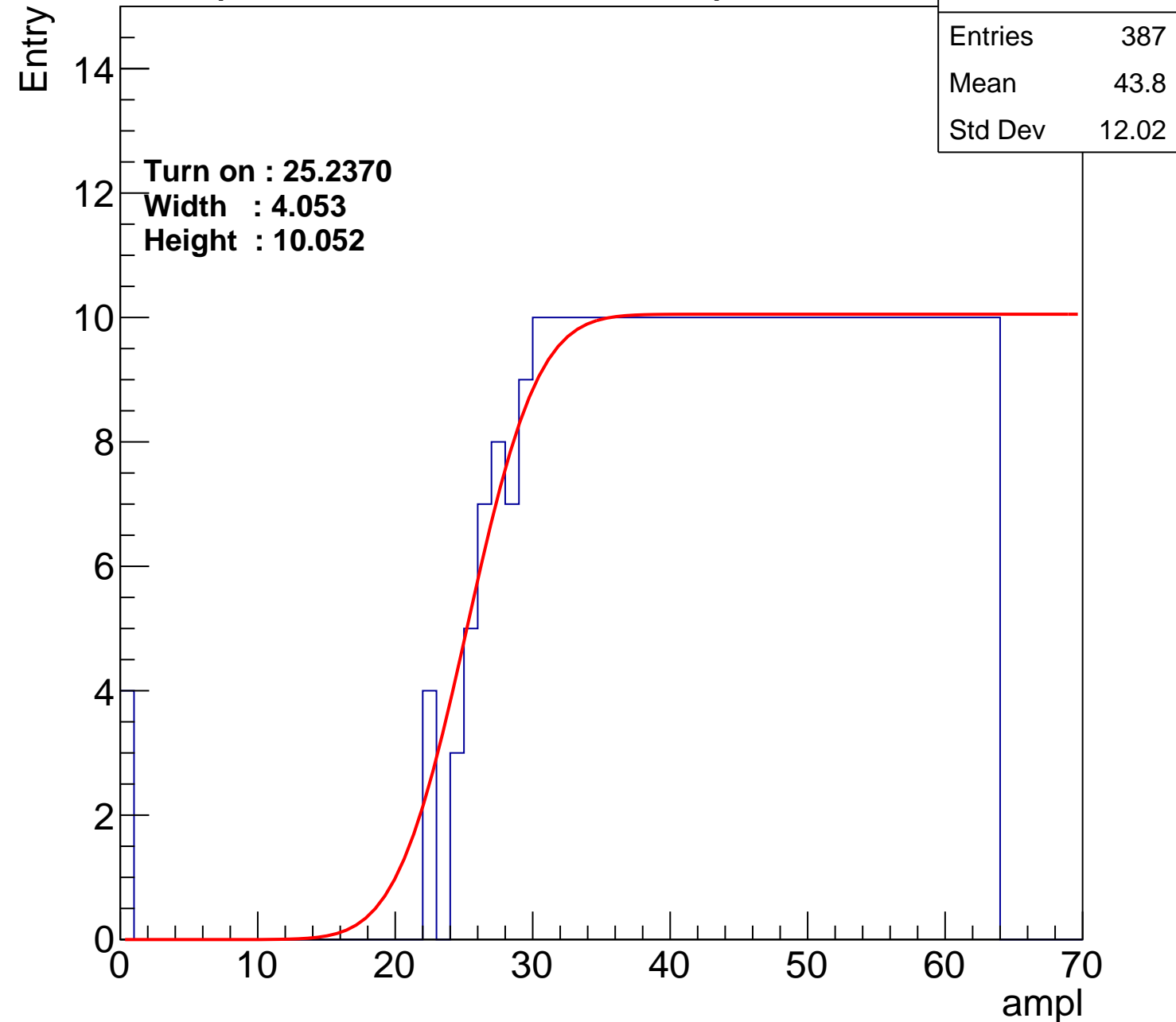
Width : 4.053

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.07
Std Dev	12.56

Turn on : 24.7314

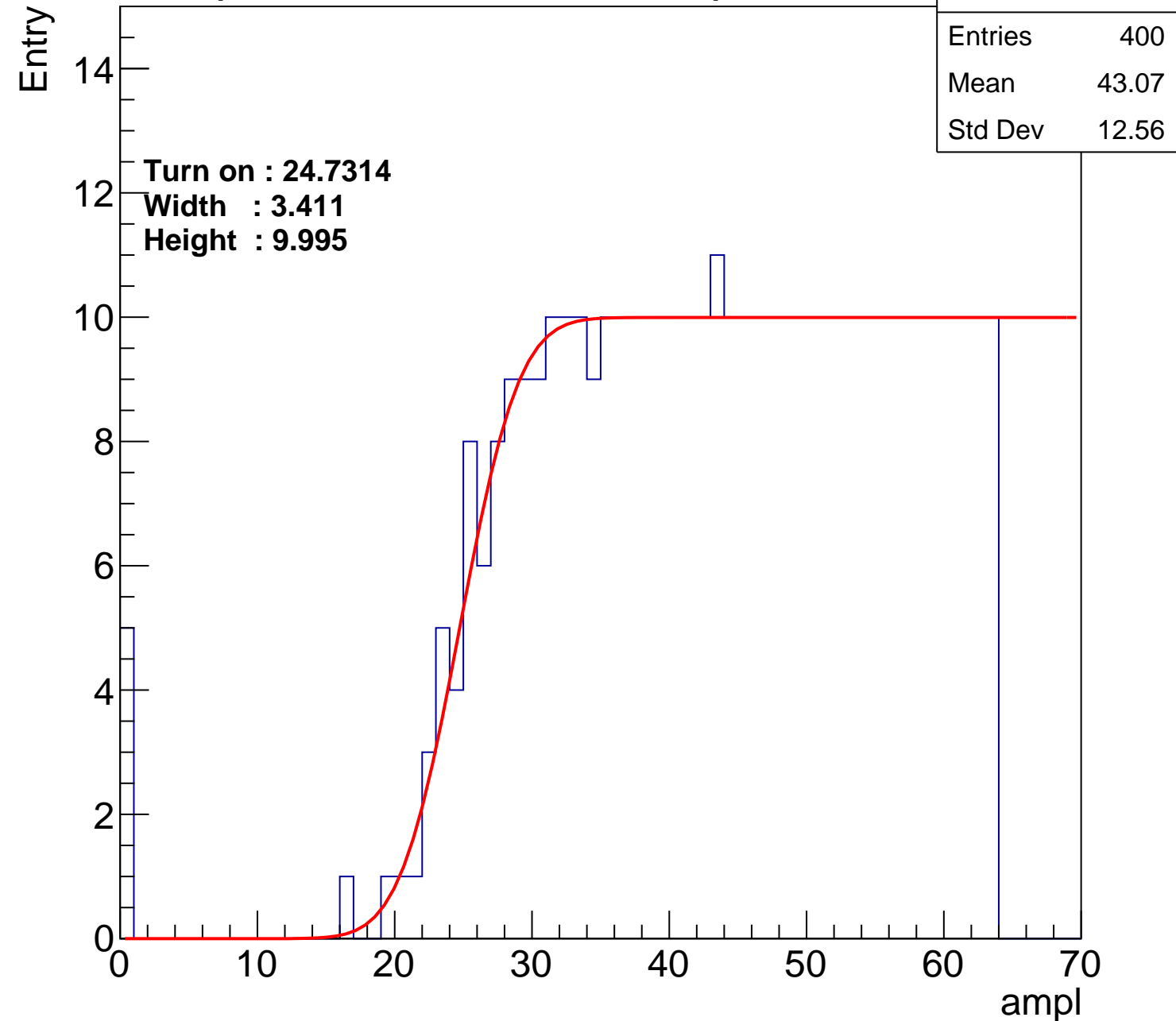
Width : 3.411

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	369
Mean	44.88
Std Dev	11.03

Turn on : 27.3526

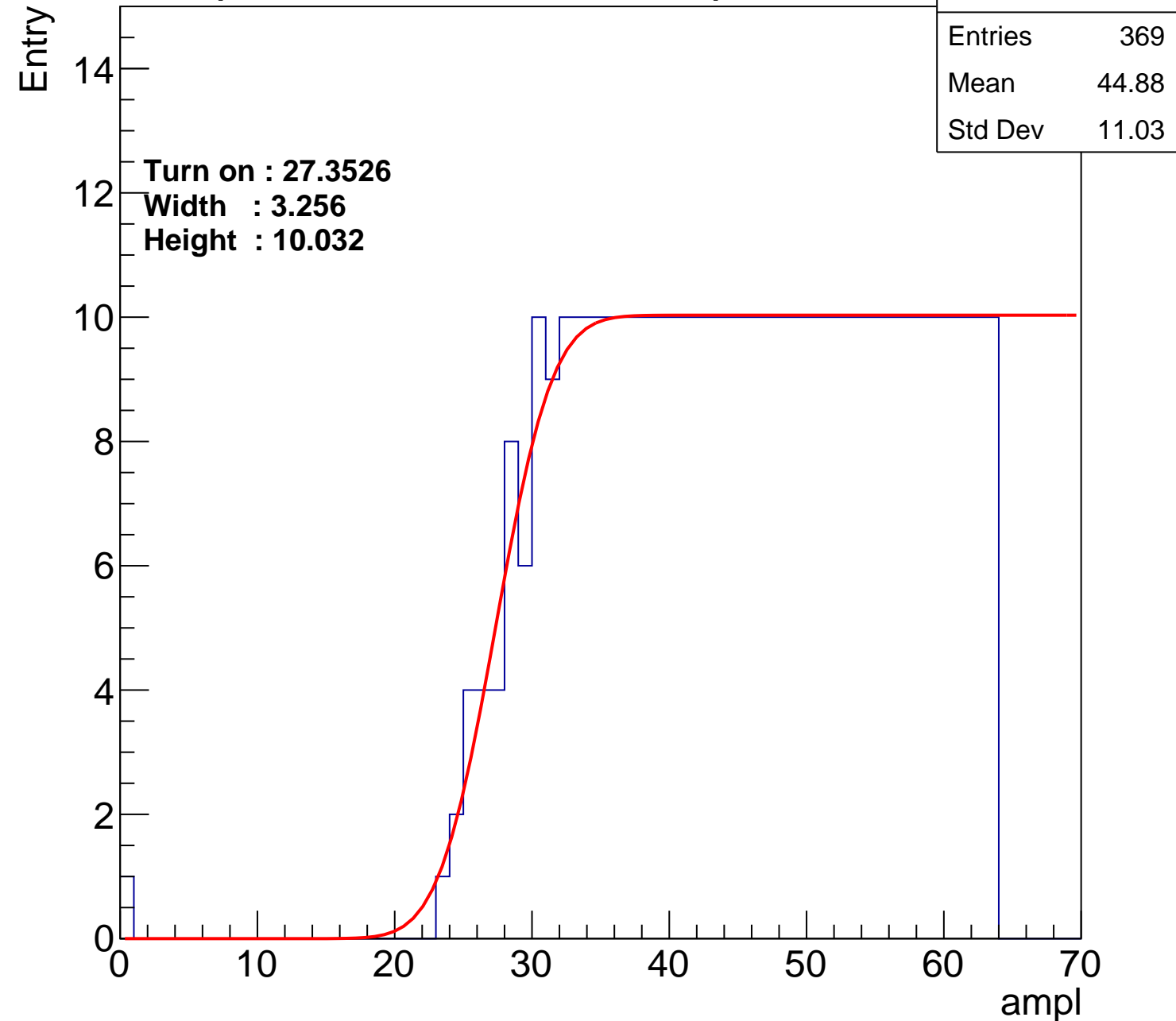
Width : 3.256

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.38
Std Dev	12.04

Turn on : 25.0813

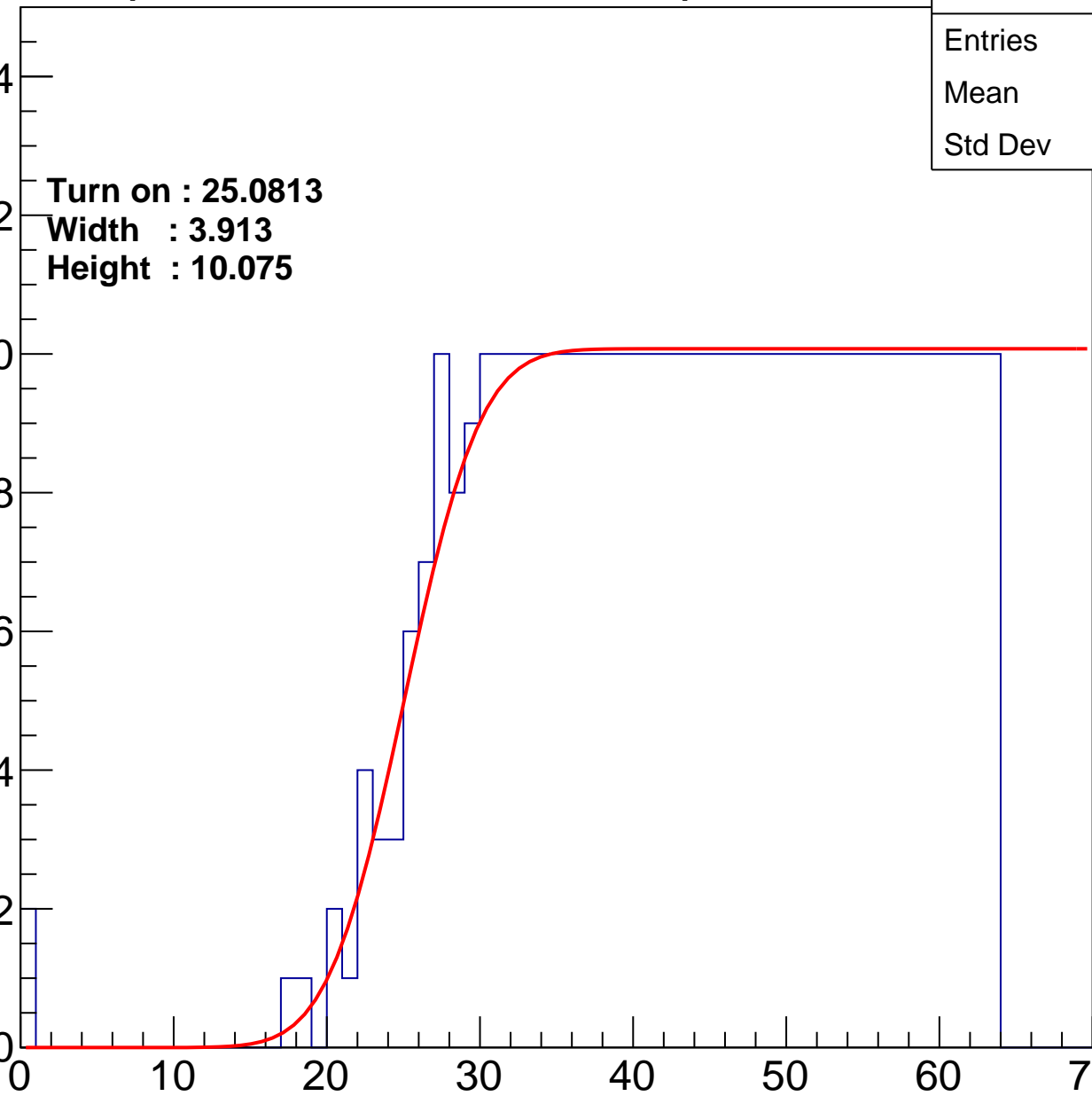
Width : 3.913

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.83
Std Dev	11.58

Turn on : 28.2128

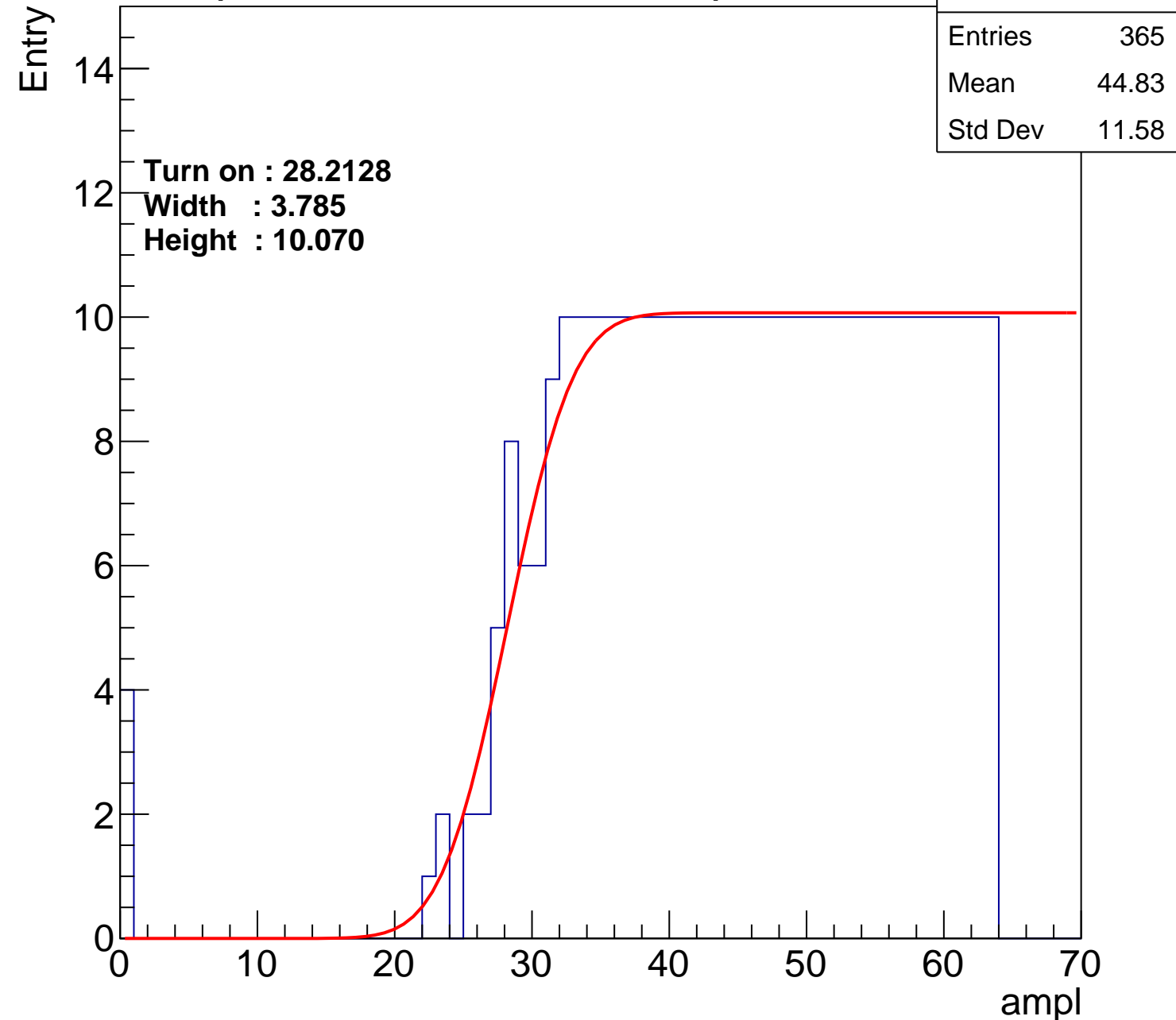
Width : 3.785

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.58
Std Dev	12.55

Turn on : 26.4554

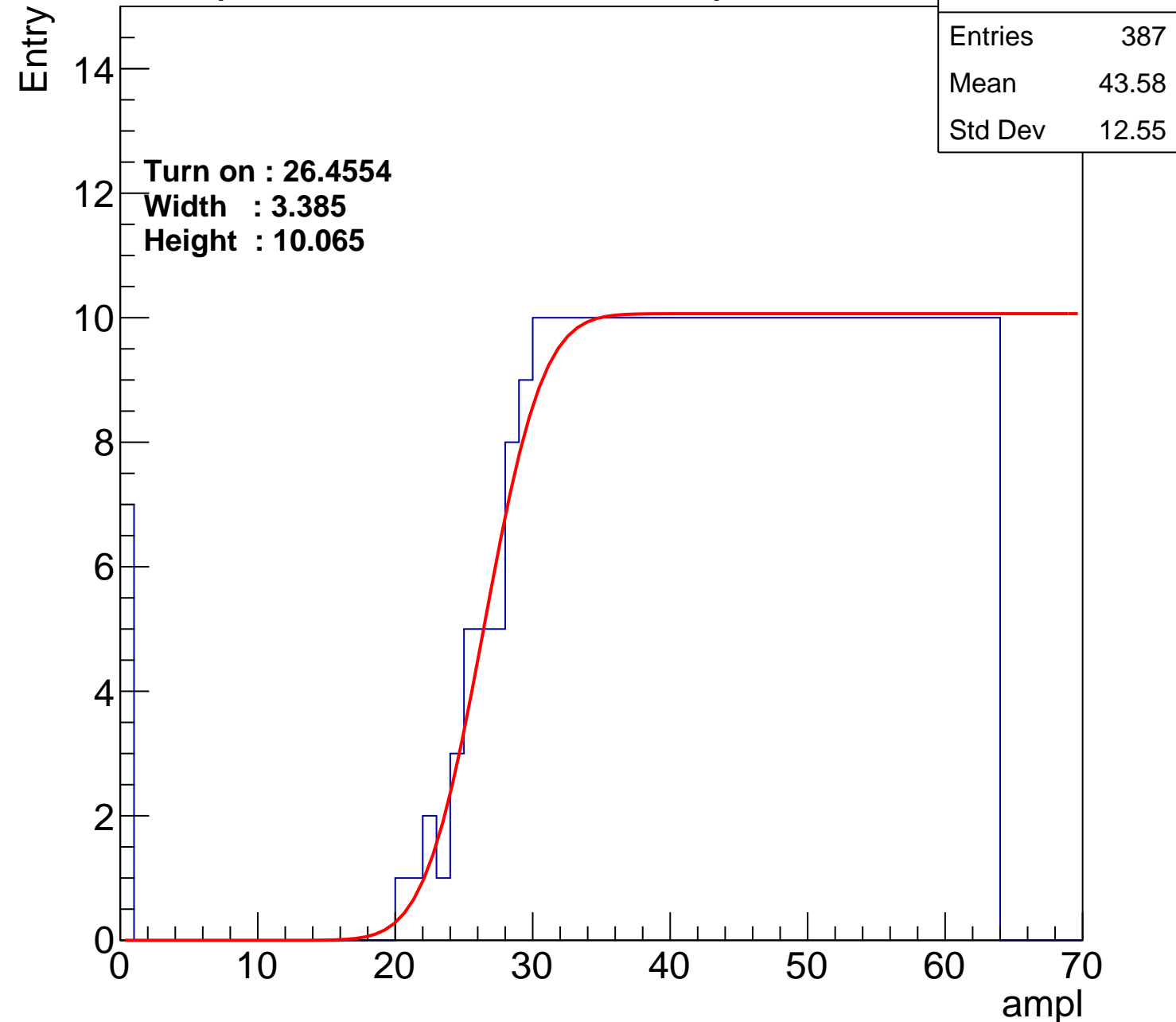
Width : 3.385

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.4920

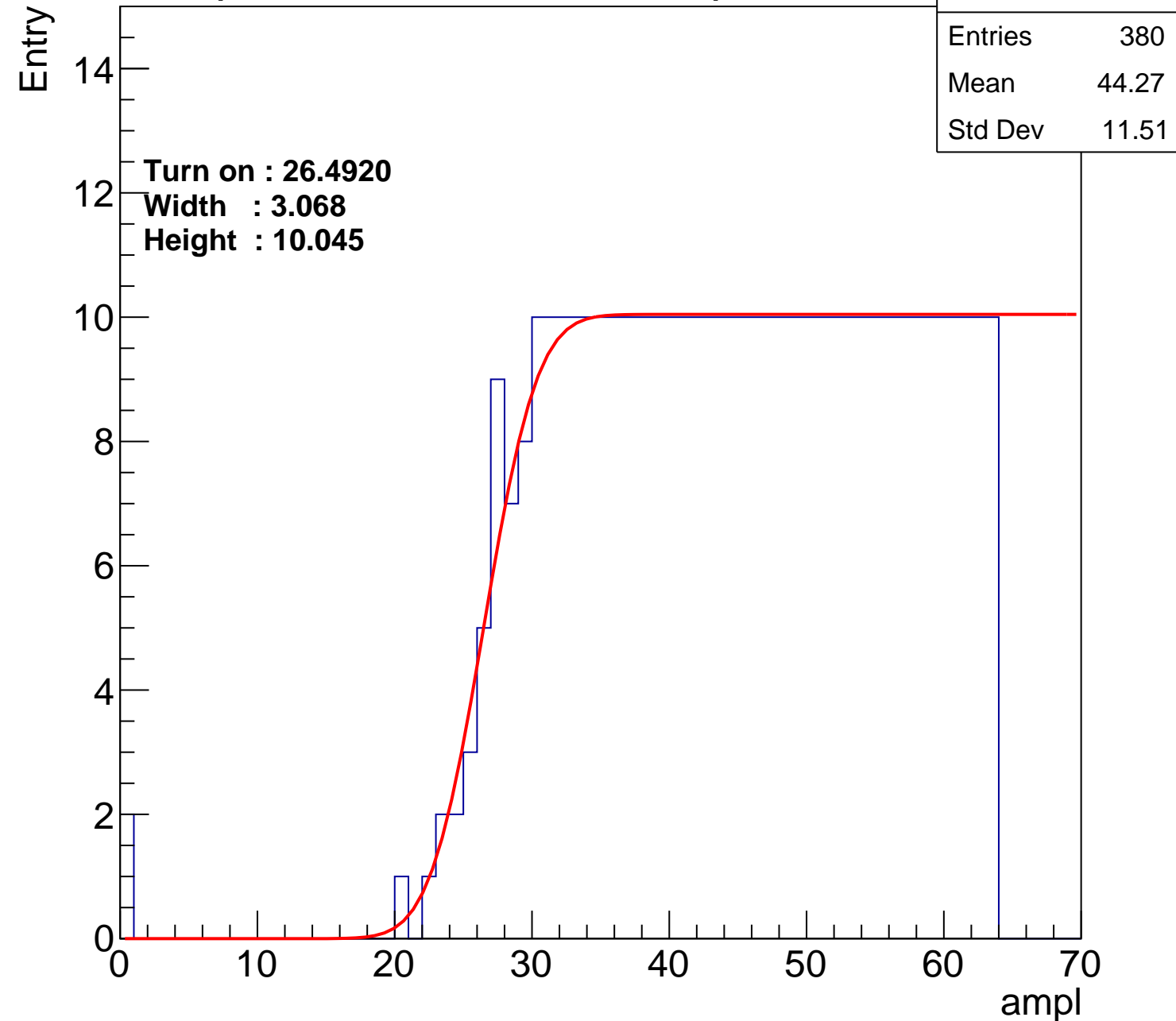
Width : 3.068

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.74
Std Dev	12.39

Turn on : 23.1818

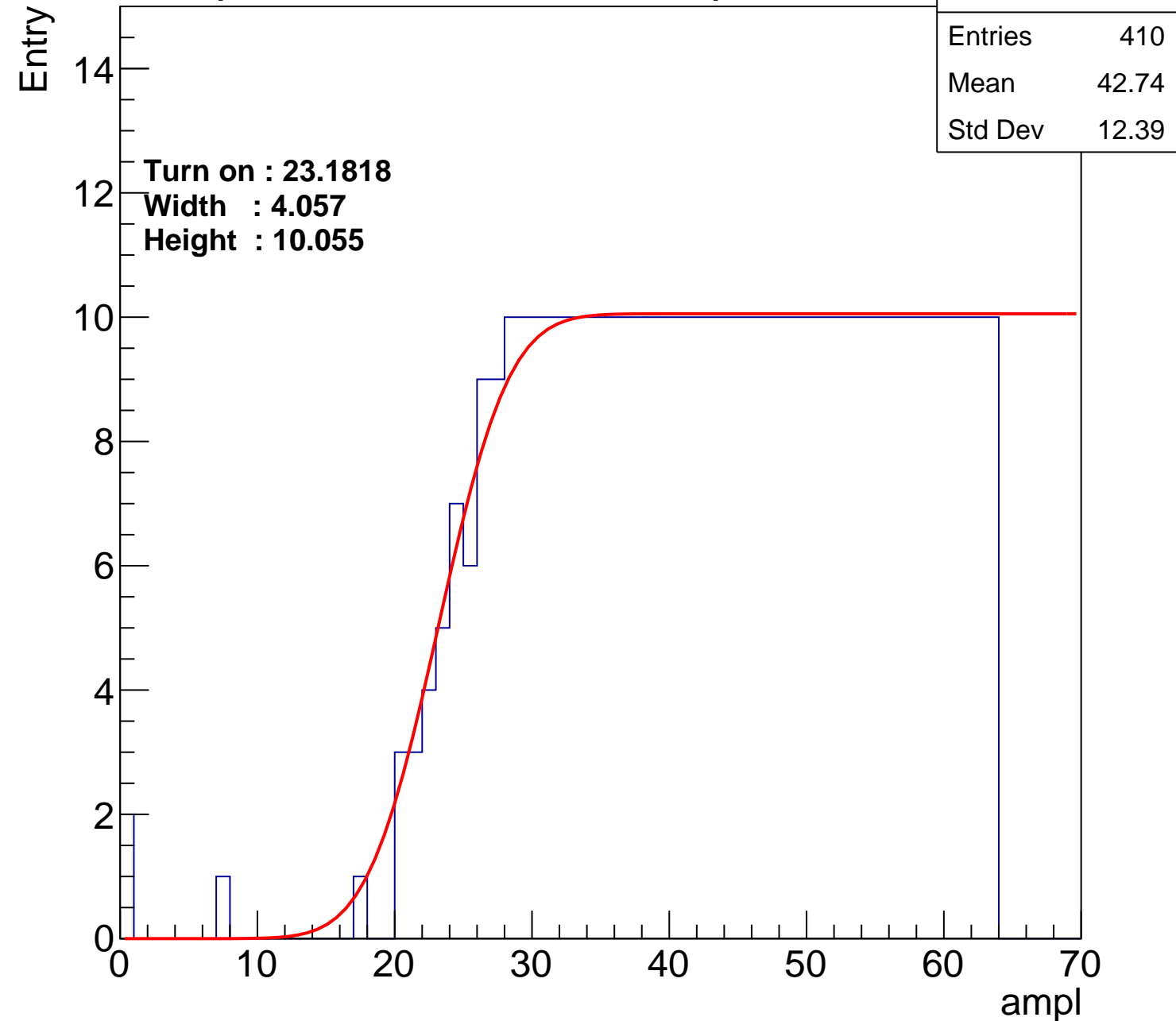
Width : 4.057

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch99

calib_packv5_042523_0143.root, FC#12, port B1

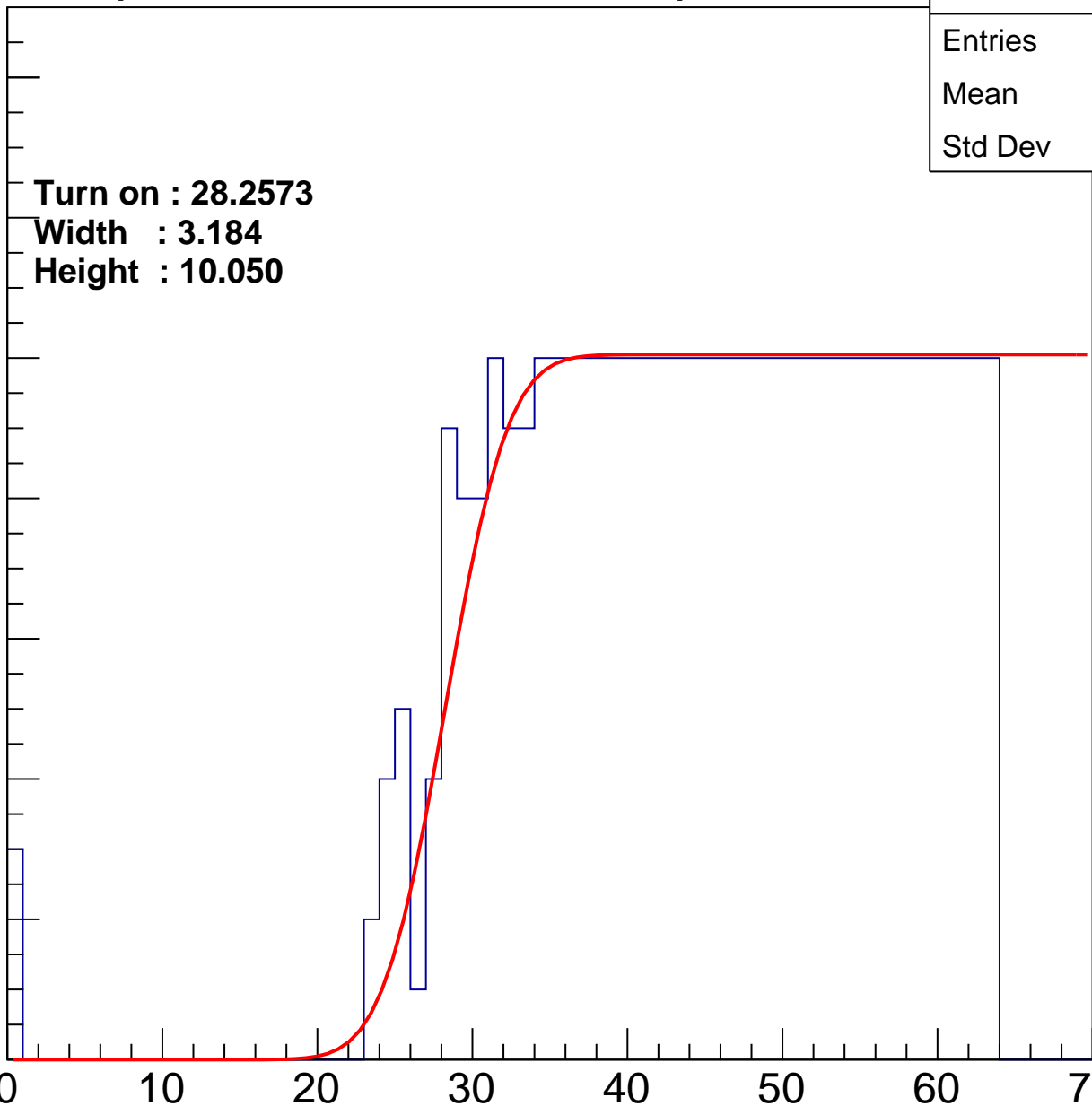
Entry

14
12
10
8
6
4
2
0

Turn on : 28.2573
Width : 3.184
Height : 10.050

Entries	372
Mean	44.54
Std Dev	11.57

ampl



B0L102S, U8-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.65
Std Dev	12.02

Turn on : 25.1696

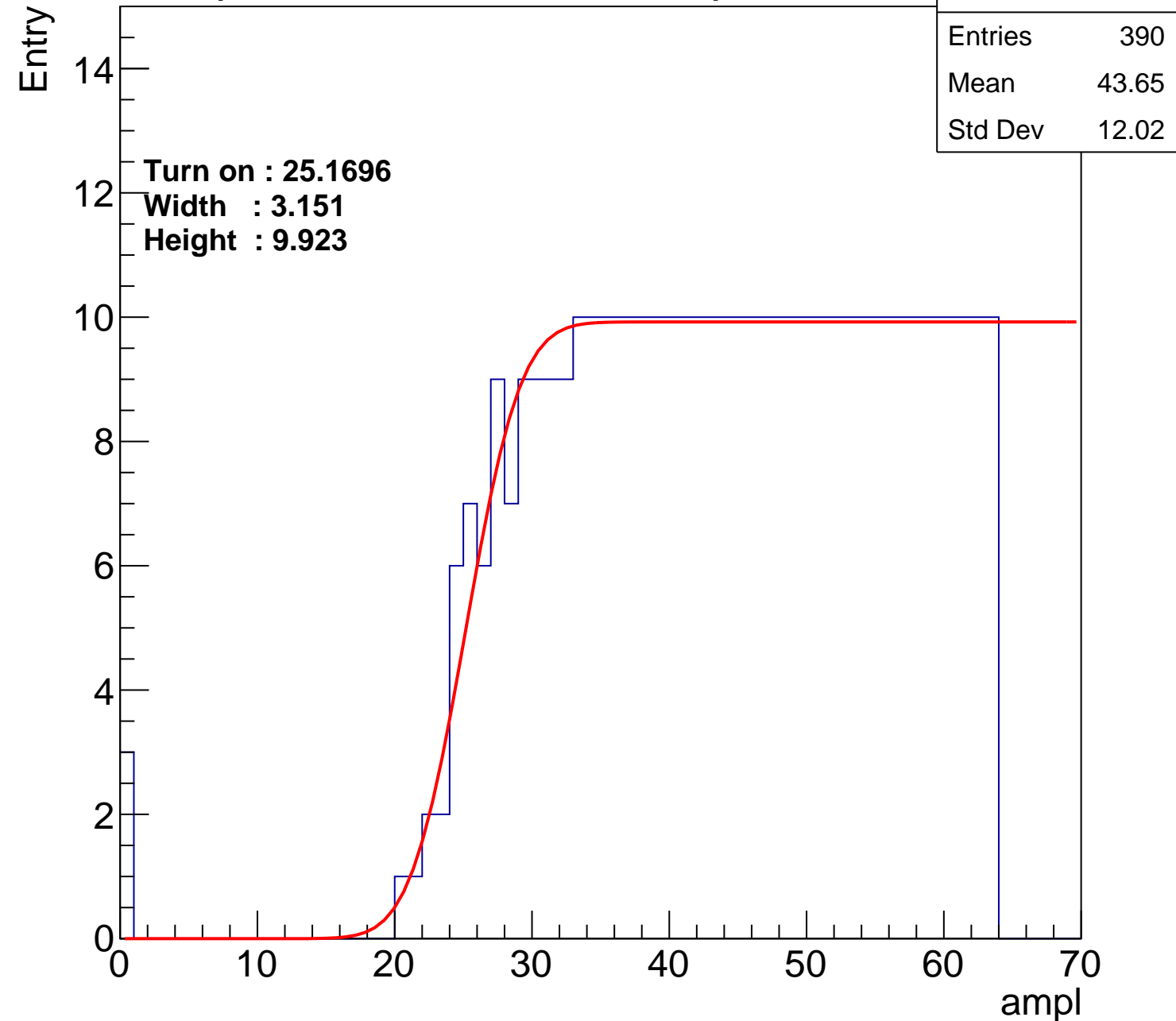
Width : 3.151

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.45
Std Dev	11.85

Turn on : 23.9688

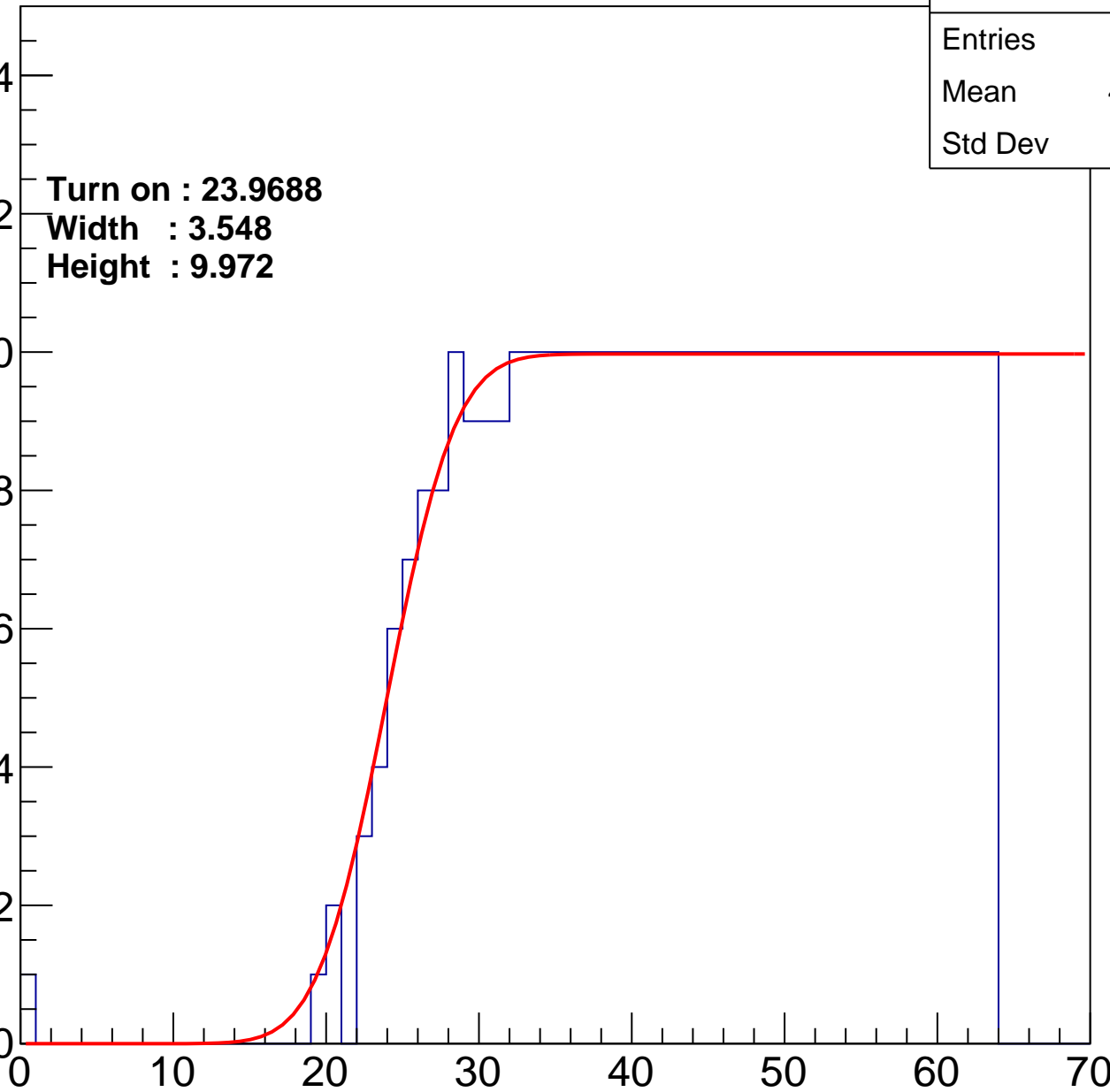
Width : 3.548

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.97
Std Dev	11.71

Turn on : 25.6233

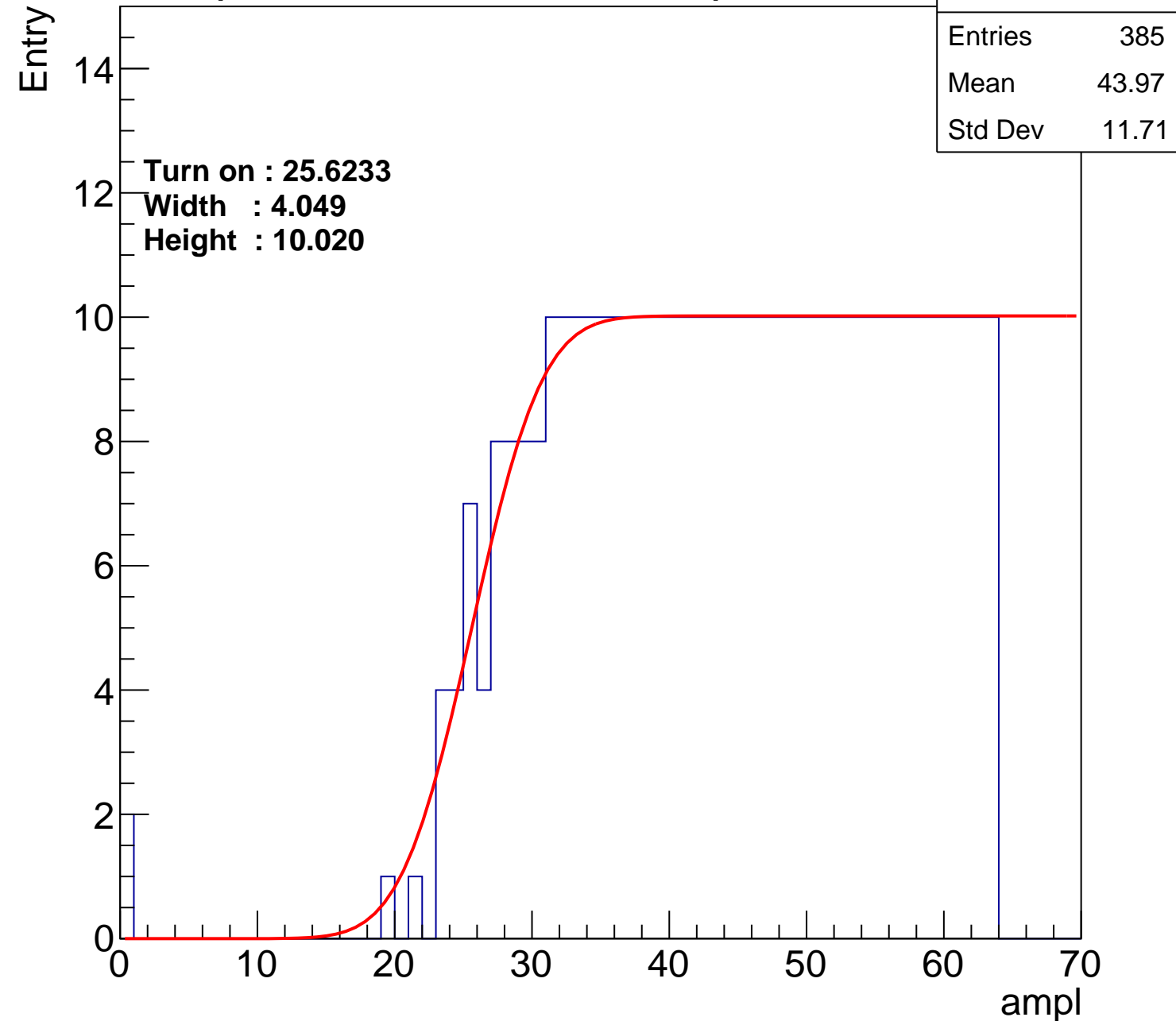
Width : 4.049

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch103

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.4467

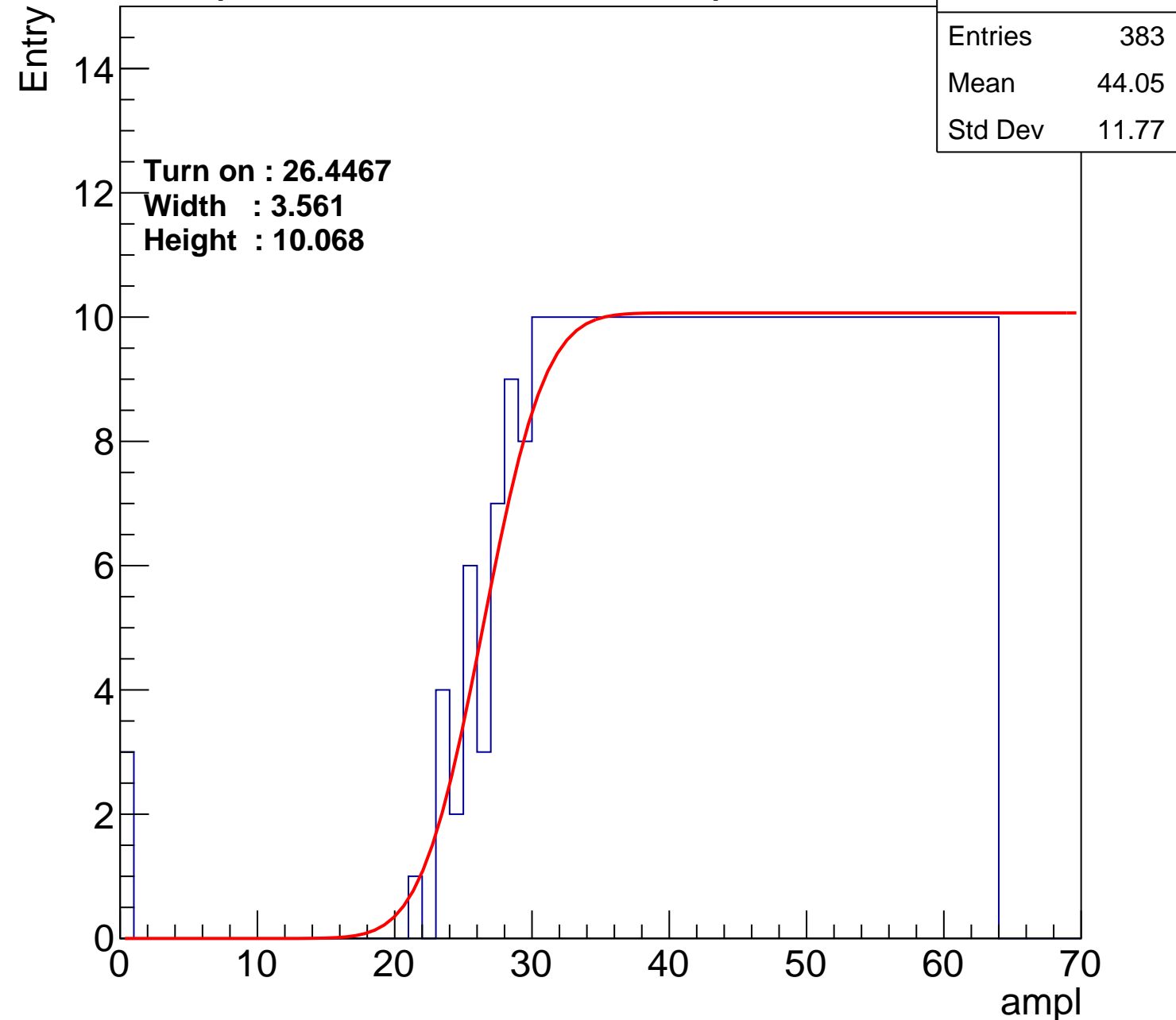
Width : 3.561

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.92
Std Dev	11.75

Turn on : 25.7797

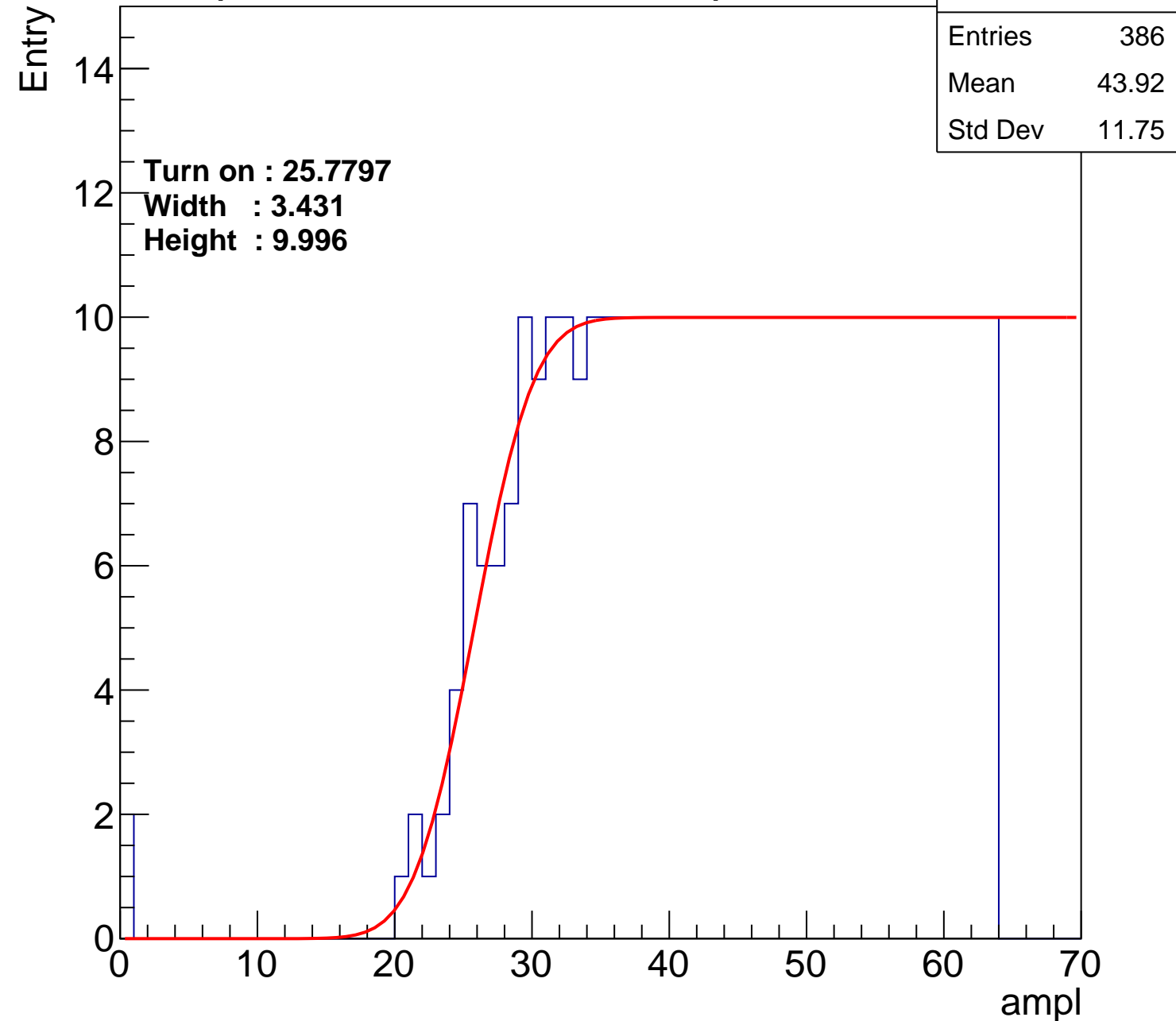
Width : 3.431

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.08
Std Dev	11.9

Turn on : 26.6509

Width : 1.979

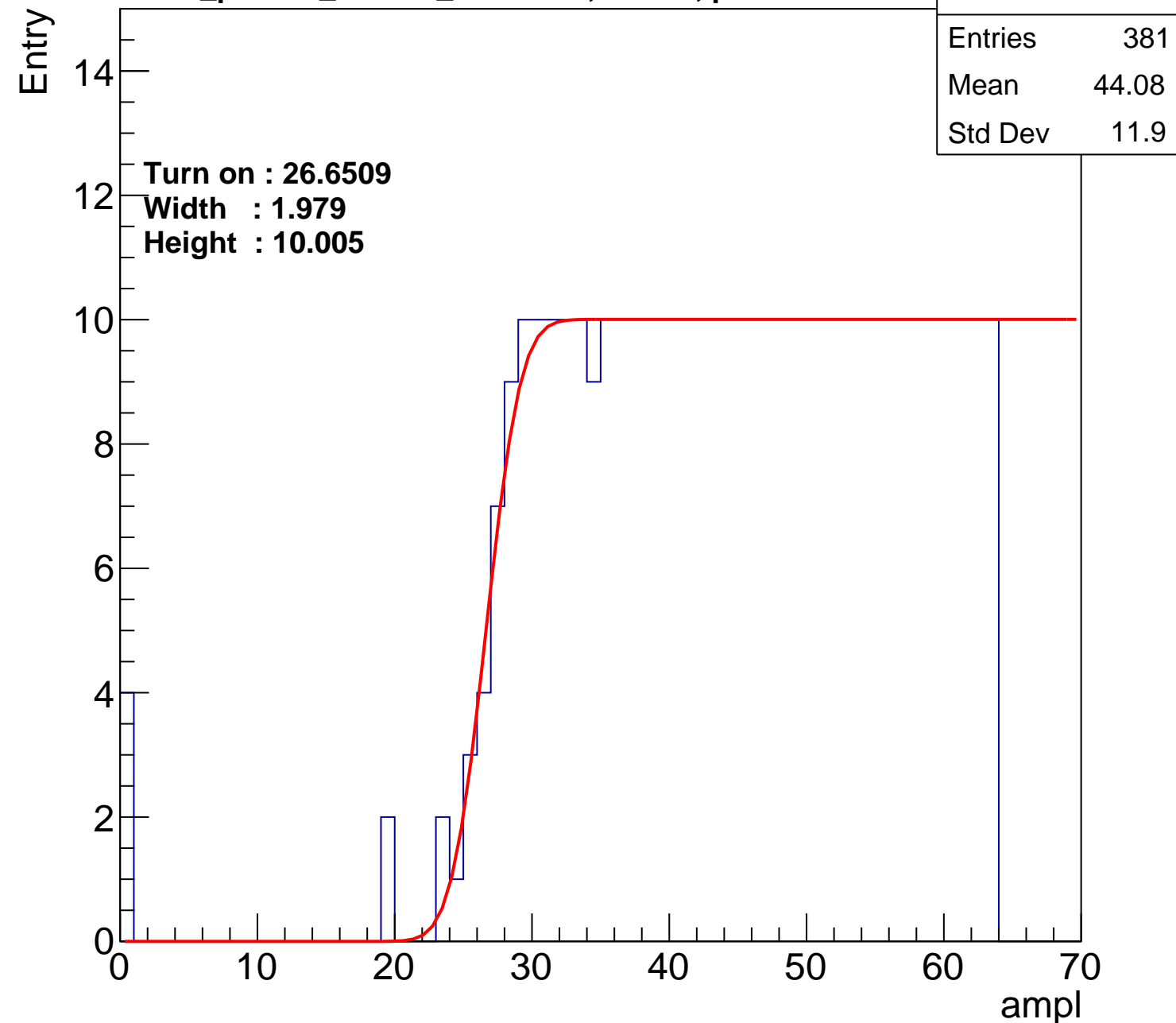
Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U8-ch106

calib_packv5_042523_0143.root, FC#12, port B1

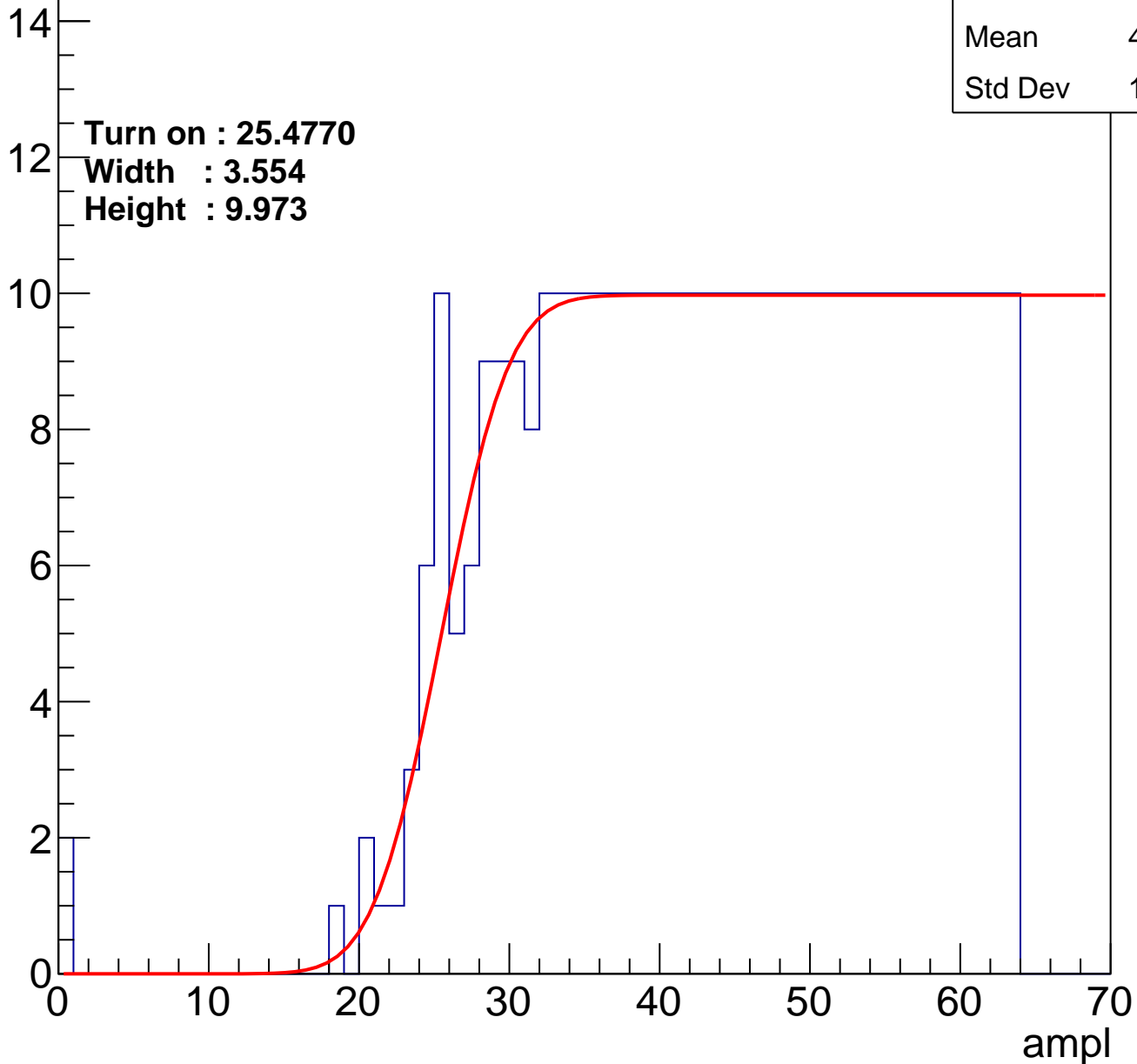
Entries	392
Mean	43.59
Std Dev	11.95

Turn on : 25.4770

Width : 3.554

Height : 9.973

Entry



B0L102S, U8-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.9
Std Dev	11.92

Turn on : 25.6086

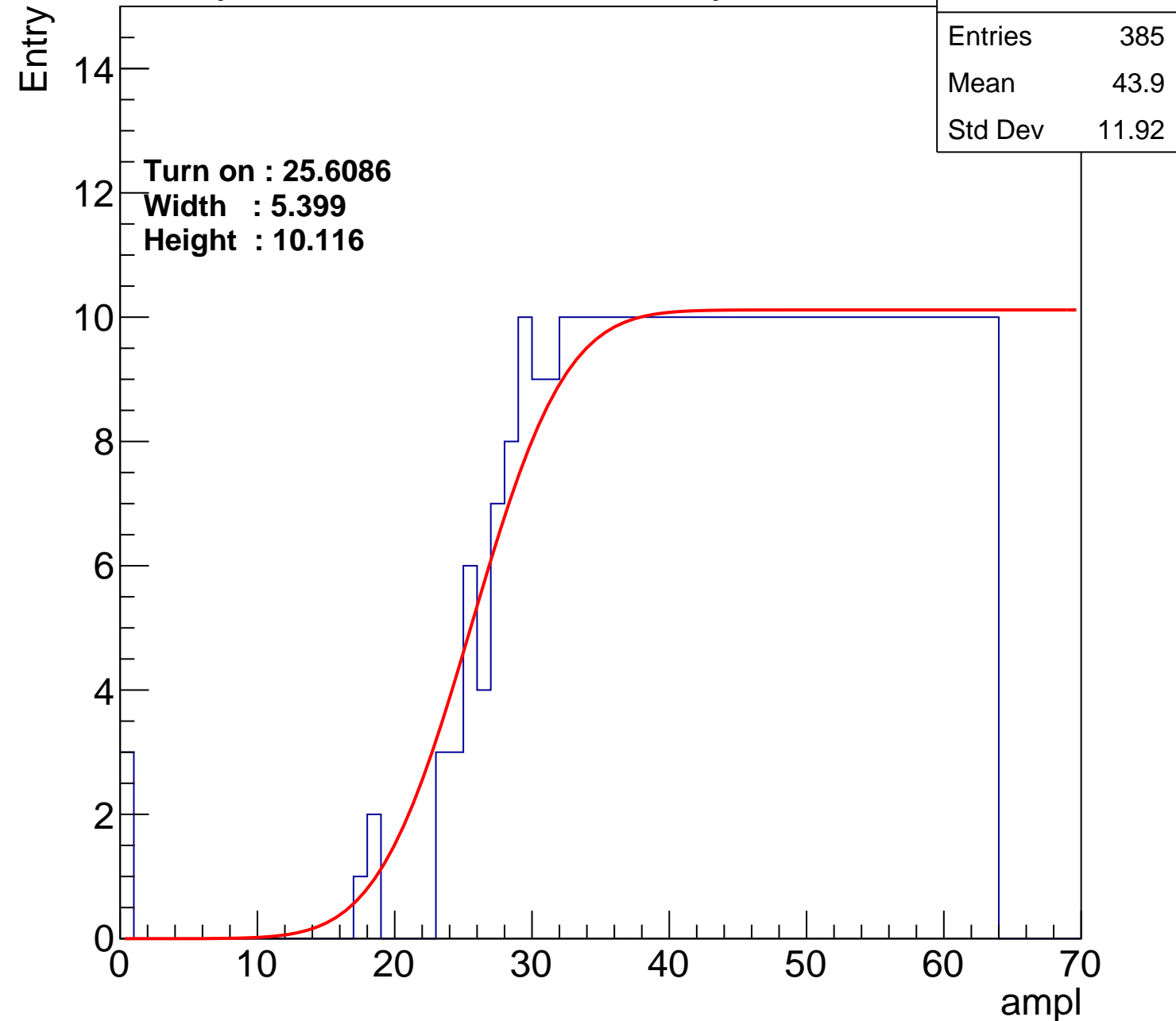
Width : 5.399

Height : 10.116

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	42.97
Std Dev	12.74

Turn on : 25.1874

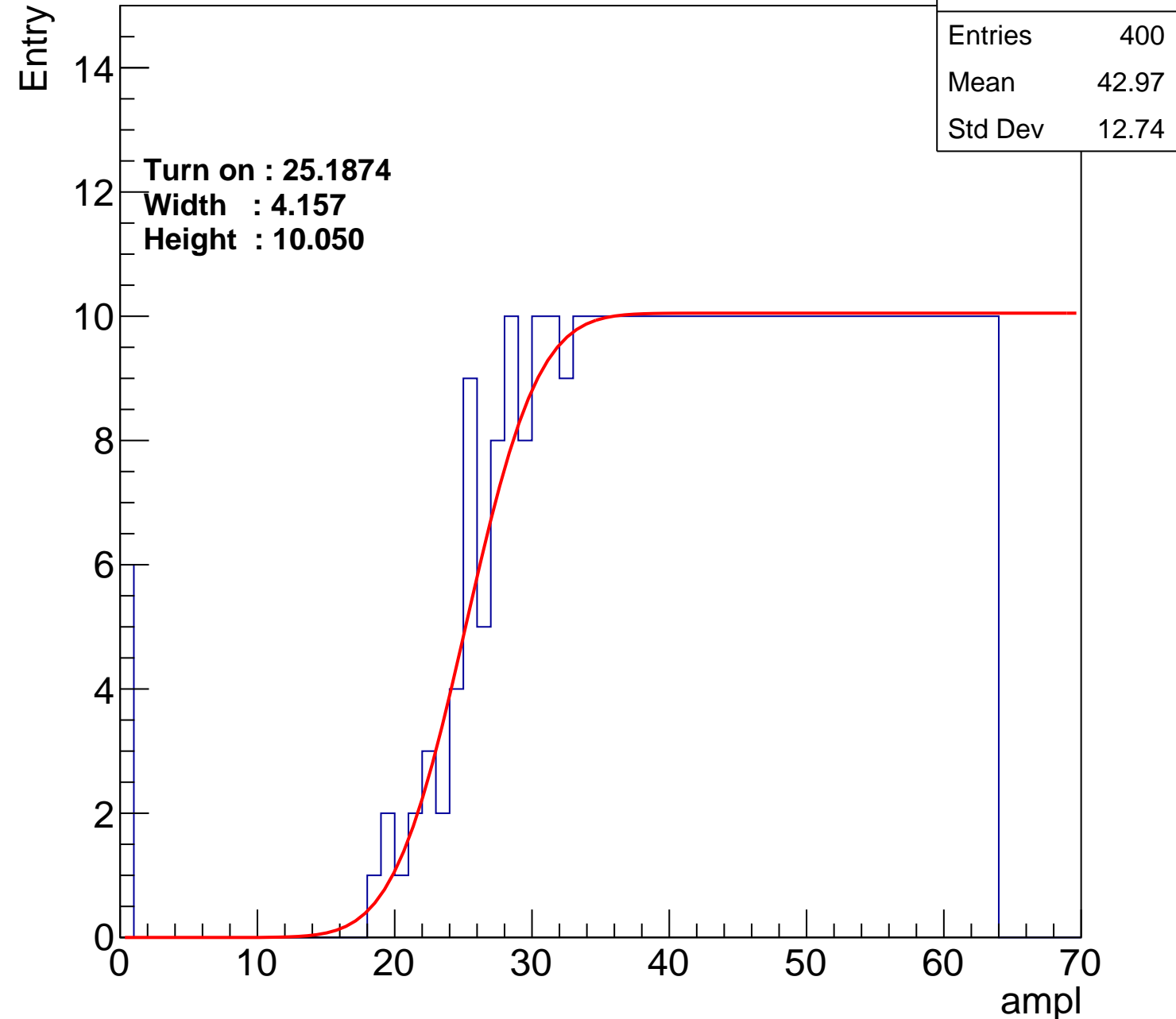
Width : 4.157

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch109

calib_packv5_042523_0143.root, FC#12, port B1

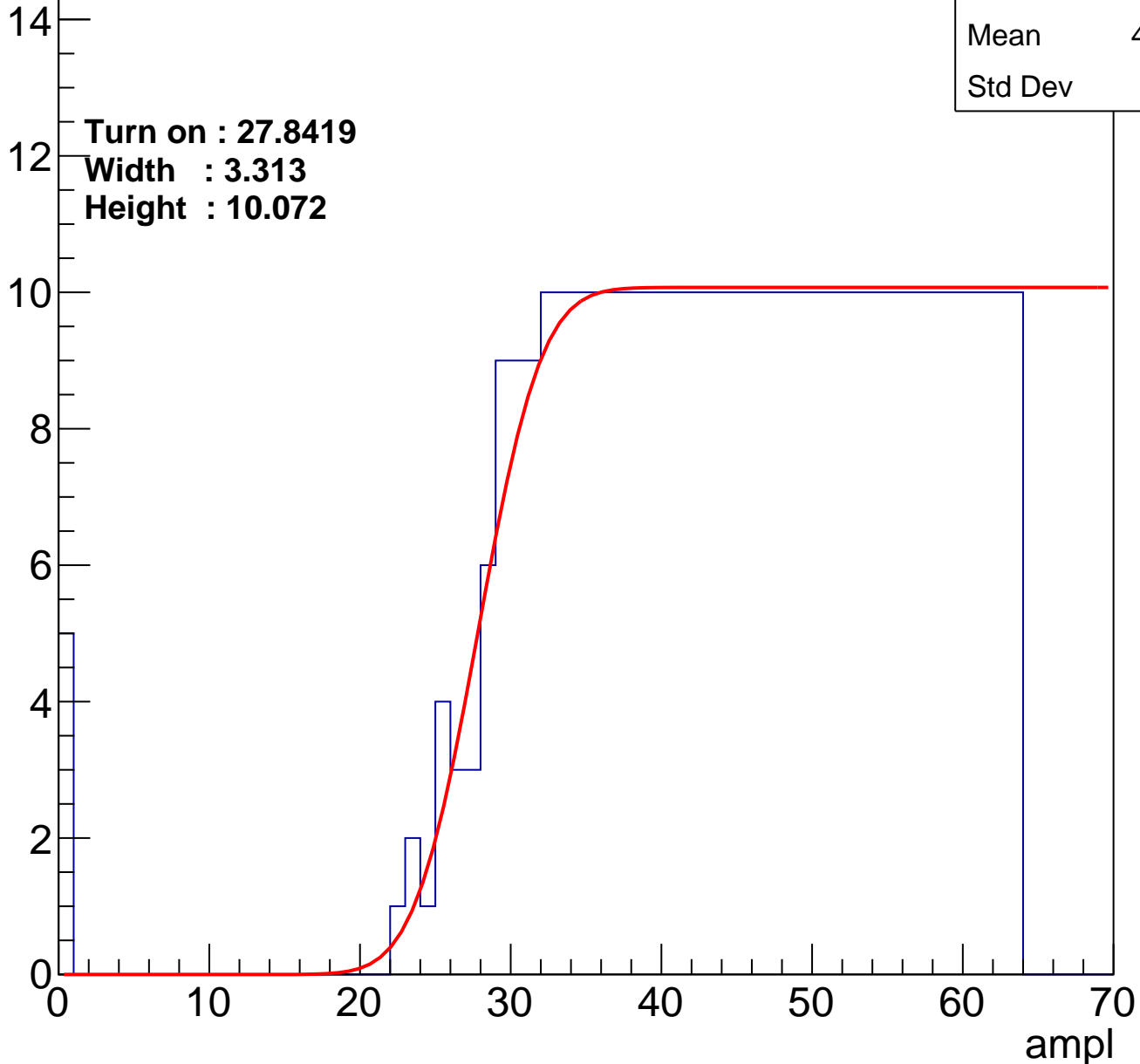
Entries	372
Mean	44.43
Std Dev	11.9

Turn on : 27.8419

Width : 3.313

Height : 10.072

Entry



B0L102S, U8-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.15
Std Dev	12.53

Turn on : 25.2267

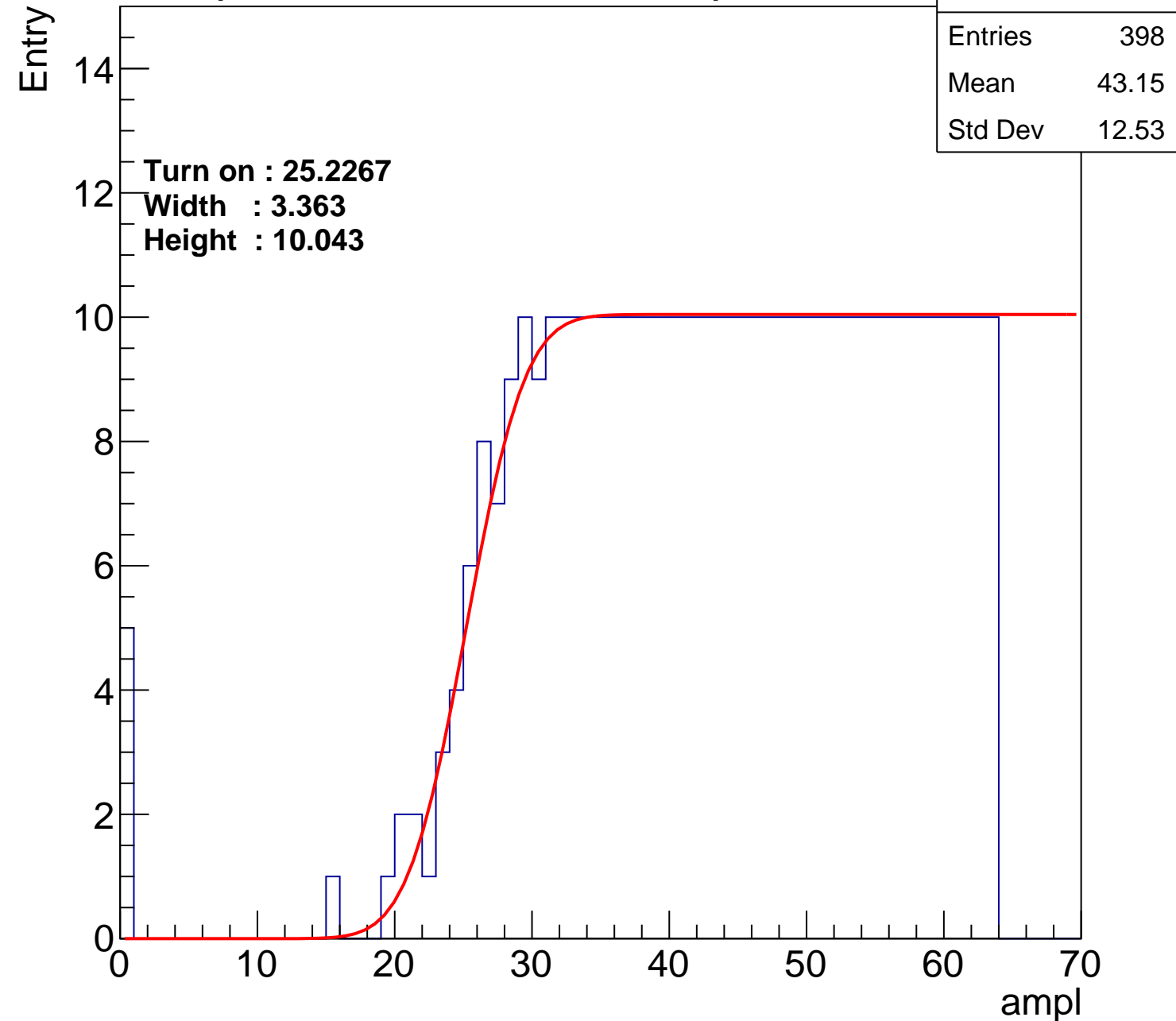
Width : 3.363

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch111

calib_packv5_042523_0143.root, FC#12, port B1

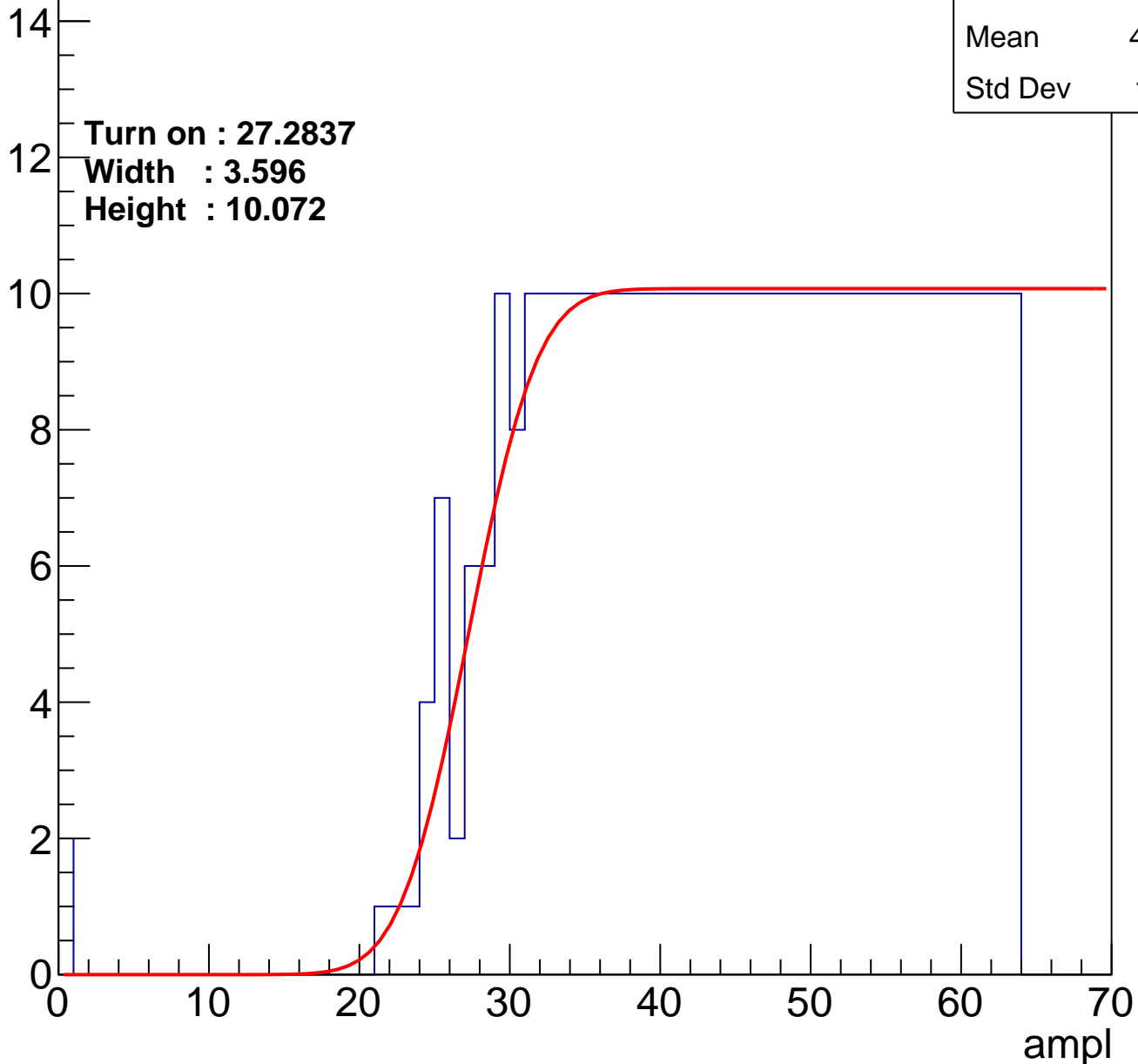
Entries	378
Mean	44.34
Std Dev	11.51

Turn on : 27.2837

Width : 3.596

Height : 10.072

Entry



B0L102S, U8-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	414
Mean	42.39
Std Dev	12.83

Turn on : 23.9942

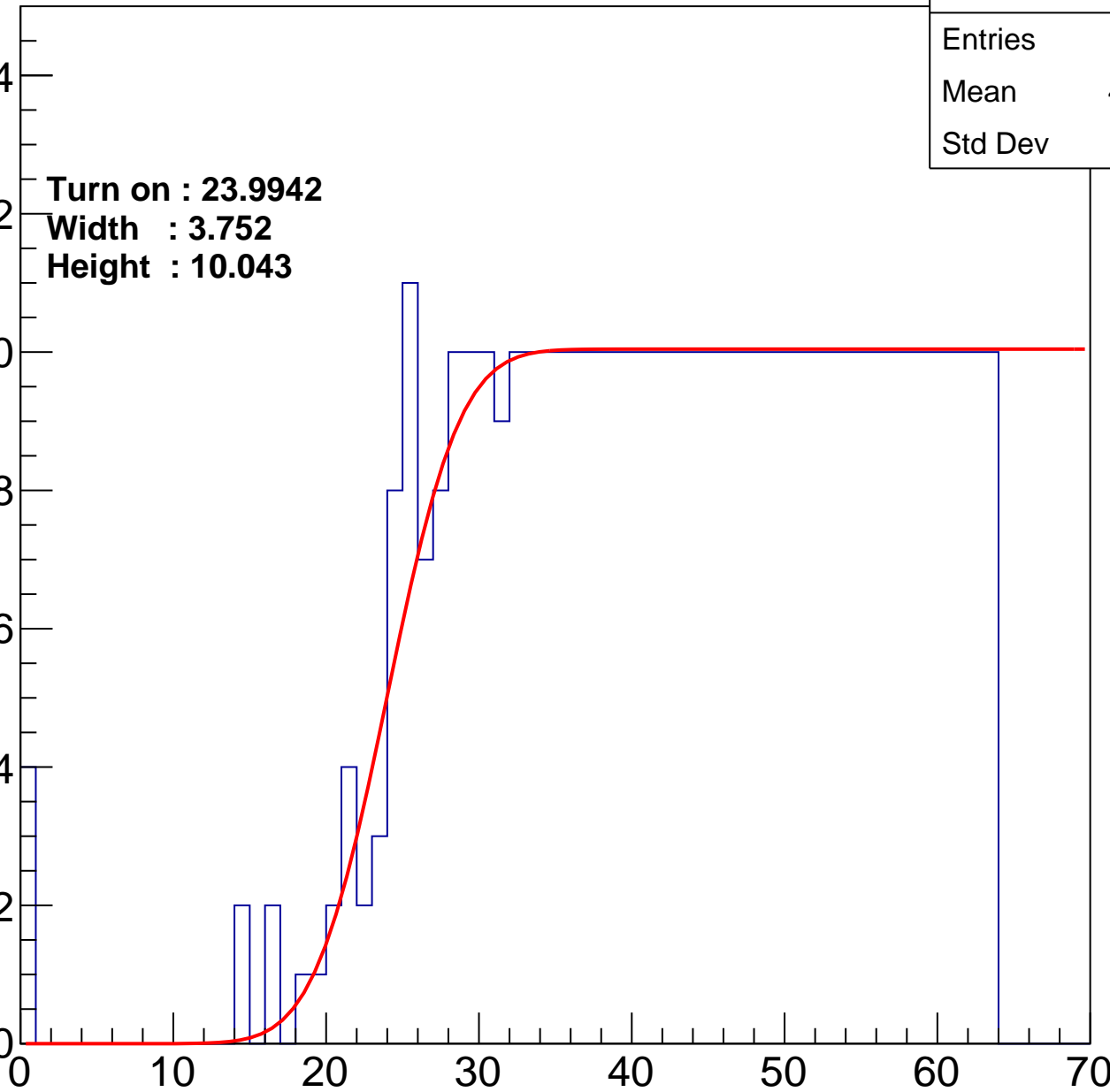
Width : 3.752

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch113

calib_packv5_042523_0143.root, FC#12, port B1

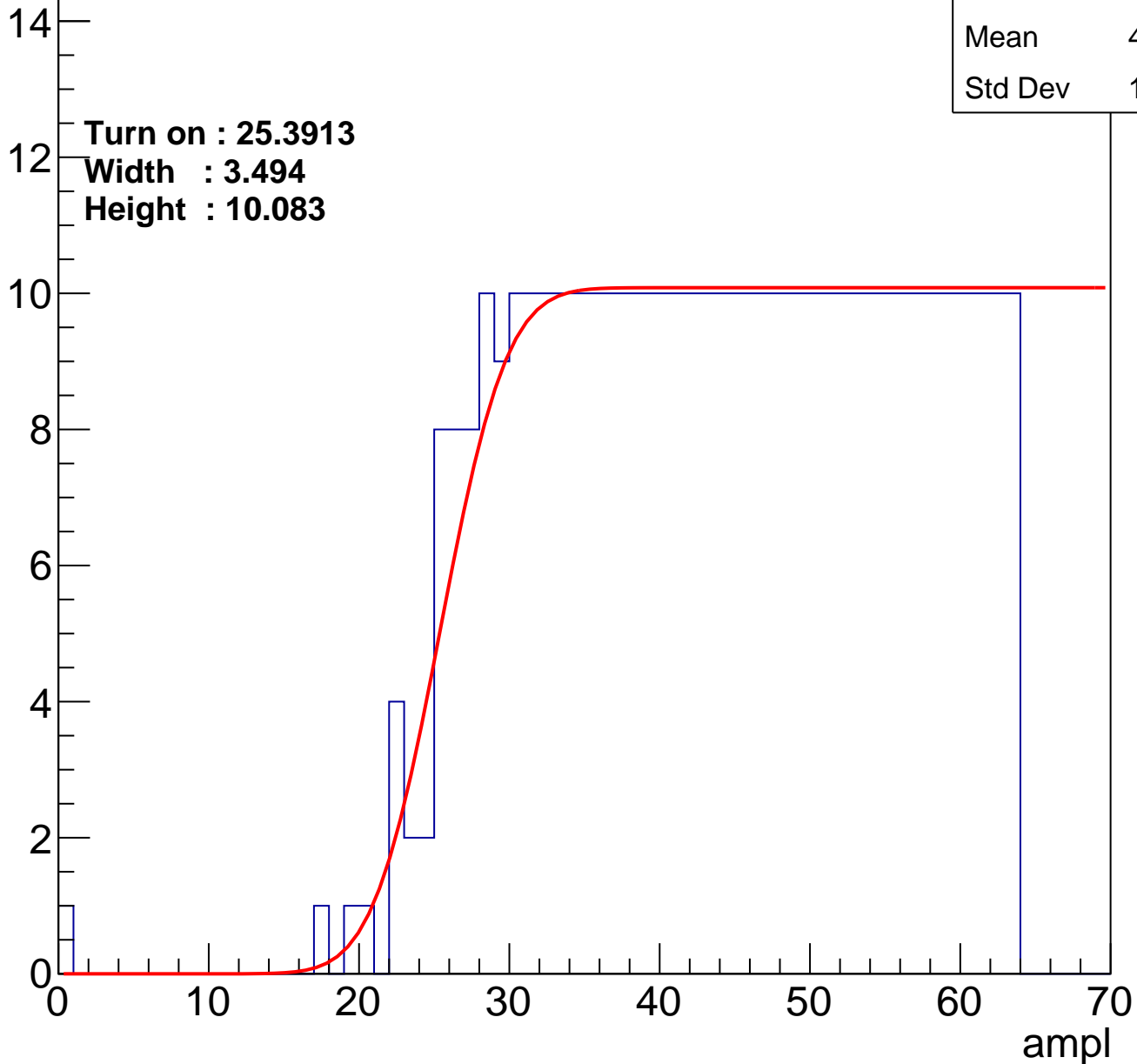
Entries	395
Mean	43.58
Std Dev	11.76

Turn on : 25.3913

Width : 3.494

Height : 10.083

Entry



B0L102S, U8-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.93
Std Dev	12.43

Turn on : 24.3683

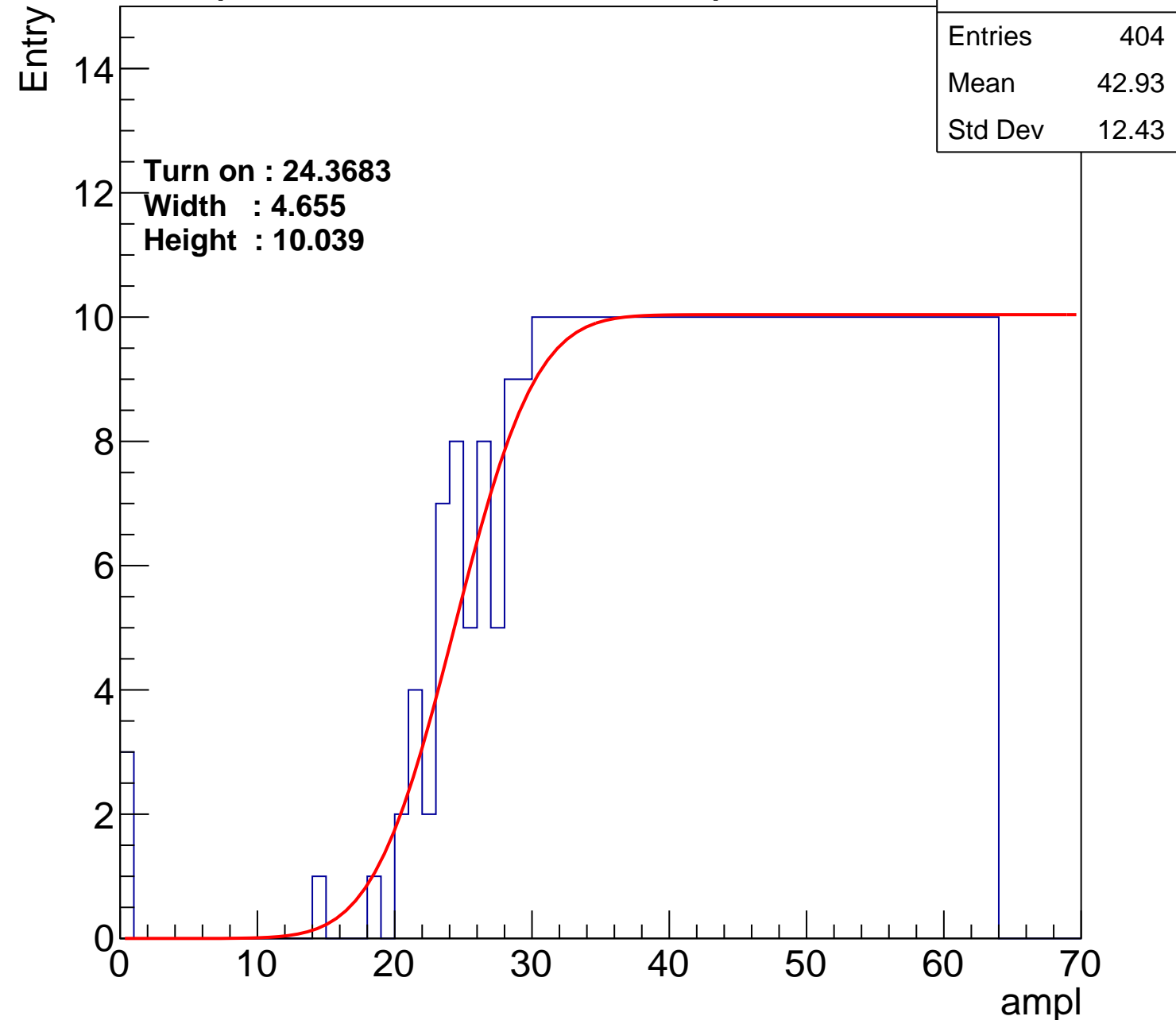
Width : 4.655

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.48
Std Dev	12.2

Turn on : 24.8015

Width : 4.782

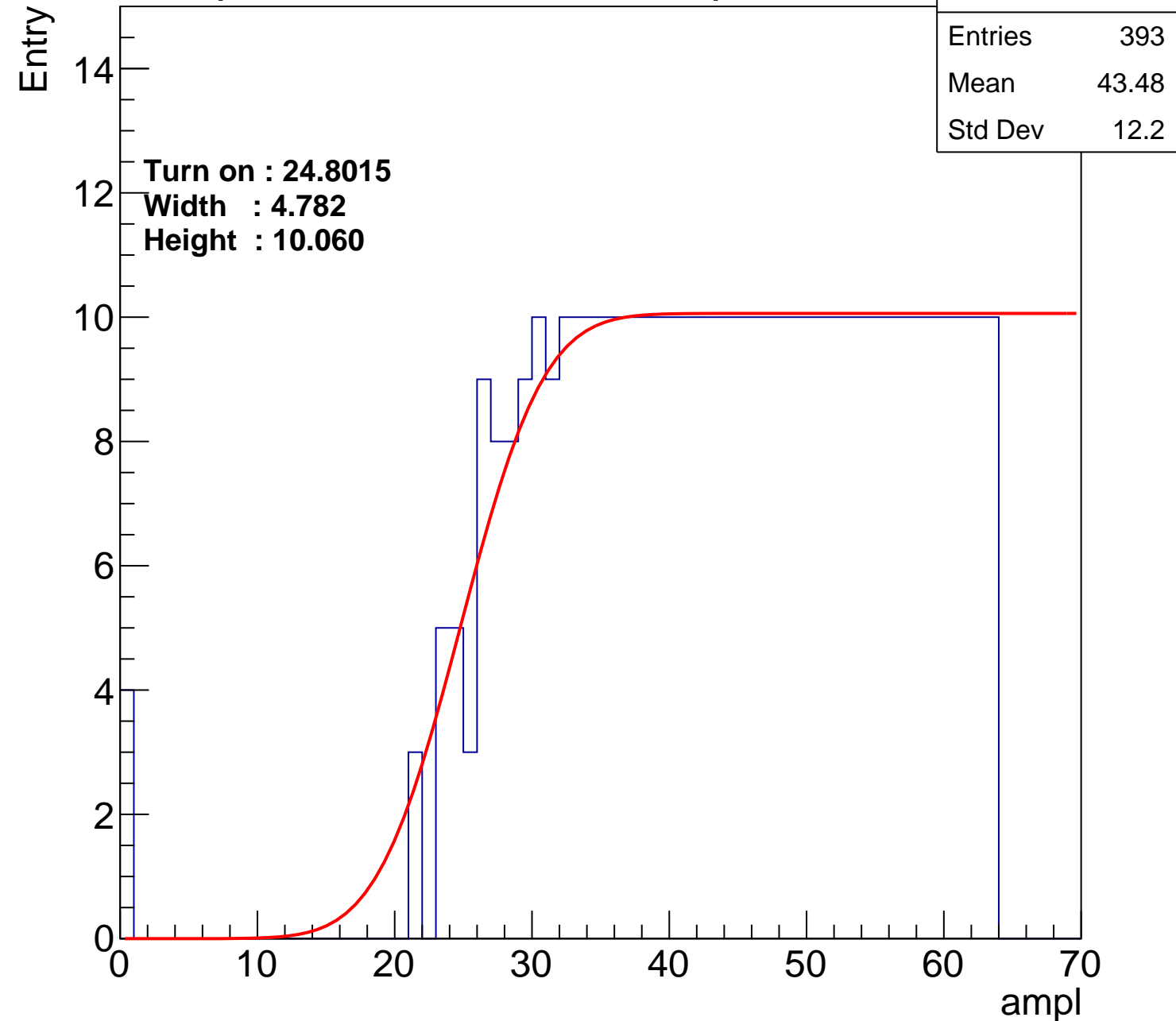
Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U8-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.61
Std Dev	12.05

Turn on : 25.8491

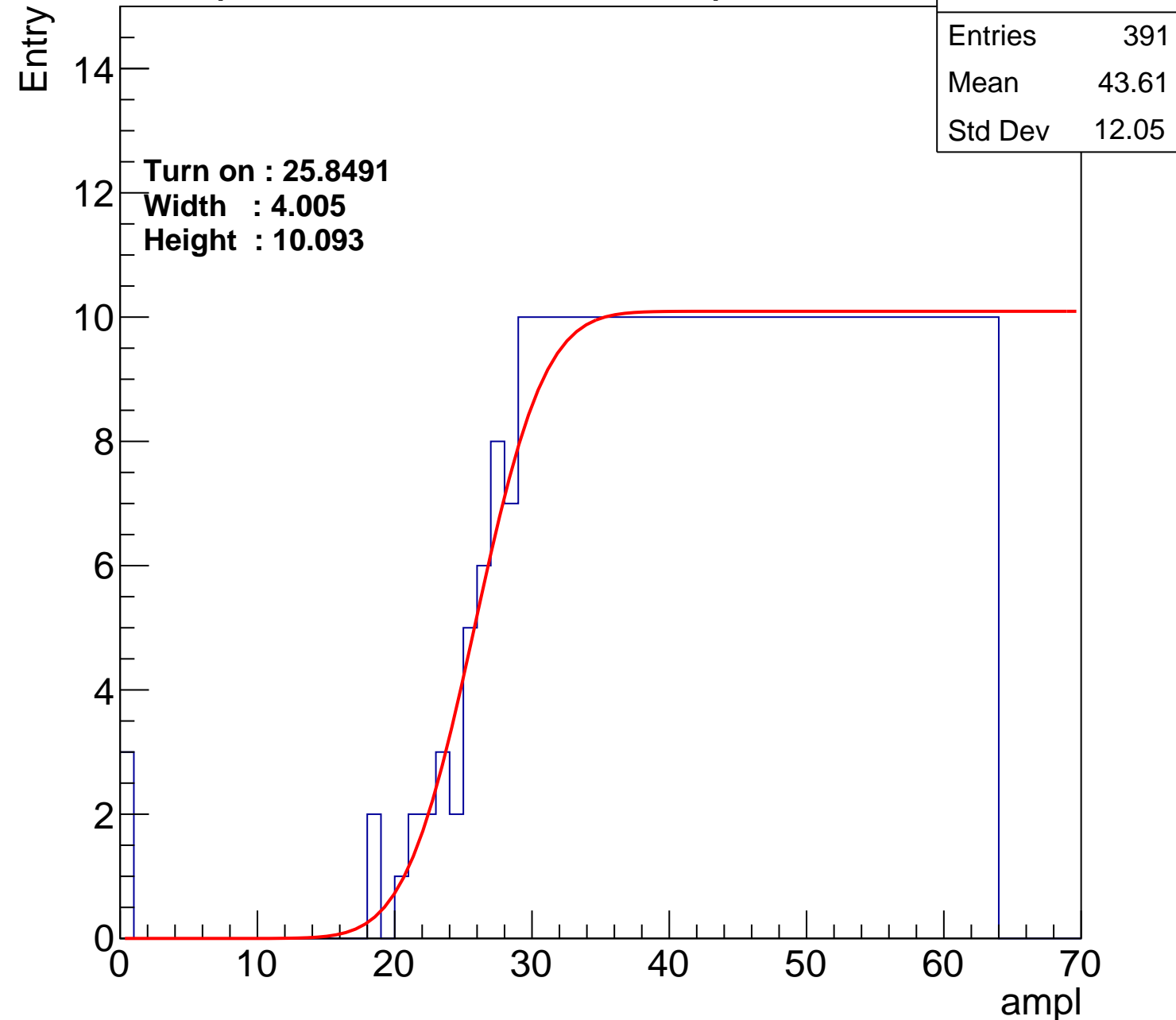
Width : 4.005

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.02
Std Dev	11.56

Turn on : 25.7118

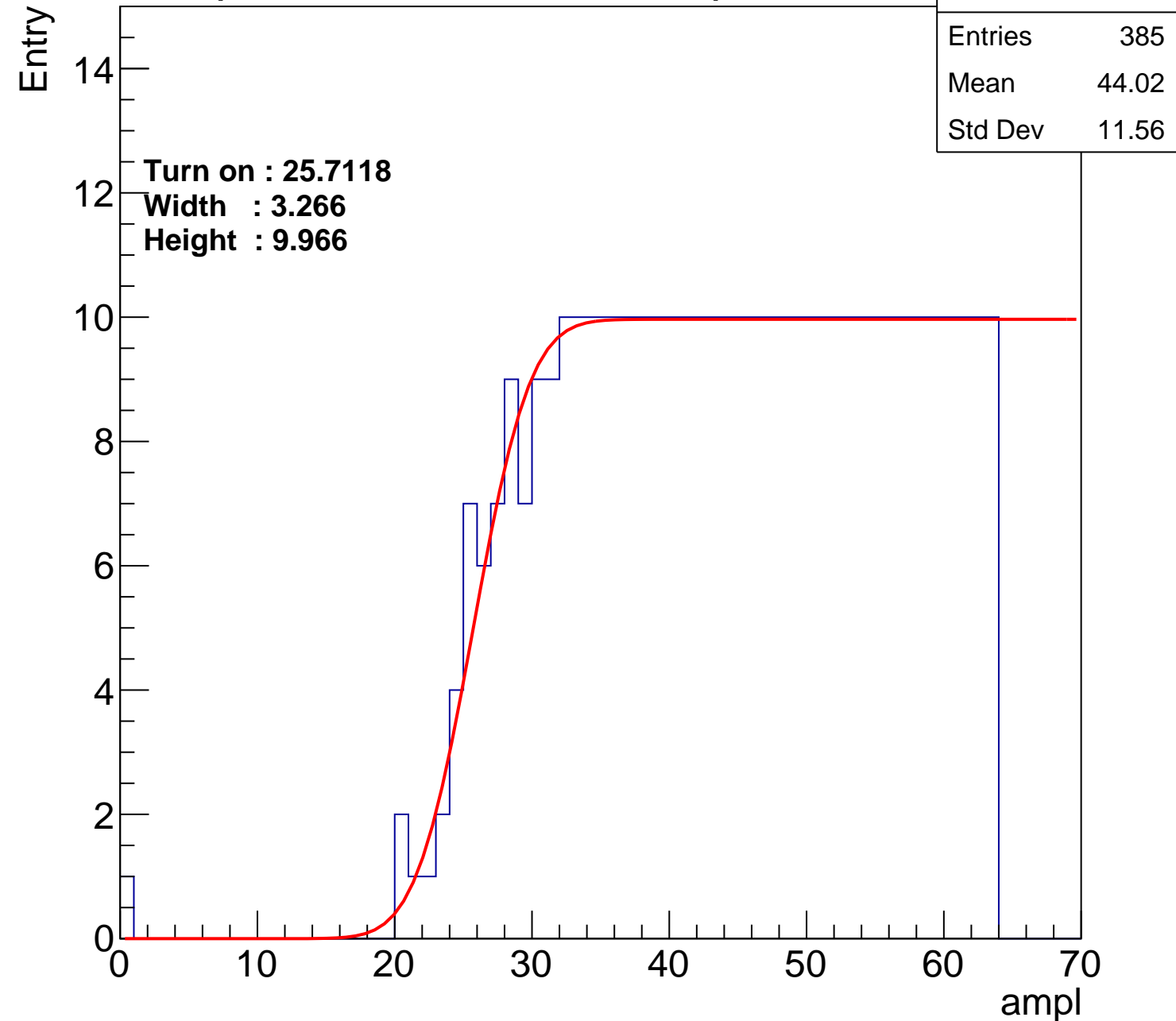
Width : 3.266

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.82

Turn on : 24.4115

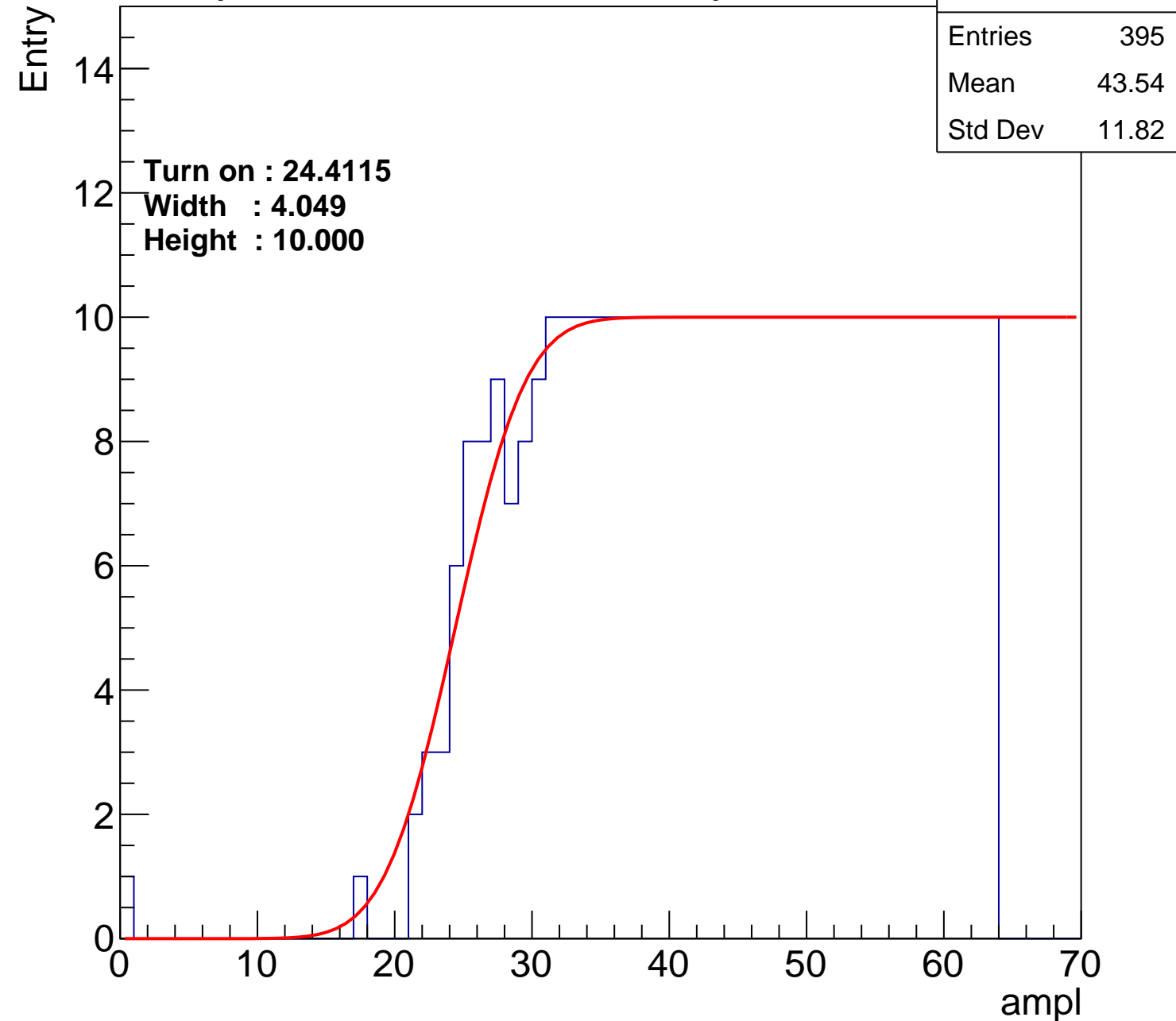
Width : 4.049

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.9
Std Dev	11.54

Turn on : 25.4389

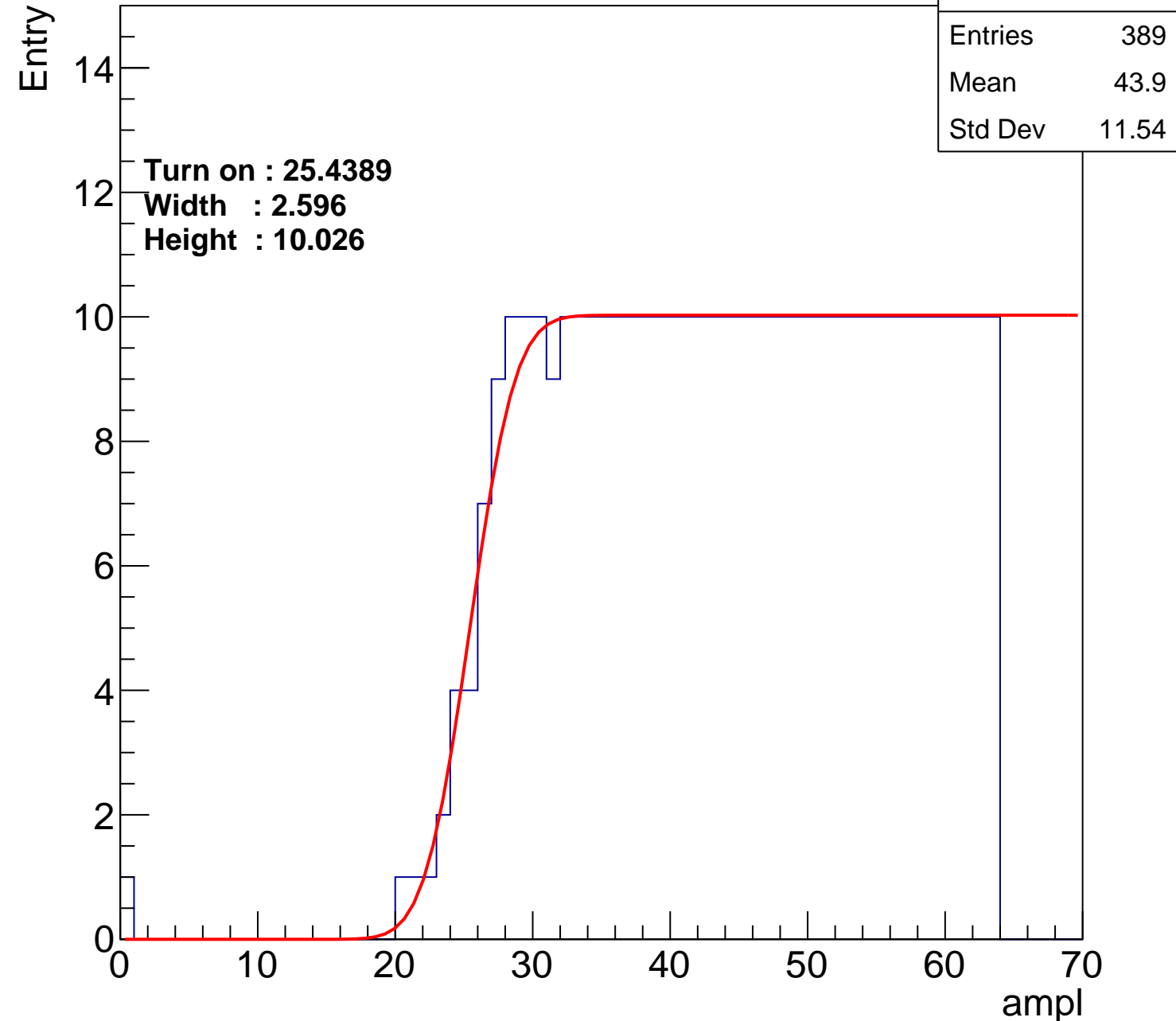
Width : 2.596

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.87
Std Dev	12.3

Turn on : 23.4939

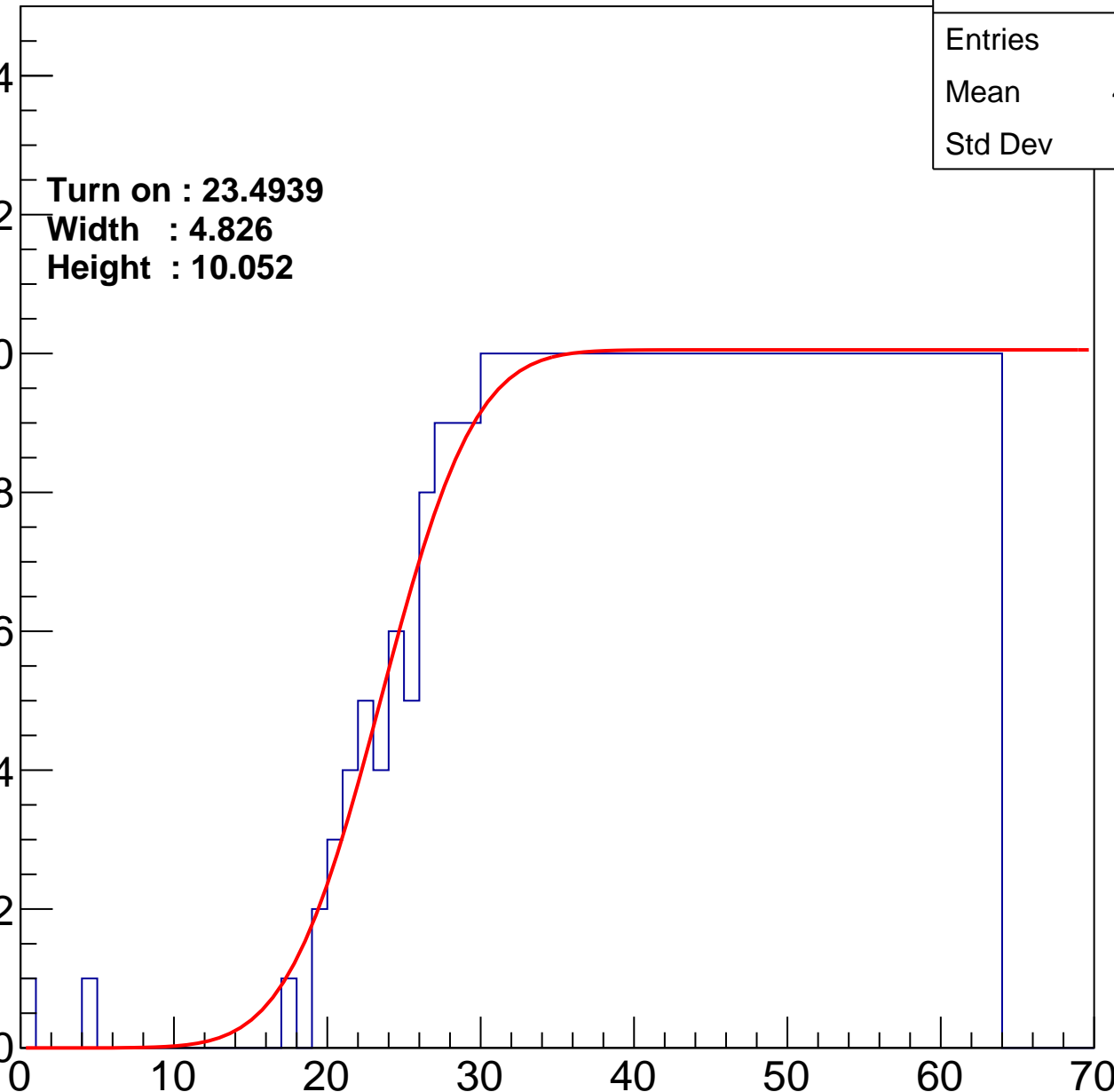
Width : 4.826

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.43
Std Dev	11.47

Turn on : 26.7450

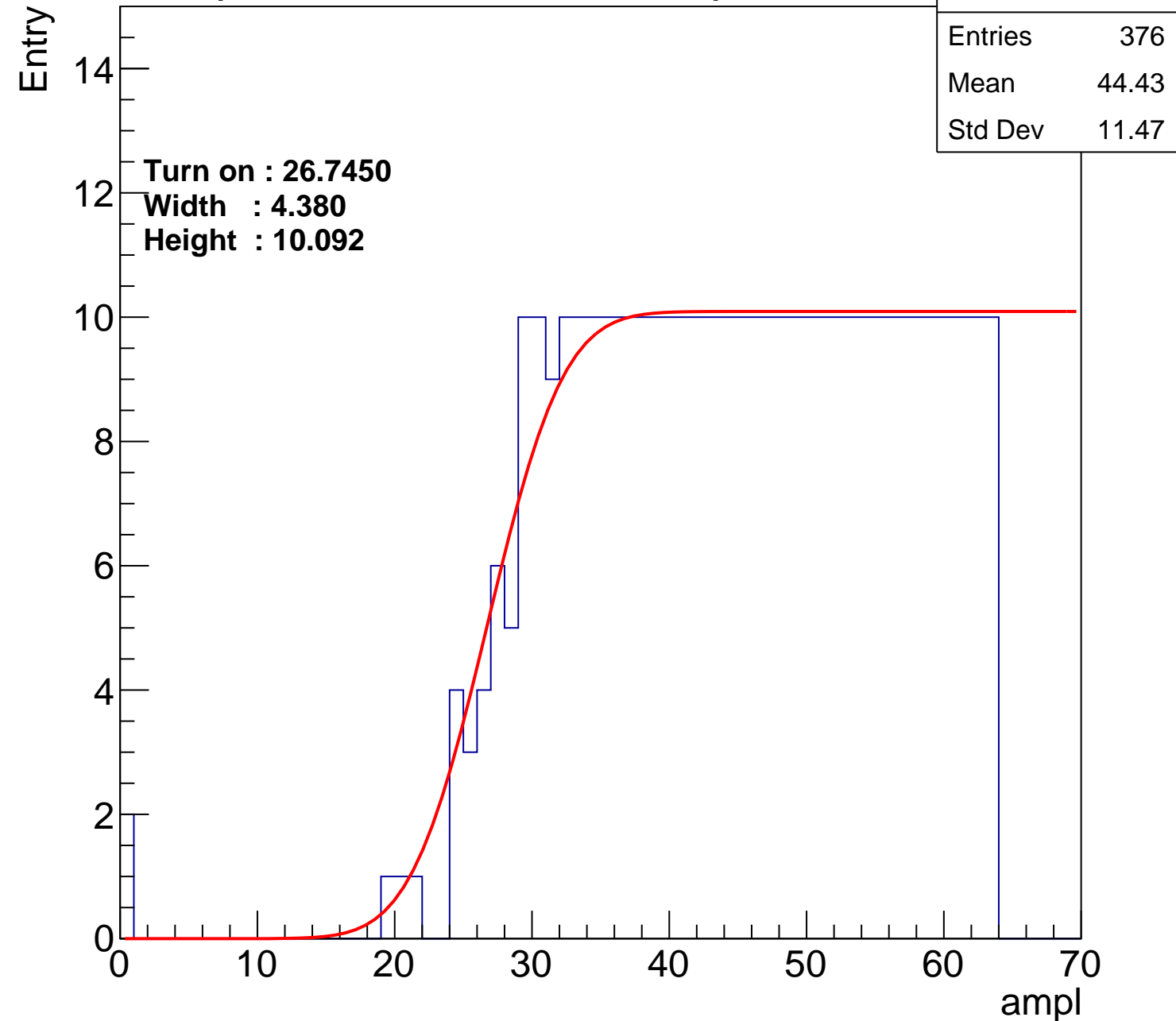
Width : 4.380

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.67
Std Dev	11.84

Turn on : 26.3352

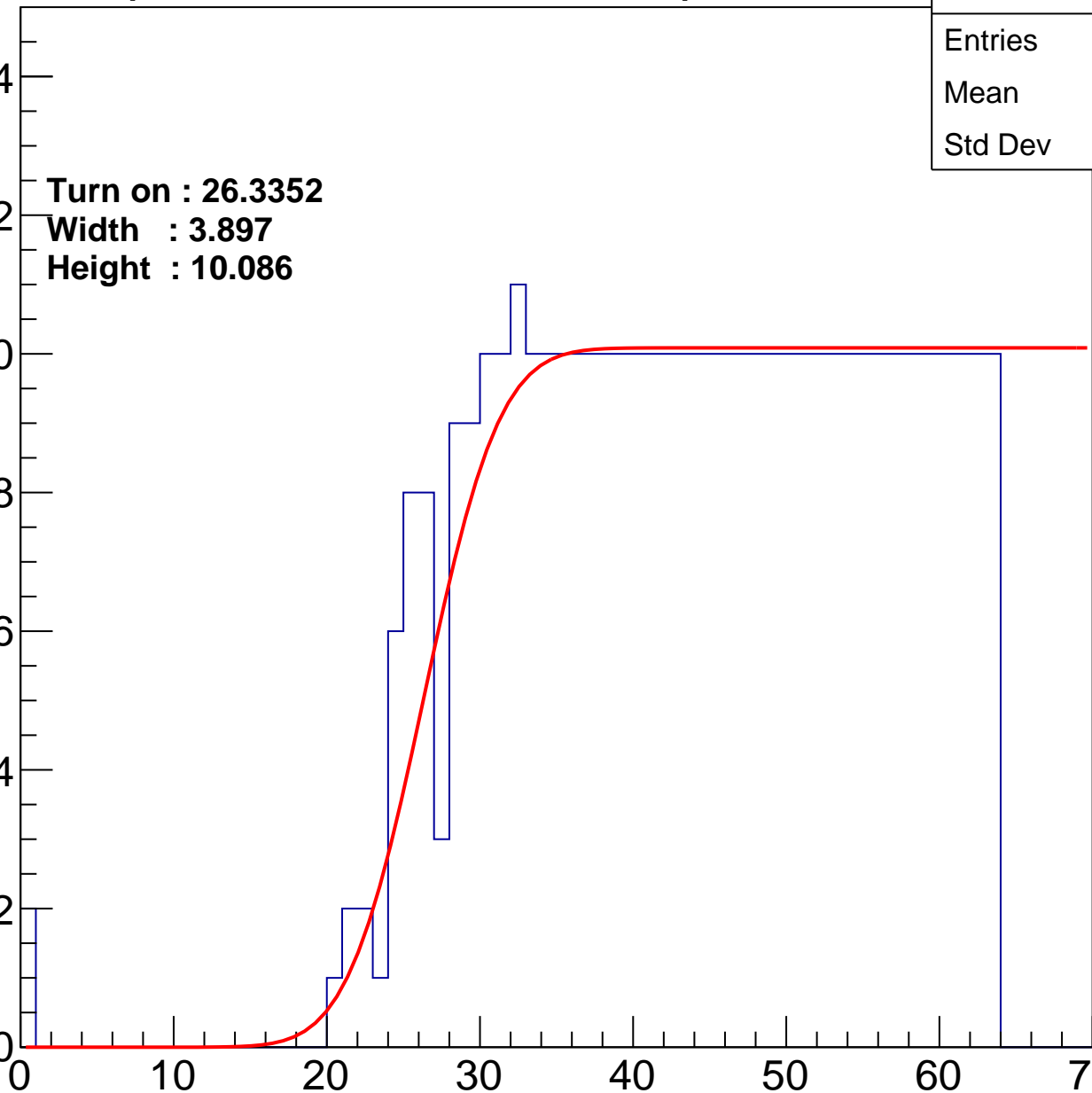
Width : 3.897

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.95
Std Dev	11.64

Turn on : 26.0719

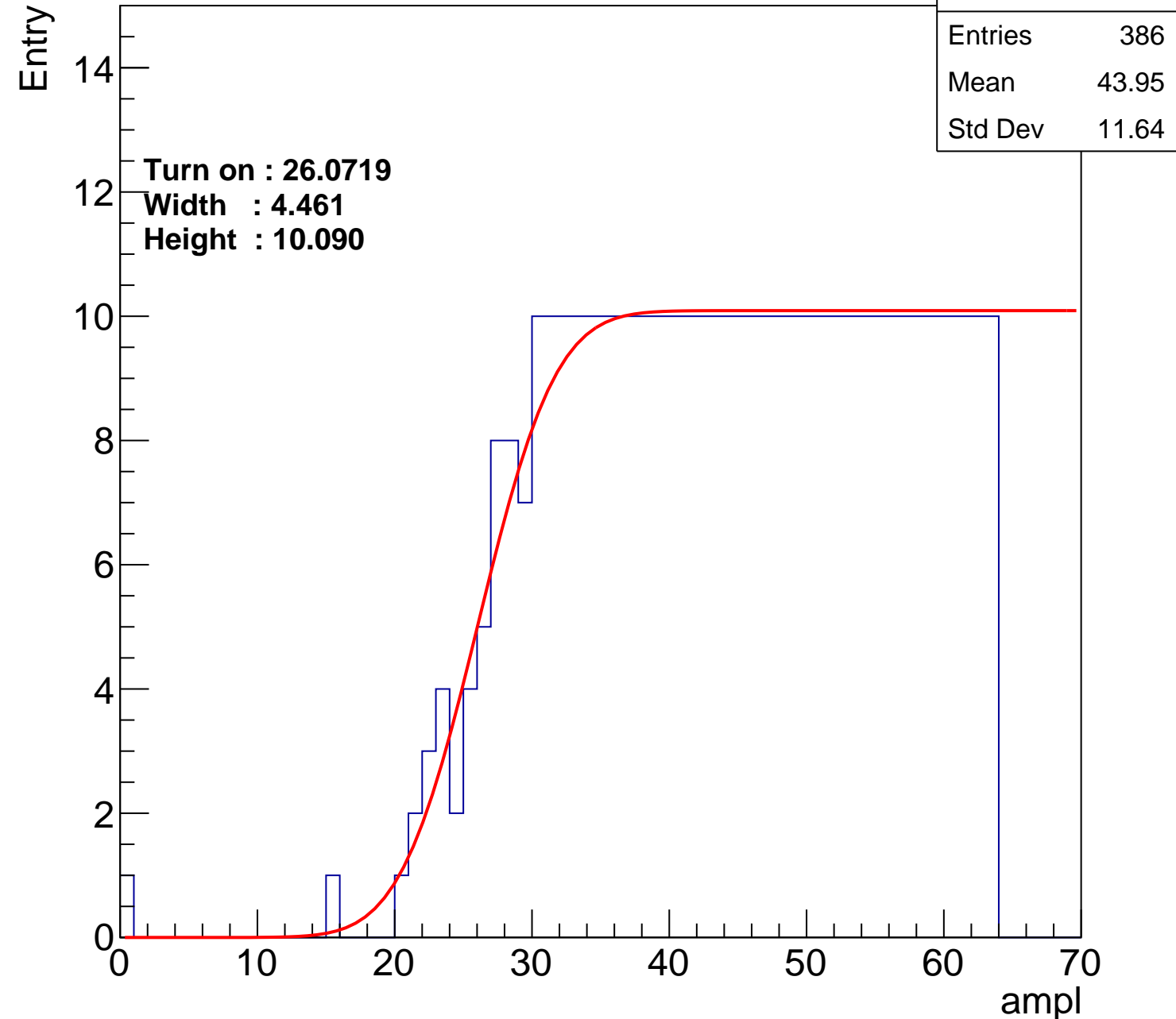
Width : 4.461

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch124

calib_packv5_042523_0143.root, FC#12, port B1

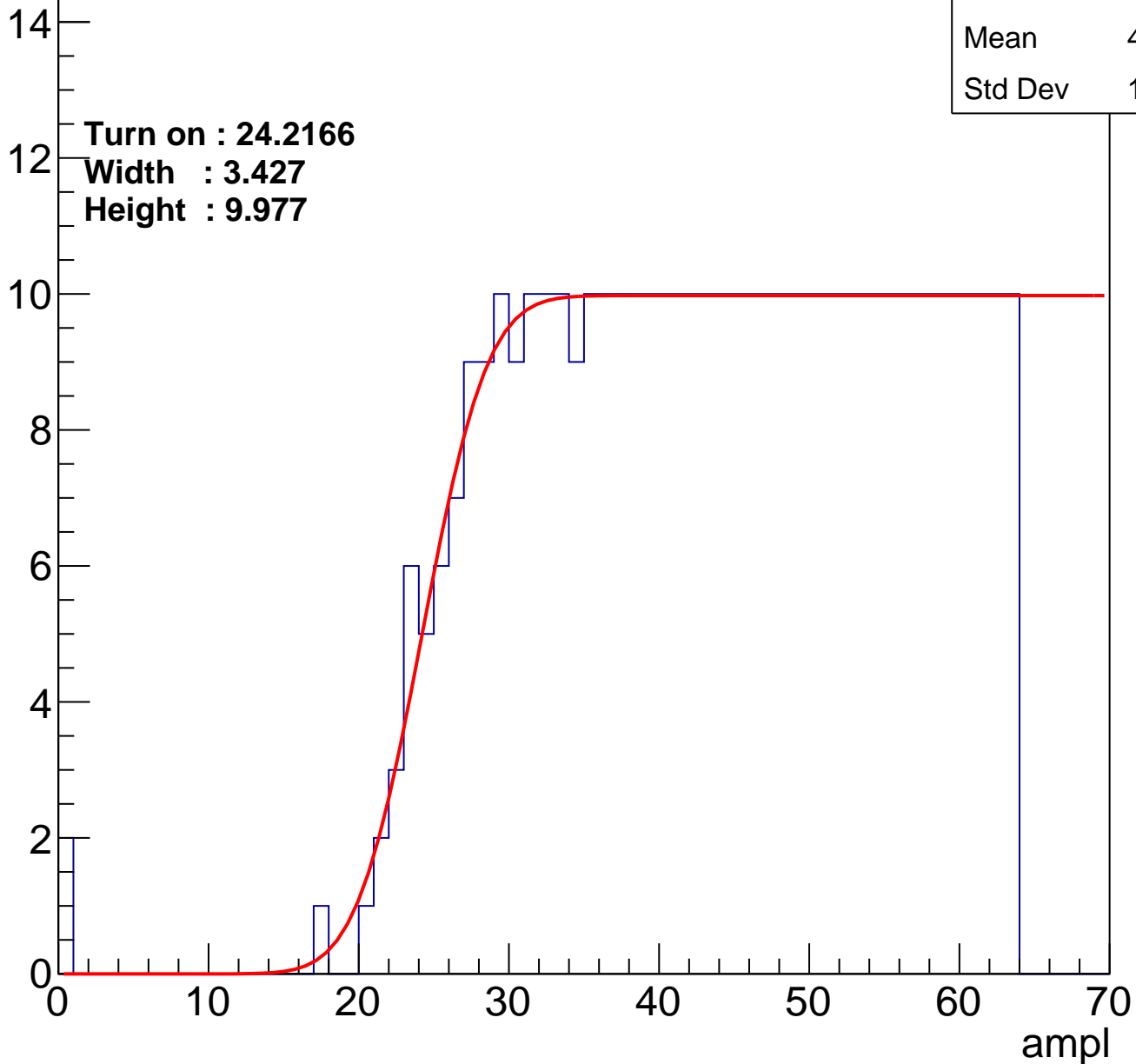
Entries	399
Mean	43.27
Std Dev	12.08

Turn on : 24.2166

Width : 3.427

Height : 9.977

Entry



B0L102S, U8-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.25
Std Dev	12.35

Turn on : 25.5146

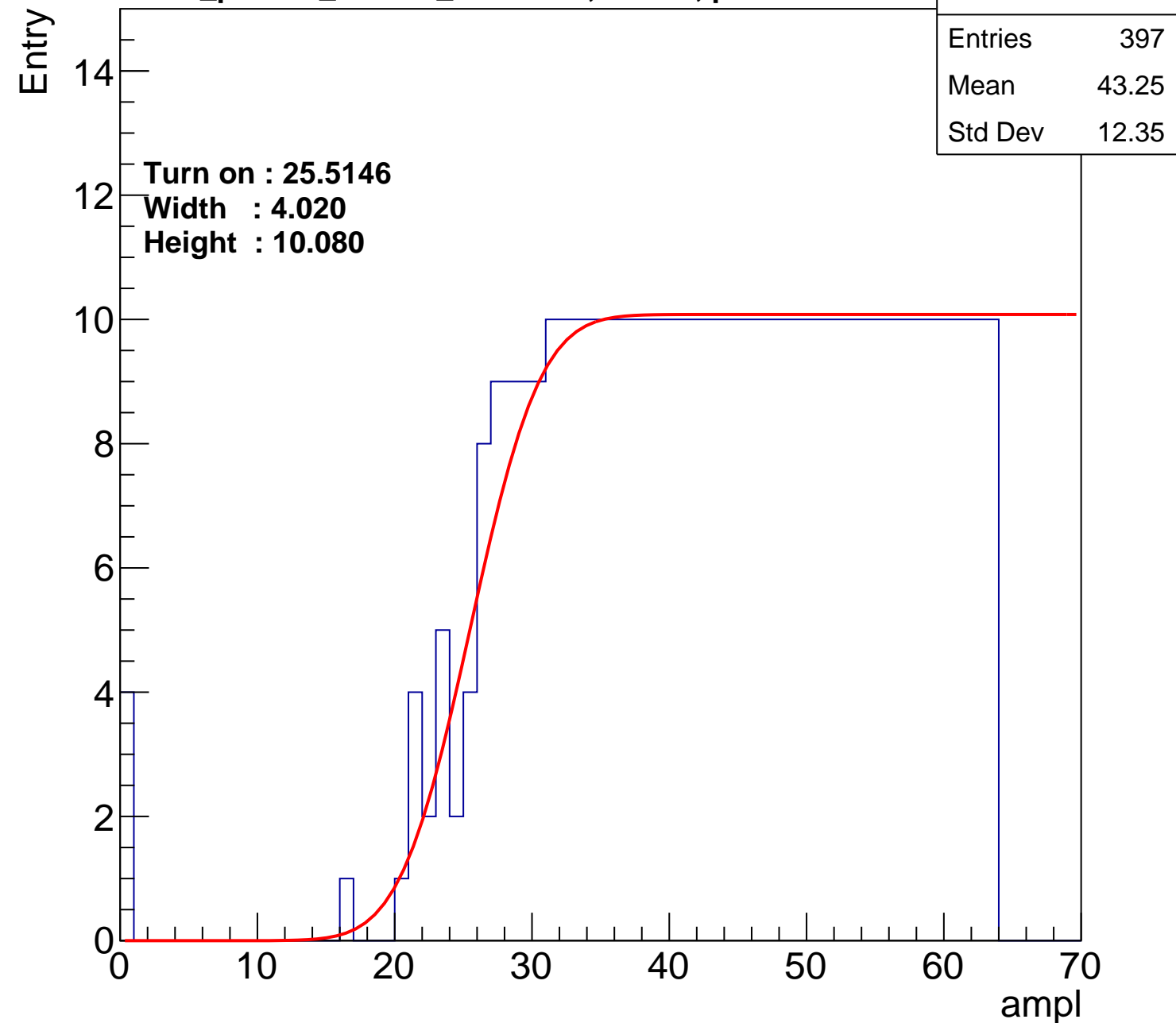
Width : 4.020

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U8-ch126

calib_packv5_042523_0143.root, FC#12, port B1

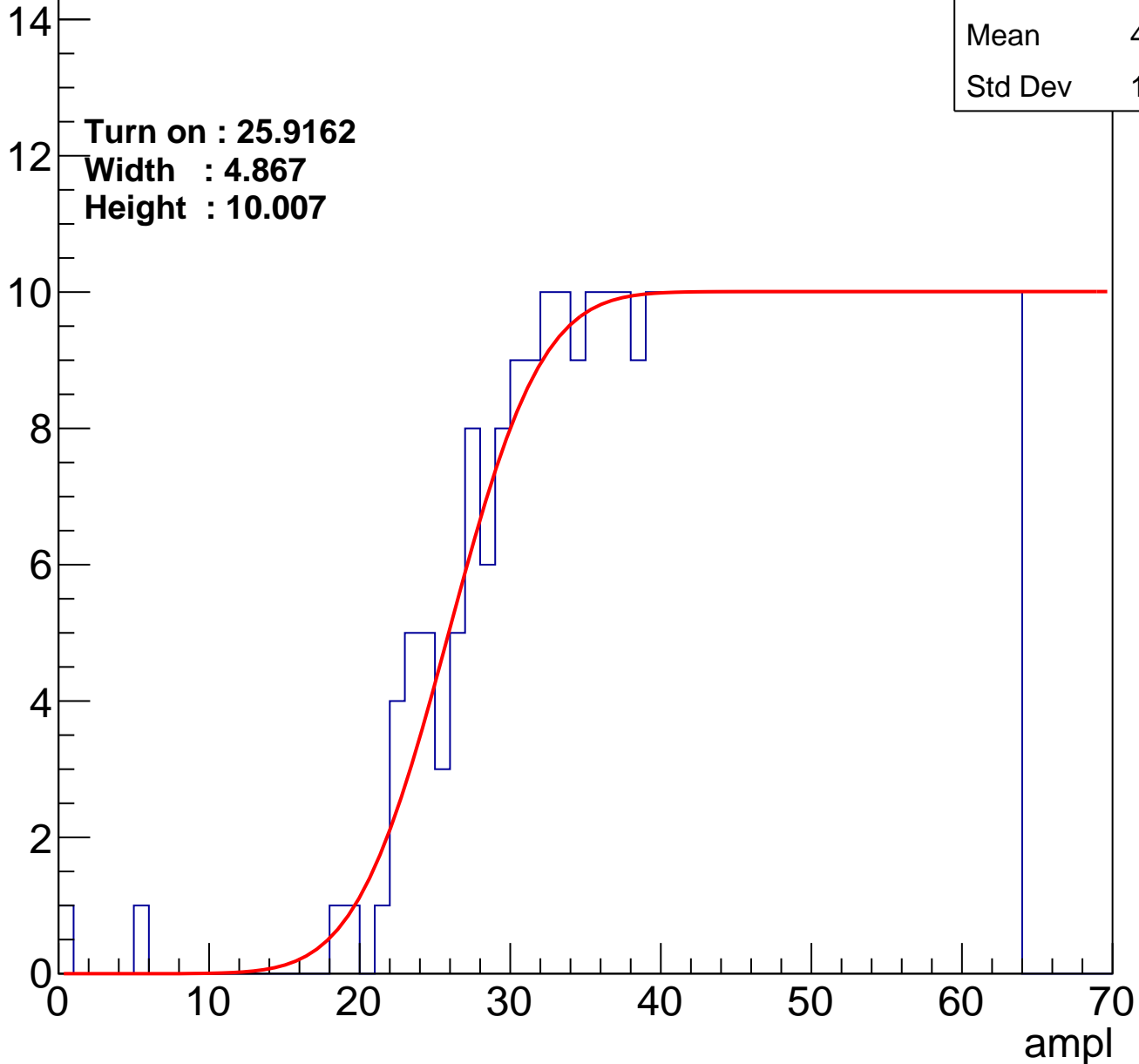
Entries	385
Mean	43.85
Std Dev	11.86

Turn on : 25.9162

Width : 4.867

Height : 10.007

Entry



B0L102S, U8-ch127

calib_packv5_042523_0143.root, FC#12, port B1

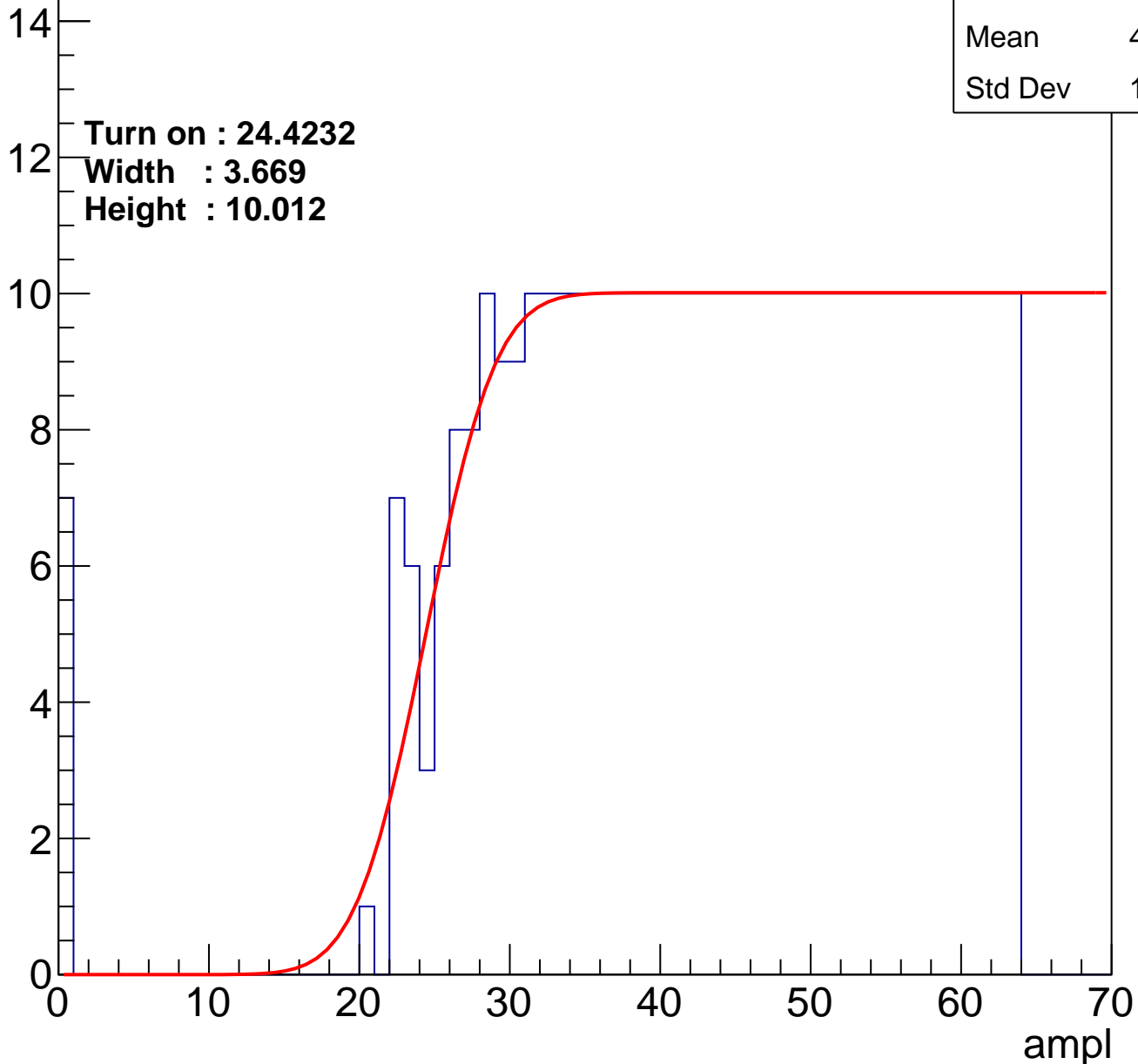
Entries	404
Mean	42.77
Std Dev	12.88

Turn on : 24.4232

Width : 3.669

Height : 10.012

Entry



B0L102S, U8-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	42.77
Std Dev	12.88

Turn on : 24.4232

Width : 3.669

Height : 10.012

Entry

