

B0L103S, U21-ch0

calib_packv5_040323_1717.root, FC#2, port C3

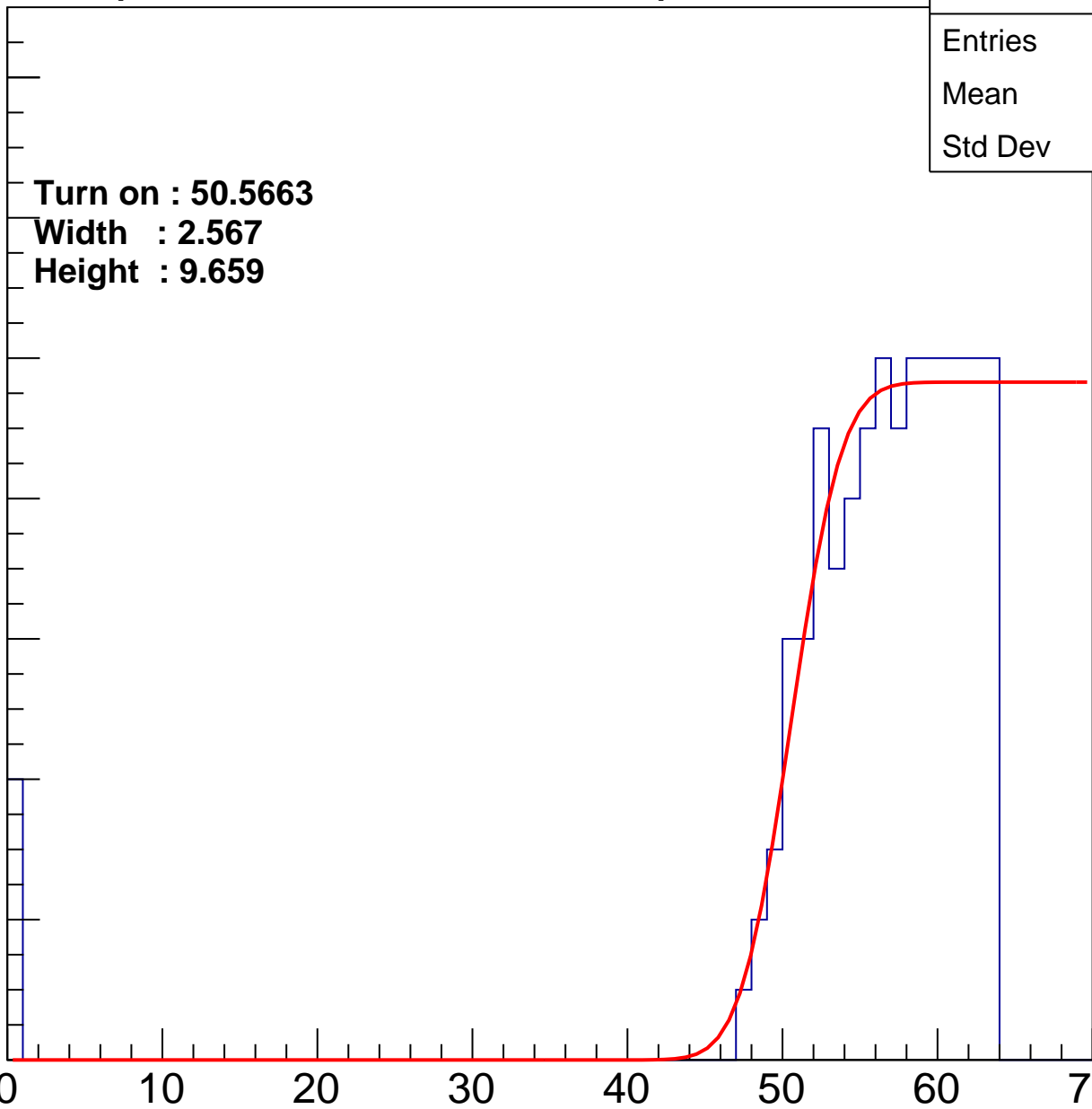
Entry

14
12
10
8
6
4
2
0

Turn on : 50.5663
Width : 2.567
Height : 9.659

Entries	134
Mean	54.96
Std Dev	10.5

ampl



B0L103S, U21-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	56.25
Std Dev	6.498

Turn on : 51.2051

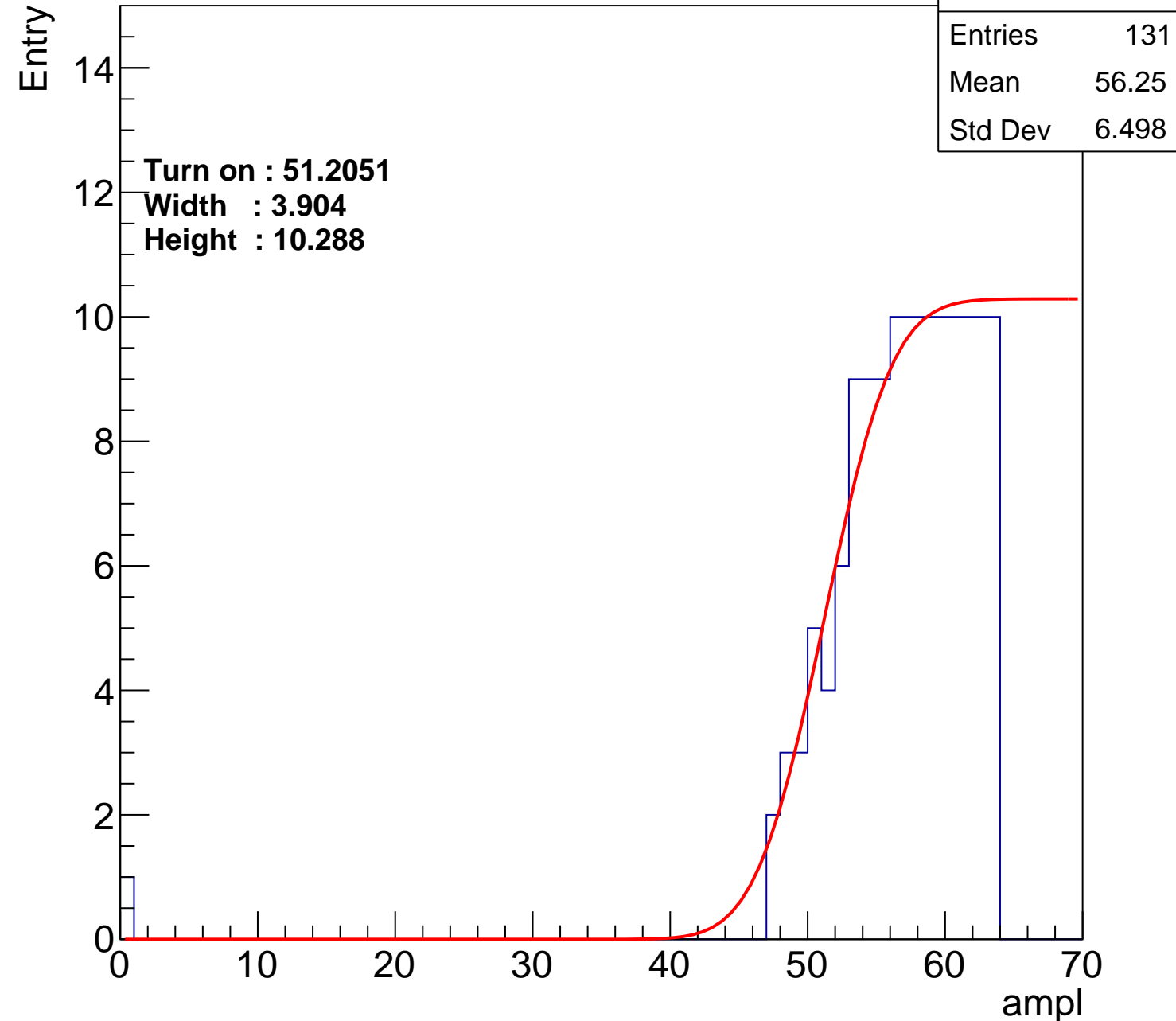
Width : 3.904

Height : 10.288

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch2

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.47
Std Dev	9.376

Turn on : 51.6477

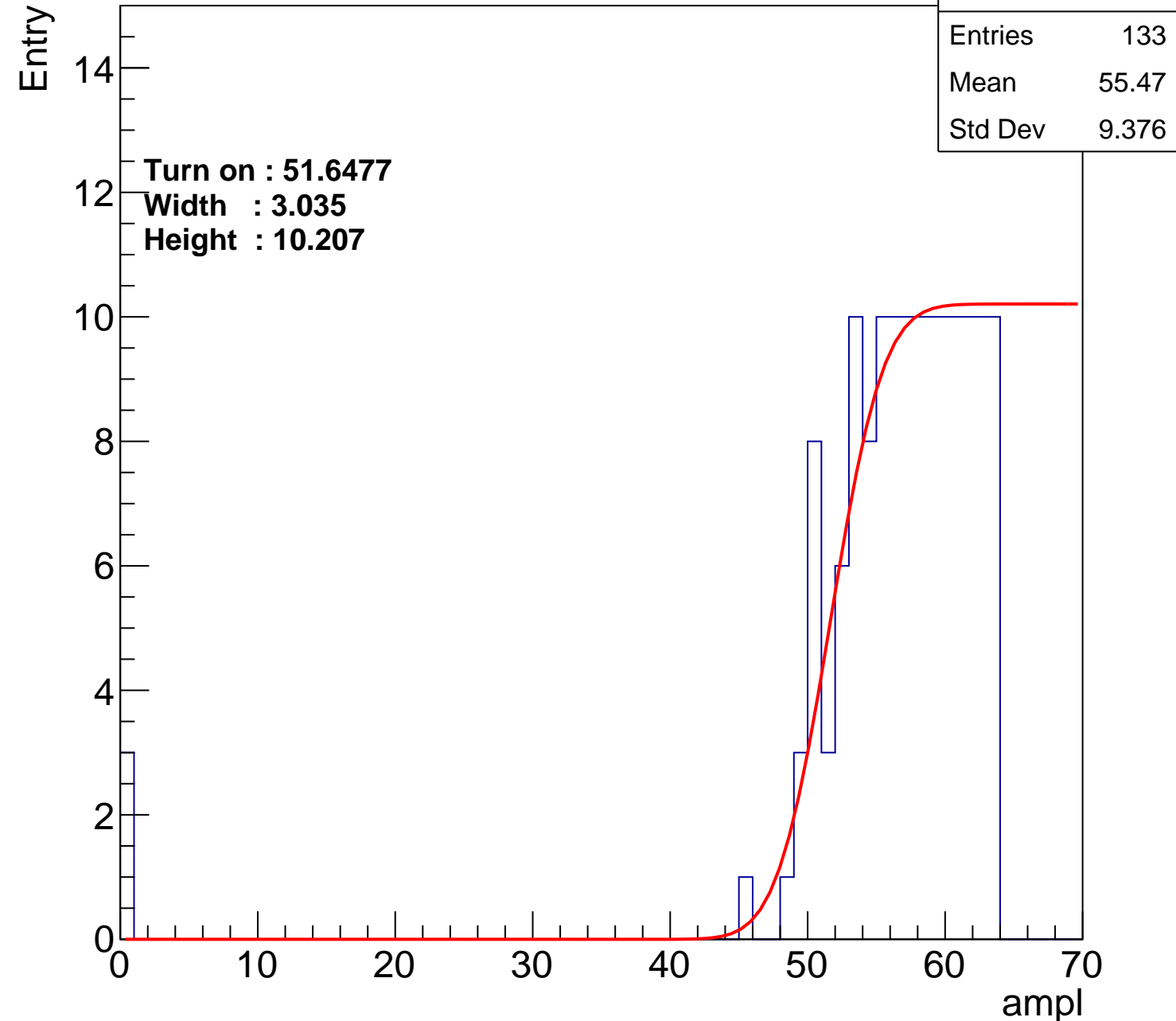
Width : 3.035

Height : 10.207

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch3

calib_packv5_040323_1717.root, FC#2, port C3

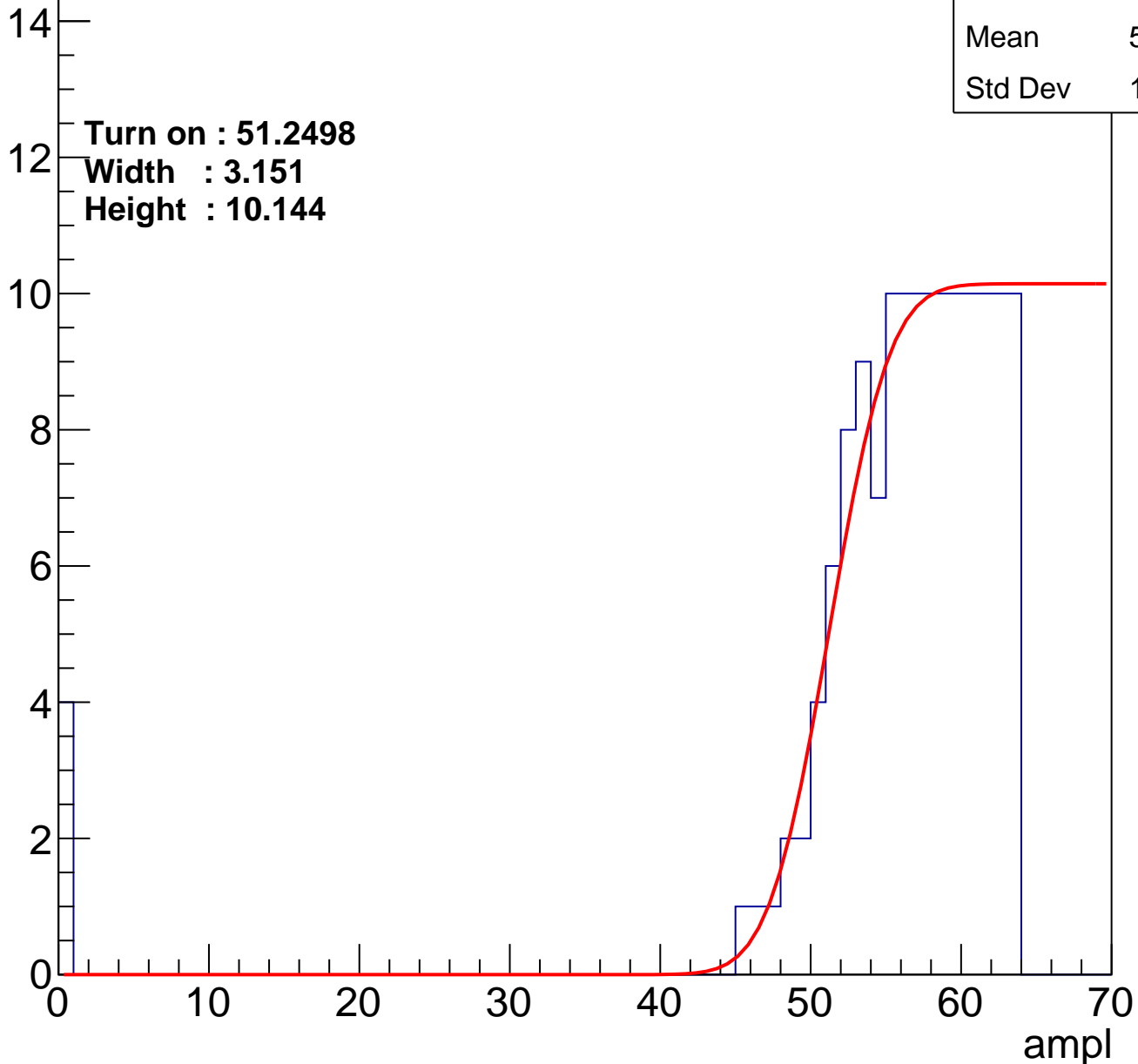
Entry

Entries	135
Mean	54.96
Std Dev	10.49

Turn on : 51.2498

Width : 3.151

Height : 10.144



B0L103S, U21-ch4

calib_packv5_040323_1717.root, FC#2, port C3

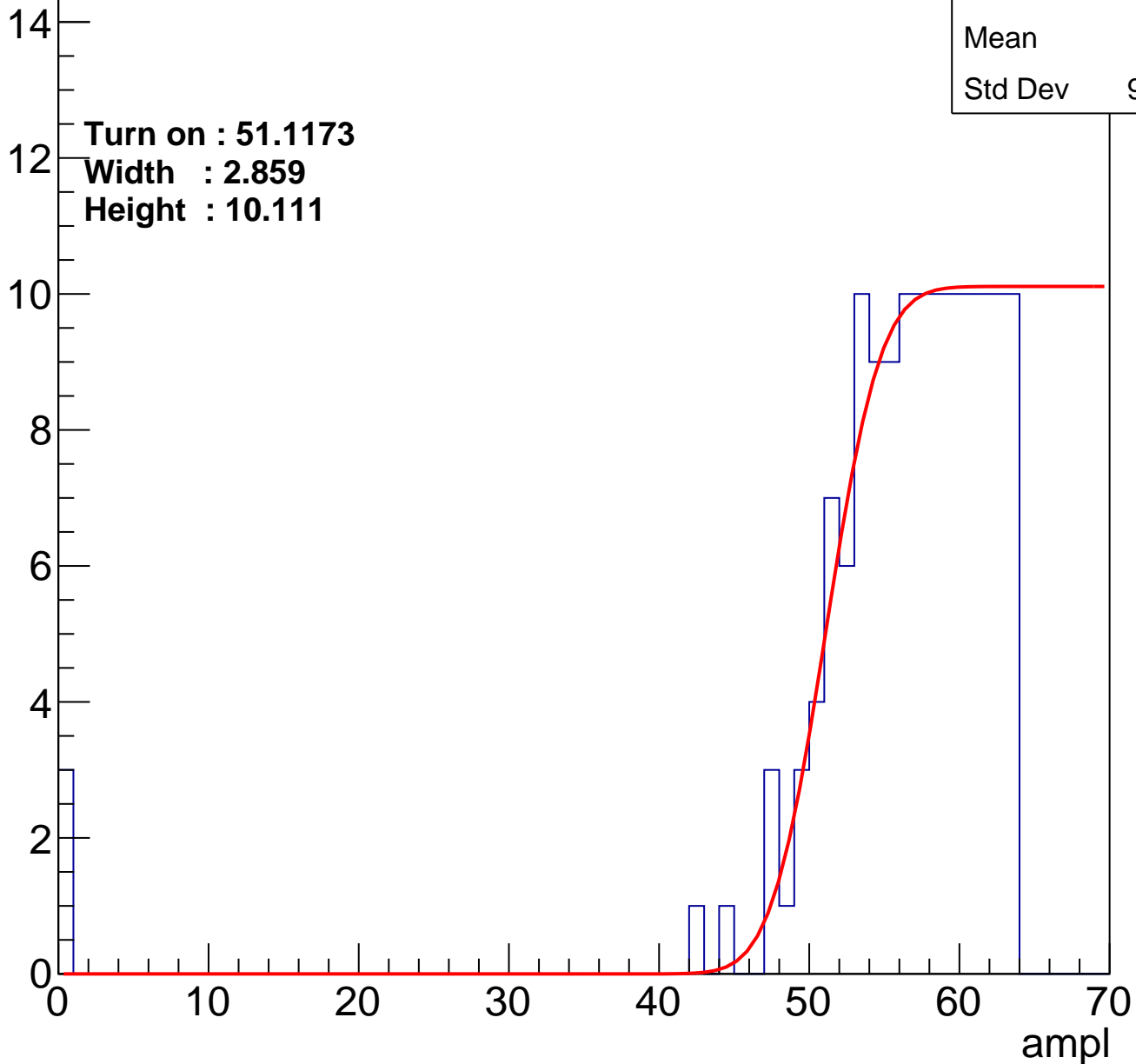
Entry

Entries	137
Mean	55.2
Std Dev	9.384

Turn on : 51.1173

Width : 2.859

Height : 10.111



B0L103S, U21-ch5

calib_packv5_040323_1717.root, FC#2, port C3

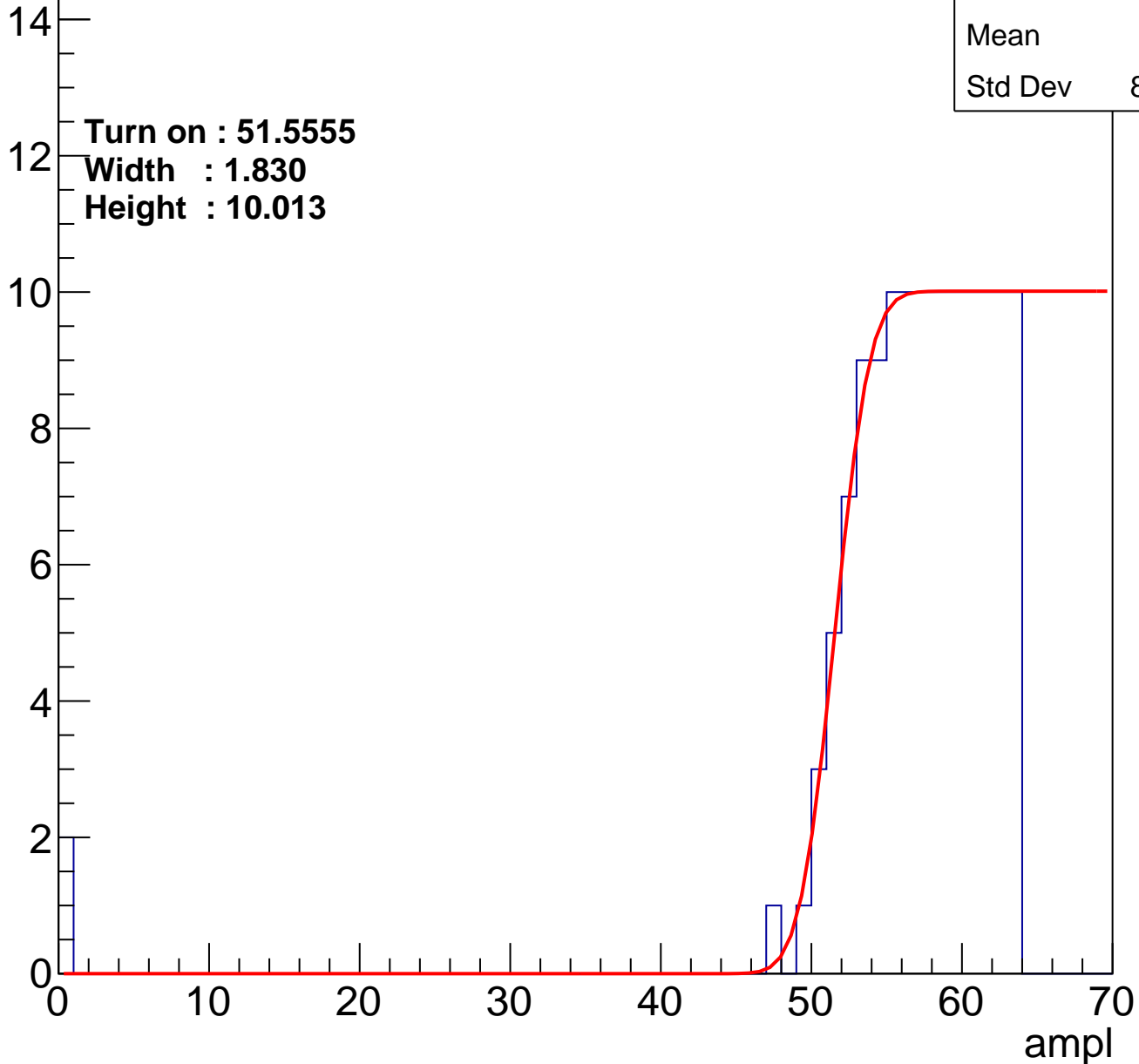
Entries	127
Mean	56.2
Std Dev	8.069

Turn on : 51.5555

Width : 1.830

Height : 10.013

Entry



B0L103S, U21-ch6

calib_packv5_040323_1717.root, FC#2, port C3

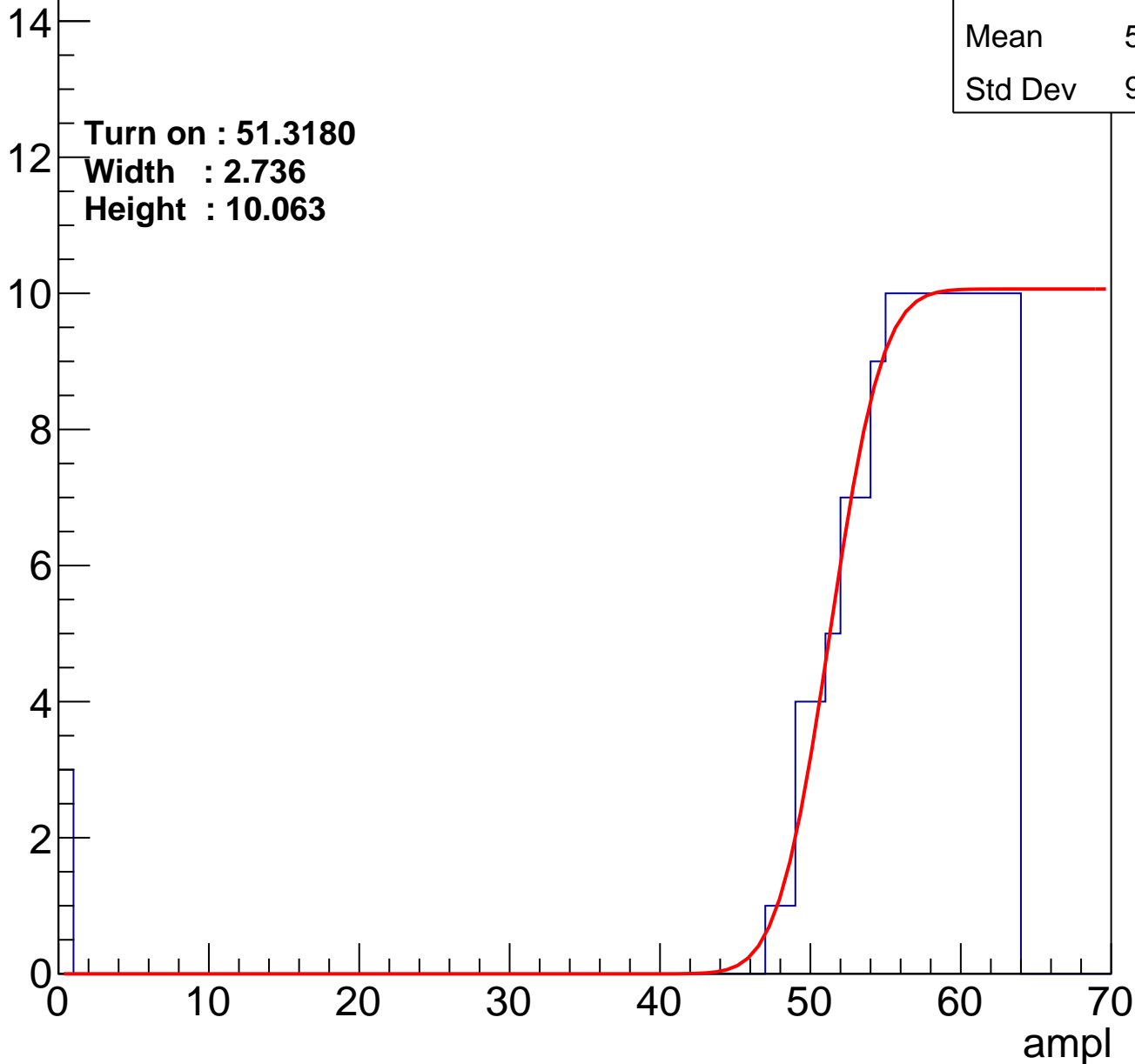
Entry

Entries	131
Mean	55.55
Std Dev	9.415

Turn on : 51.3180

Width : 2.736

Height : 10.063



B0L103S, U21-ch7

calib_packv5_040323_1717.root, FC#2, port C3

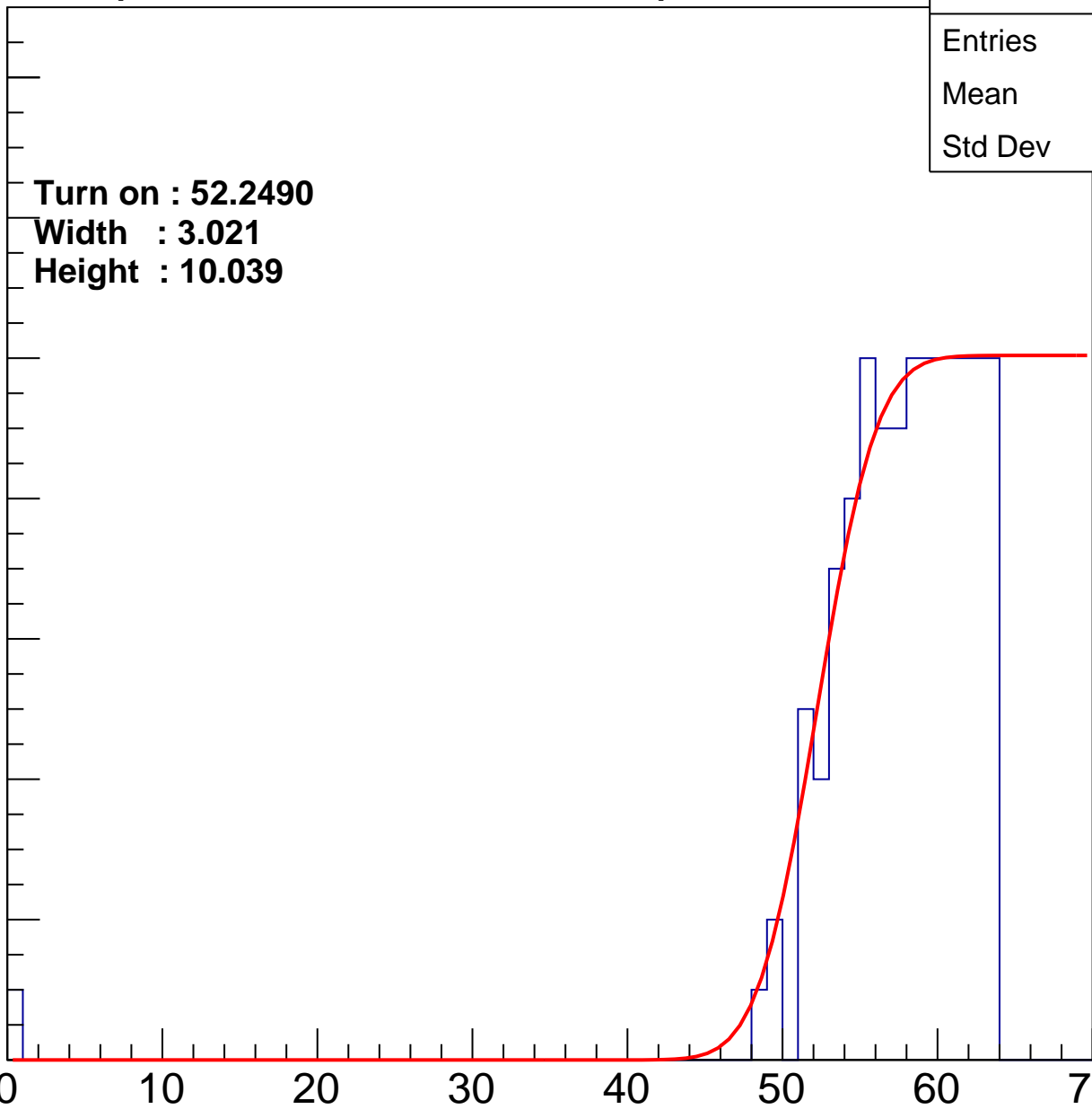
Entry

14
12
10
8
6
4
2
0

Turn on : 52.2490
Width : 3.021
Height : 10.039

Entries	116
Mean	56.97
Std Dev	6.487

ampl



B0L103S, U21-ch8

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	55.03
Std Dev	9.127

Turn on : 50.4070

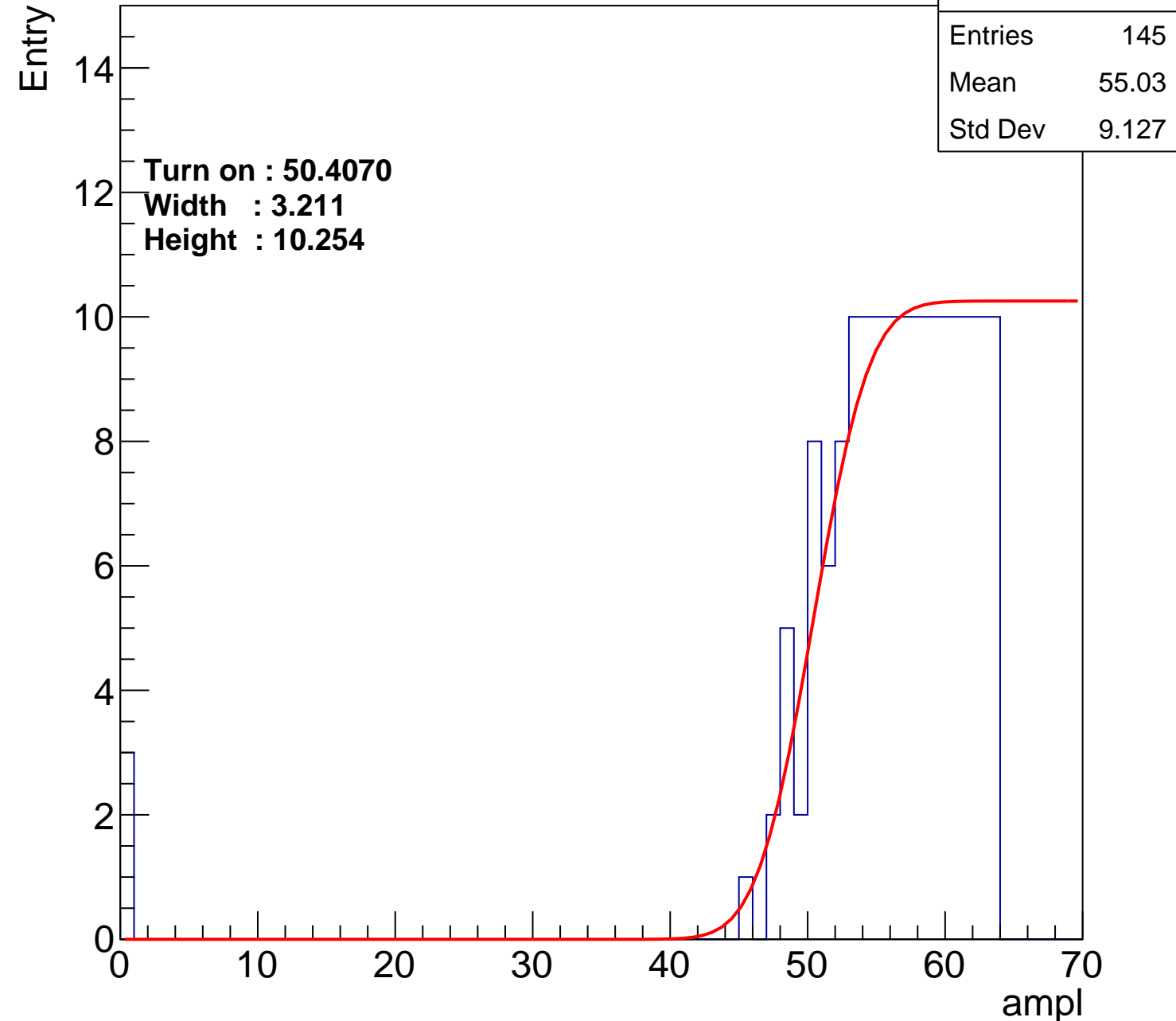
Width : 3.211

Height : 10.254

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch9

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	54.7
Std Dev	10.24

Turn on : 49.7309

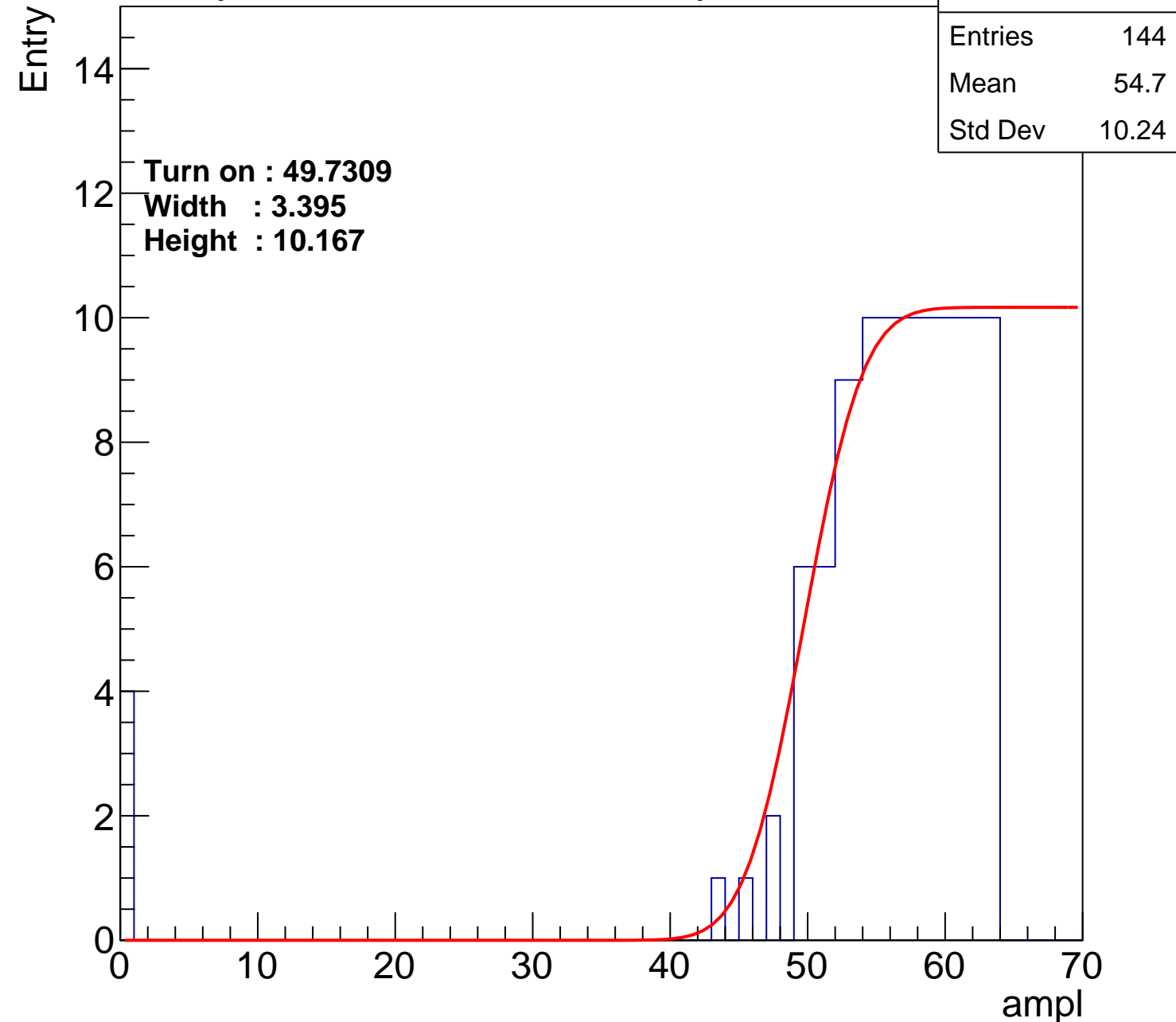
Width : 3.395

Height : 10.167

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch10

calib_packv5_040323_1717.root, FC#2, port C3

Entries	151
Mean	55.32
Std Dev	6.609

Turn on : 49.6936

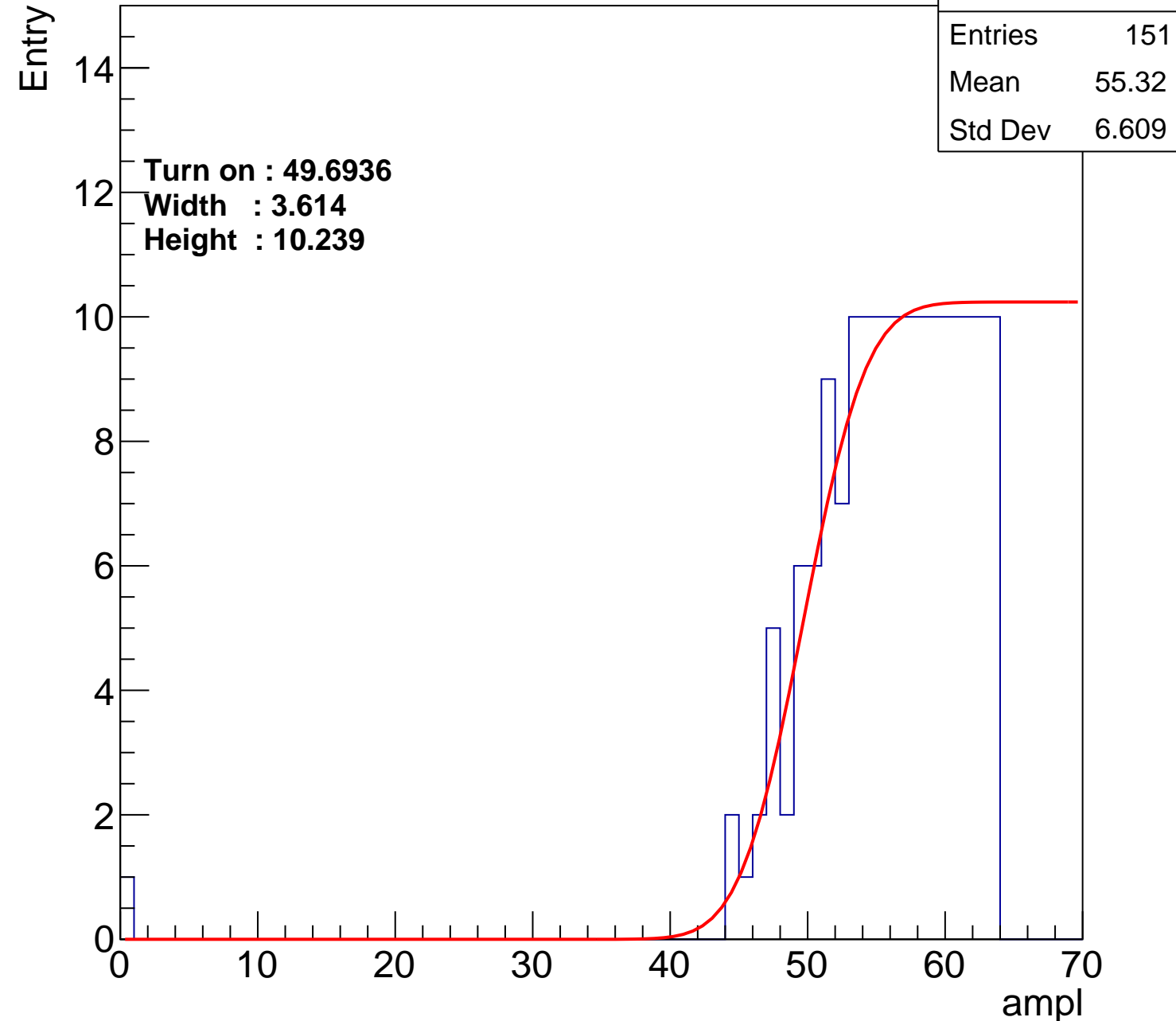
Width : 3.614

Height : 10.239

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch11

calib_packv5_040323_1717.root, FC#2, port C3

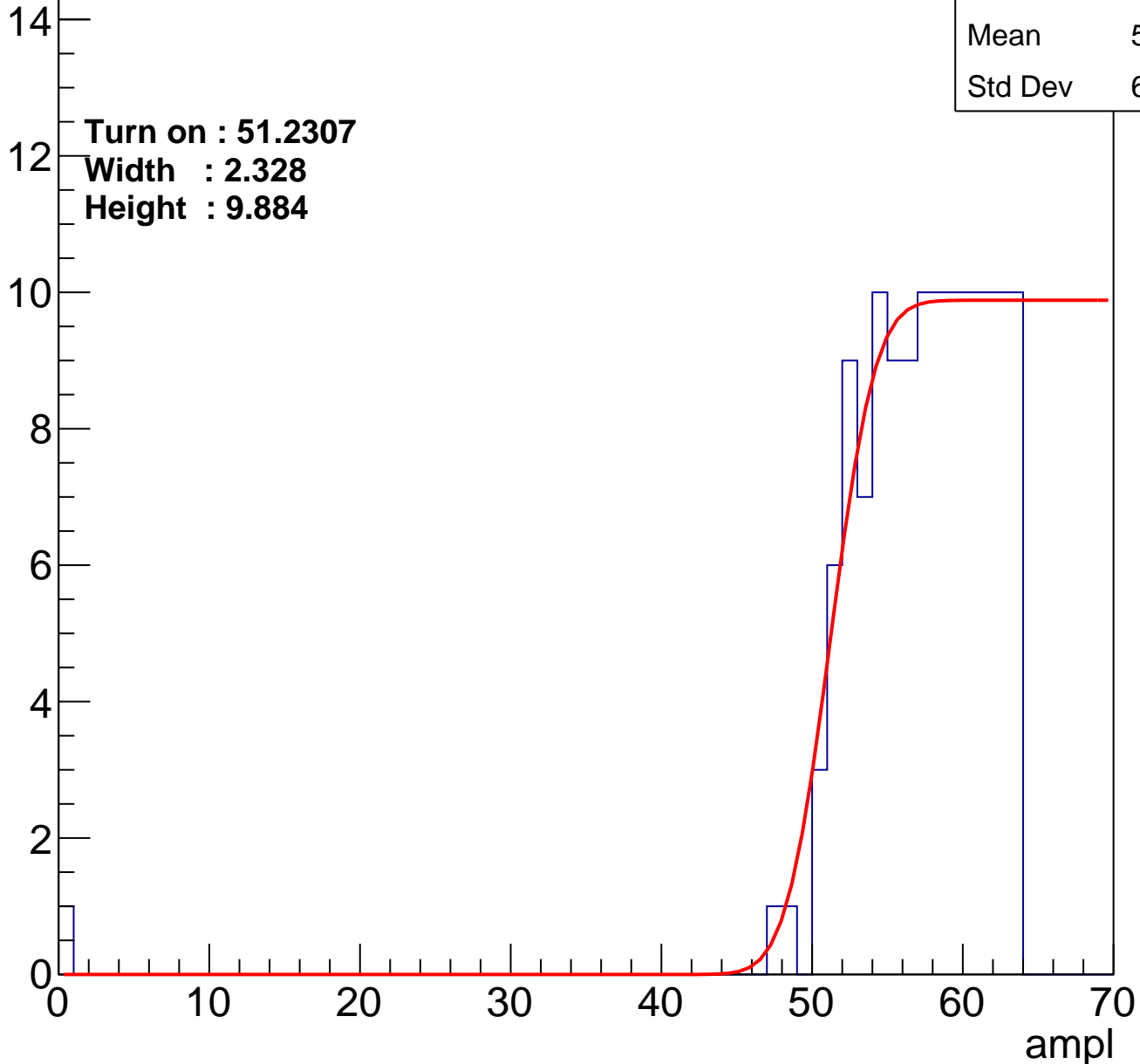
Entries	126
Mean	56.58
Std Dev	6.395

Turn on : 51.2307

Width : 2.328

Height : 9.884

Entry



B0L103S, U21-ch12

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	54.8
Std Dev	11.5

Turn on : 51.0011

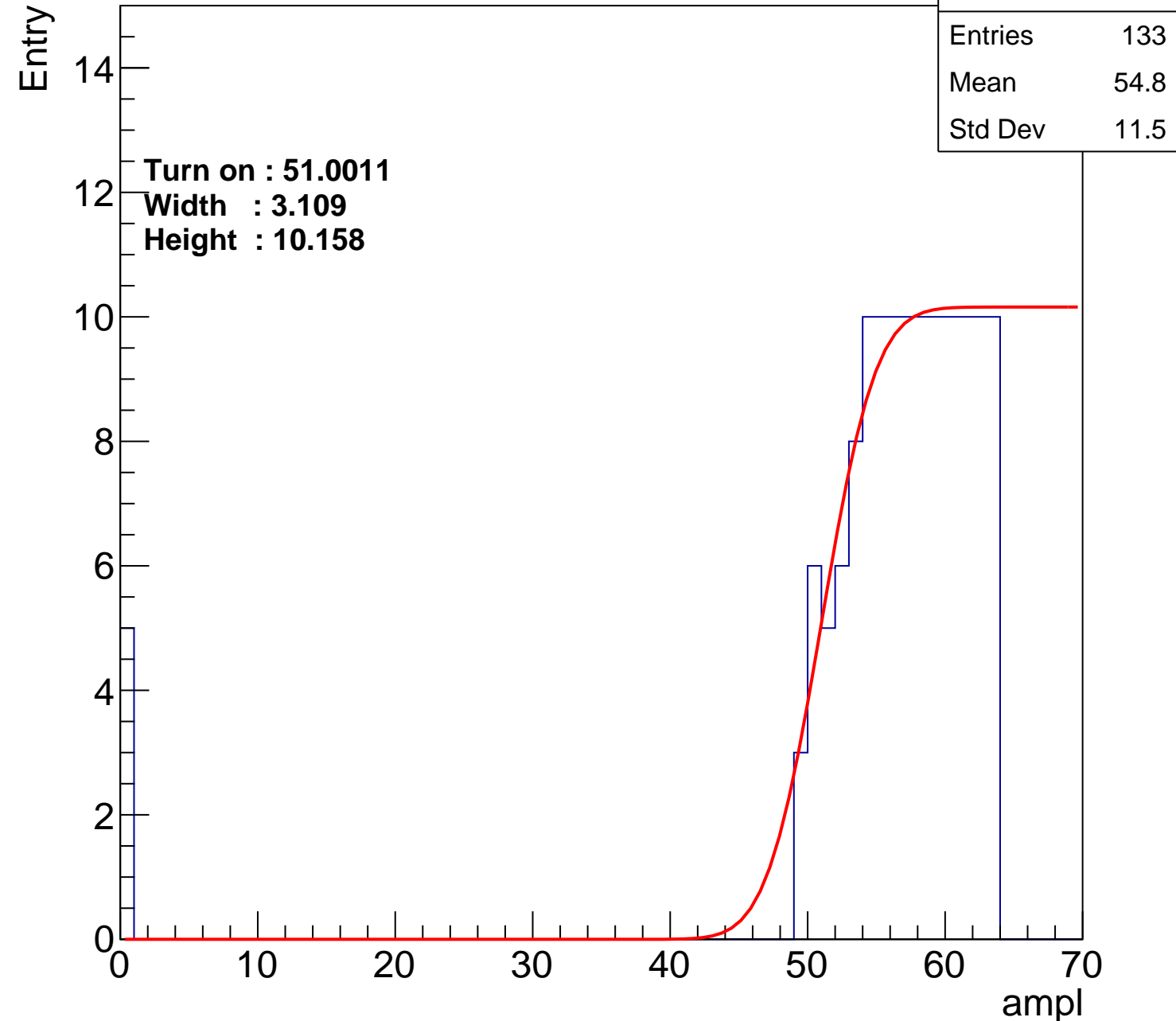
Width : 3.109

Height : 10.158

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch13

calib_packv5_040323_1717.root, FC#2, port C3

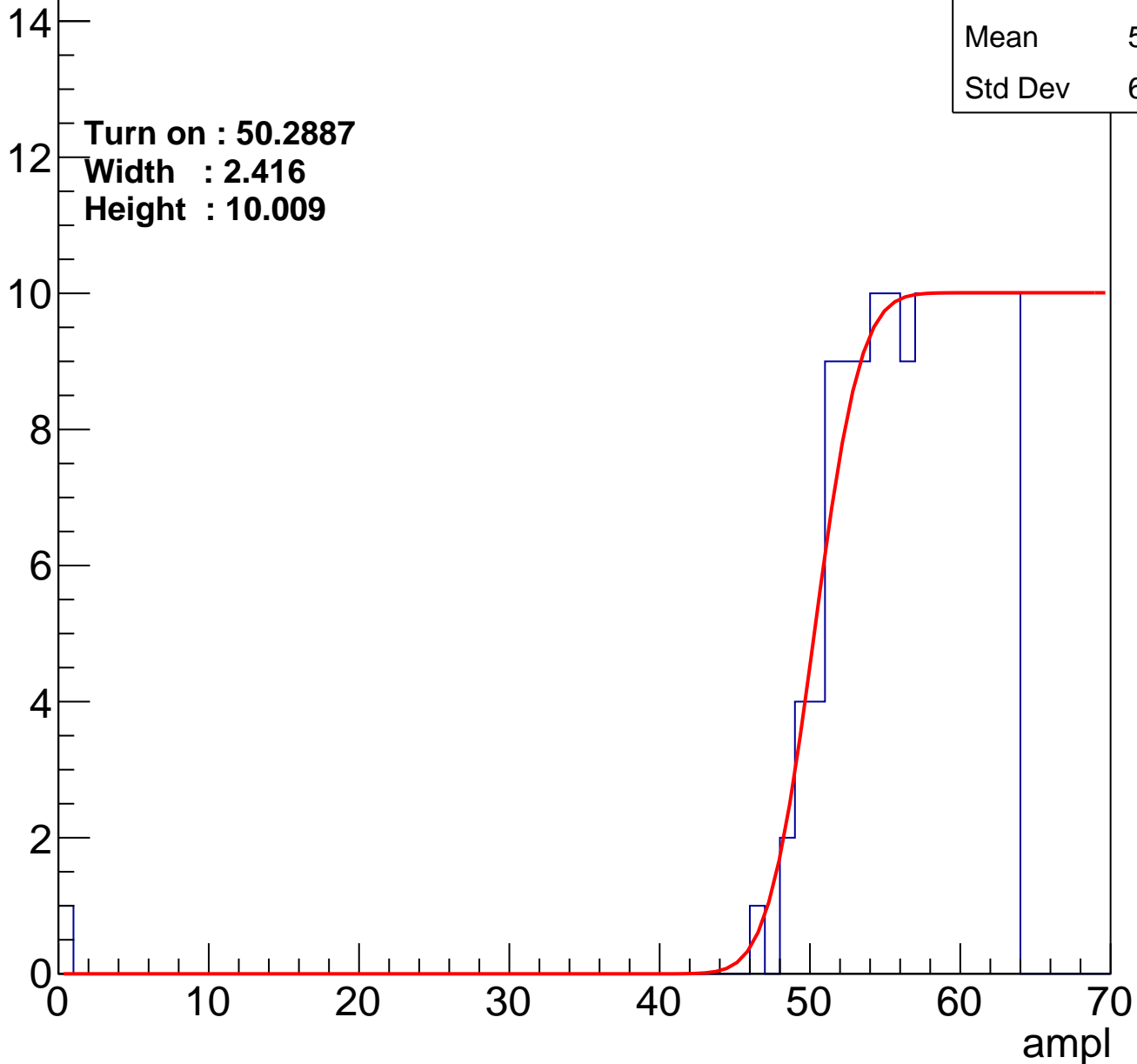
Entries	138
Mean	56.06
Std Dev	6.372

Turn on : 50.2887

Width : 2.416

Height : 10.009

Entry



B0L103S, U21-ch14

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.39
Std Dev	8.018

Turn on : 50.3275

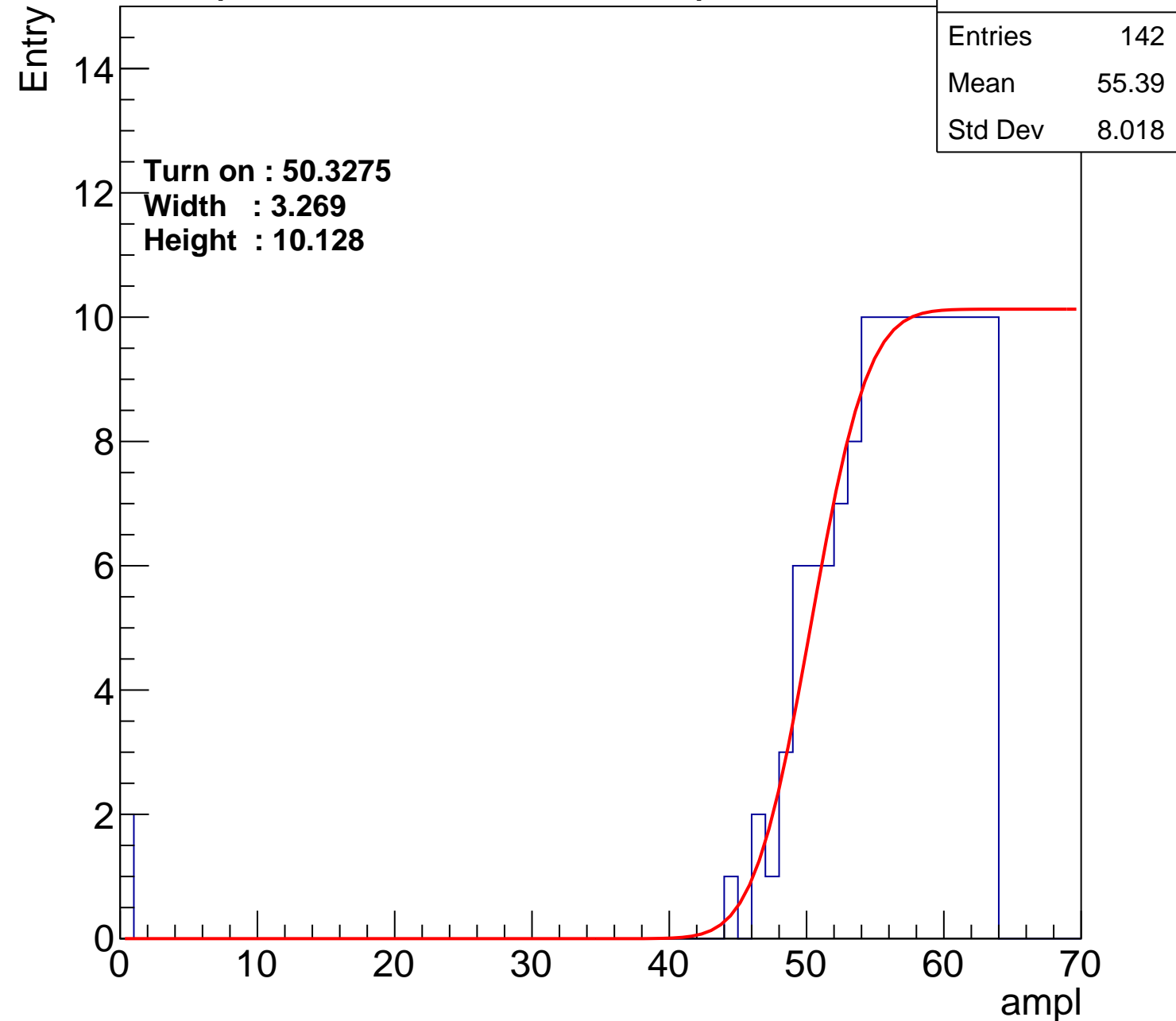
Width : 3.269

Height : 10.128

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch15

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	55.46
Std Dev	12.29

Turn on : 52.9497

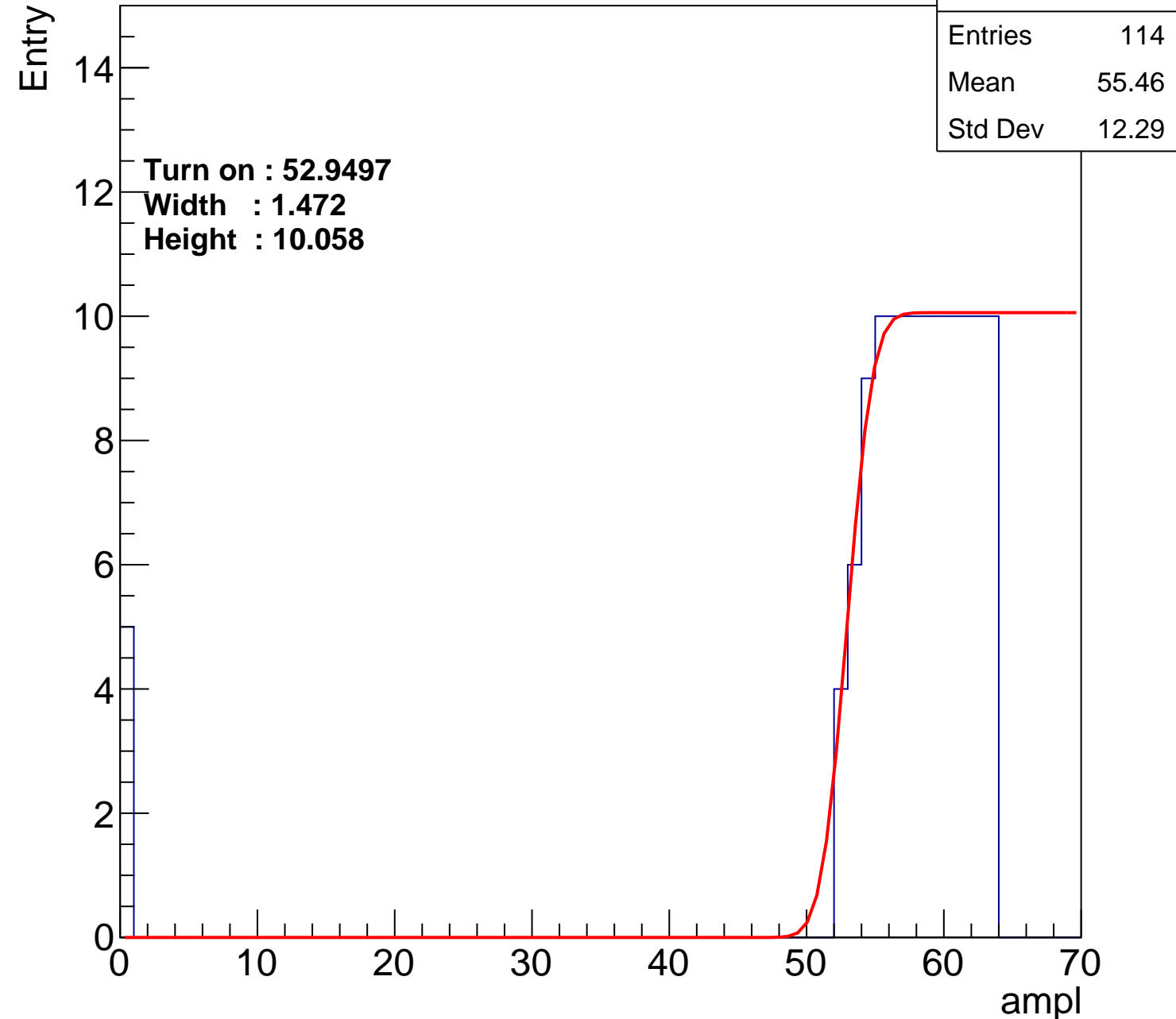
Width : 1.472

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch16

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.53
Std Dev	8

Turn on : 50.6197

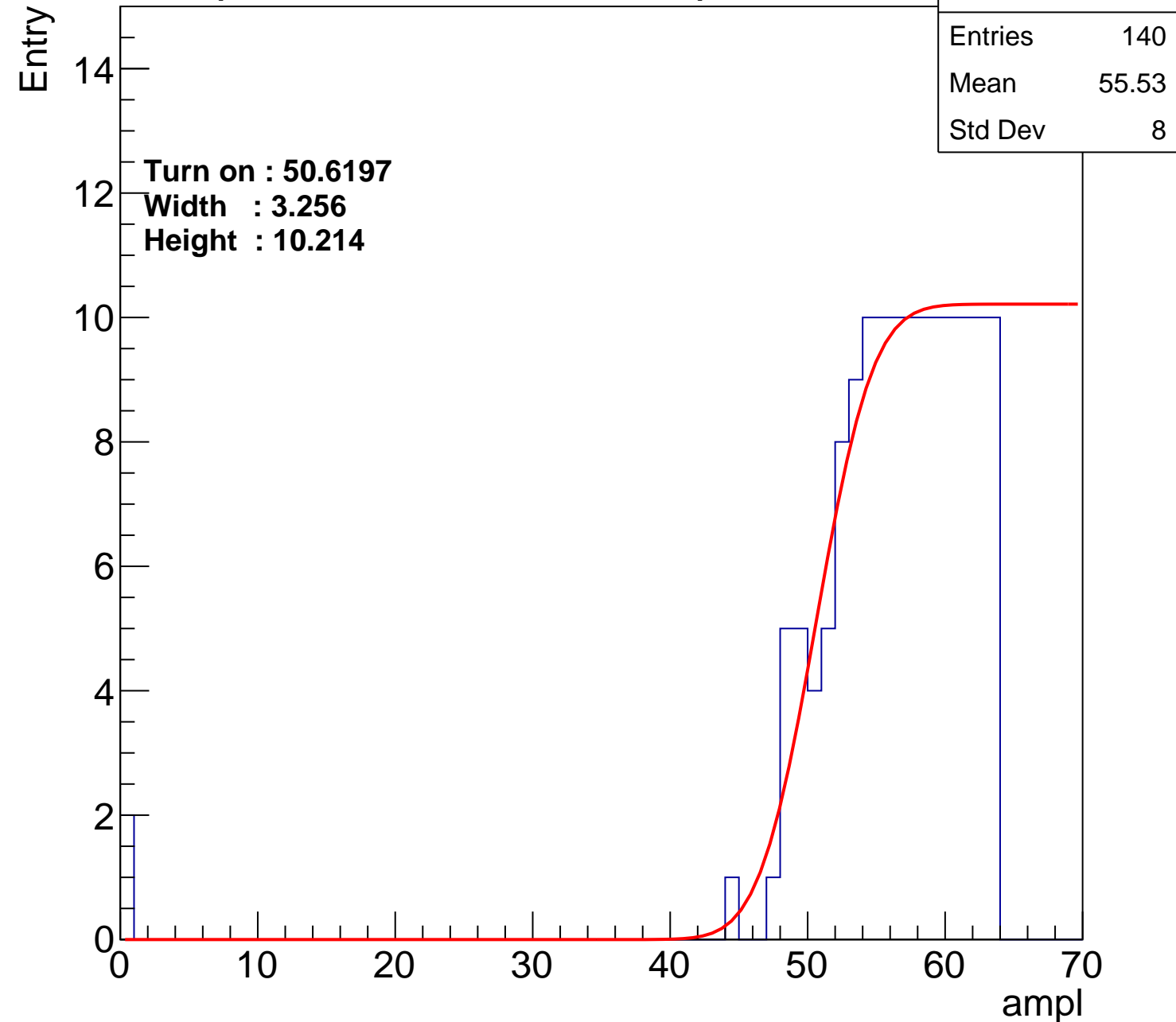
Width : 3.256

Height : 10.214

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch17

calib_packv5_040323_1717.root, FC#2, port C3

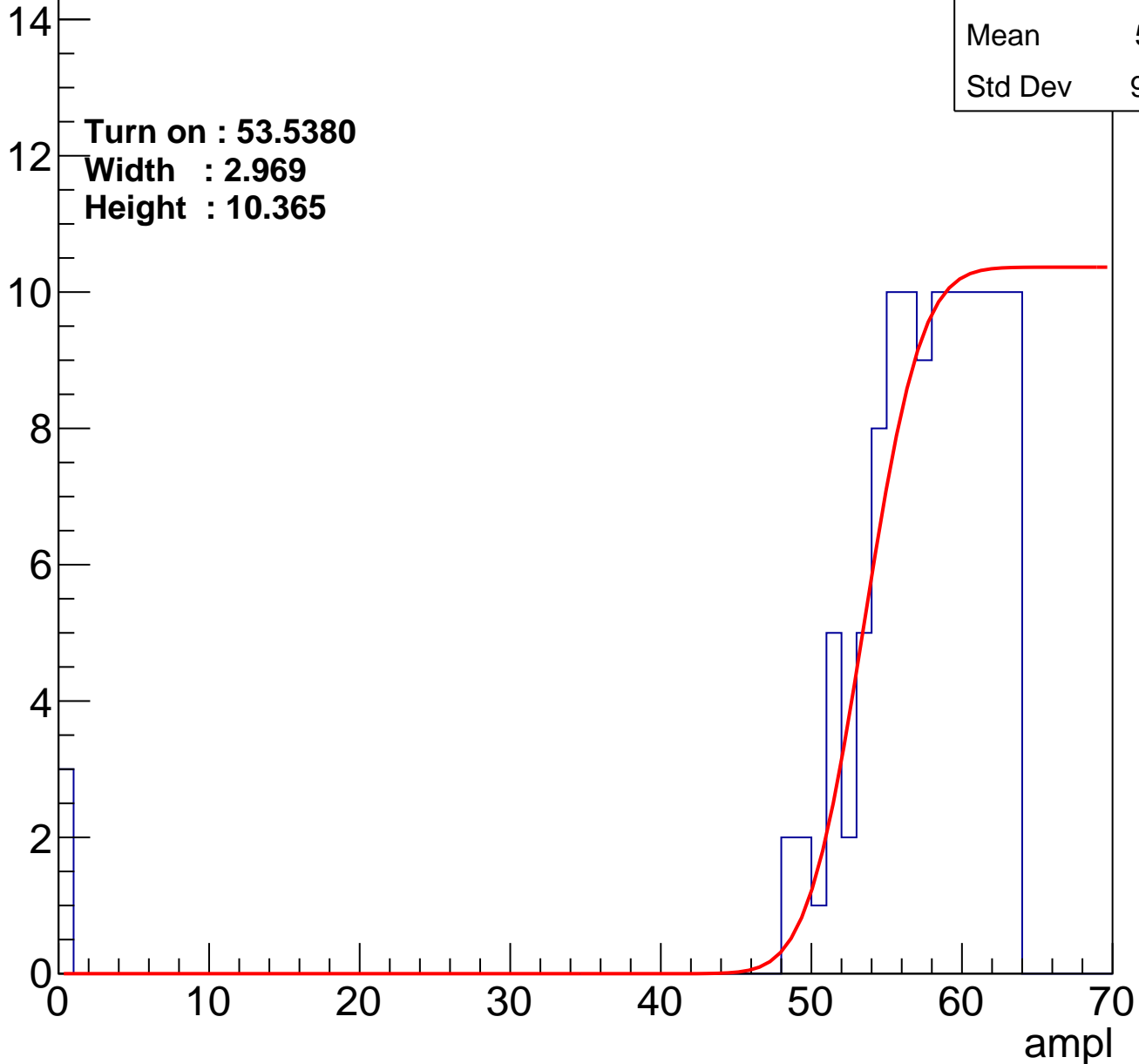
Entries	117
Mean	56.01
Std Dev	9.833

Turn on : 53.5380

Width : 2.969

Height : 10.365

Entry



B0L103S, U21-ch18

calib_packv5_040323_1717.root, FC#2, port C3

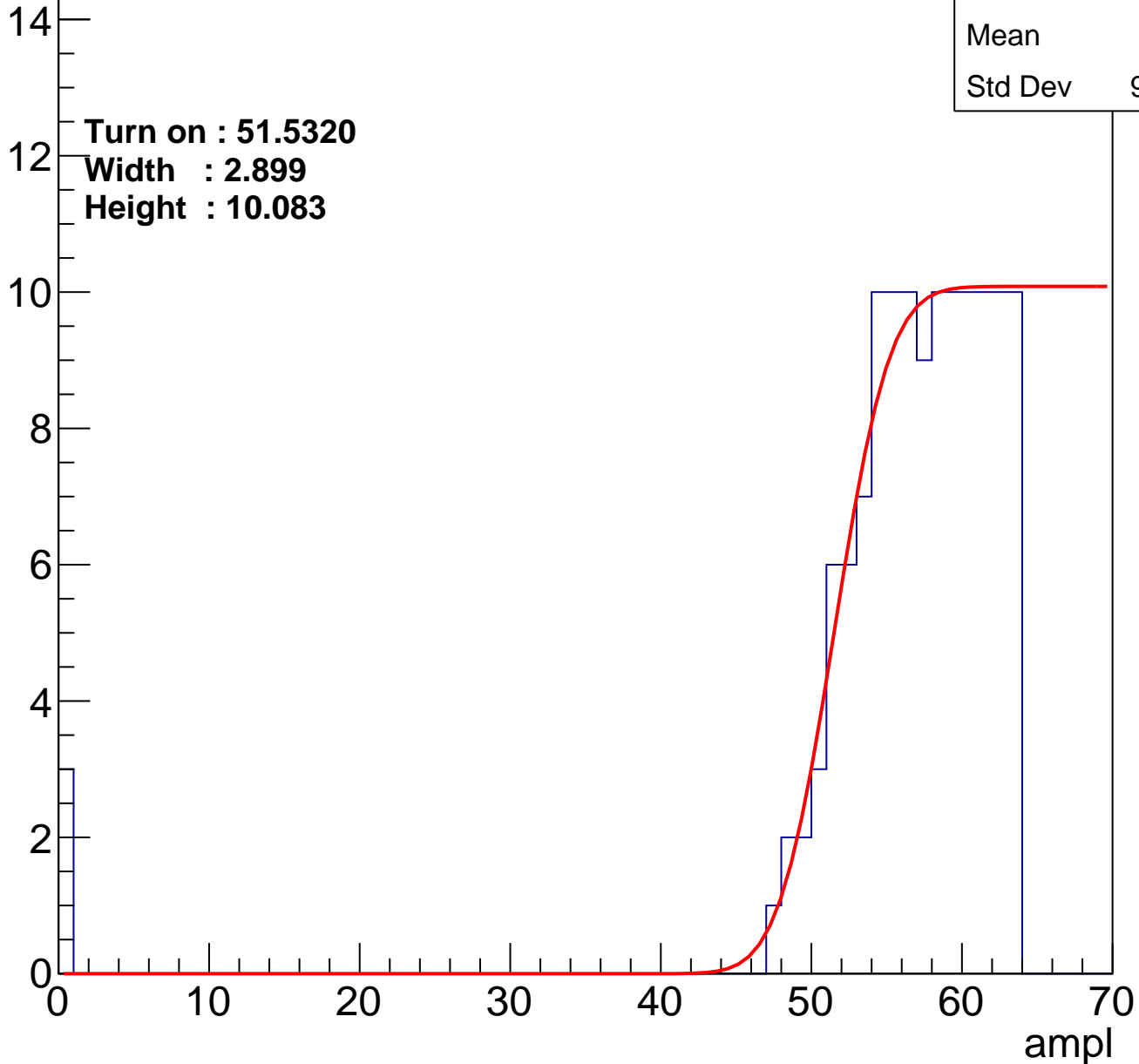
Entries	129
Mean	55.6
Std Dev	9.467

Turn on : 51.5320

Width : 2.899

Height : 10.083

Entry



B0L103S, U21-ch19

calib_packv5_040323_1717.root, FC#2, port C3

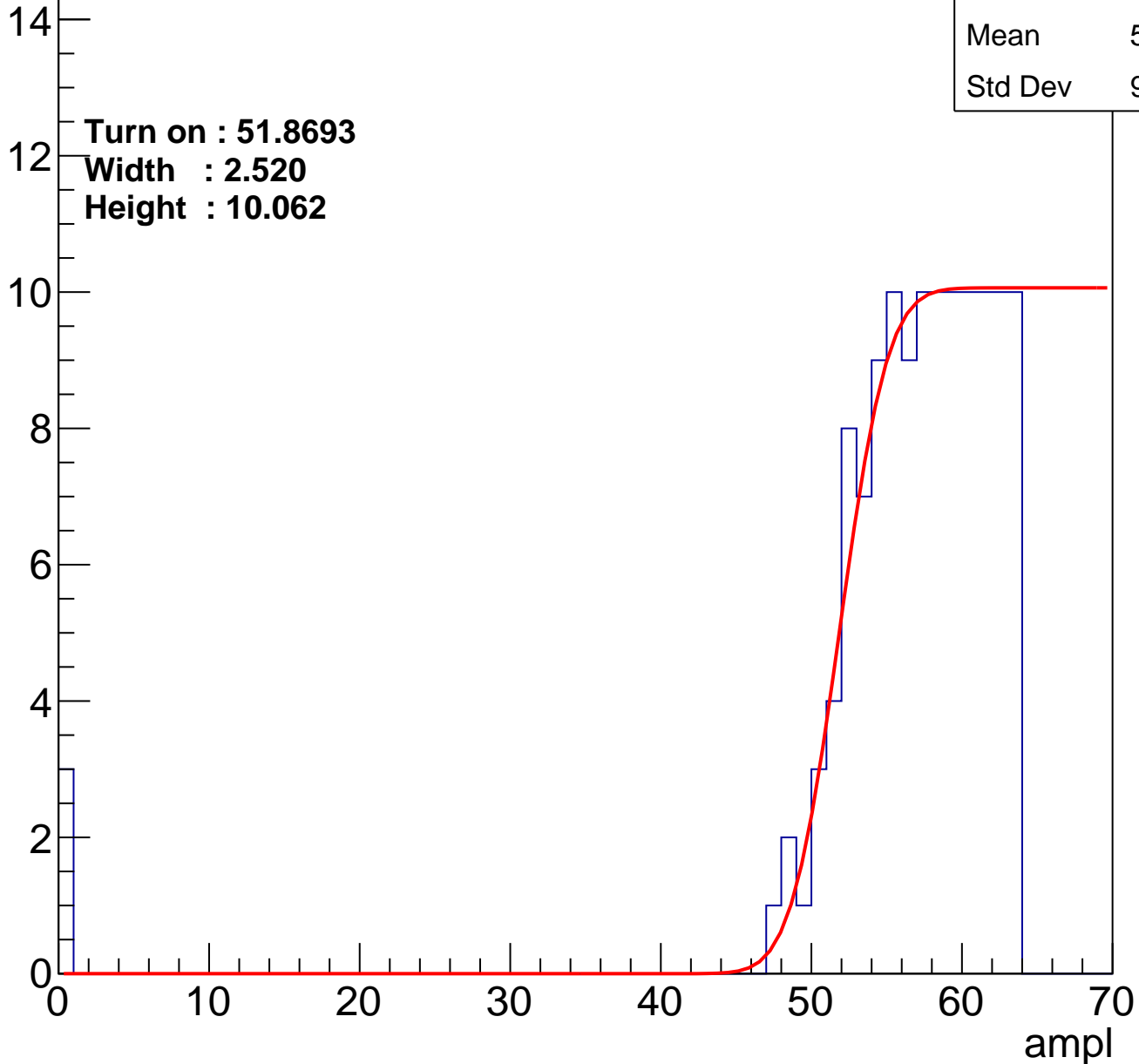
Entries	127
Mean	55.69
Std Dev	9.515

Turn on : 51.8693

Width : 2.520

Height : 10.062

Entry



B0L103S, U21-ch20

calib_packv5_040323_1717.root, FC#2, port C3

Entries	155
Mean	54.19
Std Dev	10.01

Turn on : 50.0562

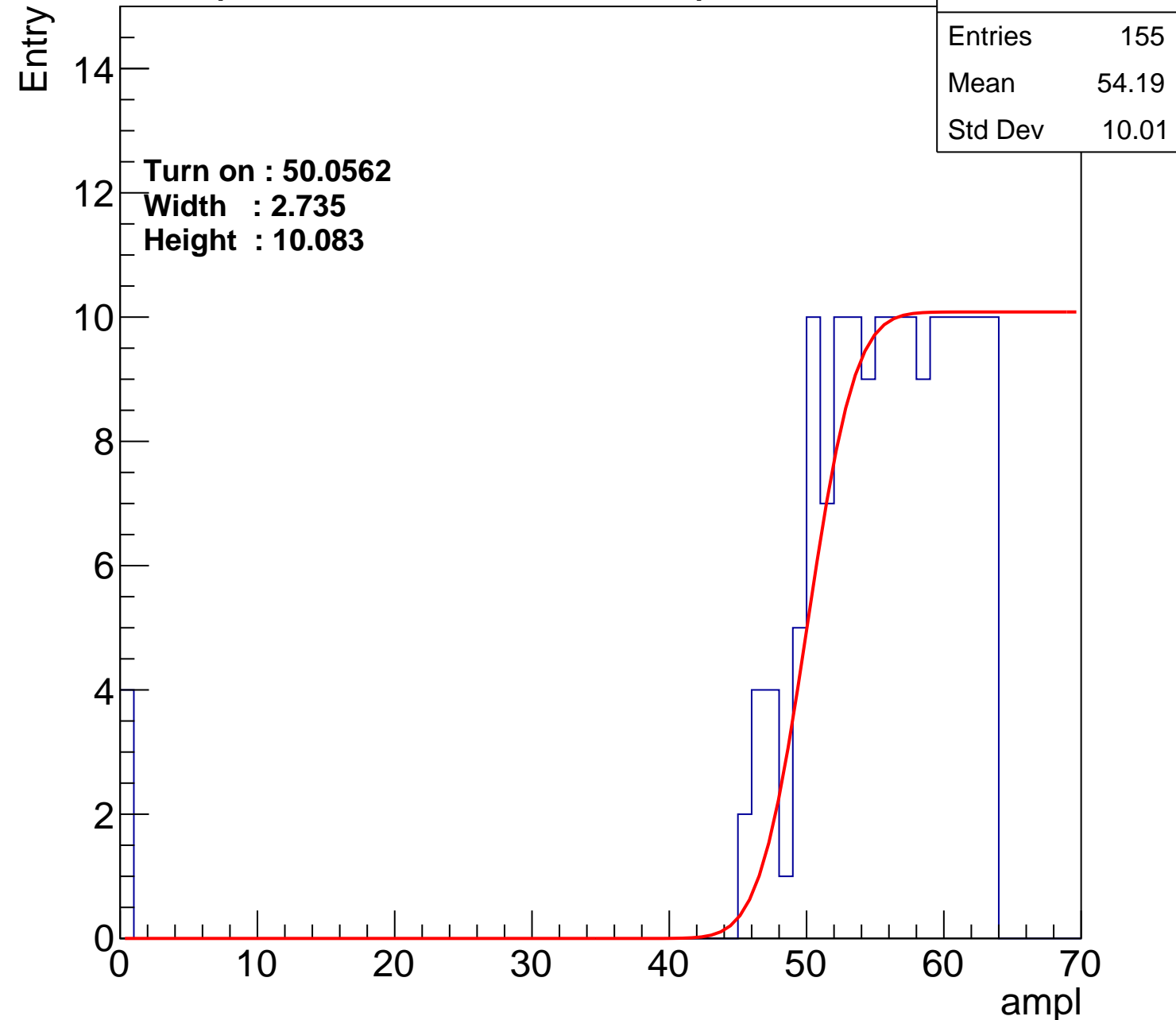
Width : 2.735

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch21

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.46
Std Dev	11.32

Turn on : 50.8336

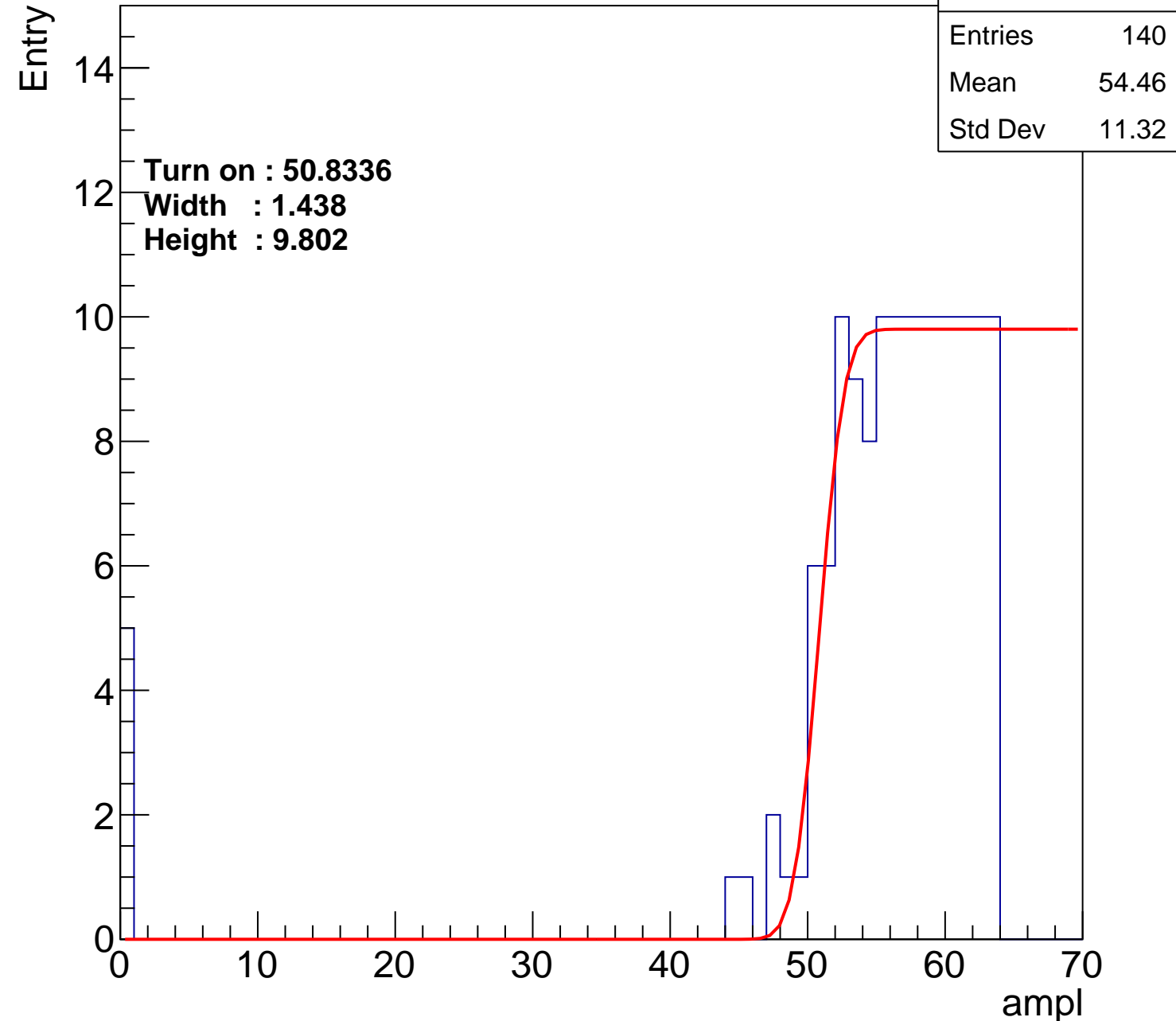
Width : 1.438

Height : 9.802

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch22

calib_packv5_040323_1717.root, FC#2, port C3

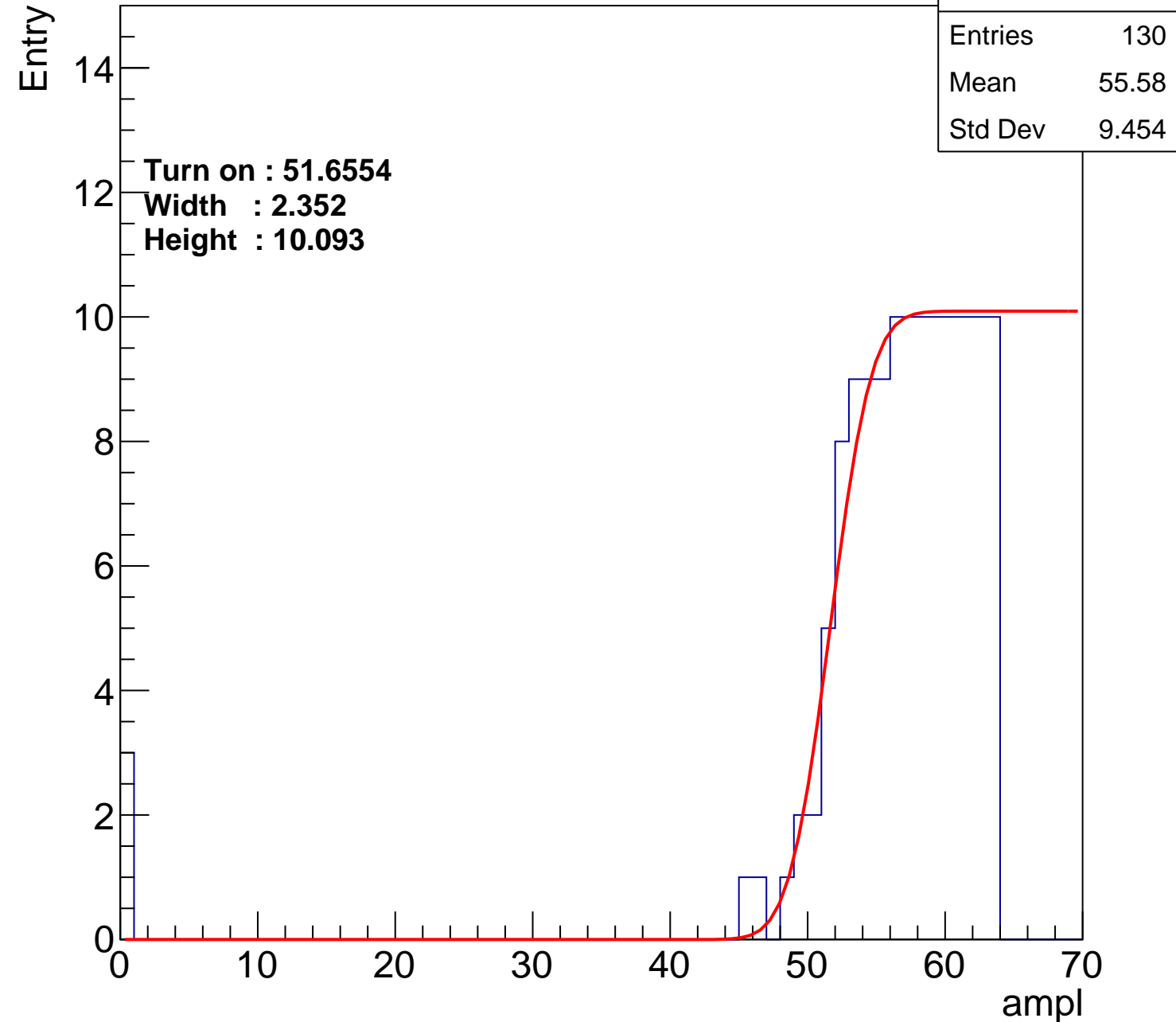
Entry

14
12
10
8
6
4
2
0

Turn on : 51.6554
Width : 2.352
Height : 10.093

Entries	130
Mean	55.58
Std Dev	9.454

ampl



B0L103S, U21-ch23

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.27
Std Dev	8.006

Turn on : 49.8765

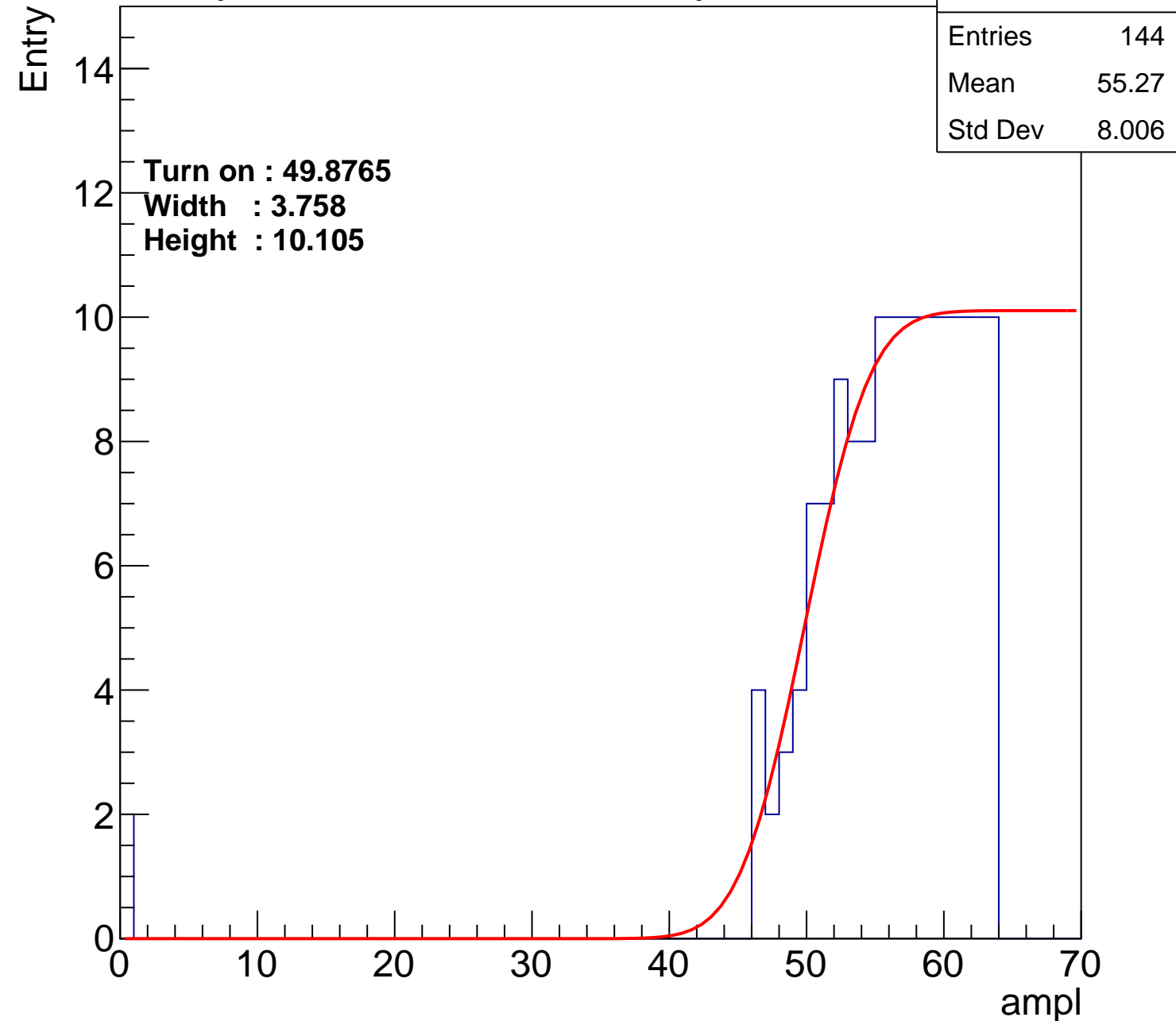
Width : 3.758

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	54.52
Std Dev	11.19

Turn on : 50.1137

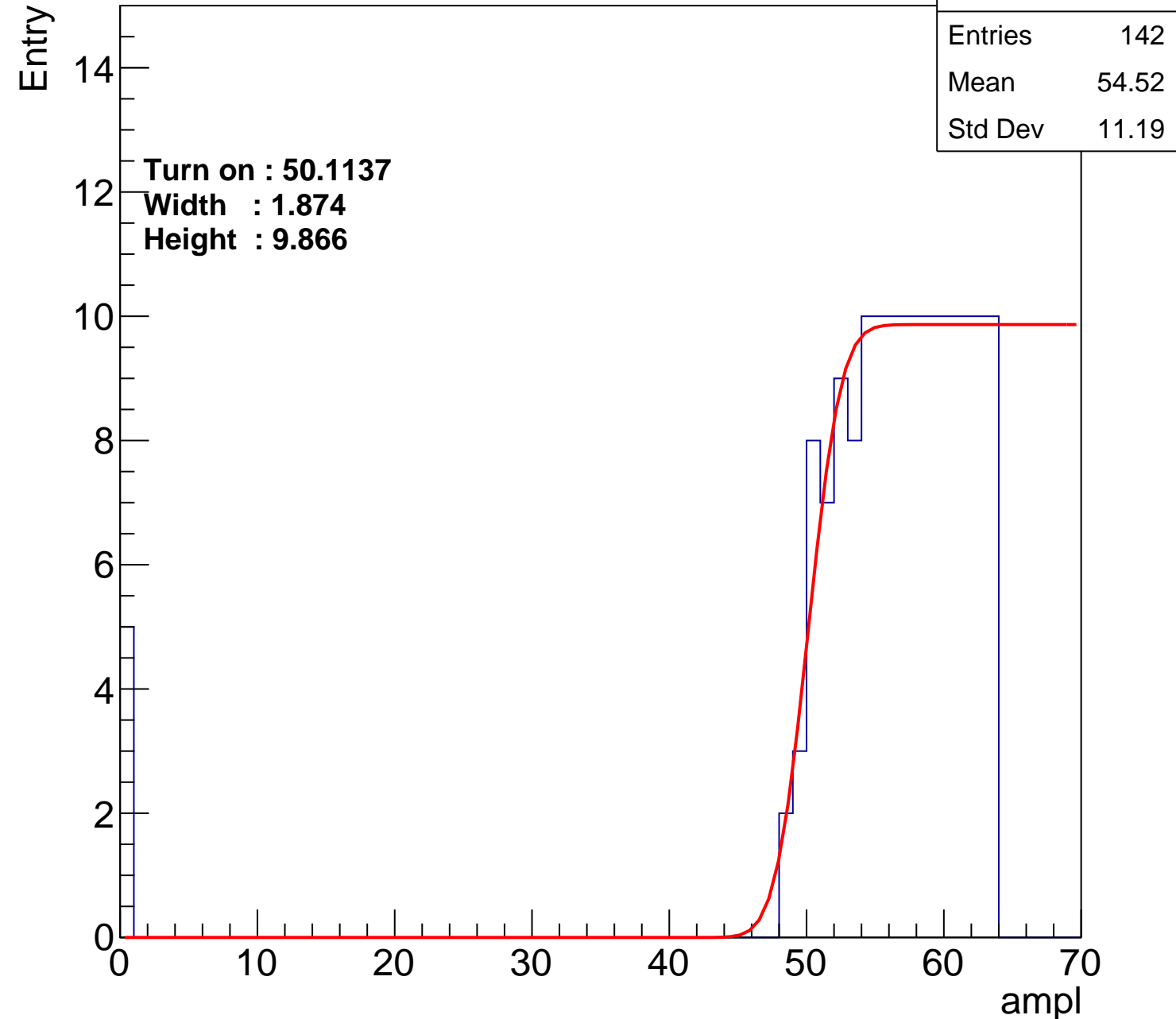
Width : 1.874

Height : 9.866

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.16
Std Dev	9.92

Turn on : 53.2951

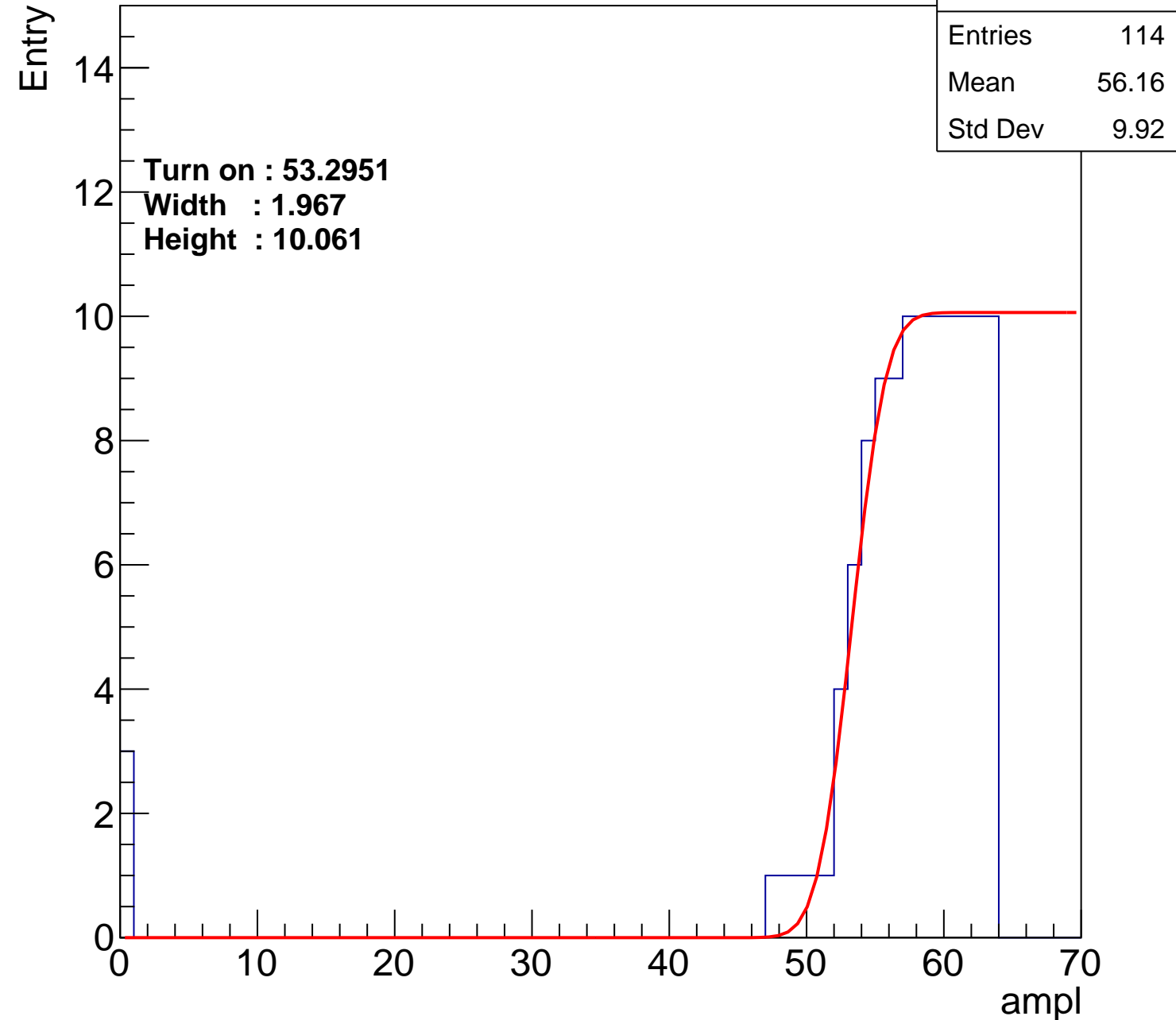
Width : 1.967

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch26

calib_packv5_040323_1717.root, FC#2, port C3

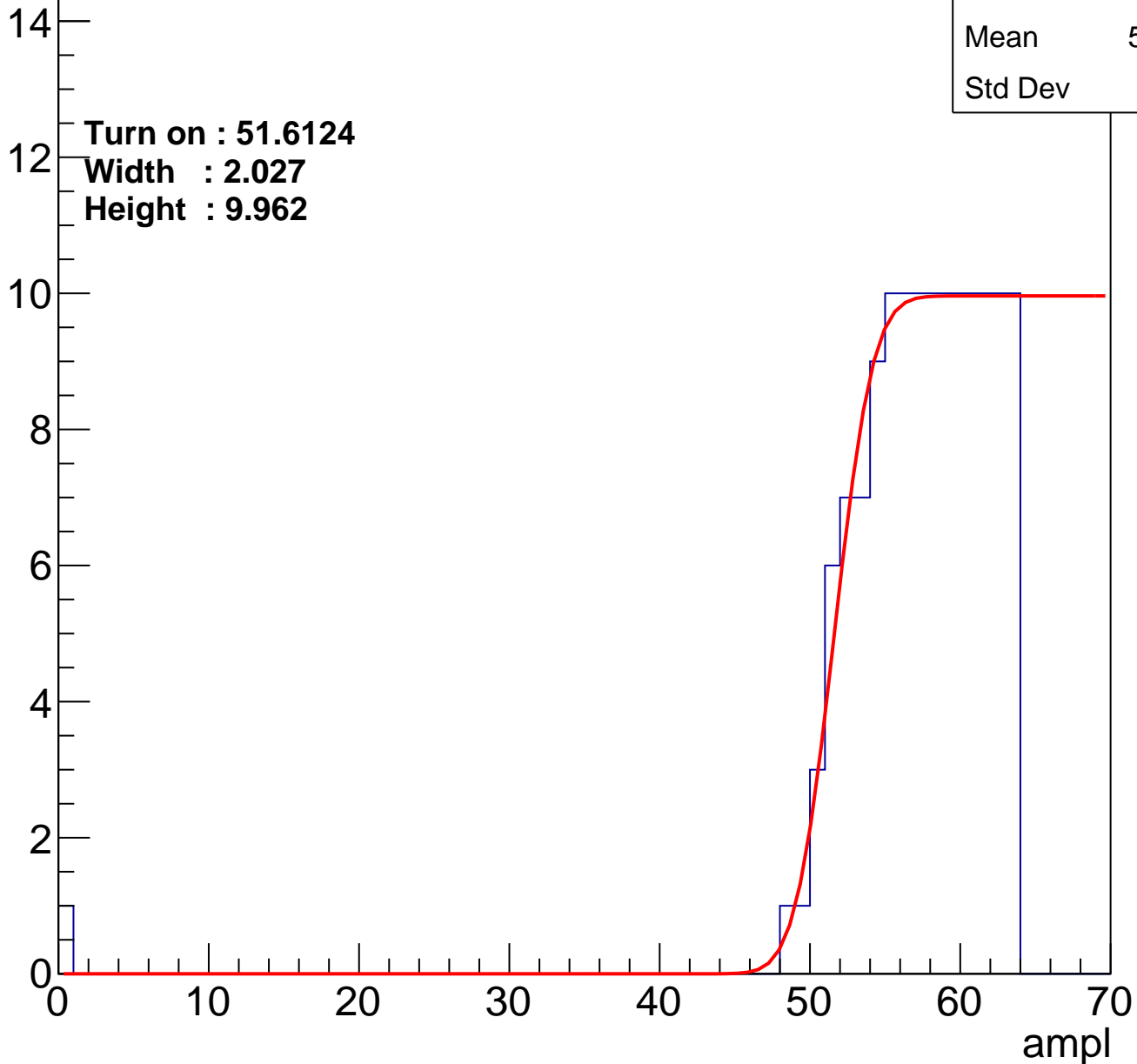
Entries	125
Mean	56.67
Std Dev	6.37

Turn on : 51.6124

Width : 2.027

Height : 9.962

Entry



B0L103S, U21-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.38
Std Dev	7.96

Turn on : 49.2720

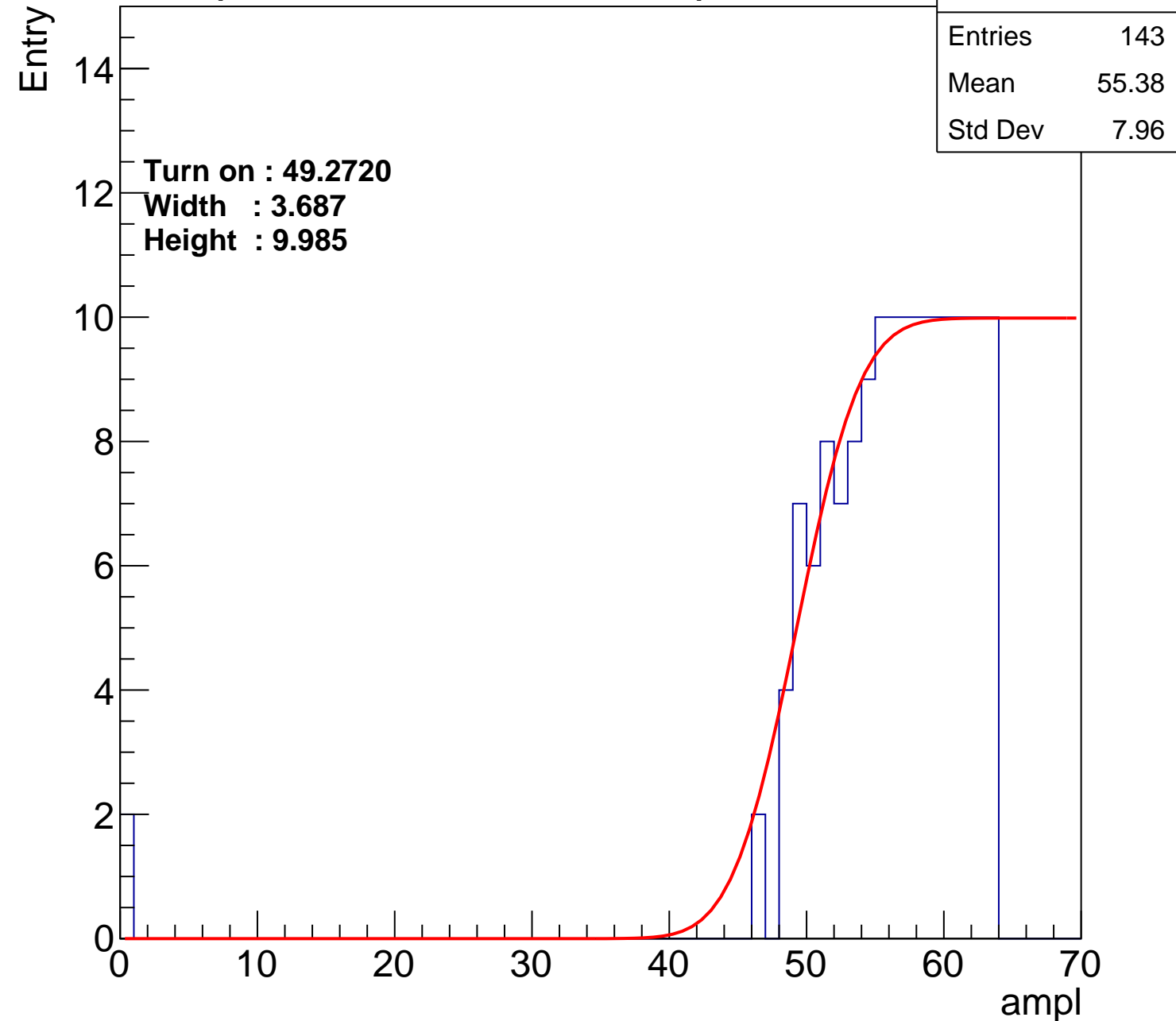
Width : 3.687

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch28

calib_packv5_040323_1717.root, FC#2, port C3

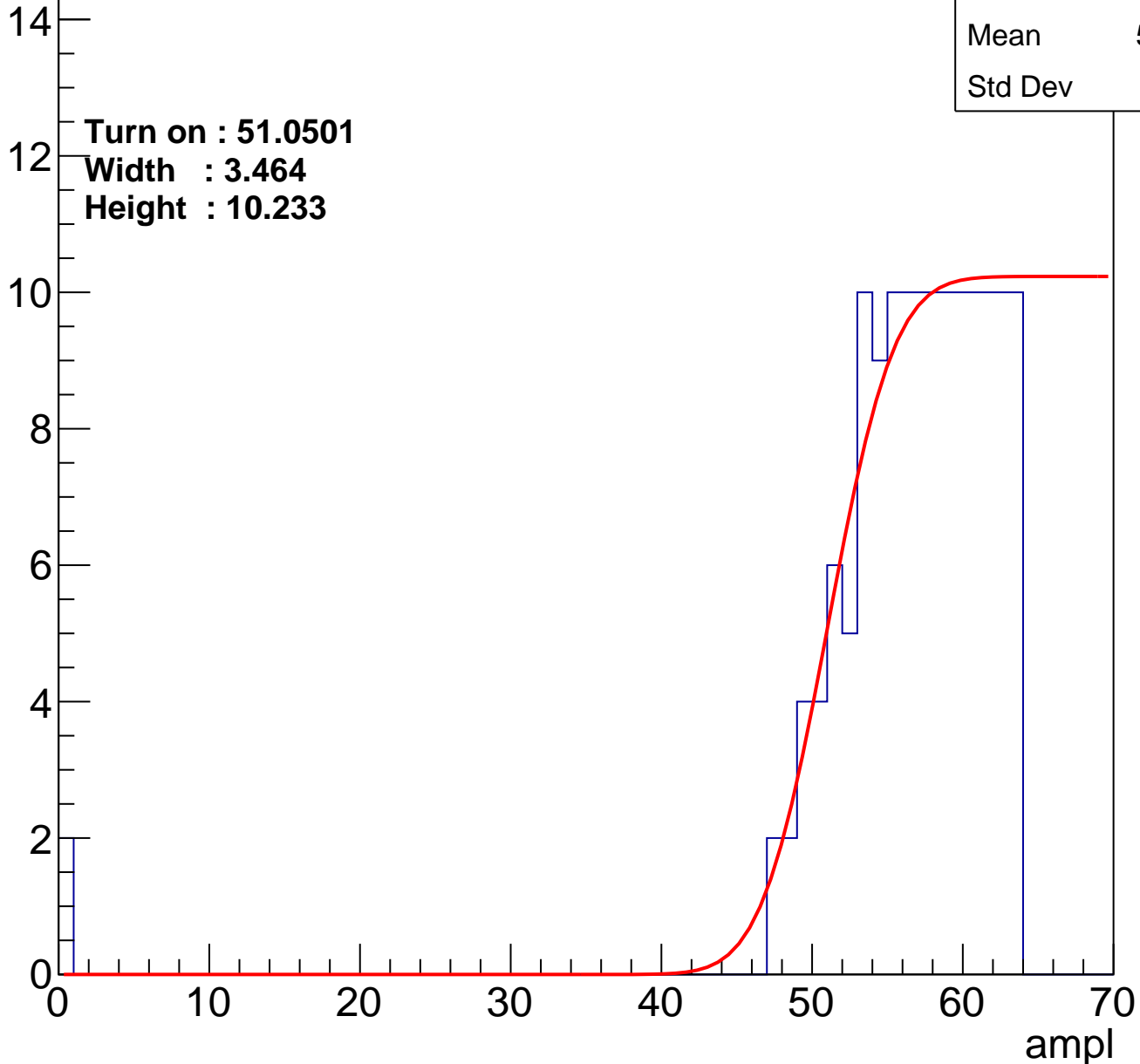
Entries	134
Mean	55.81
Std Dev	8.04

Turn on : 51.0501

Width : 3.464

Height : 10.233

Entry



B0L103S, U21-ch29

calib_packv5_040323_1717.root, FC#2, port C3

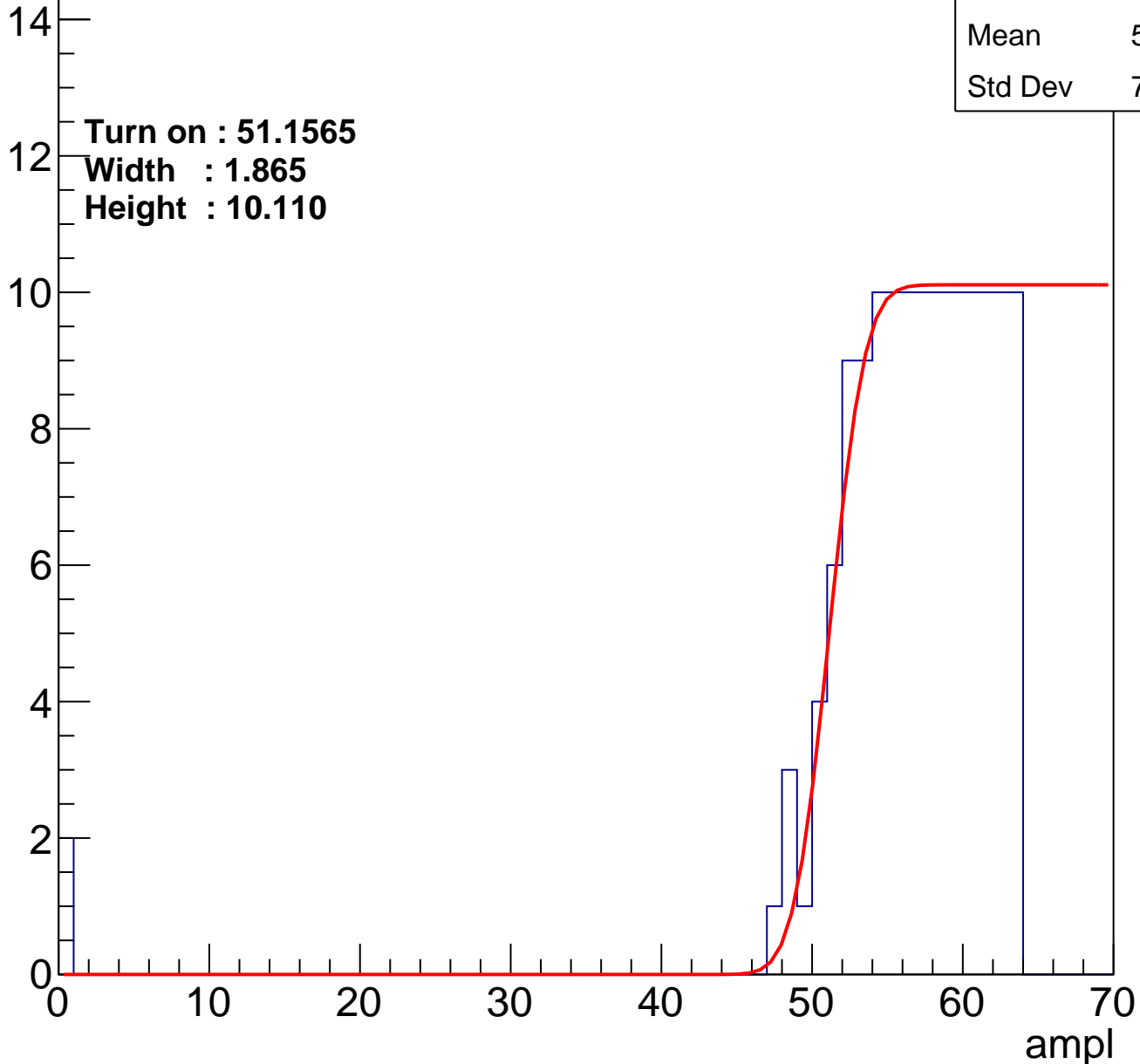
Entries	135
Mean	55.86
Std Dev	7.963

Turn on : 51.1565

Width : 1.865

Height : 10.110

Entry



B0L103S, U21-ch30

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.31
Std Dev	9.377

Turn on : 51.4777

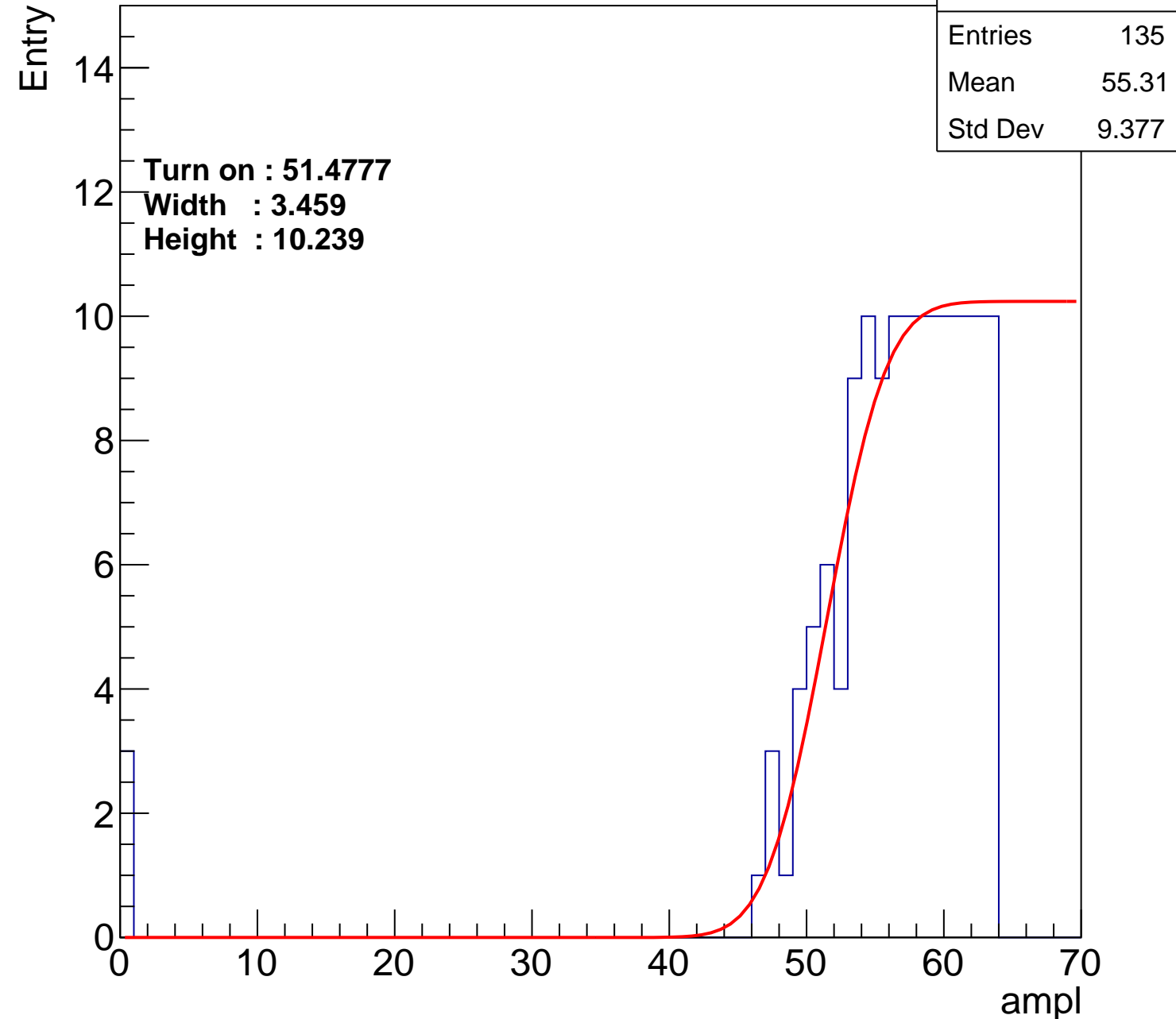
Width : 3.459

Height : 10.239

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch31

calib_packv5_040323_1717.root, FC#2, port C3

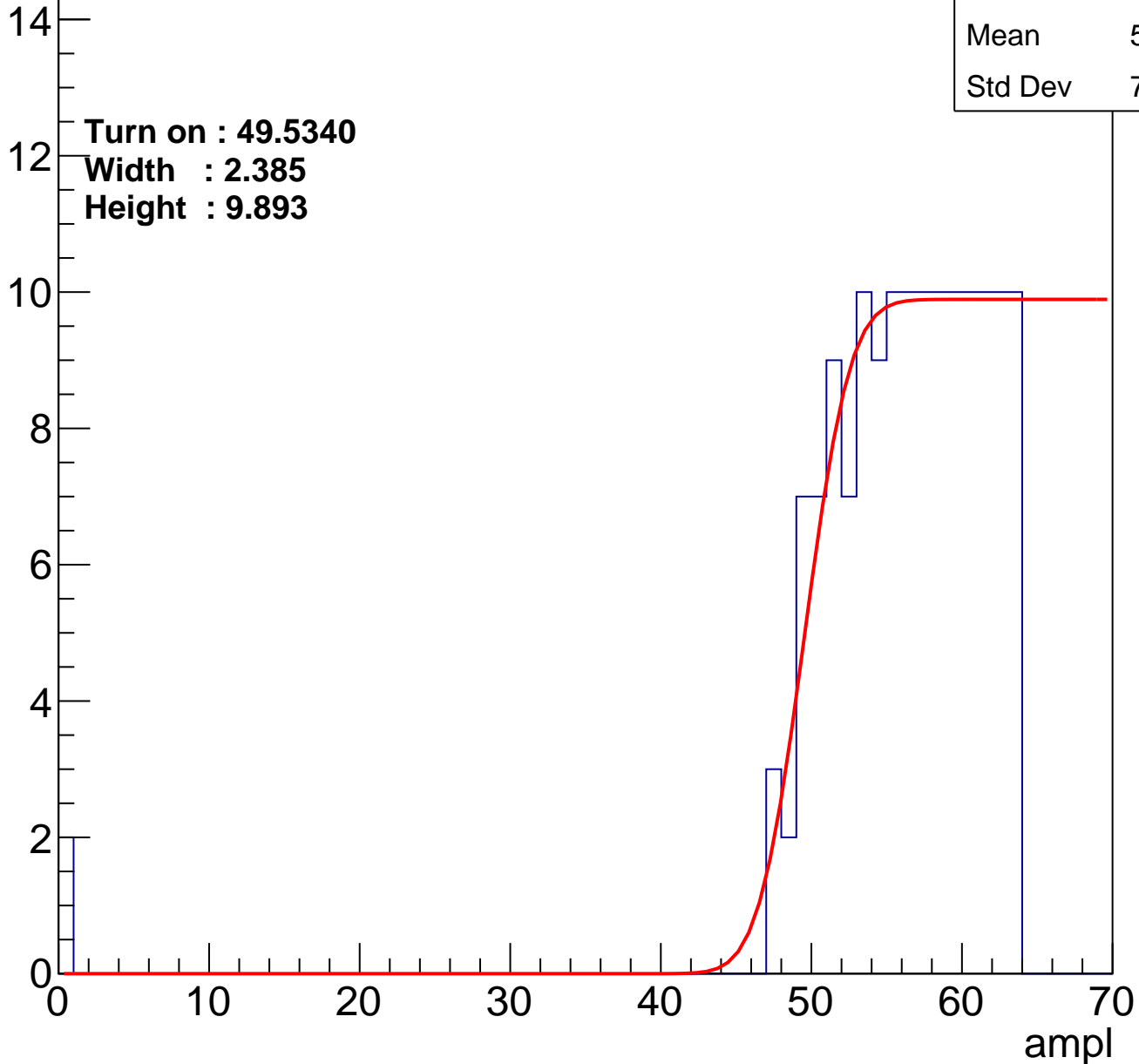
Entries	146
Mean	55.34
Std Dev	7.872

Turn on : 49.5340

Width : 2.385

Height : 9.893

Entry



B0L103S, U21-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	55.75
Std Dev	9.533

Turn on : 51.9517

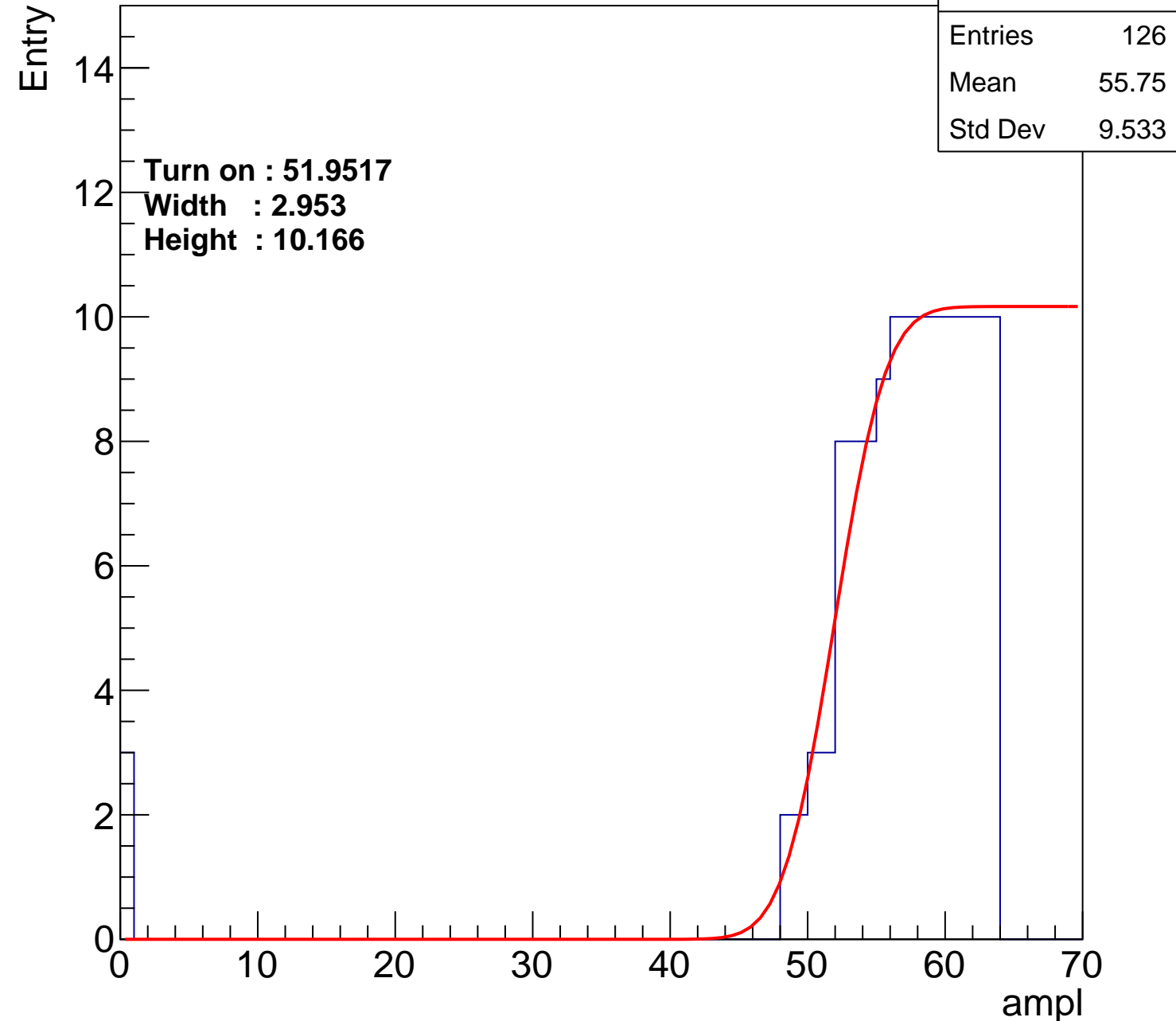
Width : 2.953

Height : 10.166

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	56.63
Std Dev	8.342

Turn on : 52.9303

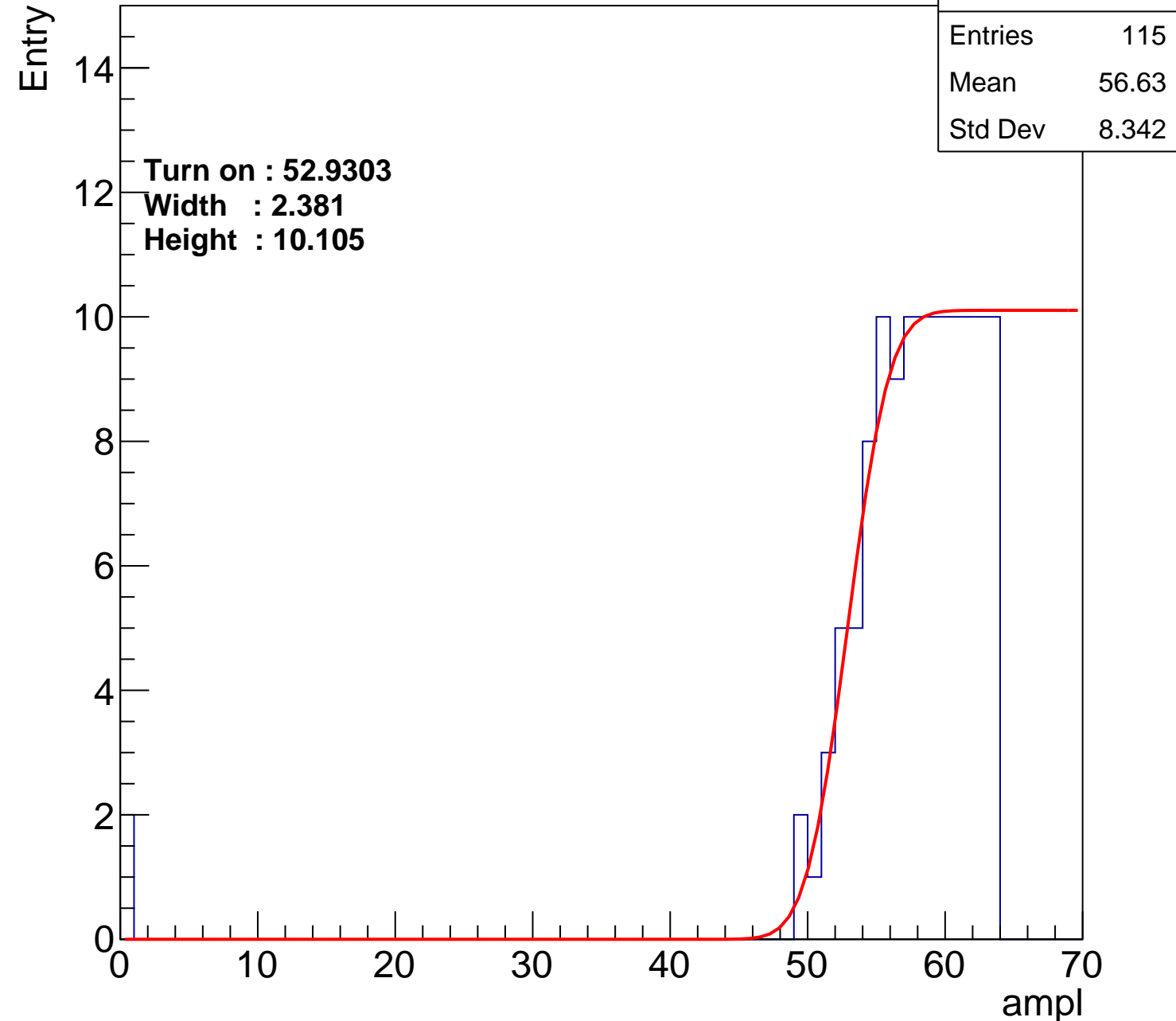
Width : 2.381

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	142
Mean	55.47
Std Dev	7.929

Turn on : 49.7488

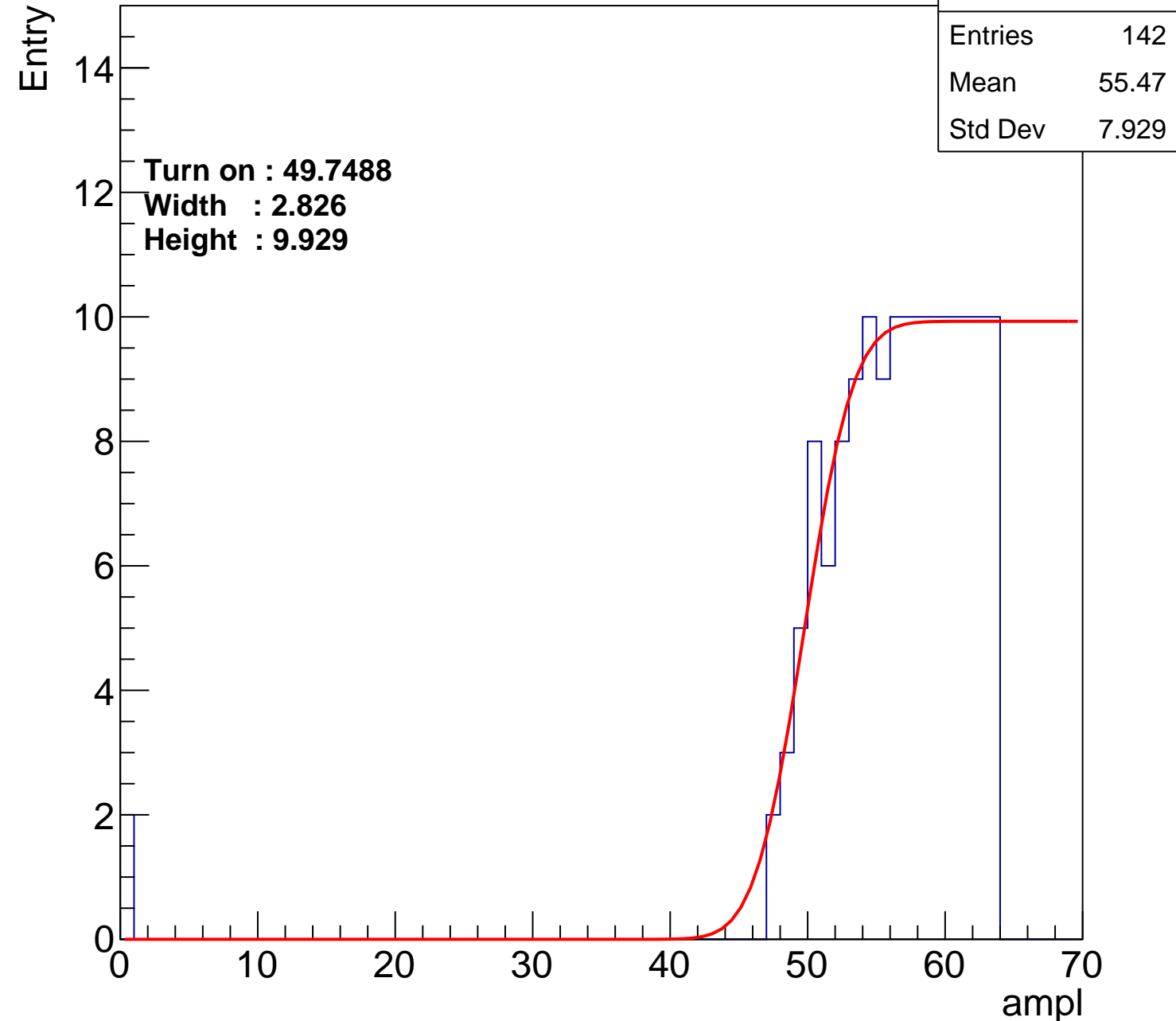
Width : 2.826

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch35

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	55.19
Std Dev	10.66

Turn on : 52.4213

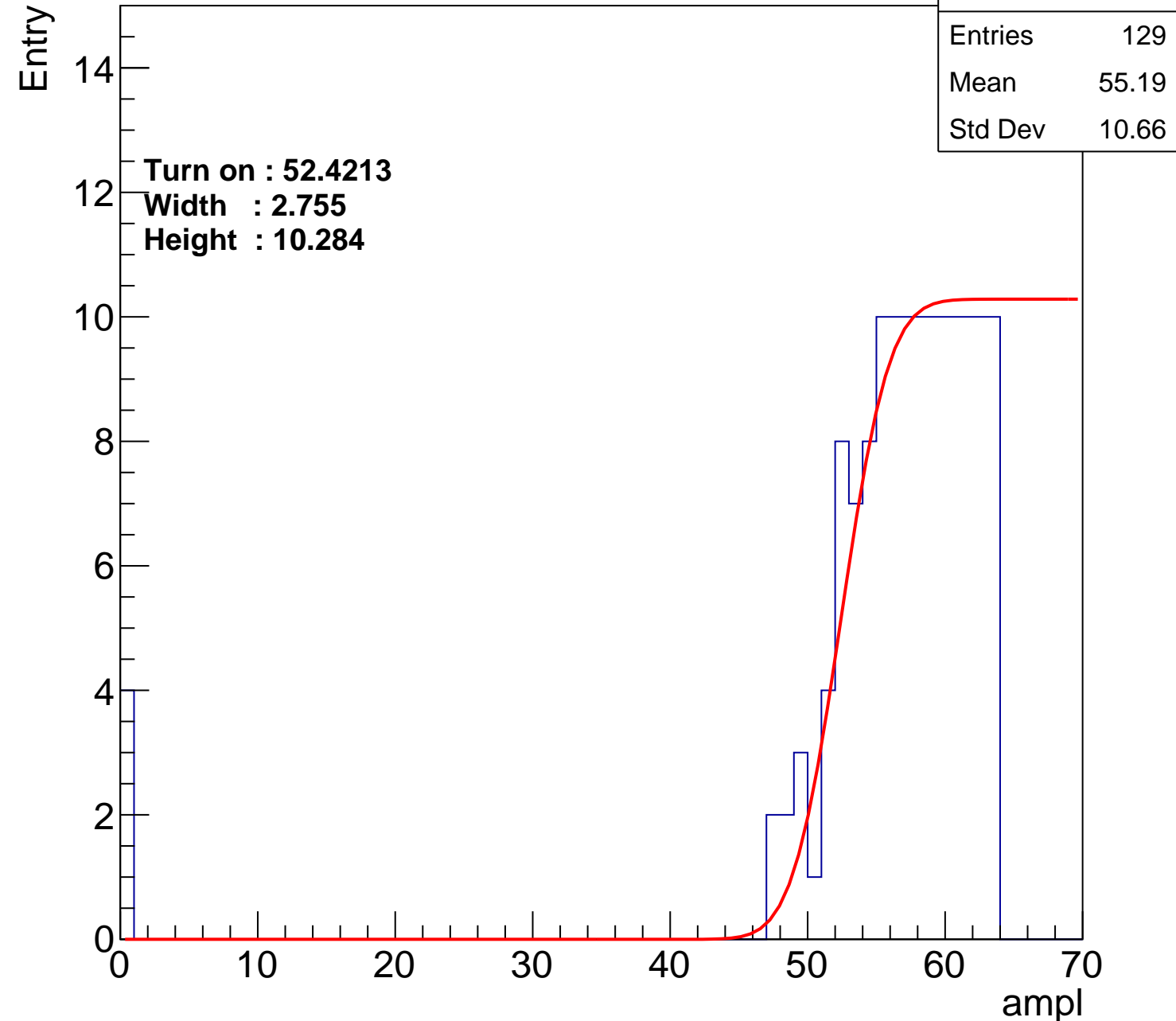
Width : 2.755

Height : 10.284

Entry

14
12
10
8
6
4
2
0

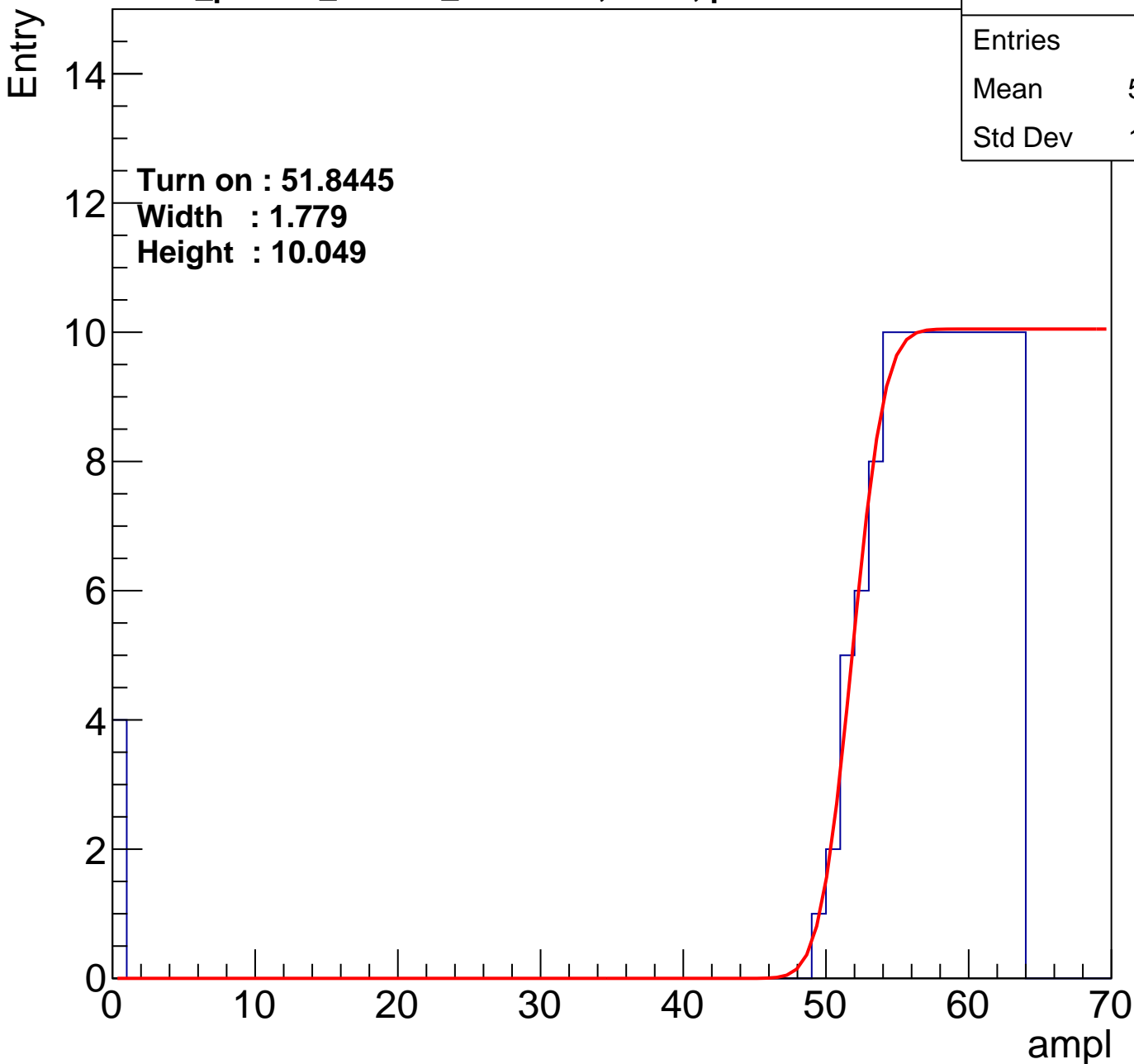
ampl



calib_packv5_040323_1717.root, FC#2, port C3

calib_packv5_040323_1717.root, FC#2, port C3

Turn on : 51.8445
Width : 1.779
Height : 10.049



B0L103S, U21-ch37

calib_packv5_040323_1717.root, FC#2, port C3

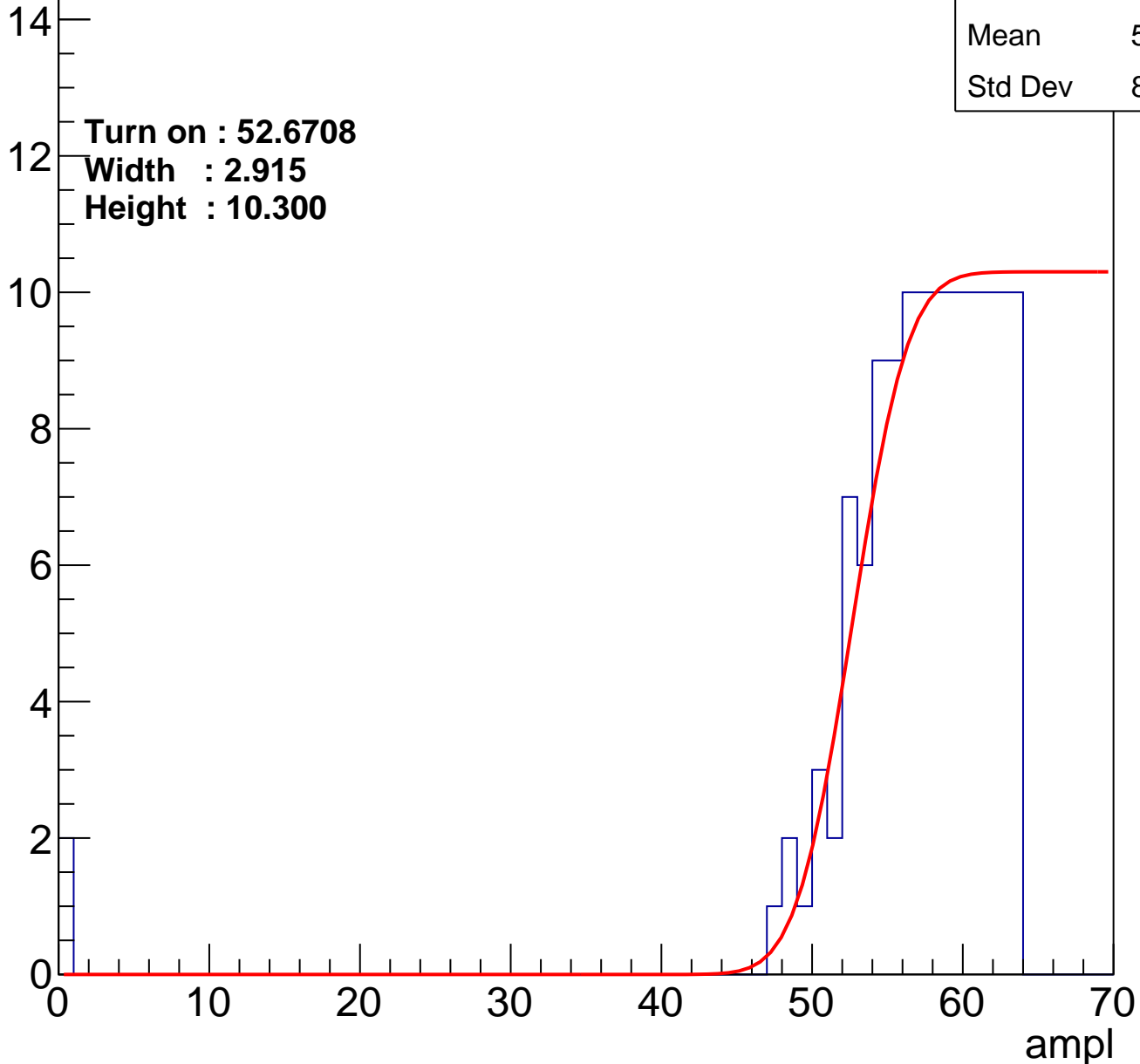
Entries	122
Mean	56.29
Std Dev	8.243

Turn on : 52.6708

Width : 2.915

Height : 10.300

Entry



B0L103S, U21-ch38

calib_packv5_040323_1717.root, FC#2, port C3

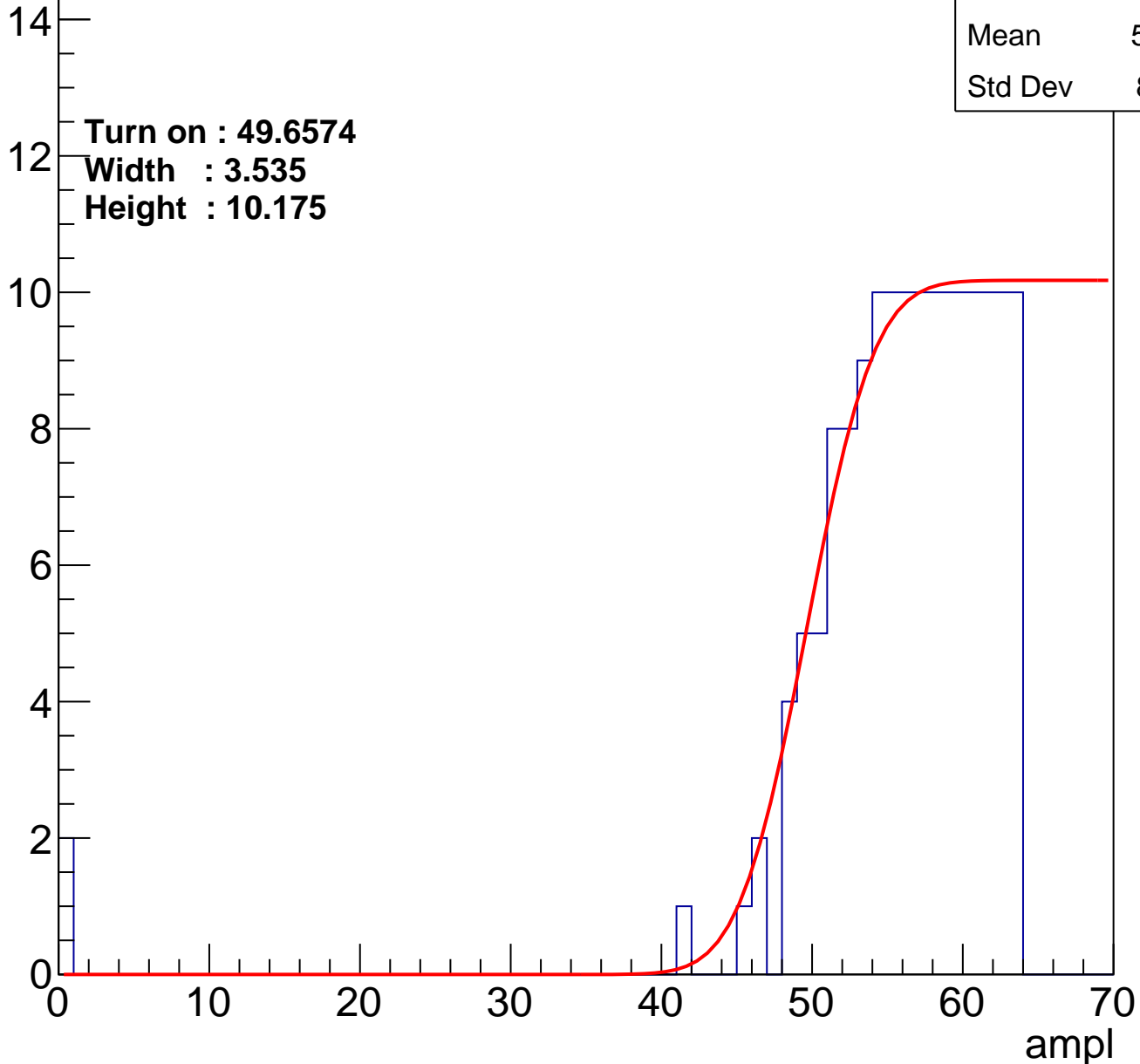
Entries	145
Mean	55.28
Std Dev	8.001

Turn on : 49.6574

Width : 3.535

Height : 10.175

Entry



B0L103S, U21-ch39

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.34
Std Dev	6.534

Turn on : 51.7711

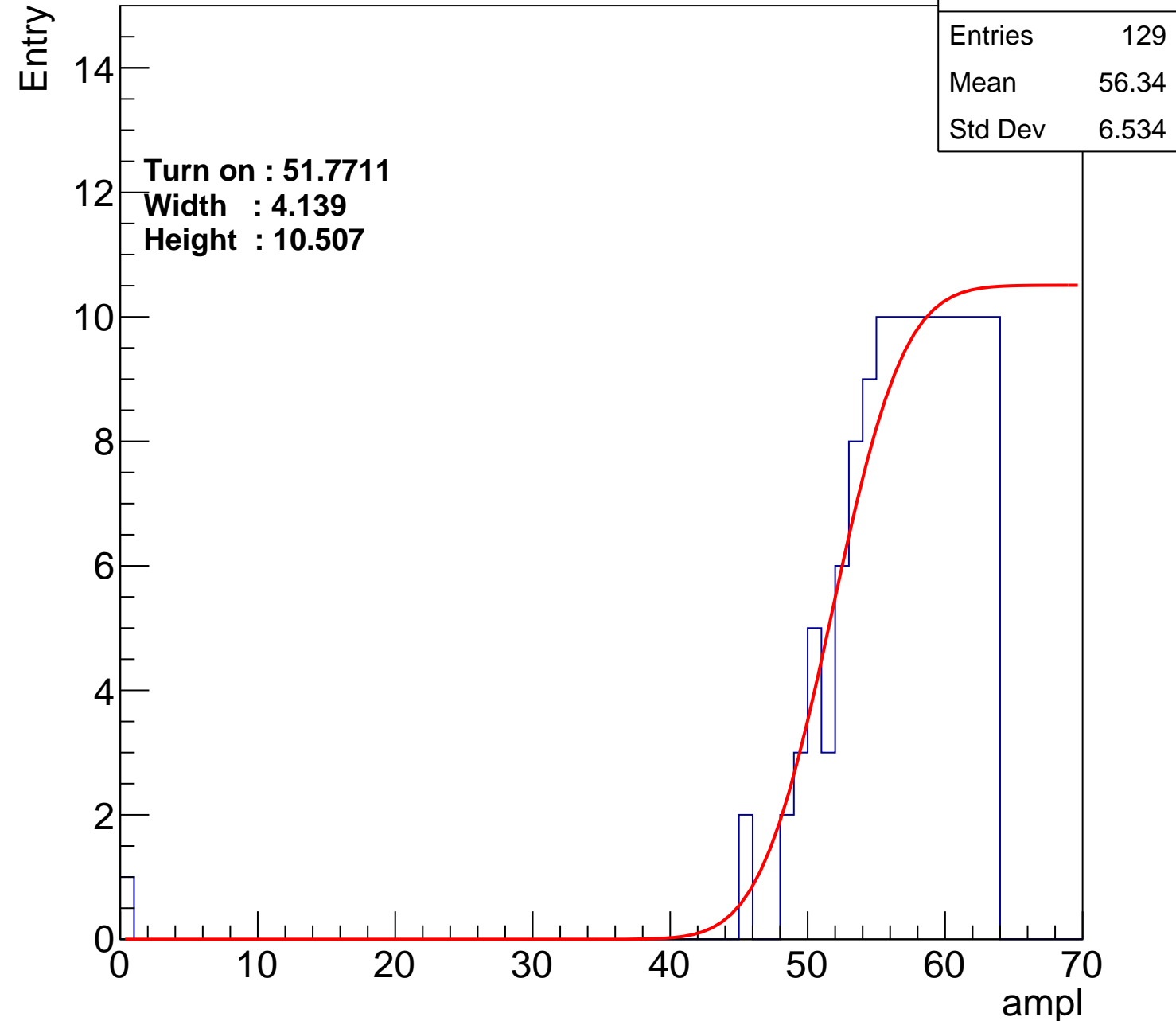
Width : 4.139

Height : 10.507

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch40

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	54.95
Std Dev	10.31

Turn on : 50.5927

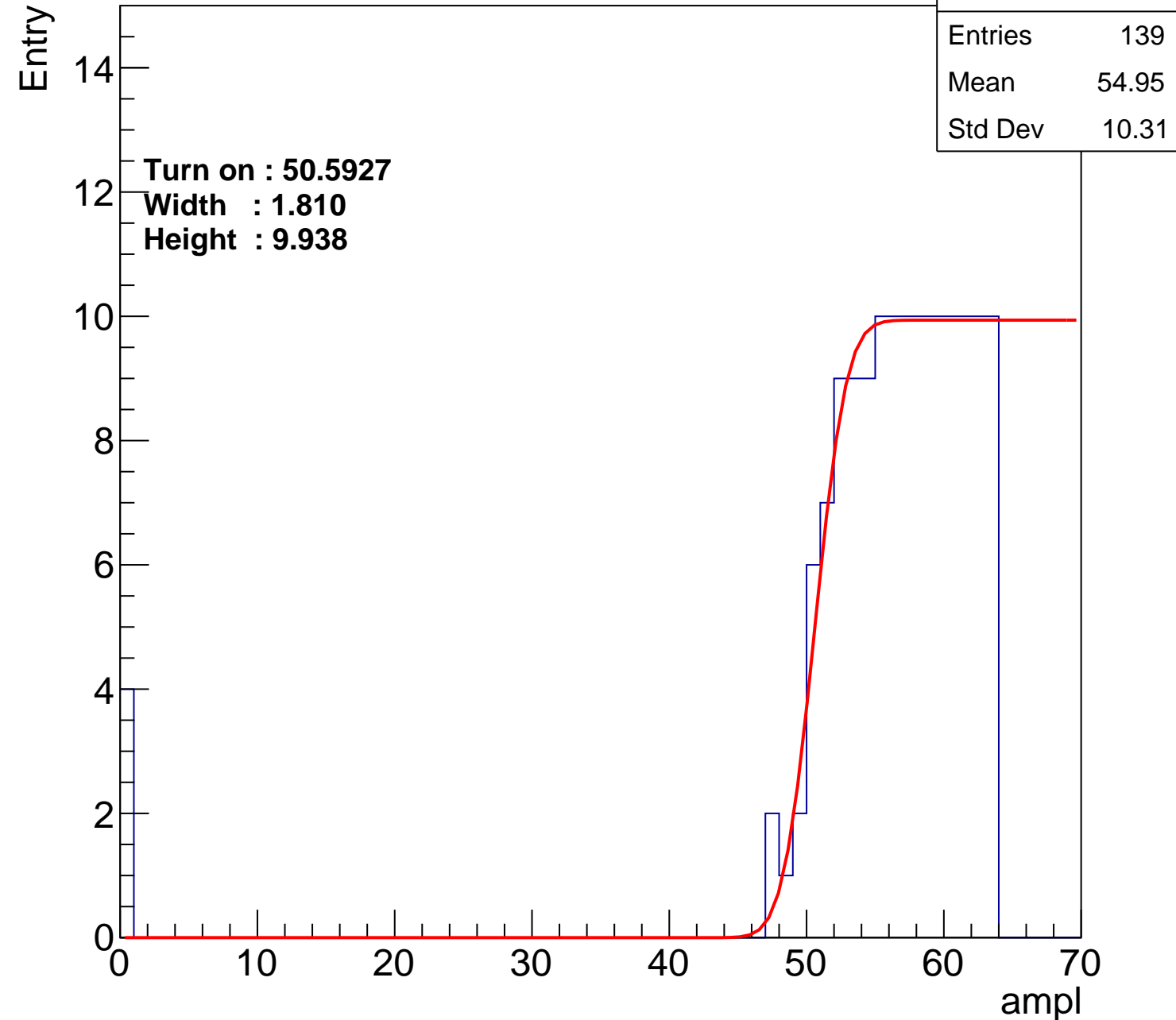
Width : 1.810

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch41

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	56.1
Std Dev	8.13

Turn on : 50.4160

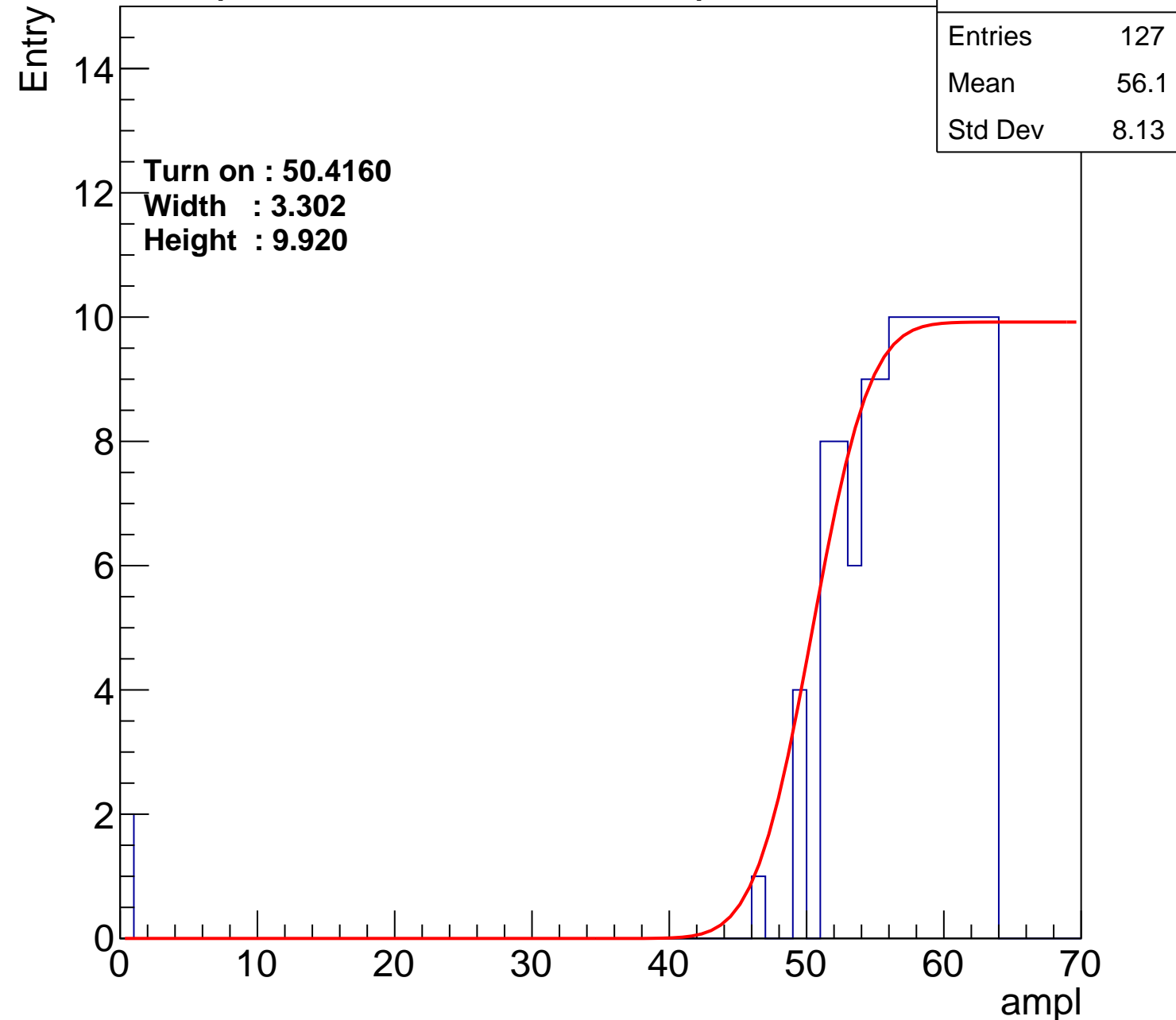
Width : 3.302

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch42

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	55.25
Std Dev	10.77

Turn on : 51.7002

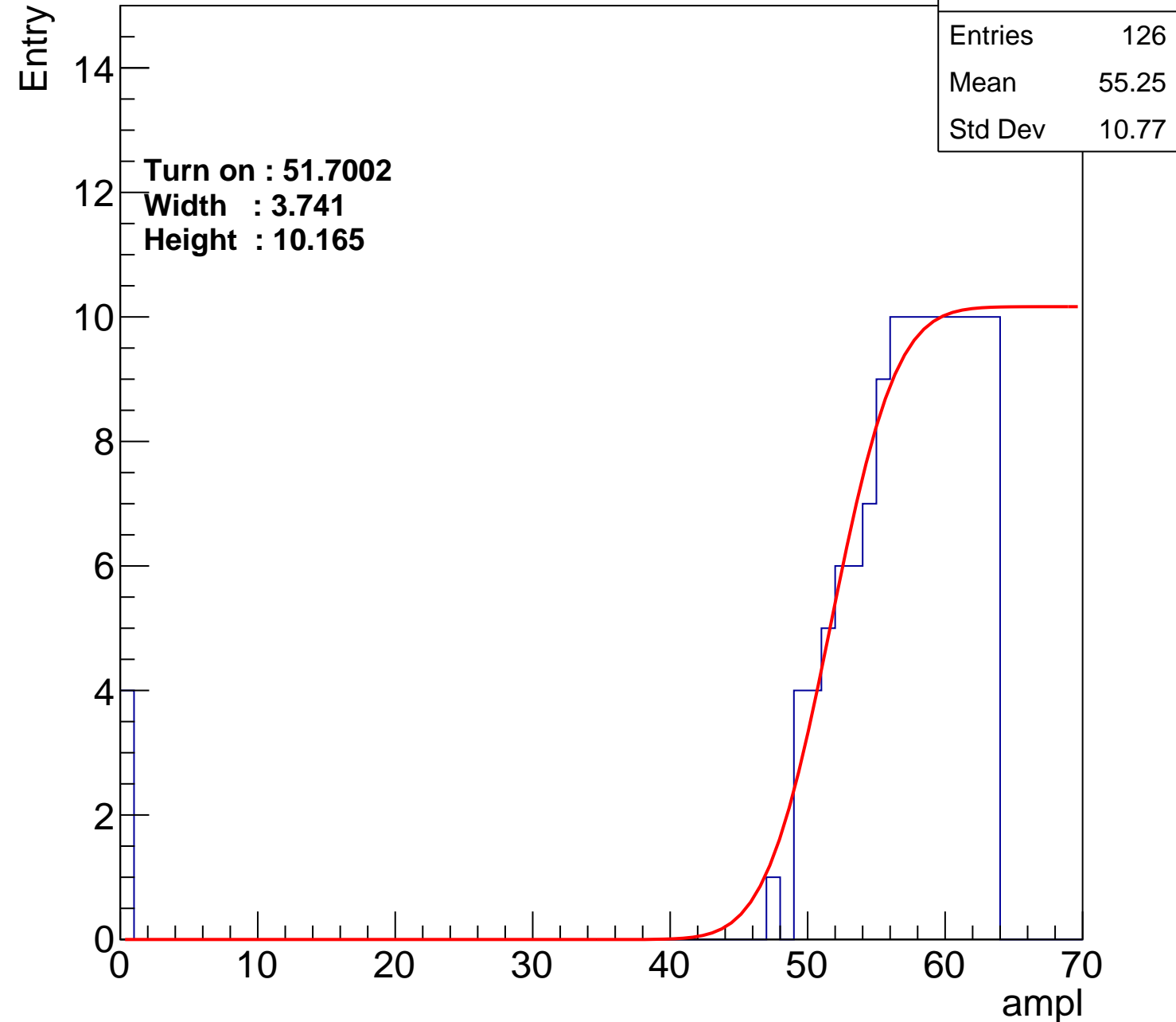
Width : 3.741

Height : 10.165

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch43

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	55.57
Std Dev	9.59

Turn on : 51.5670

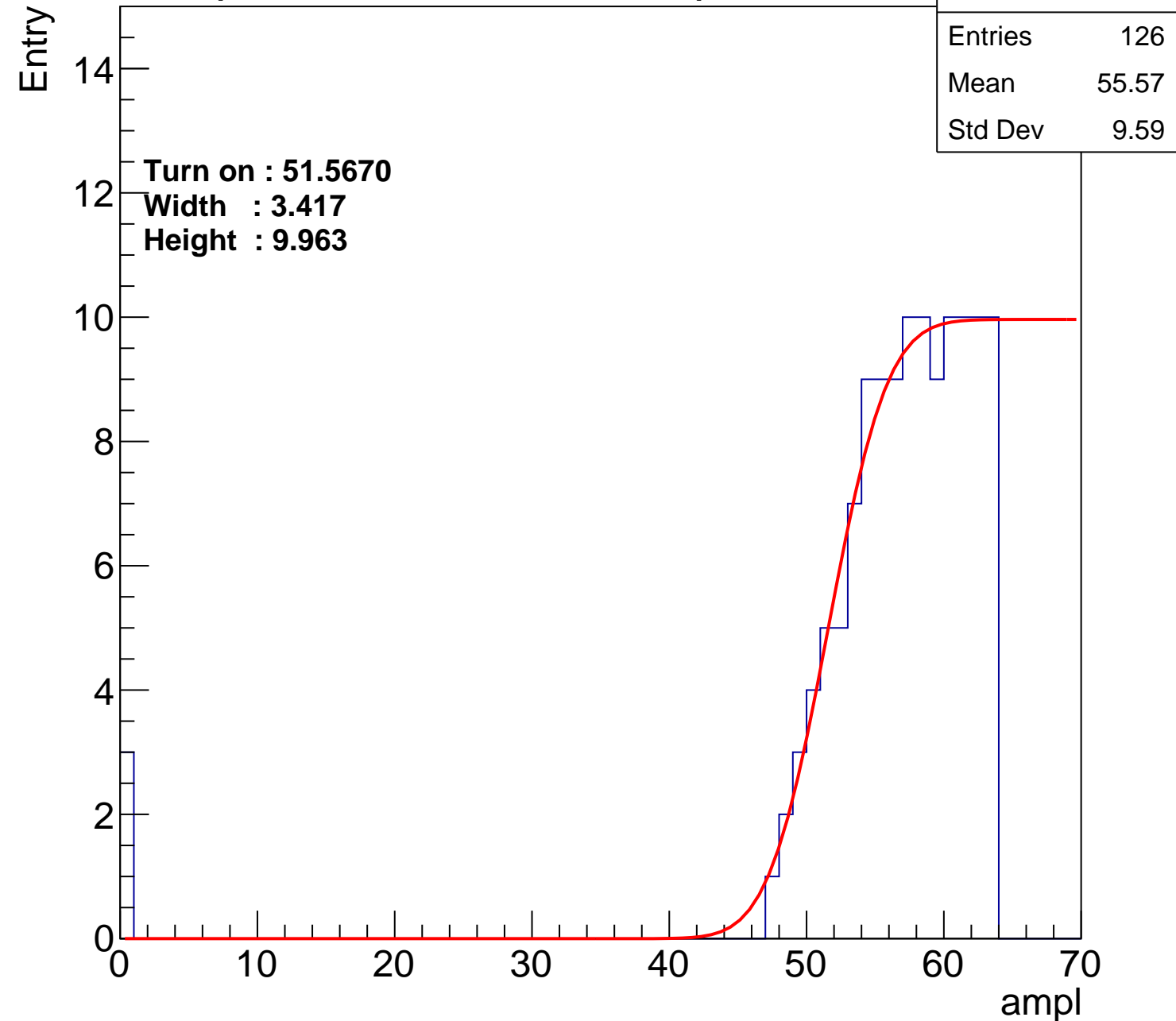
Width : 3.417

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch44

calib_packv5_040323_1717.root, FC#2, port C3

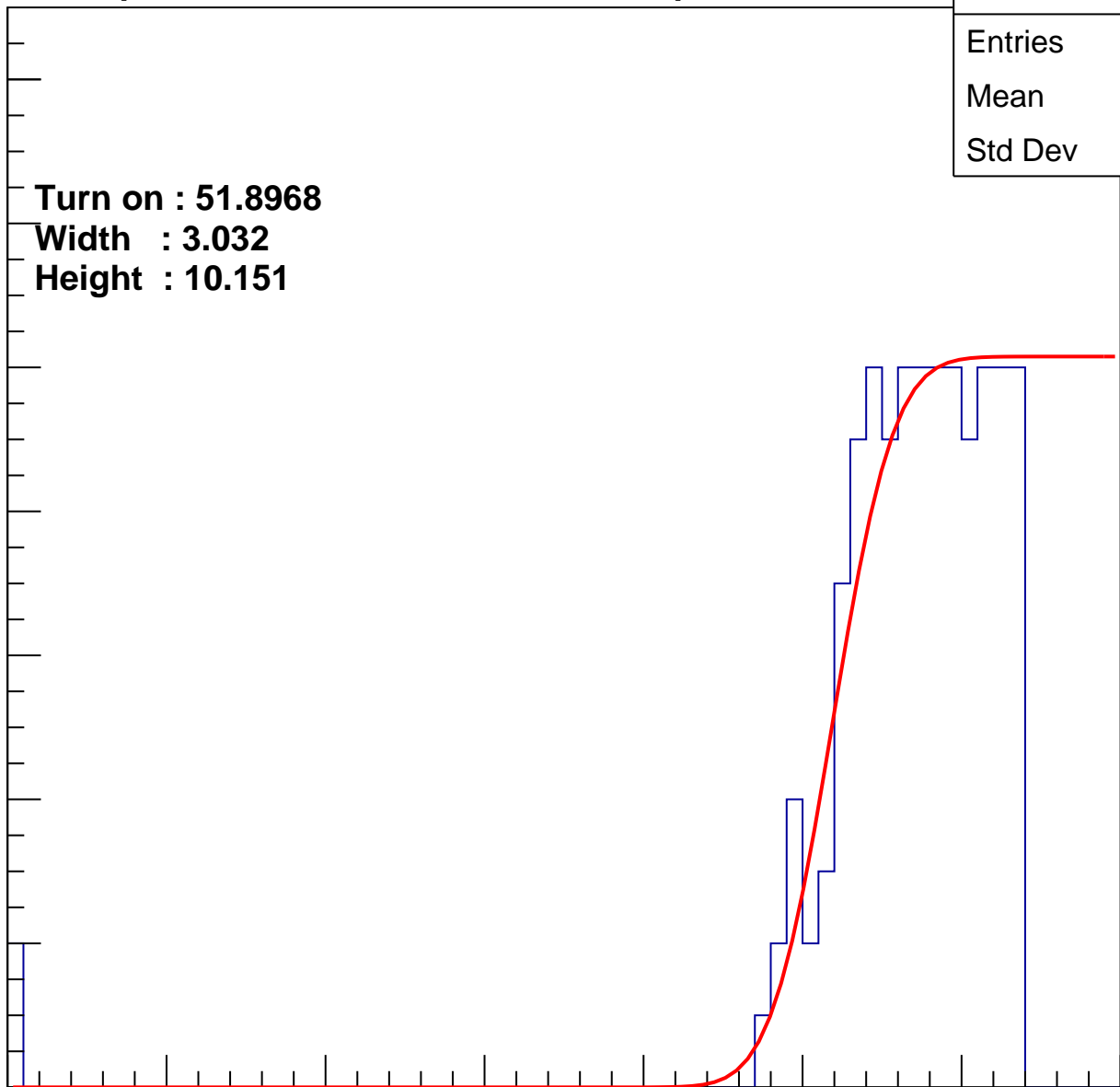
Entry

14
12
10
8
6
4
2
0

Turn on : 51.8968
Width : 3.032
Height : 10.151

Entries	128
Mean	56
Std Dev	8.125

ampl



B0L103S, U21-ch45

calib_packv5_040323_1717.root, FC#2, port C3

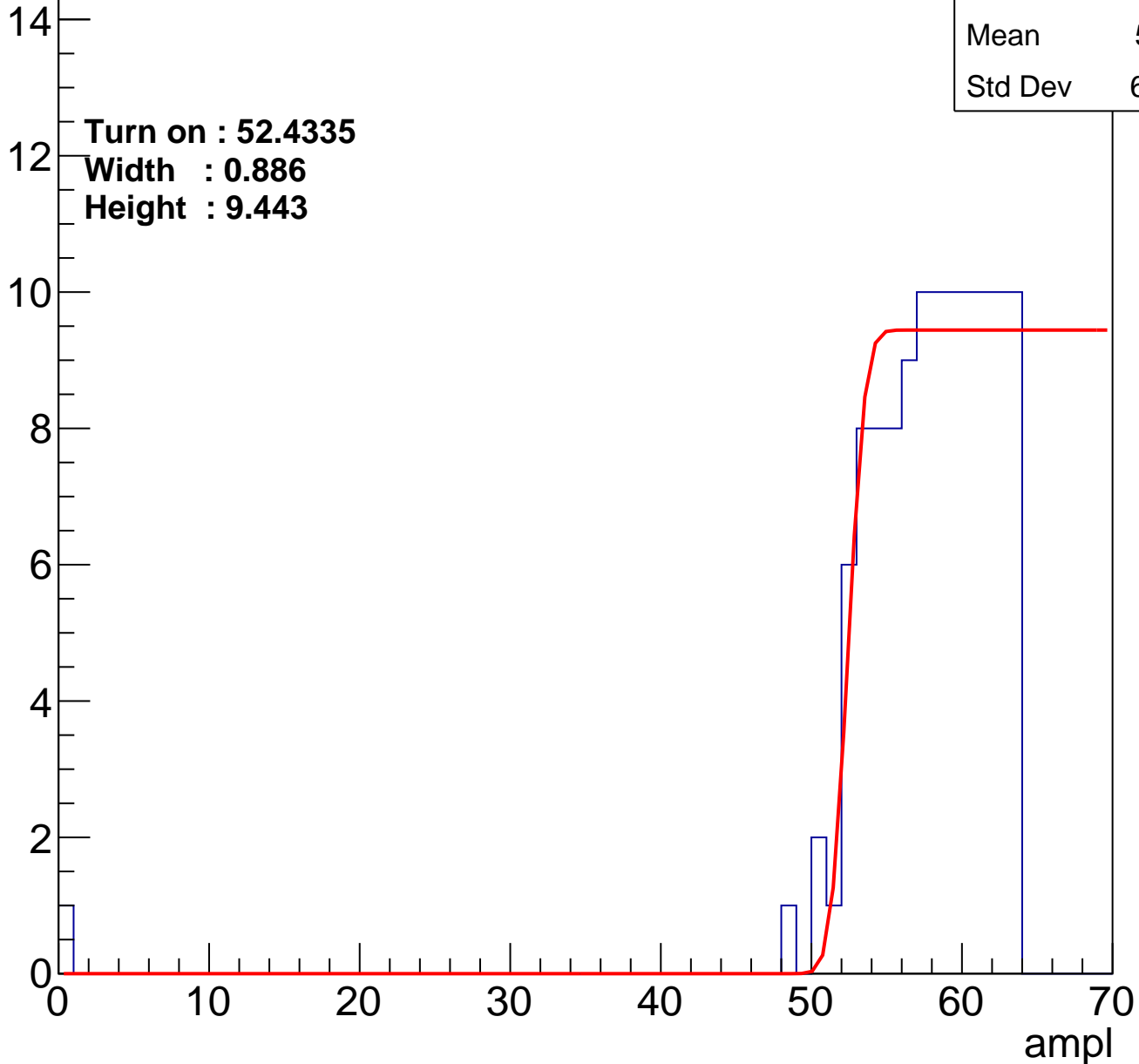
Entries	114
Mean	57.11
Std Dev	6.464

Turn on : 52.4335

Width : 0.886

Height : 9.443

Entry



B0L103S, U21-ch46

calib_packv5_040323_1717.root, FC#2, port C3

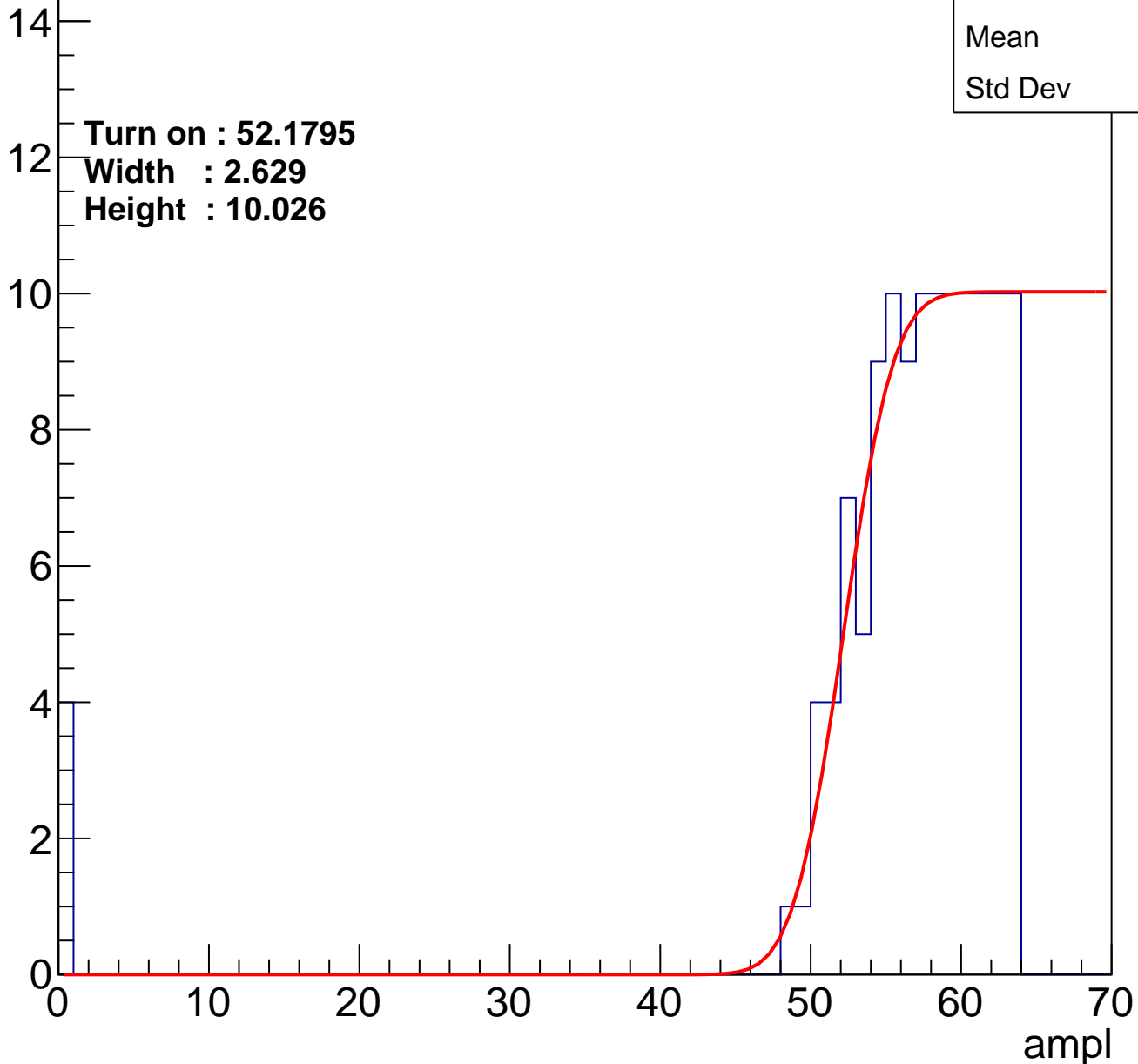
Entries	124
Mean	55.4
Std Dev	10.8

Turn on : 52.1795

Width : 2.629

Height : 10.026

Entry



B0L103S, U21-ch47

calib_packv5_040323_1717.root, FC#2, port C3

Entries	108
Mean	56.74
Std Dev	8.579

Turn on : 53.4924

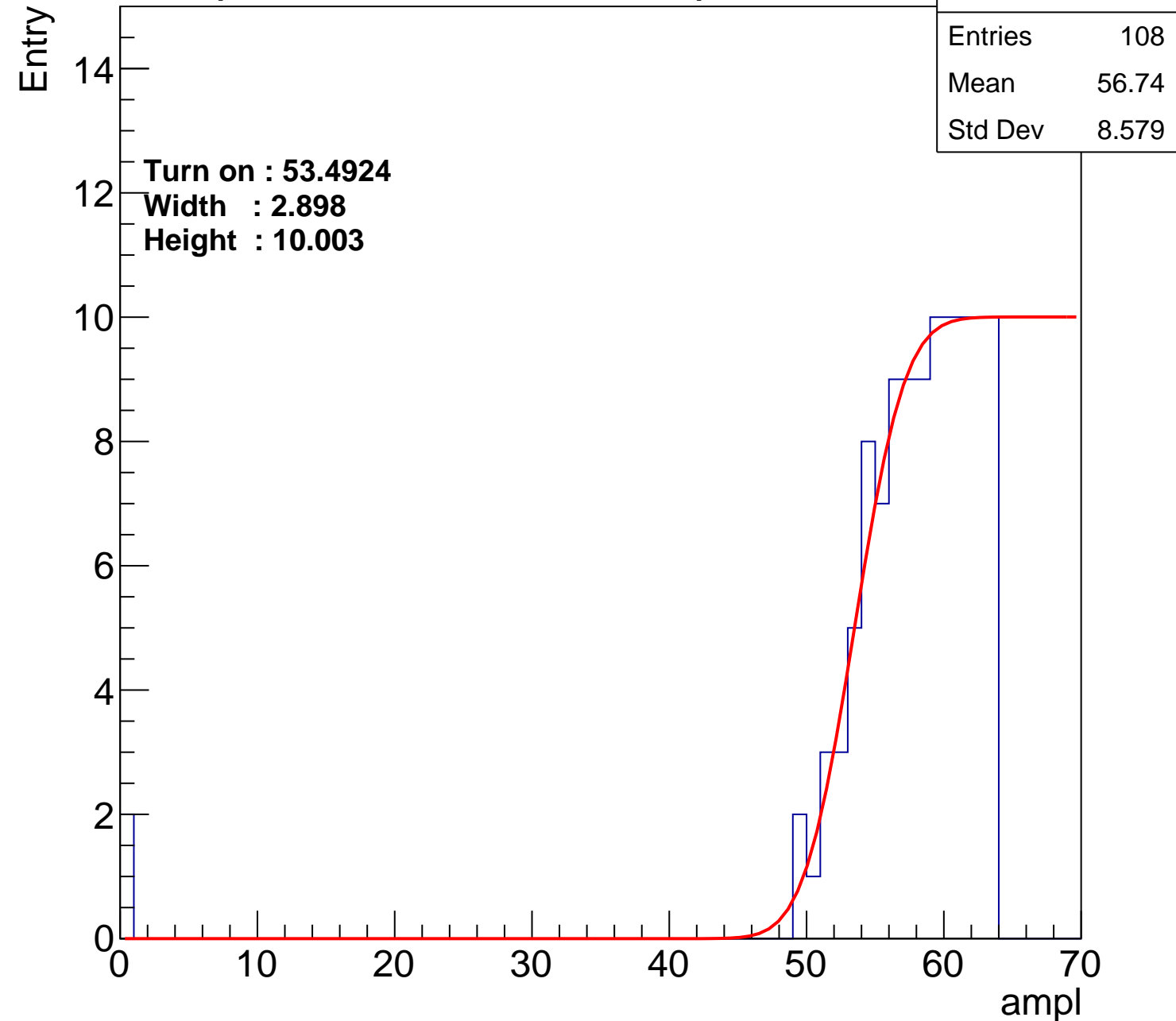
Width : 2.898

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch48

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	56.23
Std Dev	8.344

Turn on : 52.4688

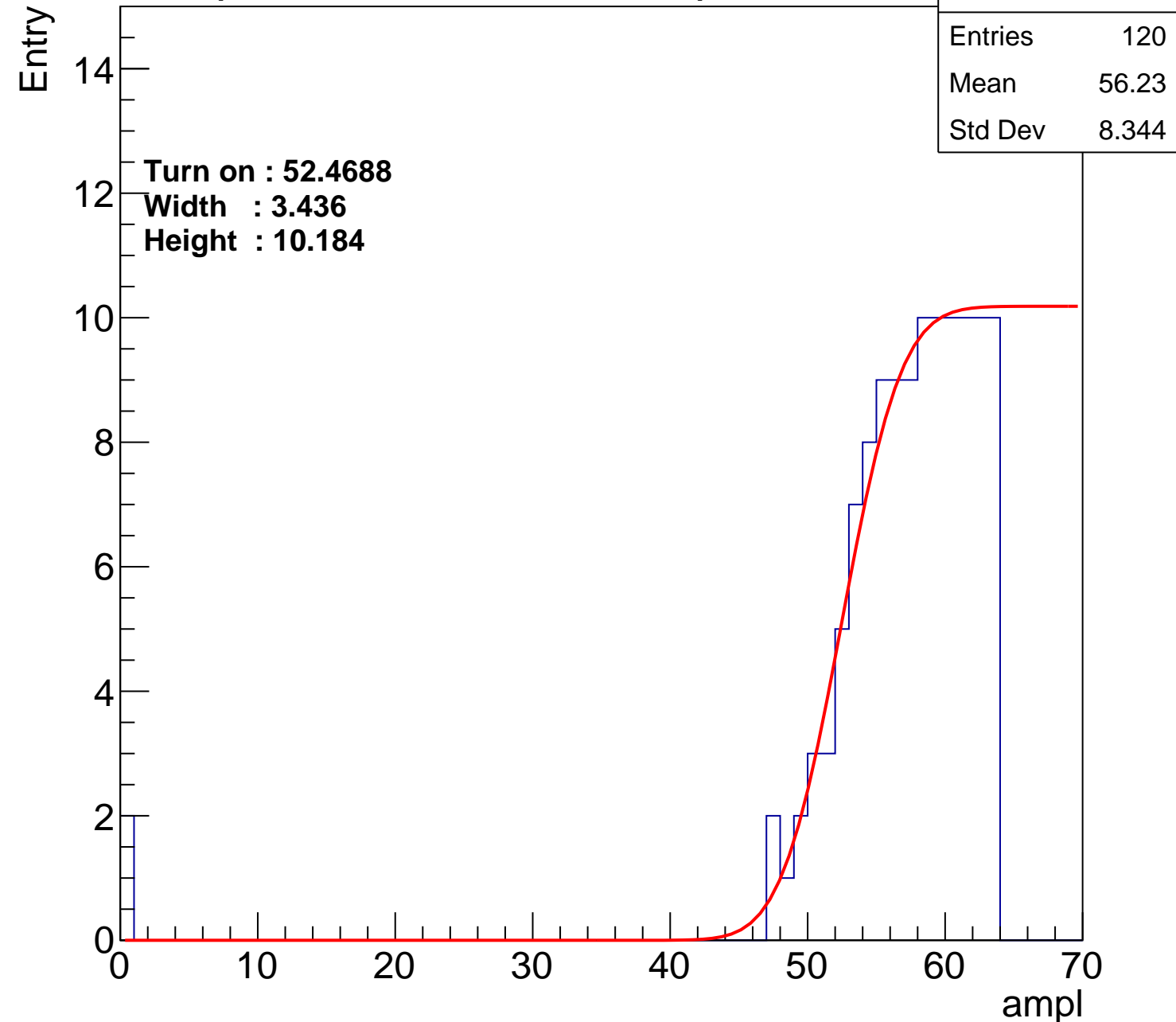
Width : 3.436

Height : 10.184

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch49

calib_packv5_040323_1717.root, FC#2, port C3

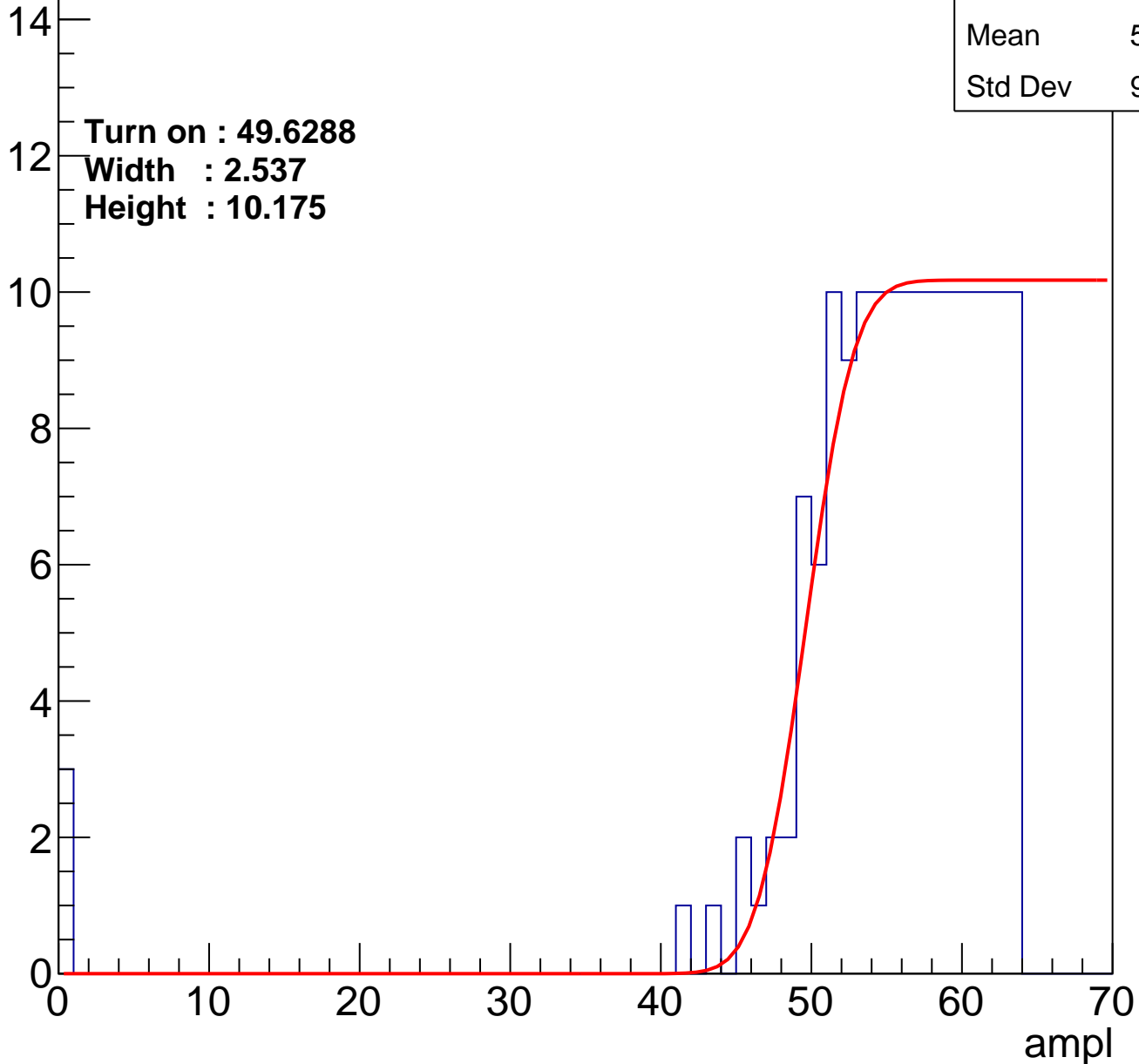
Entries	154
Mean	54.62
Std Dev	9.057

Turn on : 49.6288

Width : 2.537

Height : 10.175

Entry



B0L103S, U21-ch50

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	54.69
Std Dev	10.35

Turn on : 50.3926

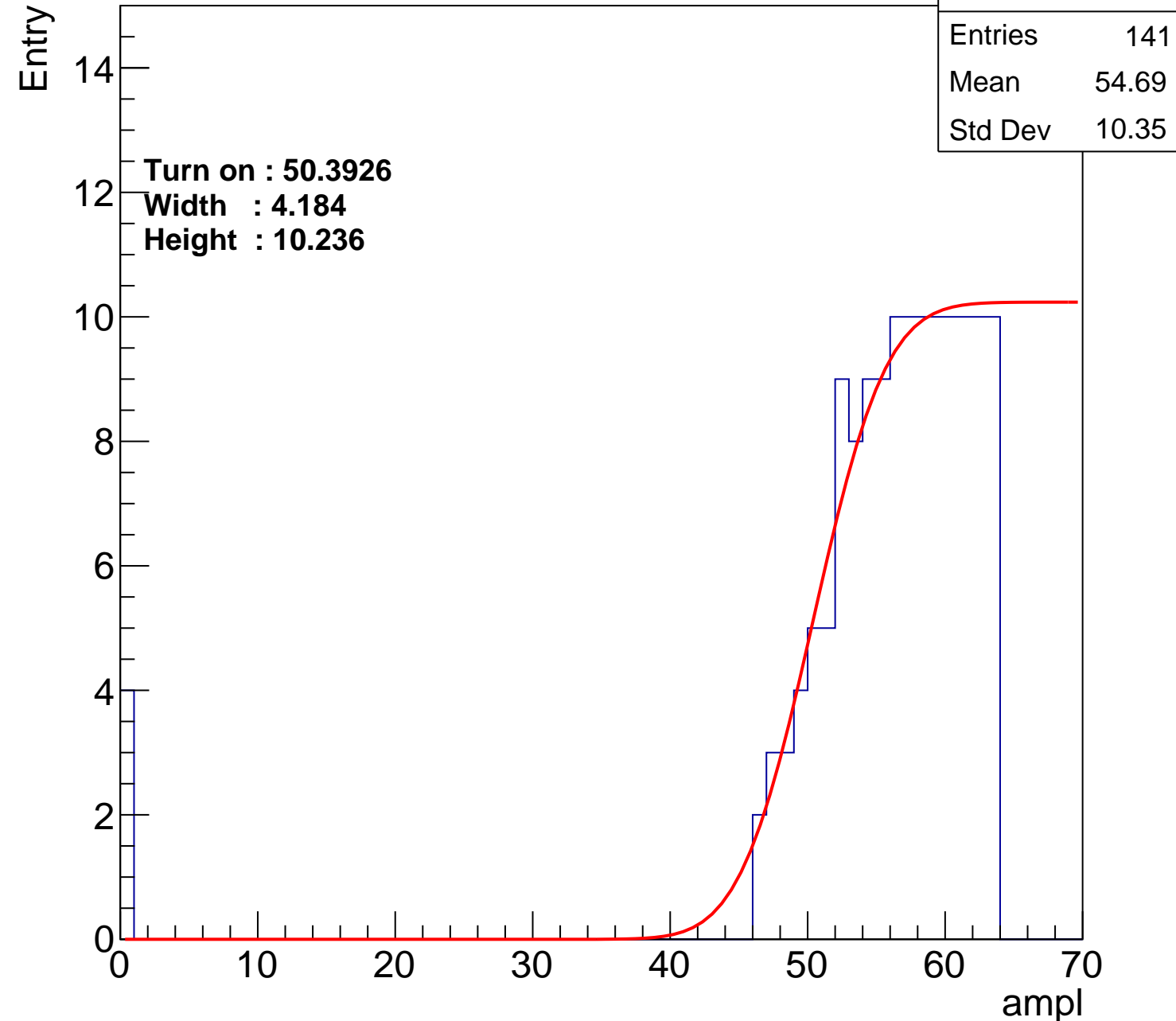
Width : 4.184

Height : 10.236

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch51

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
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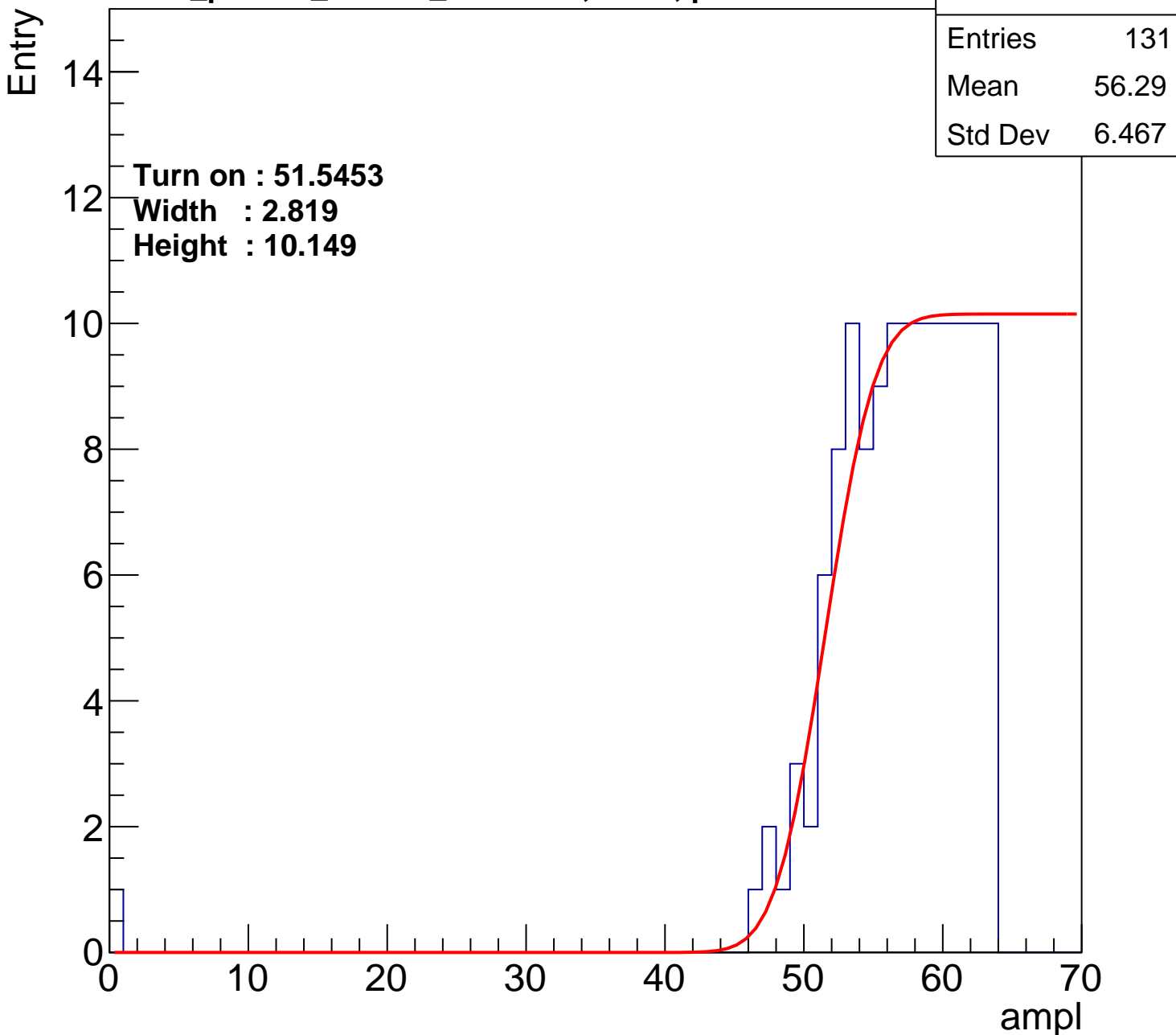
Mean	56.29
------	-------

Std Dev	6.467
---------	-------

Turn on : 51.5453

Width : 2.819

Height : 10.149



B0L103S, U21-ch52

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.98
Std Dev	8.078

Turn on : 51.1702

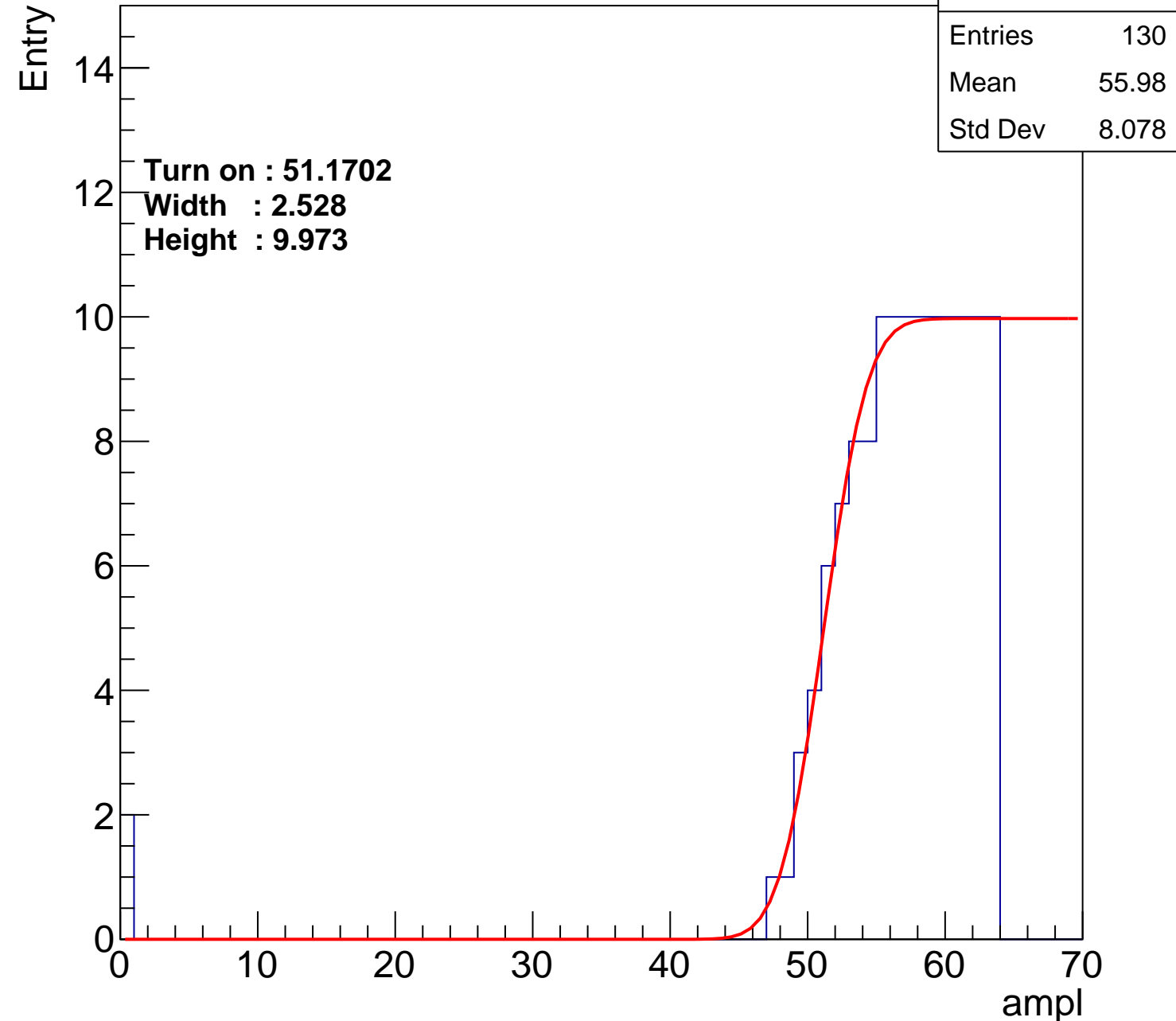
Width : 2.528

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch53

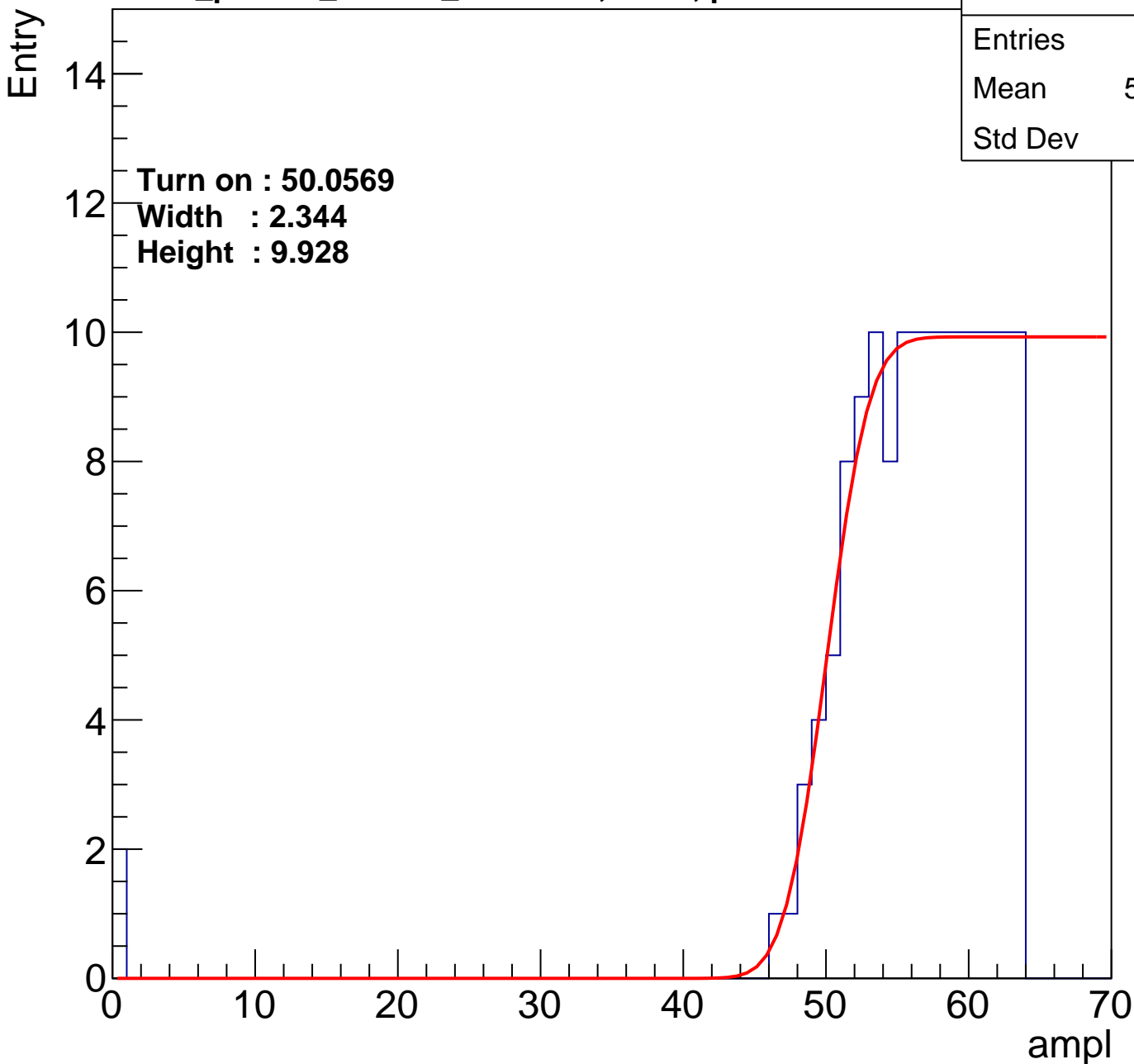
calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.54
Std Dev	7.93

Turn on : 50.0569

Width : 2.344

Height : 9.928



B0L103S, U21-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	55.98
Std Dev	8.092

Turn on : 51.3784

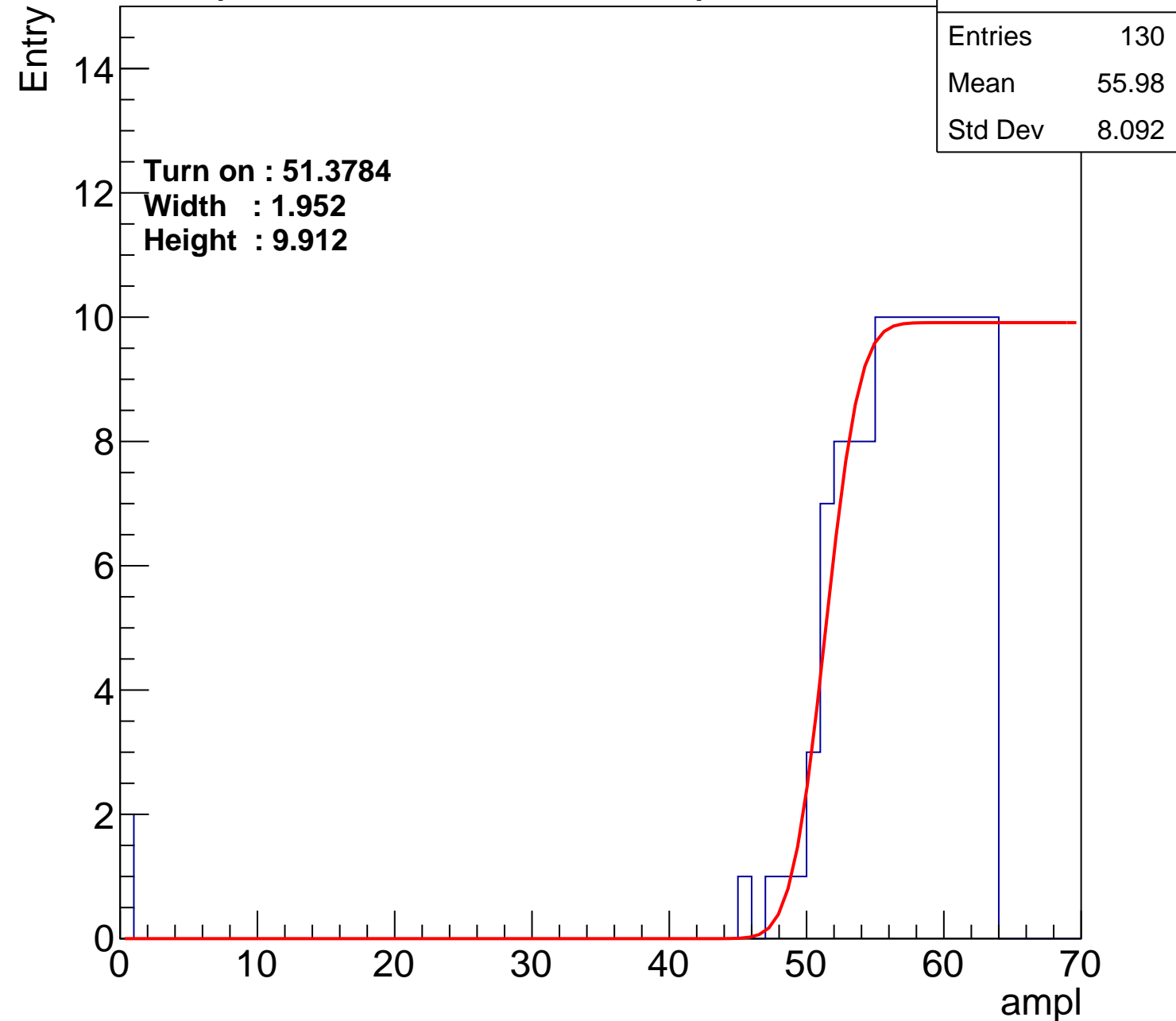
Width : 1.952

Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch55

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.08
Std Dev	11.25

Turn on : 50.3098

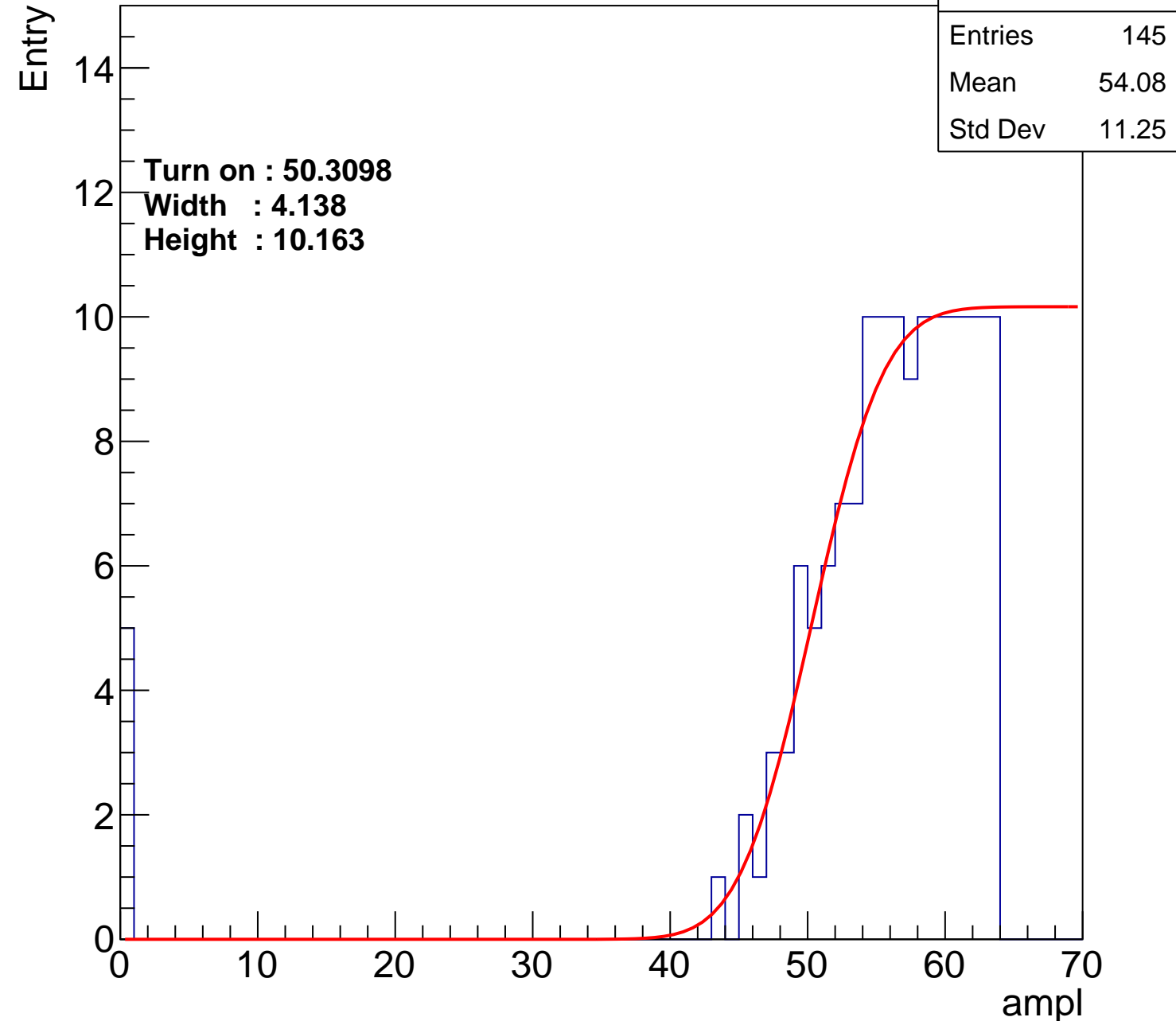
Width : 4.138

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch56

calib_packv5_040323_1717.root, FC#2, port C3

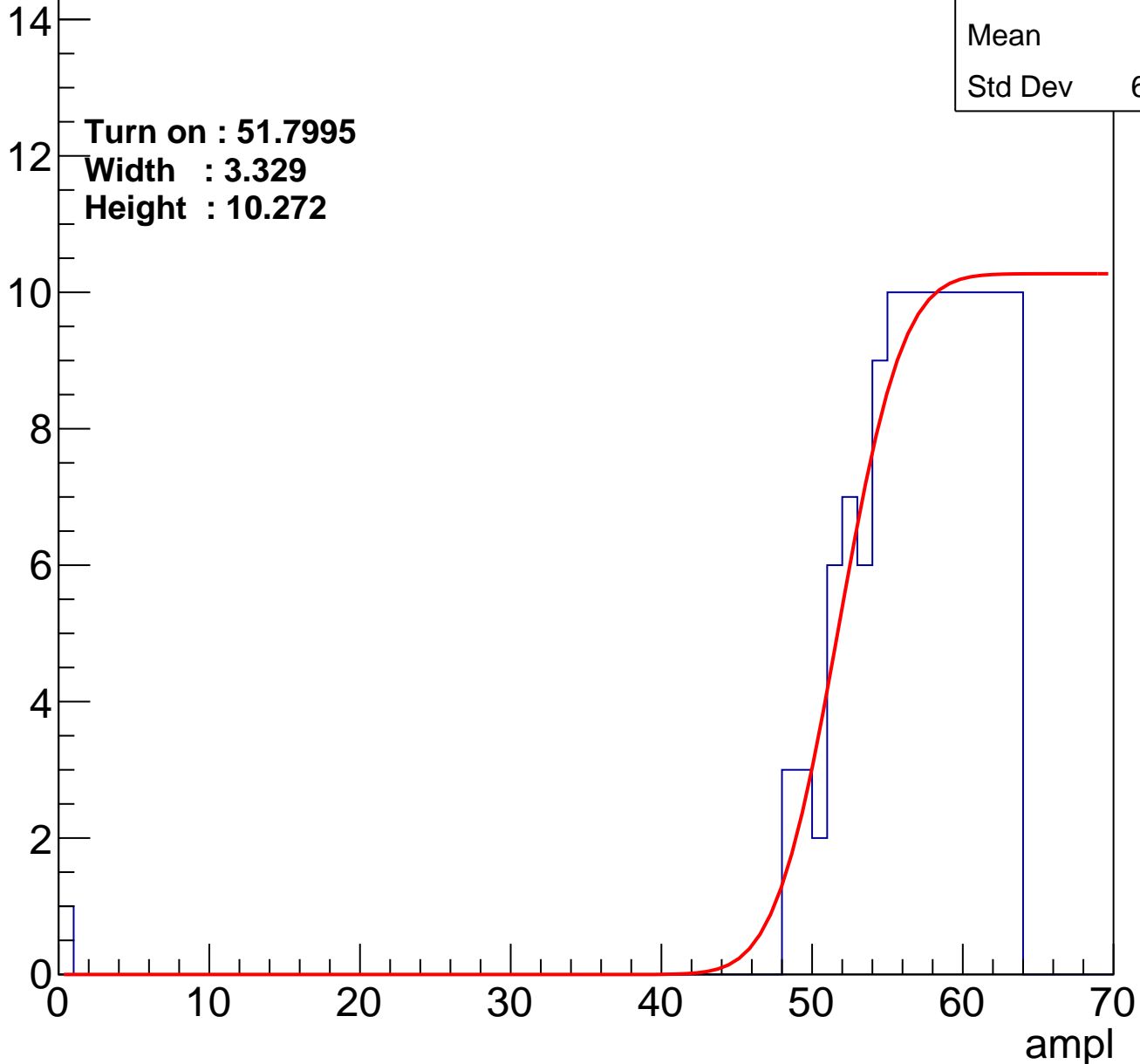
Entries	127
Mean	56.5
Std Dev	6.446

Turn on : 51.7995

Width : 3.329

Height : 10.272

Entry



B0L103S, U21-ch57

calib_packv5_040323_1717.root, FC#2, port C3

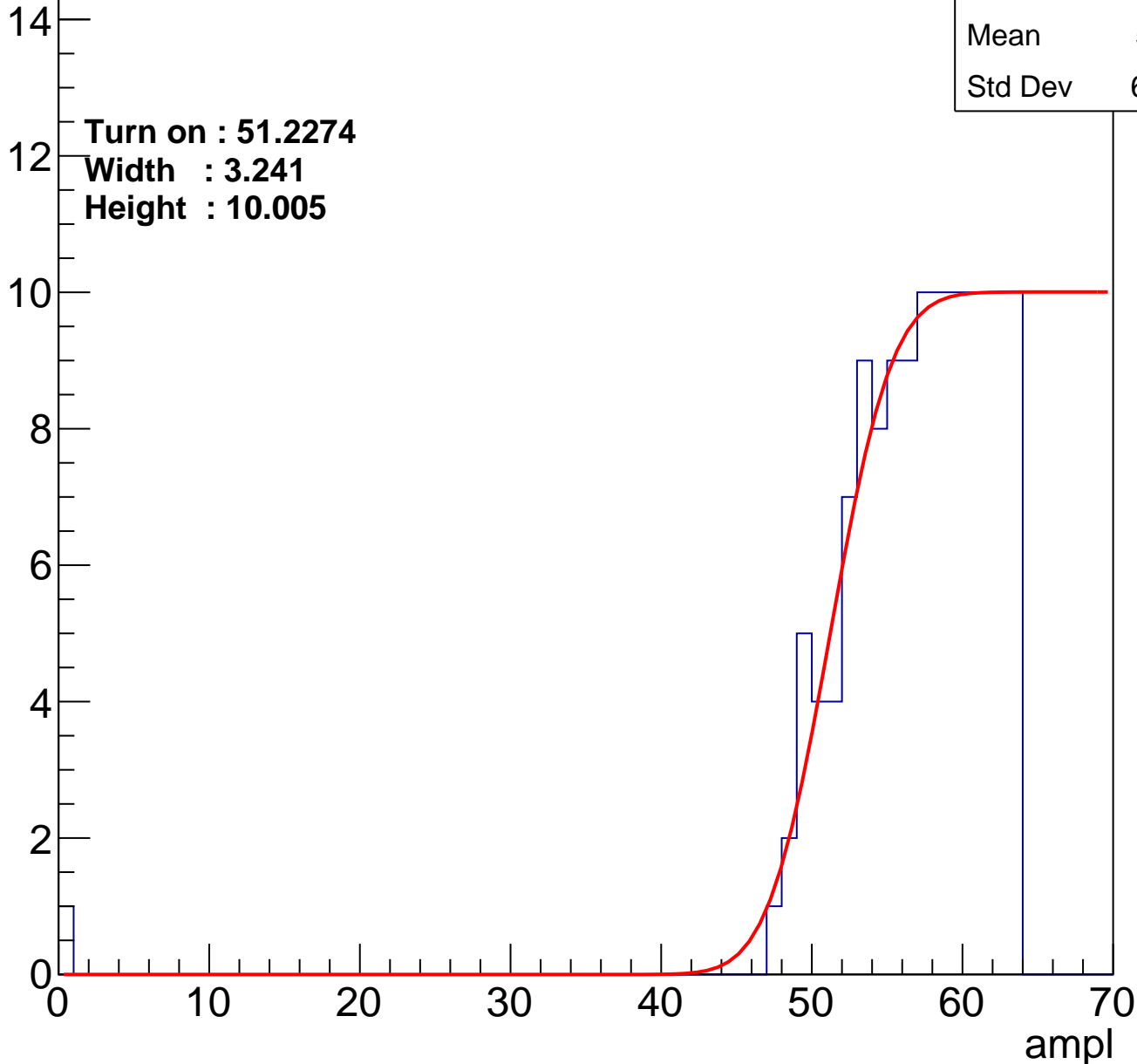
Entries	129
Mean	56.31
Std Dev	6.503

Turn on : 51.2274

Width : 3.241

Height : 10.005

Entry



B0L103S, U21-ch58

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.34
Std Dev	7.998

Turn on : 49.8928

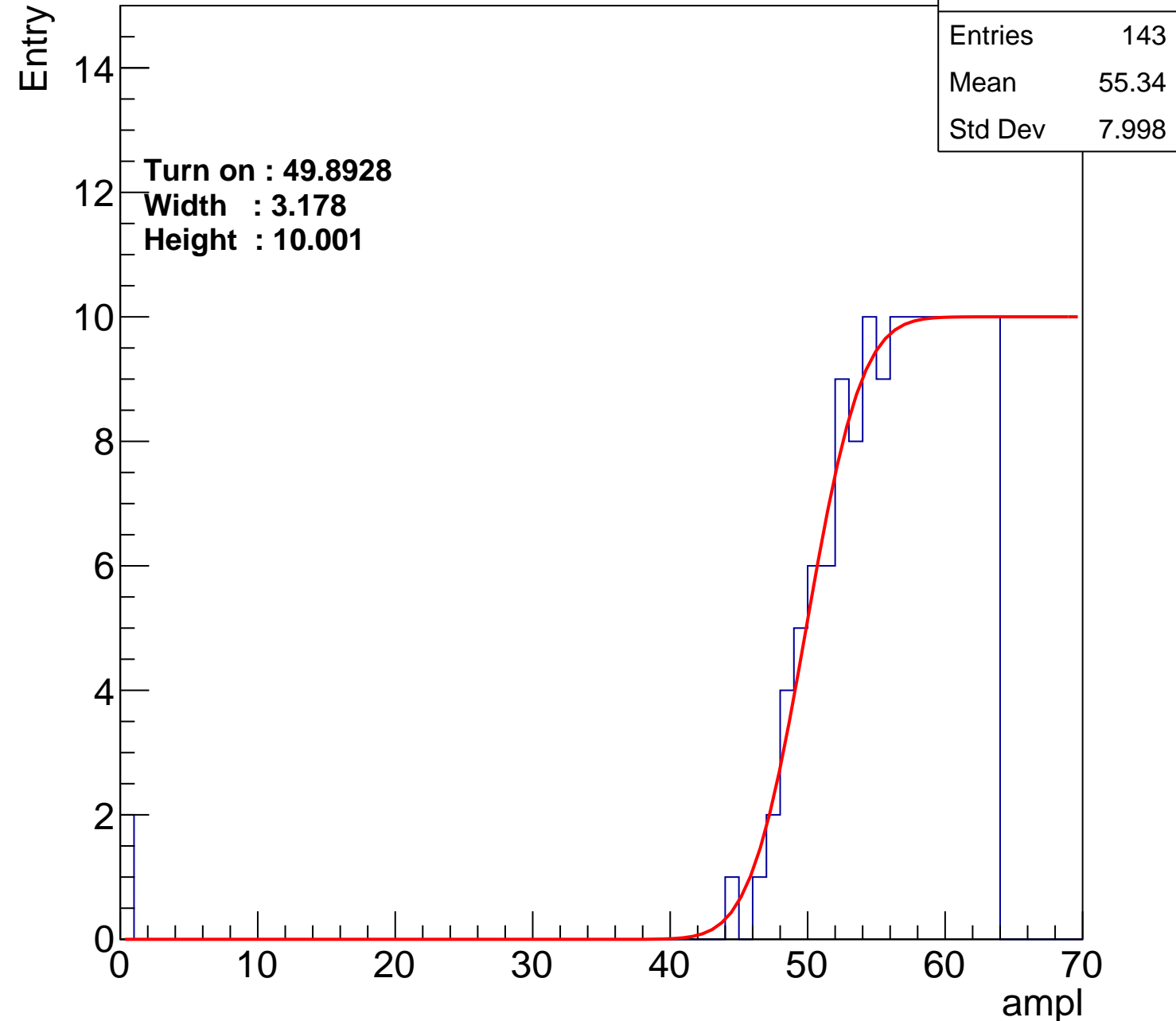
Width : 3.178

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch59

calib_packv5_040323_1717.root, FC#2, port C3

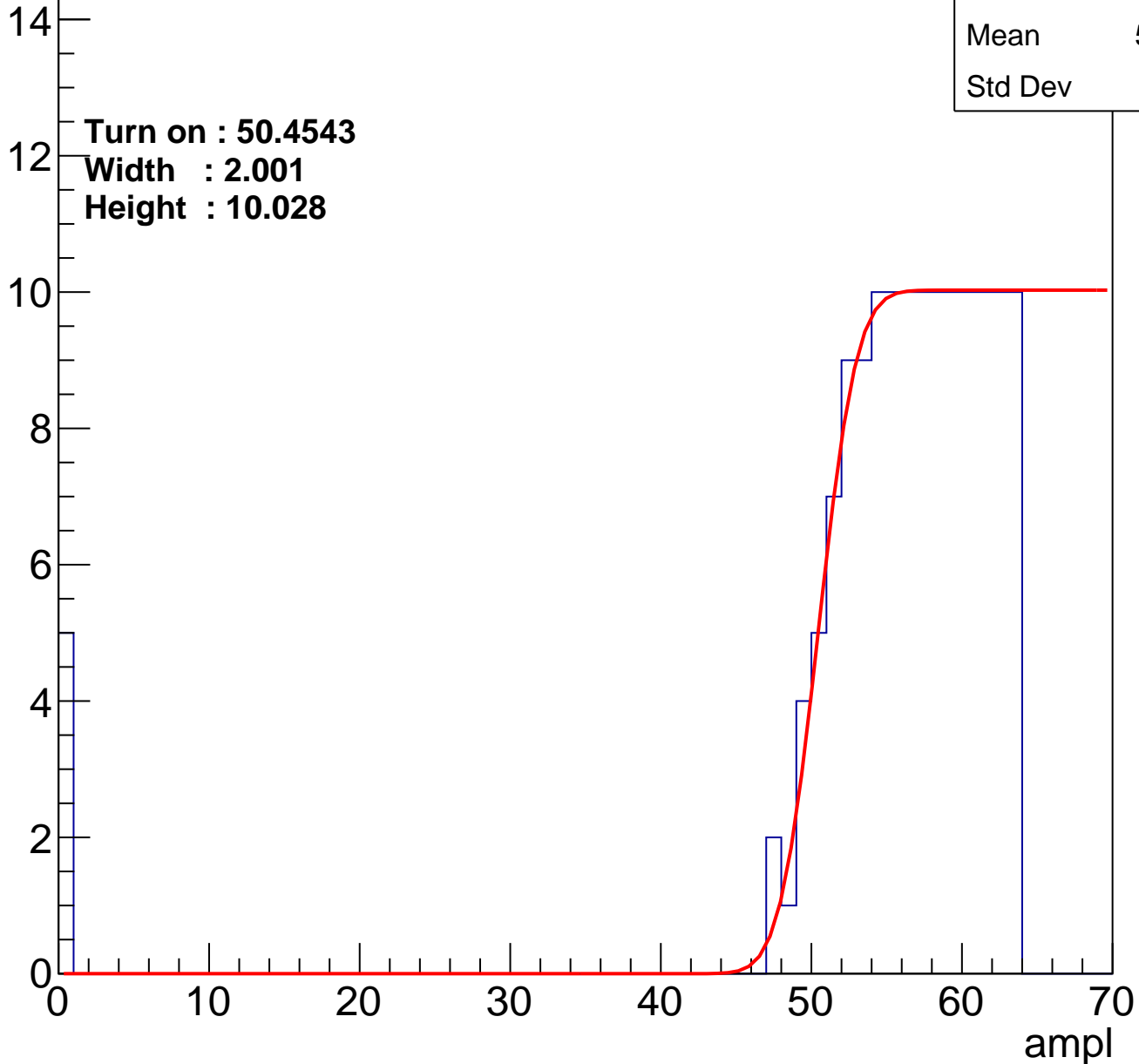
Entries	142
Mean	54.51
Std Dev	11.2

Turn on : 50.4543

Width : 2.001

Height : 10.028

Entry



B0L103S, U21-ch60

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.03
Std Dev	10.54

Turn on : 50.8672

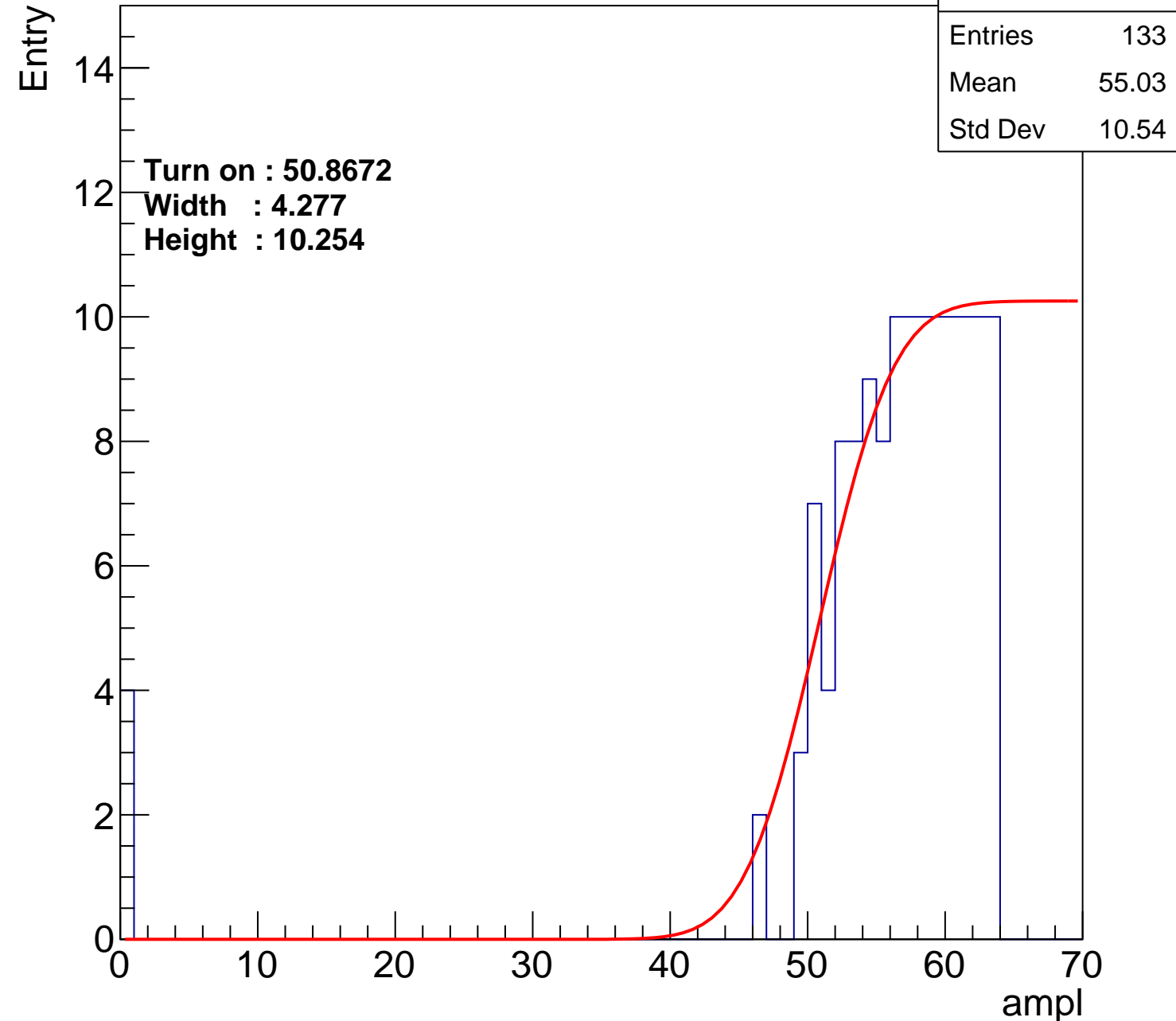
Width : 4.277

Height : 10.254

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch61

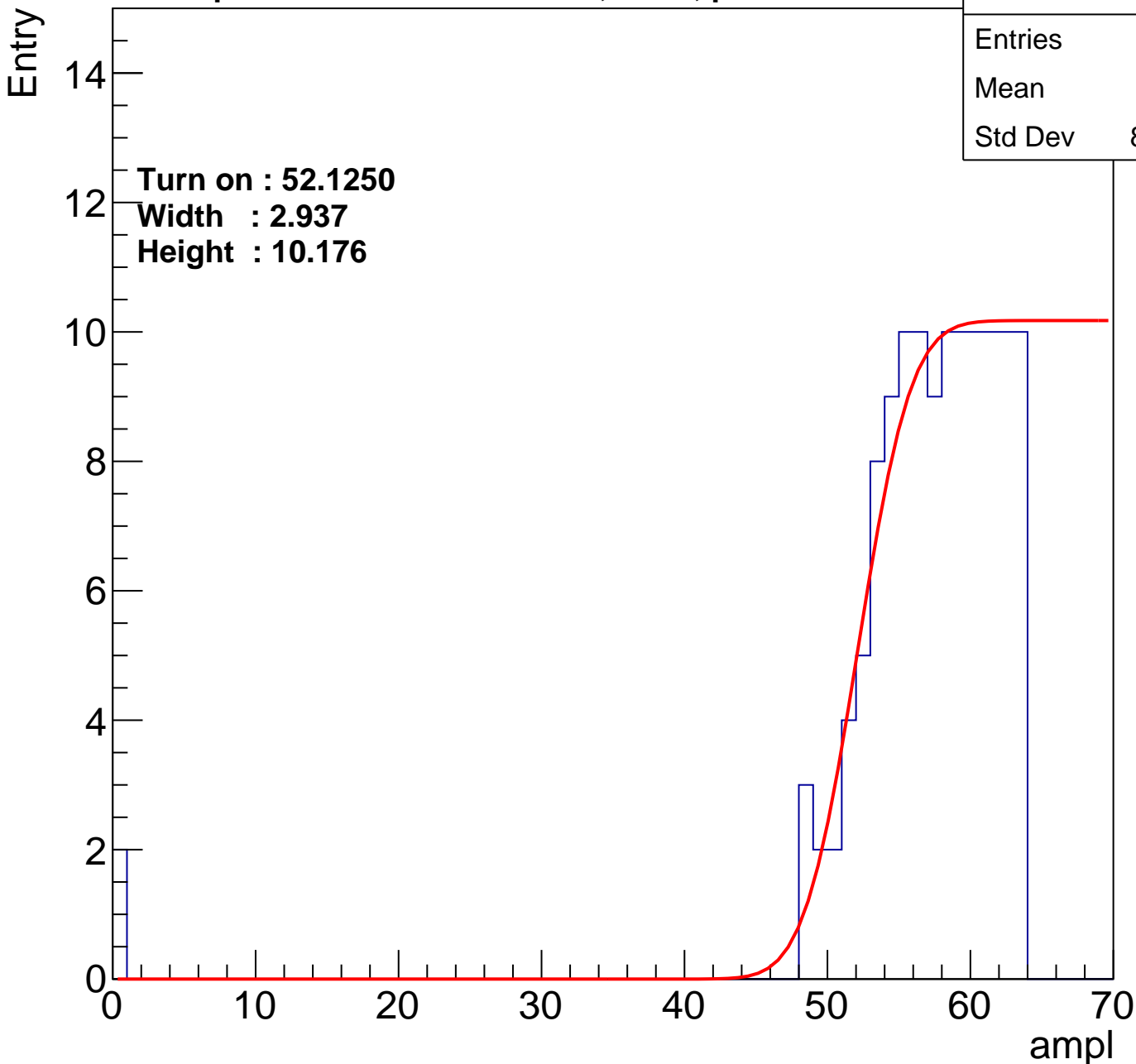
calib_packv5_040323_1717.root, FC#2, port C3

Turn on : 52.1250

Width : 2.937

Height : 10.176

Entries	124
Mean	56.2
Std Dev	8.194



B0L103S, U21-ch62

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	56.01
Std Dev	9.848

Turn on : 53.1063

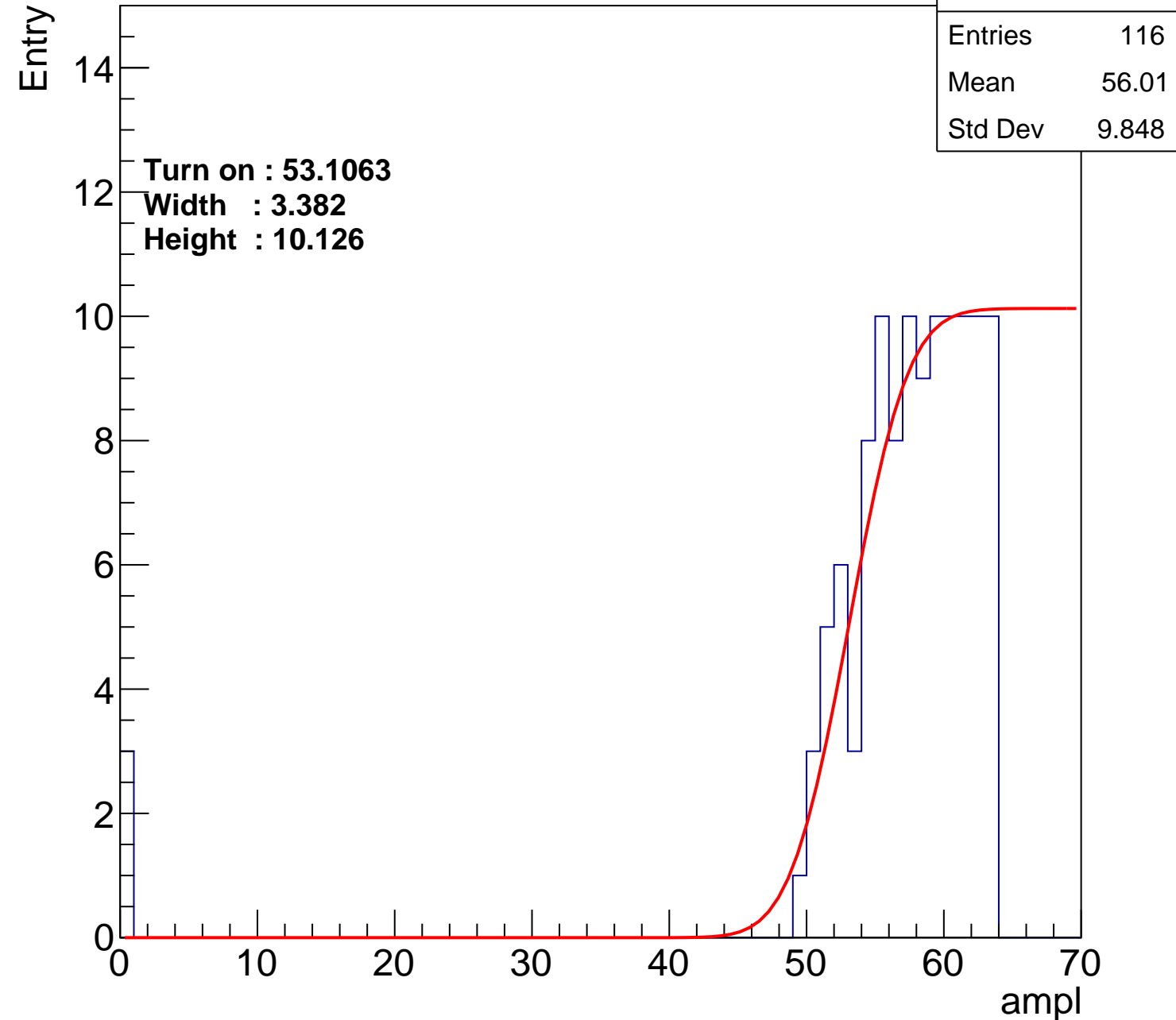
Width : 3.382

Height : 10.126

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch63

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.49
Std Dev	8.278

Turn on : 51.9016

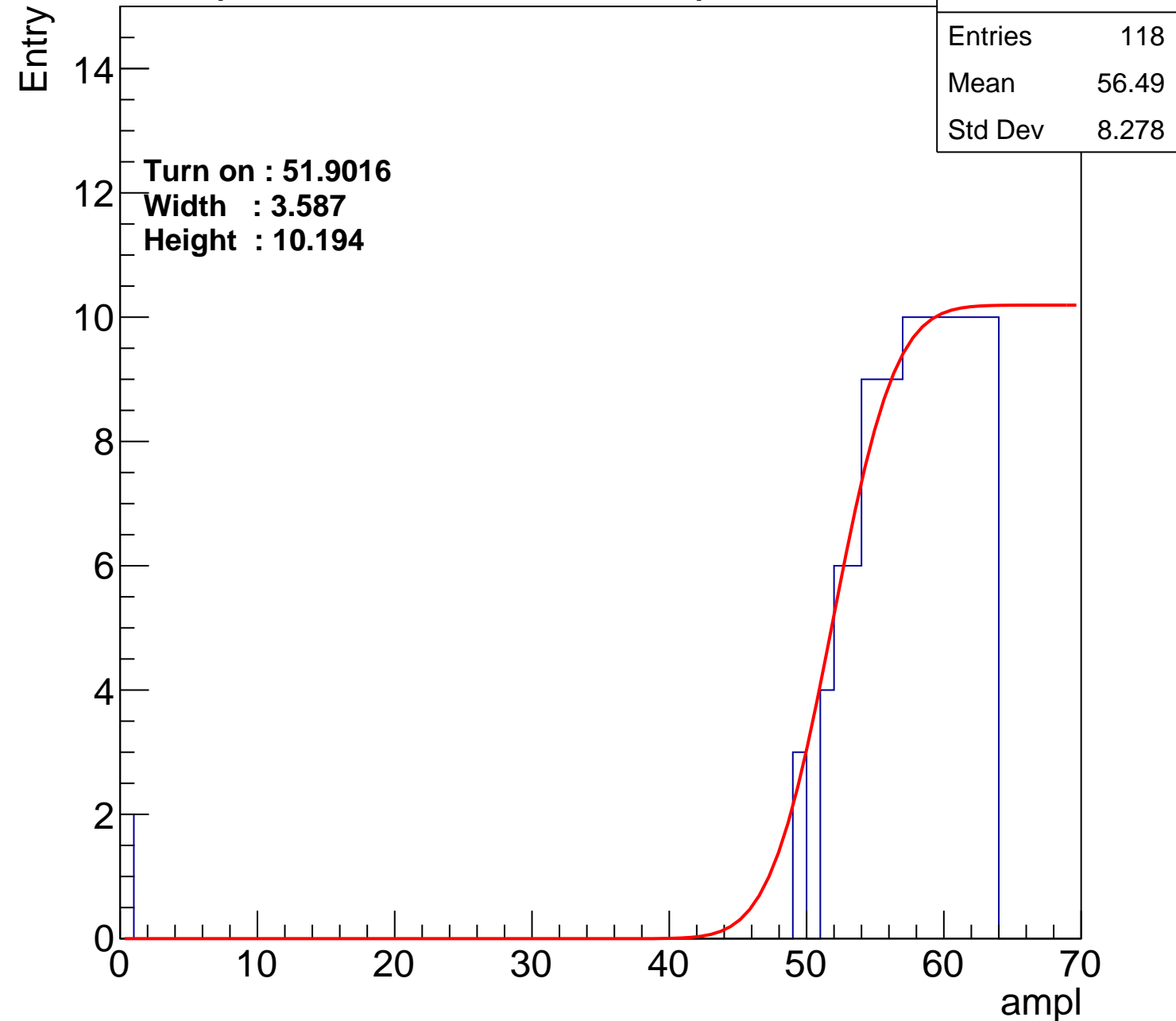
Width : 3.587

Height : 10.194

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch64

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	55.61
Std Dev	6.453

Turn on : 49.5621

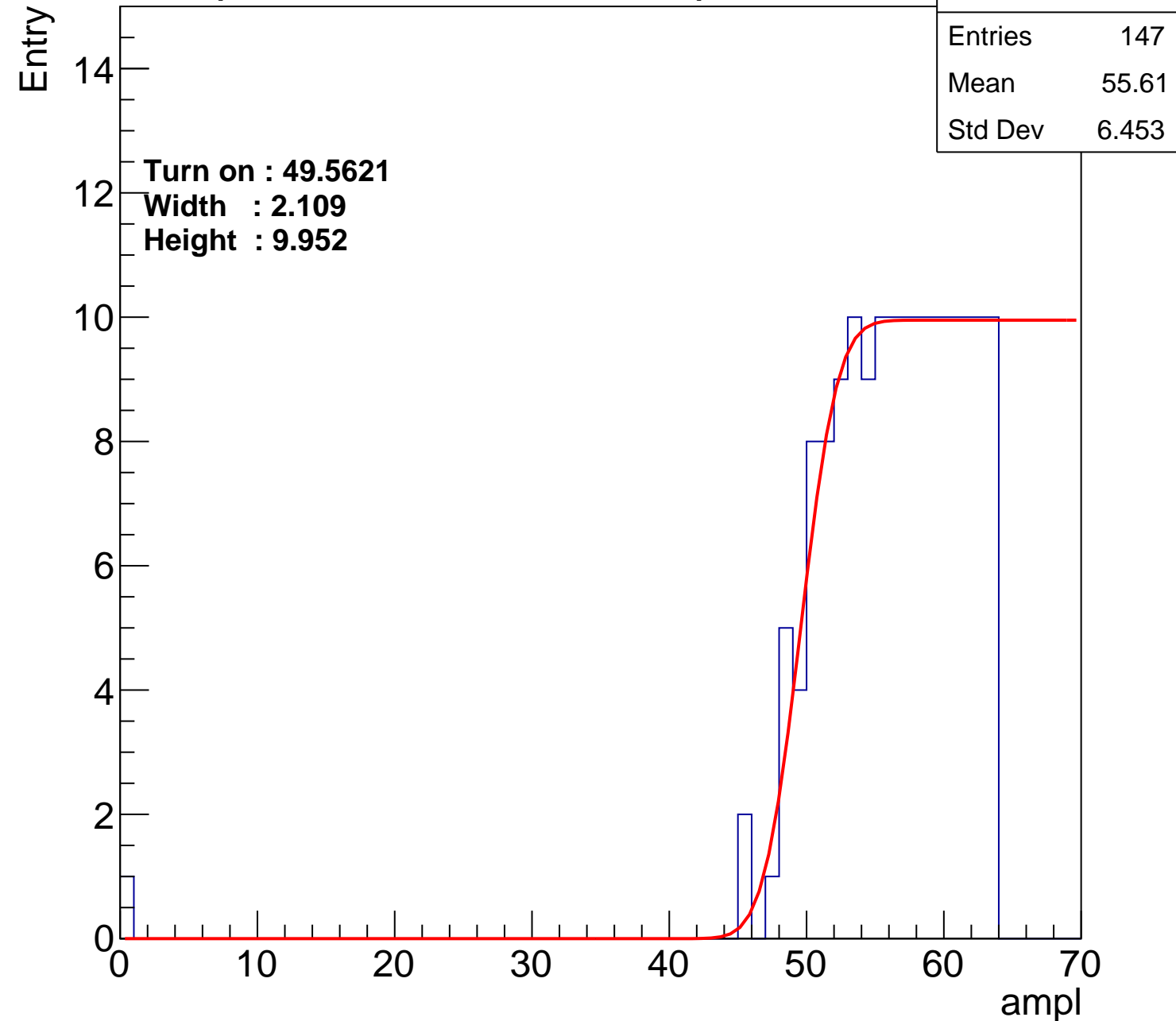
Width : 2.109

Height : 9.952

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.92
Std Dev	9.776

Turn on : 52.5360

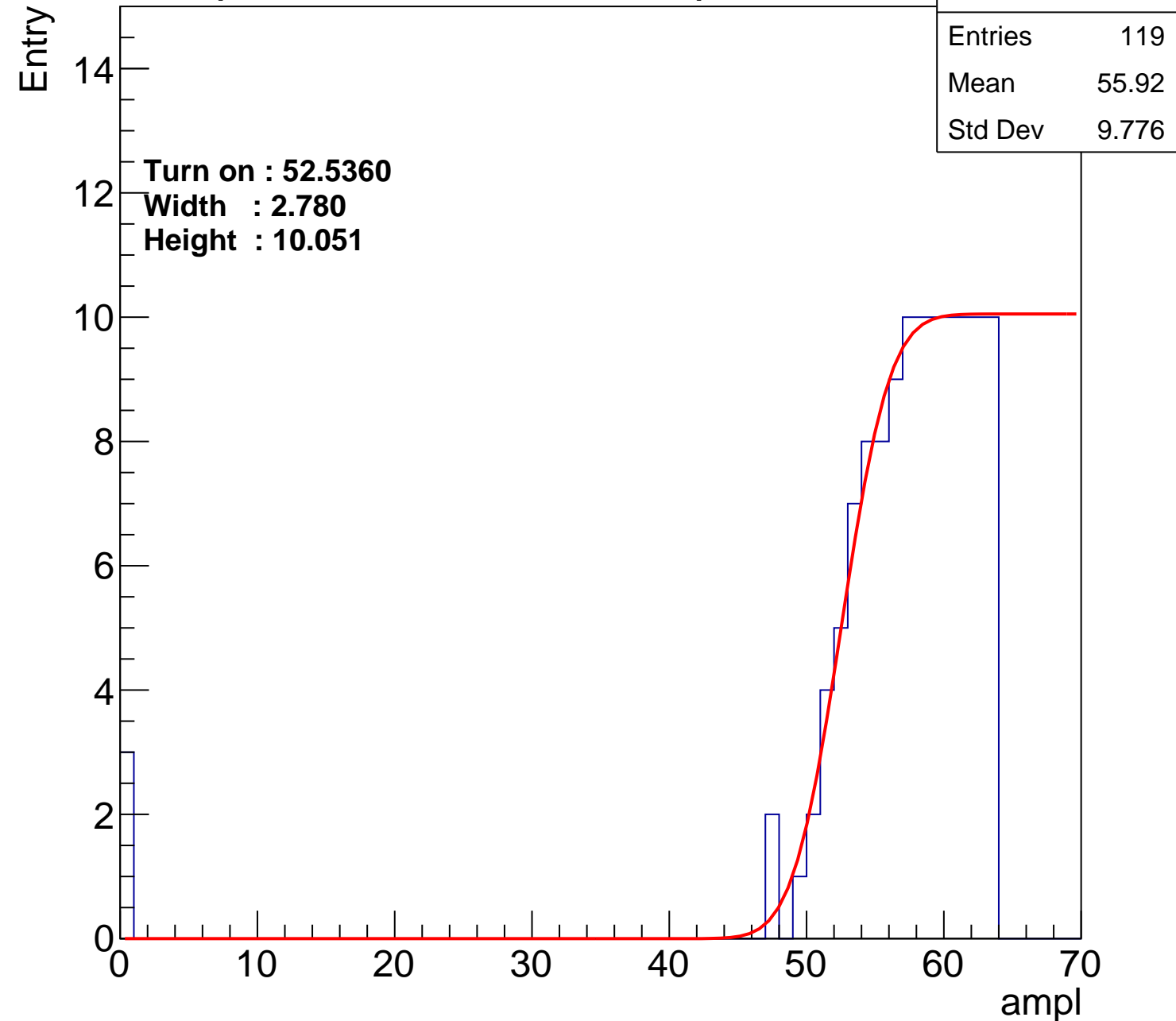
Width : 2.780

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch66

calib_packv5_040323_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

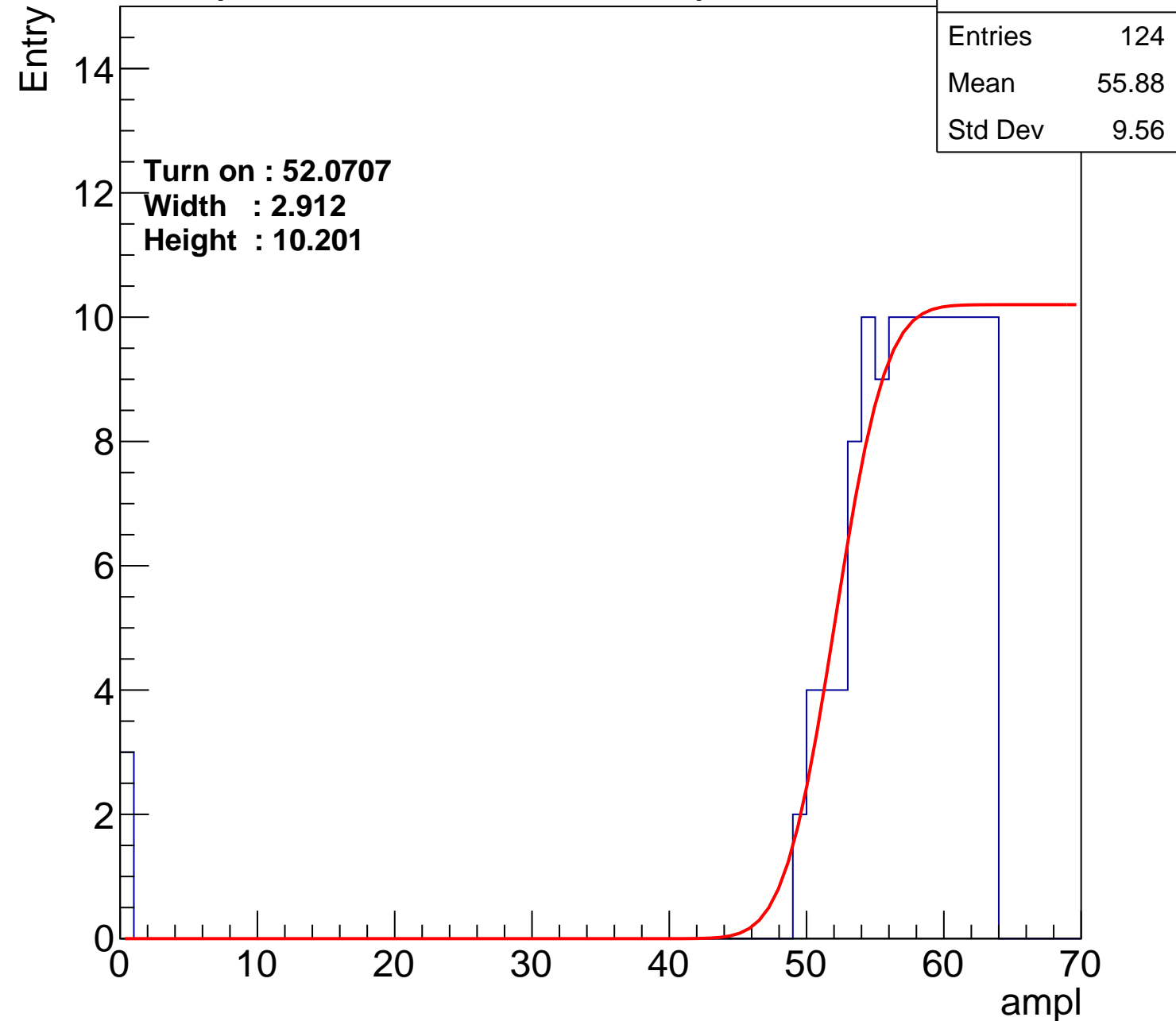
Turn on : 52.0707

Width : 2.912

Height : 10.201

Entries	124
Mean	55.88
Std Dev	9.56

ampl



B0L103S, U21-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	53.76
Std Dev	13.01

Turn on : 50.1691

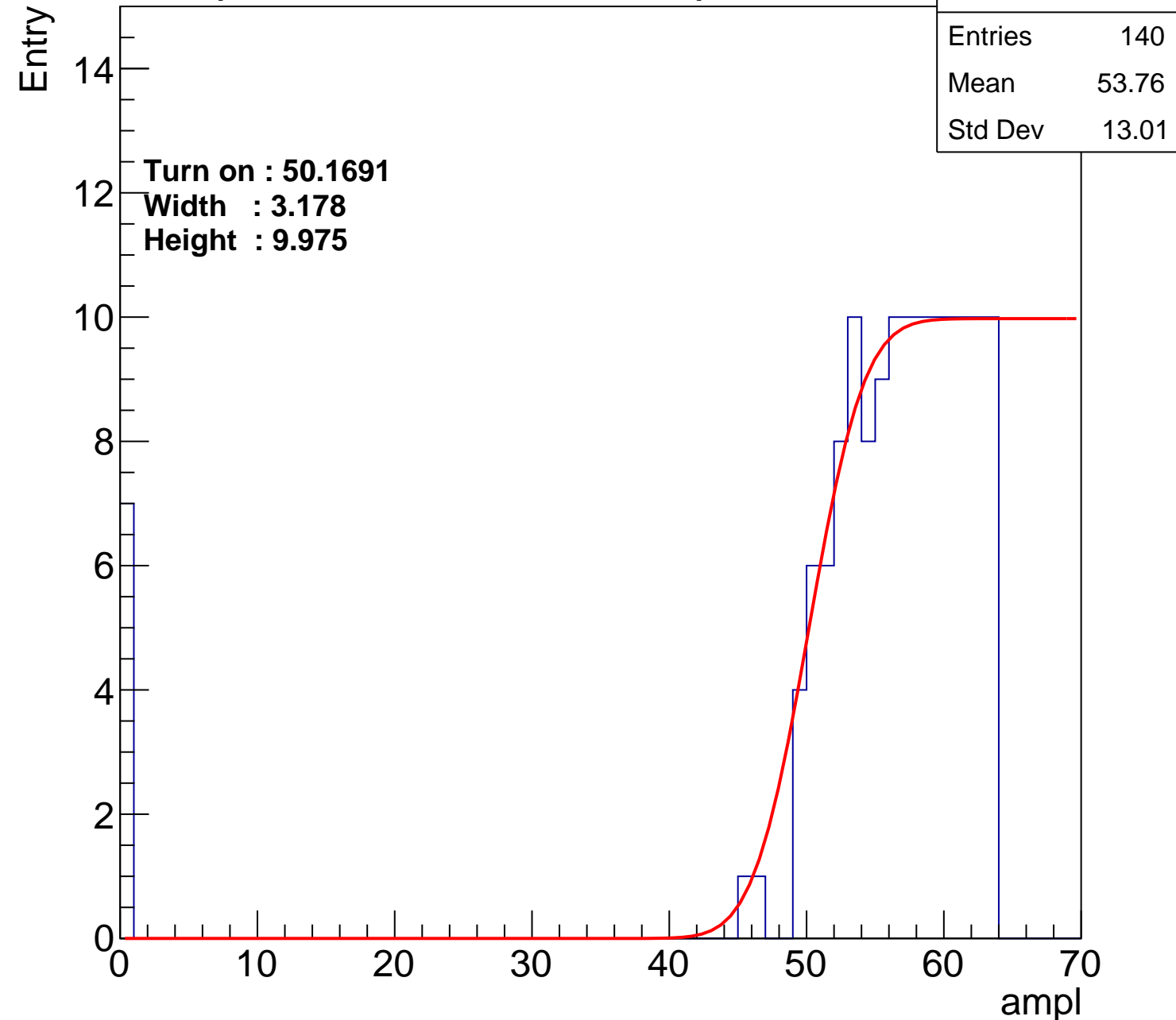
Width : 3.178

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	54.93
Std Dev	10.48

Turn on : 51.4191

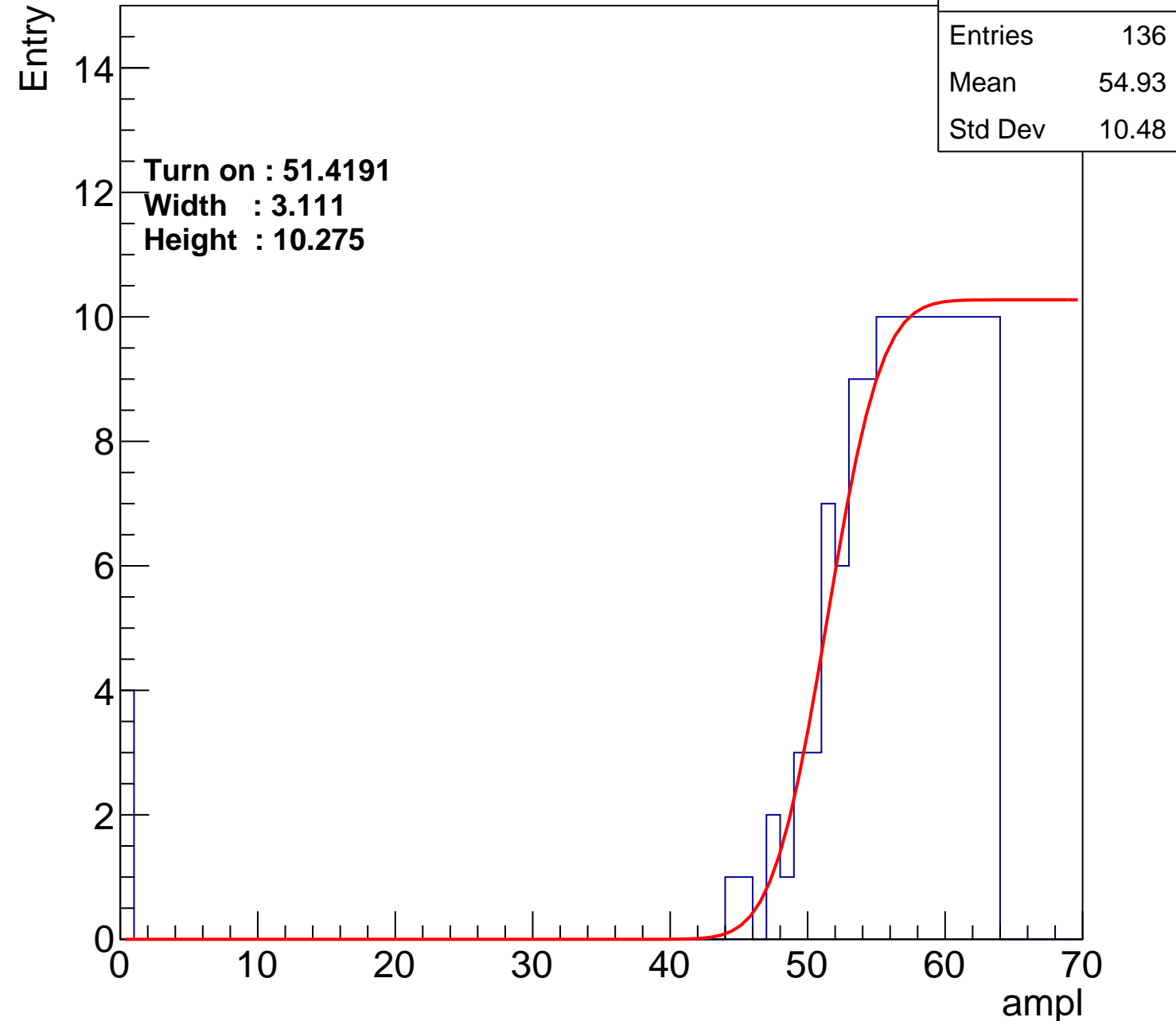
Width : 3.111

Height : 10.275

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch69

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.13
Std Dev	9.23

Turn on : 50.3812

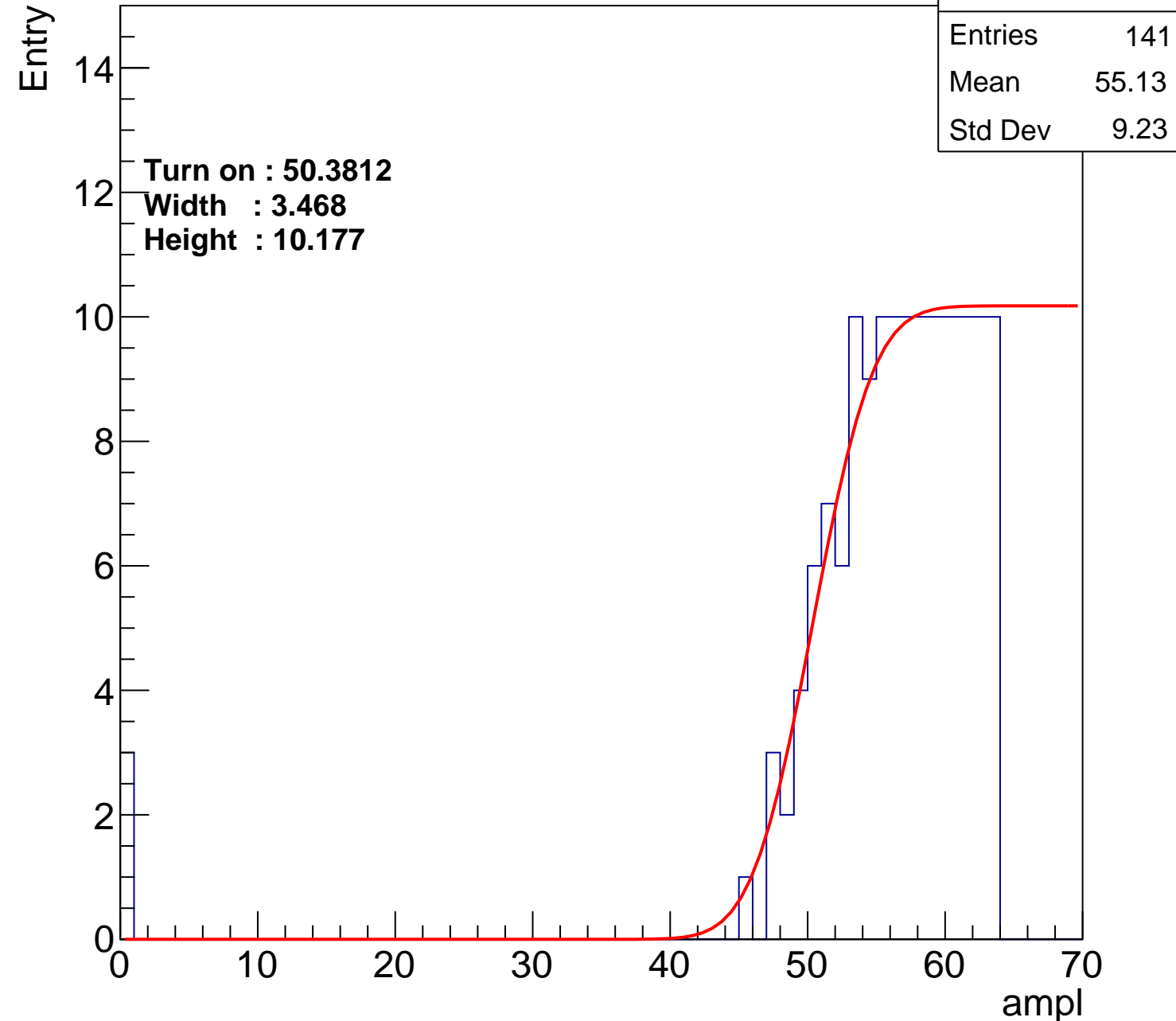
Width : 3.468

Height : 10.177

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	55.04
Std Dev	9.203

Turn on : 50.6113

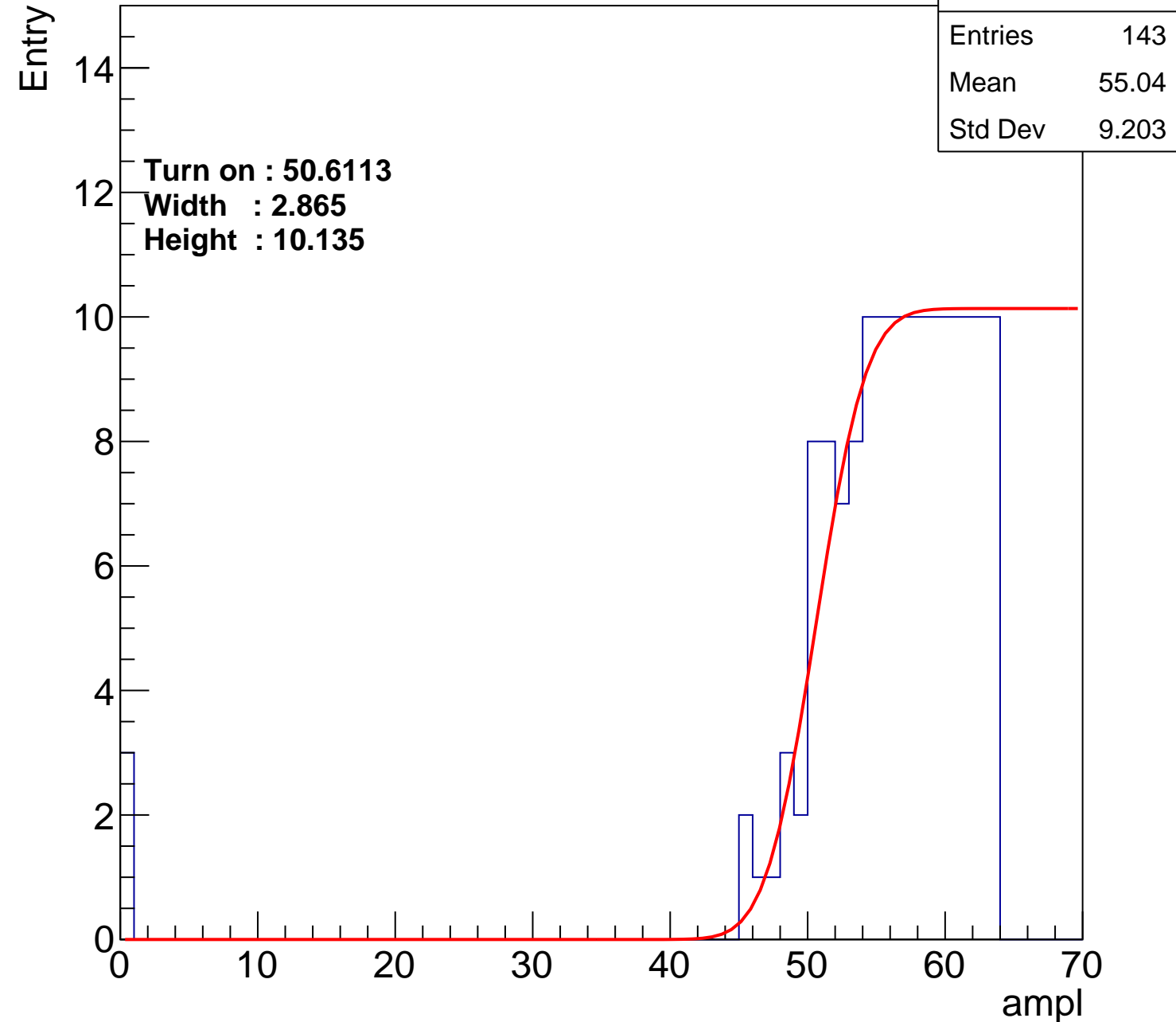
Width : 2.865

Height : 10.135

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch71

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.23
Std Dev	9.329

Turn on : 50.7036

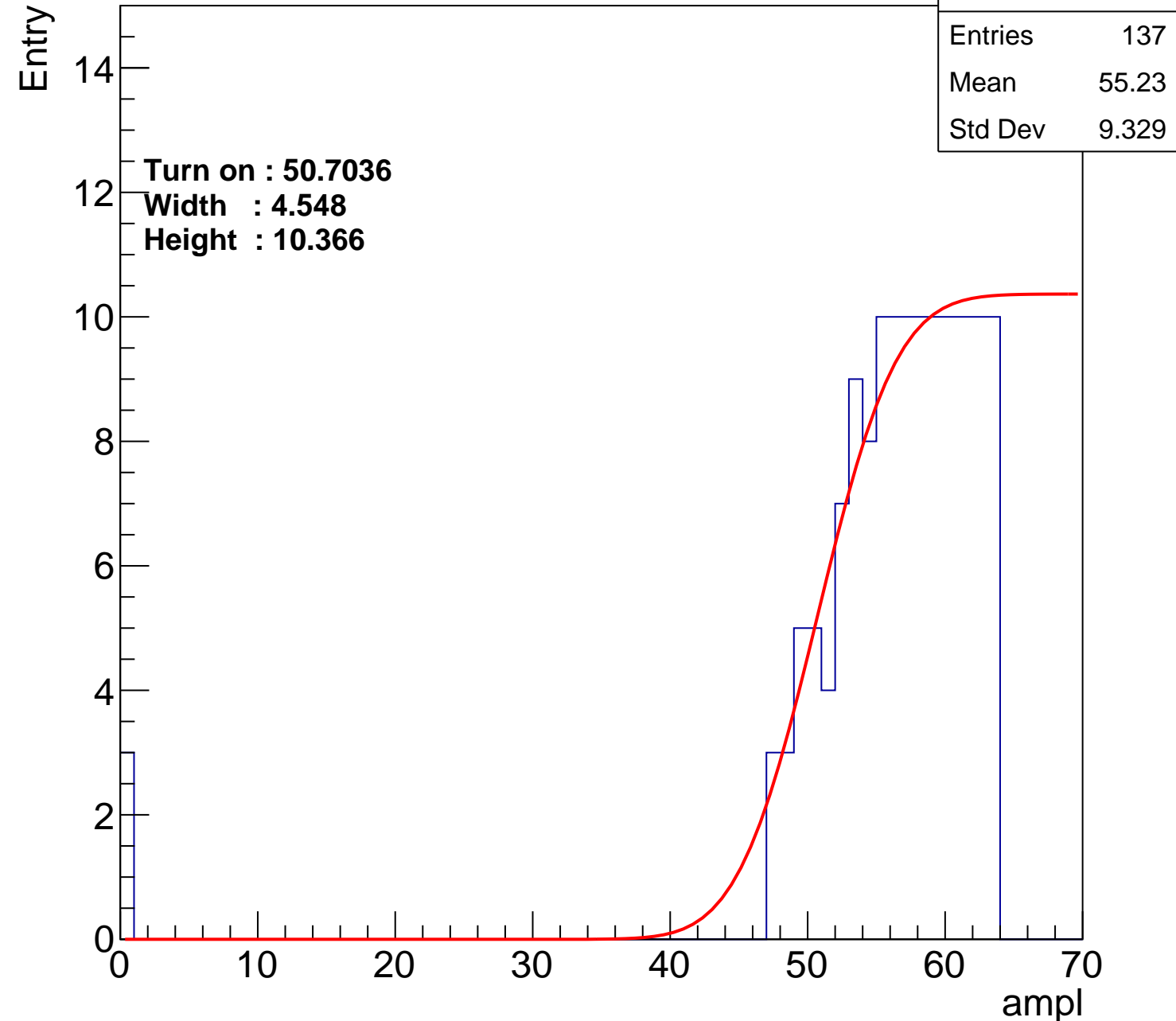
Width : 4.548

Height : 10.366

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch72

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	53.59
Std Dev	14.35

Turn on : 52.1557

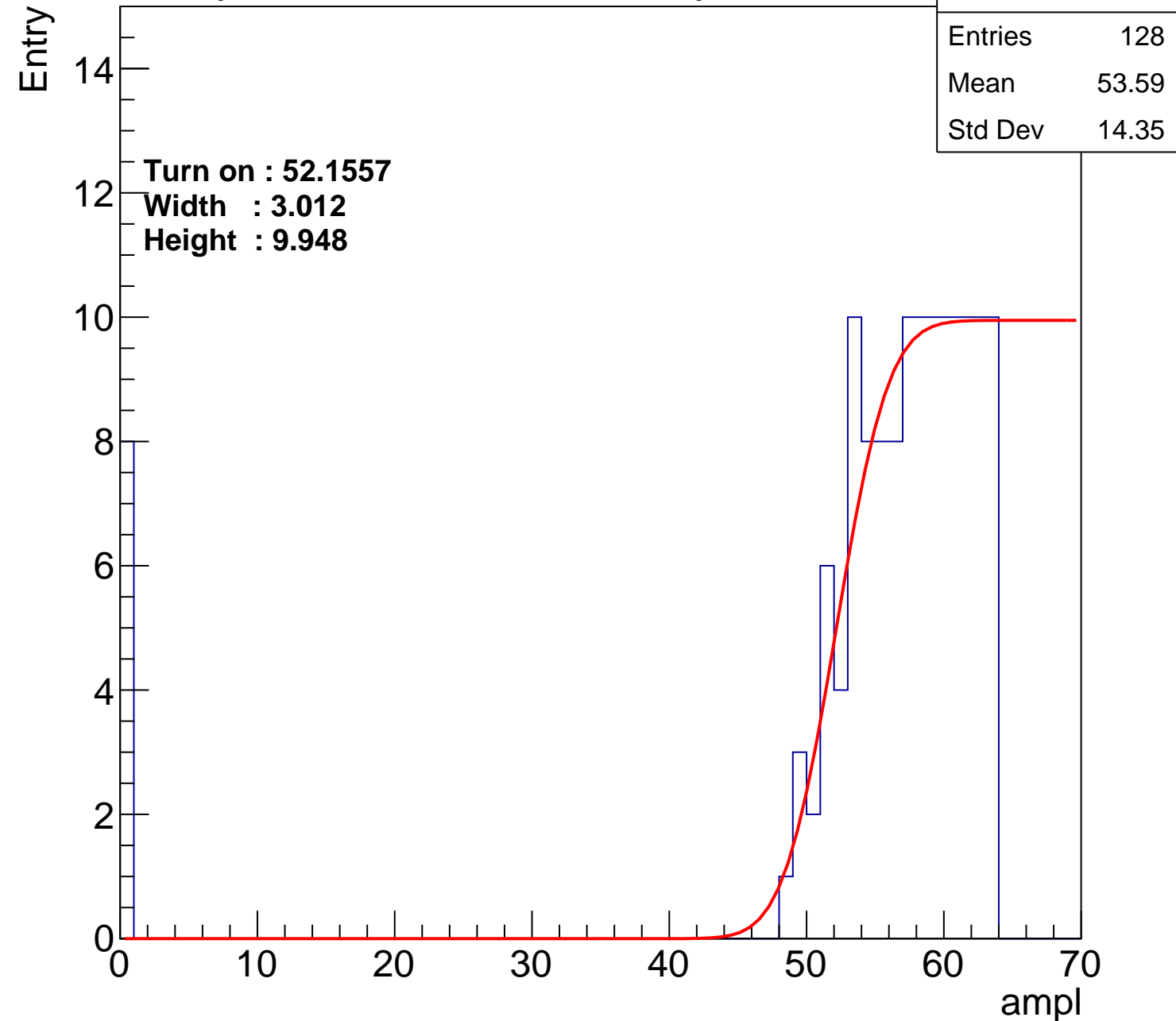
Width : 3.012

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch73

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	55.27
Std Dev	6.548

Turn on : 49.0285

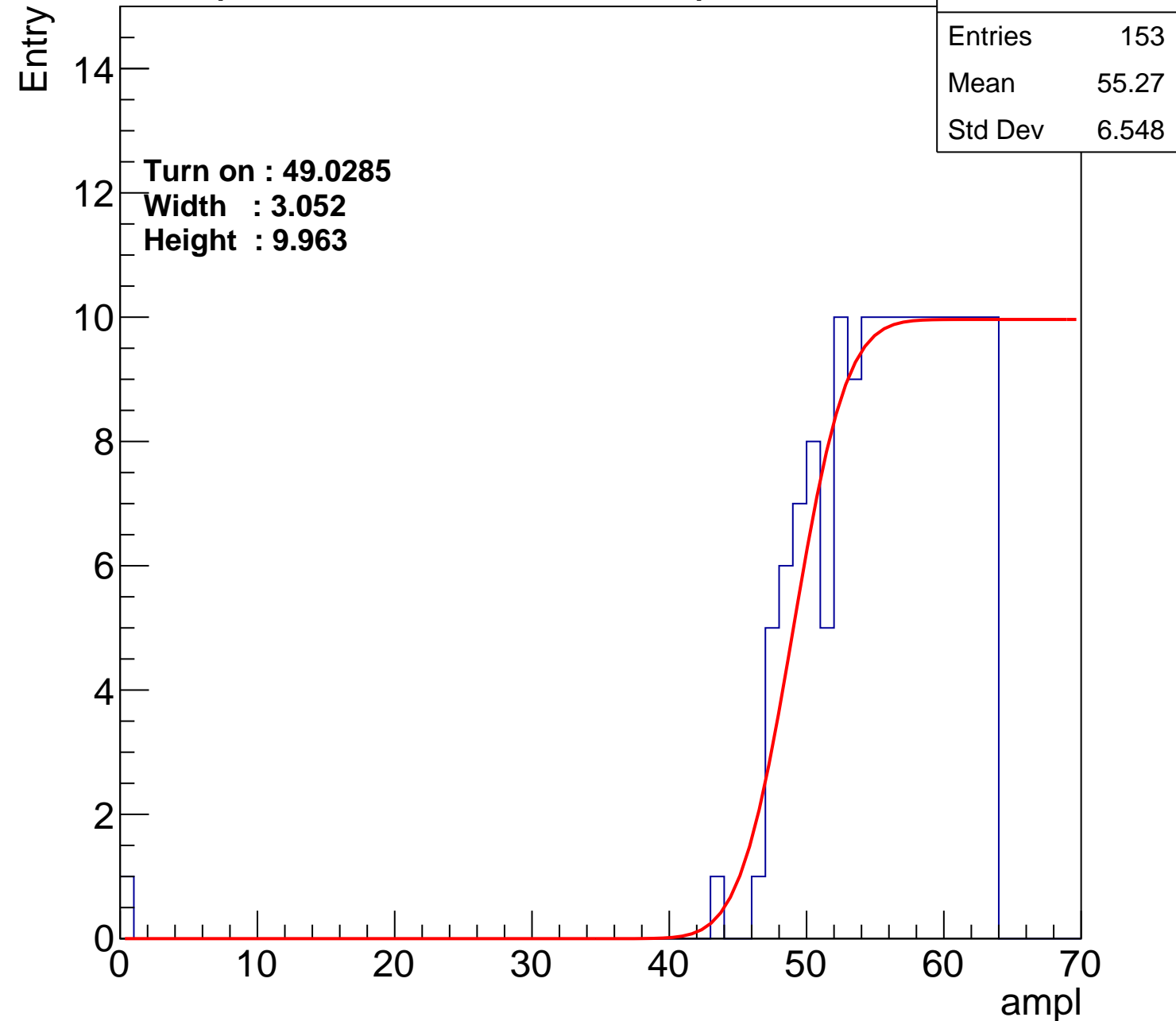
Width : 3.052

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch74

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	56.64
Std Dev	6.526

Turn on : 51.6441

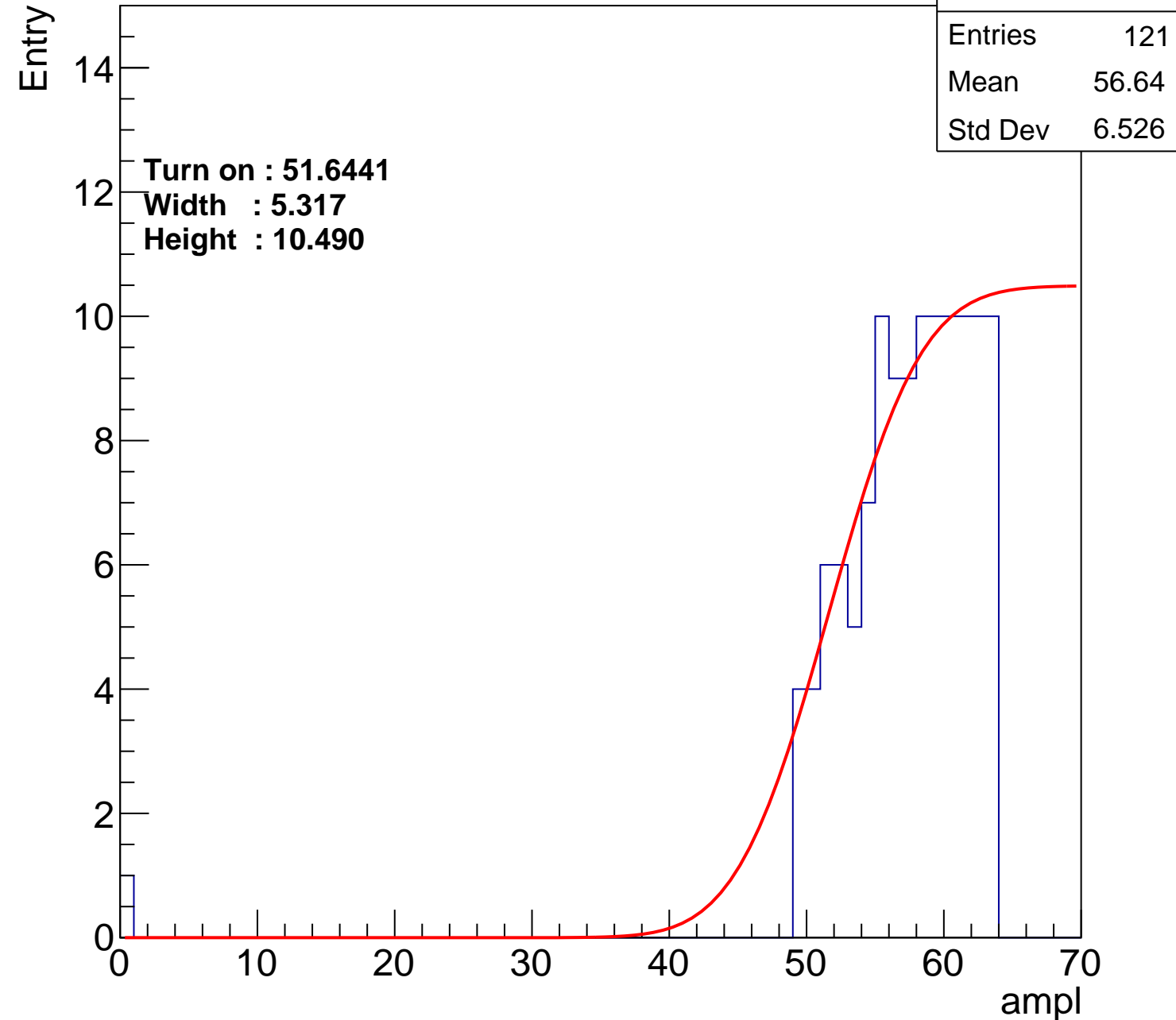
Width : 5.317

Height : 10.490

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	55.58
Std Dev	9.577

Turn on : 51.9718

Width : 2.970

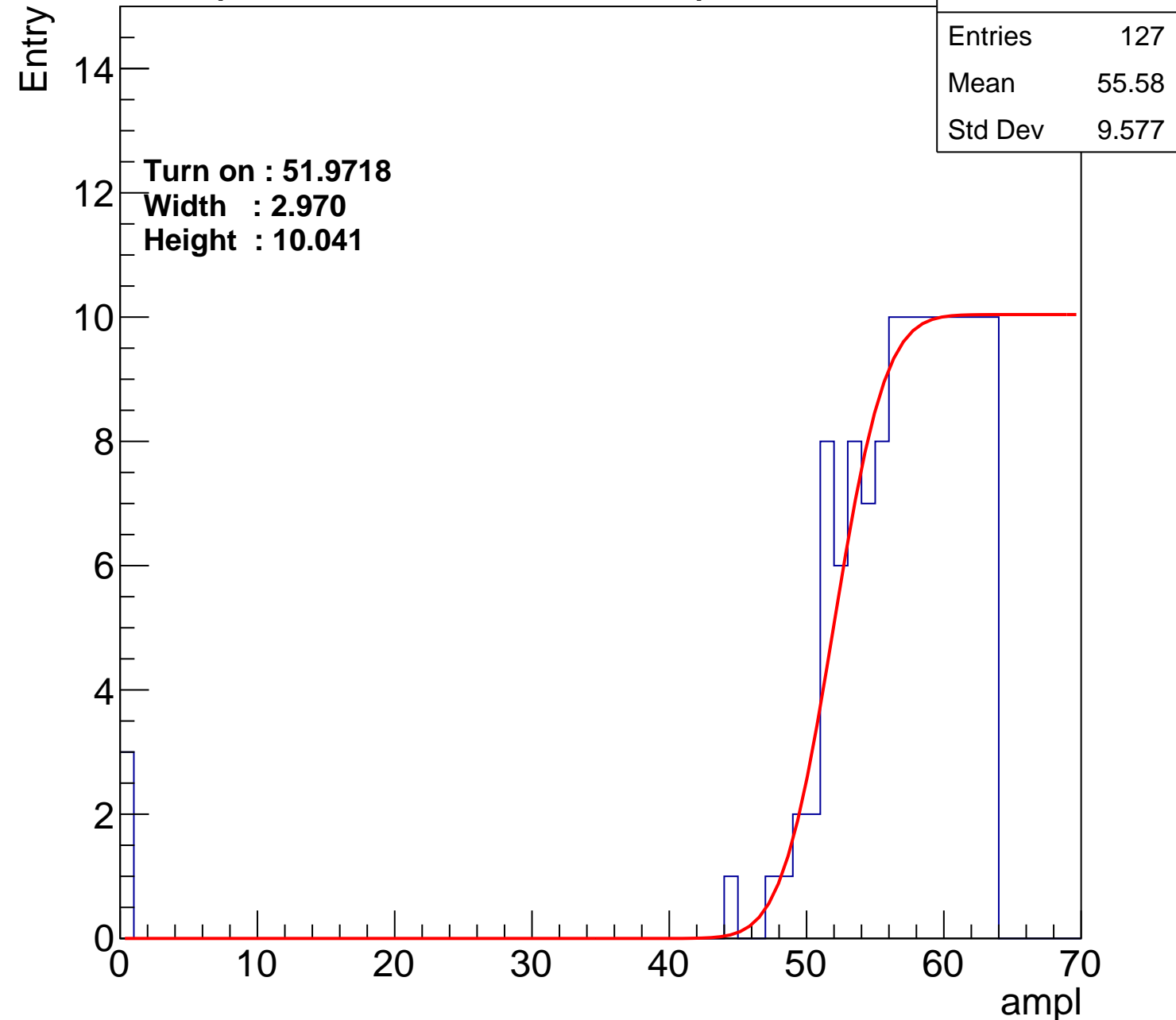
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U21-ch76

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	55.23
Std Dev	9.296

Turn on : 50.5539

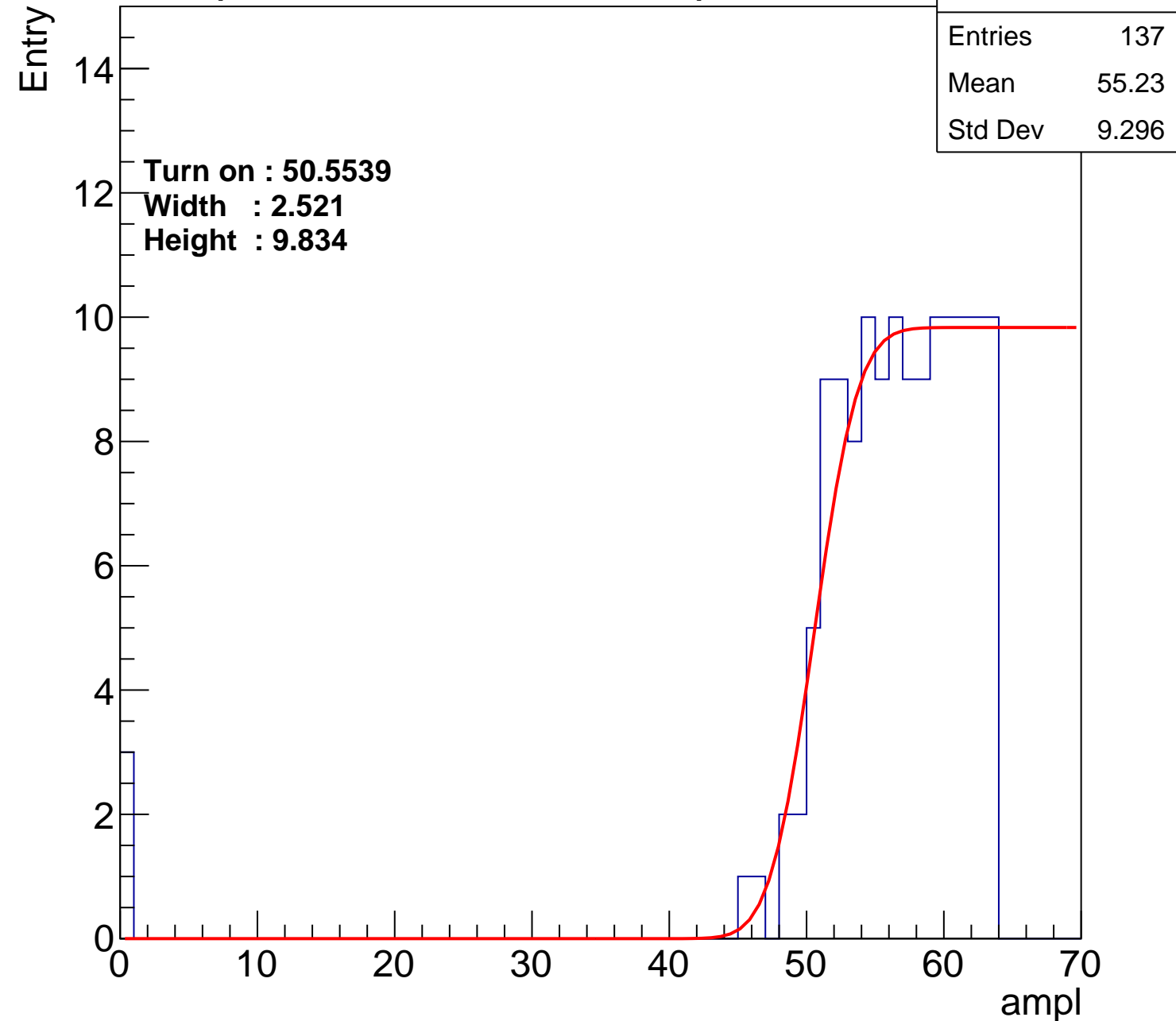
Width : 2.521

Height : 9.834

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch77

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	55.29
Std Dev	12.42

Turn on : 53.0535

Width : 2.216

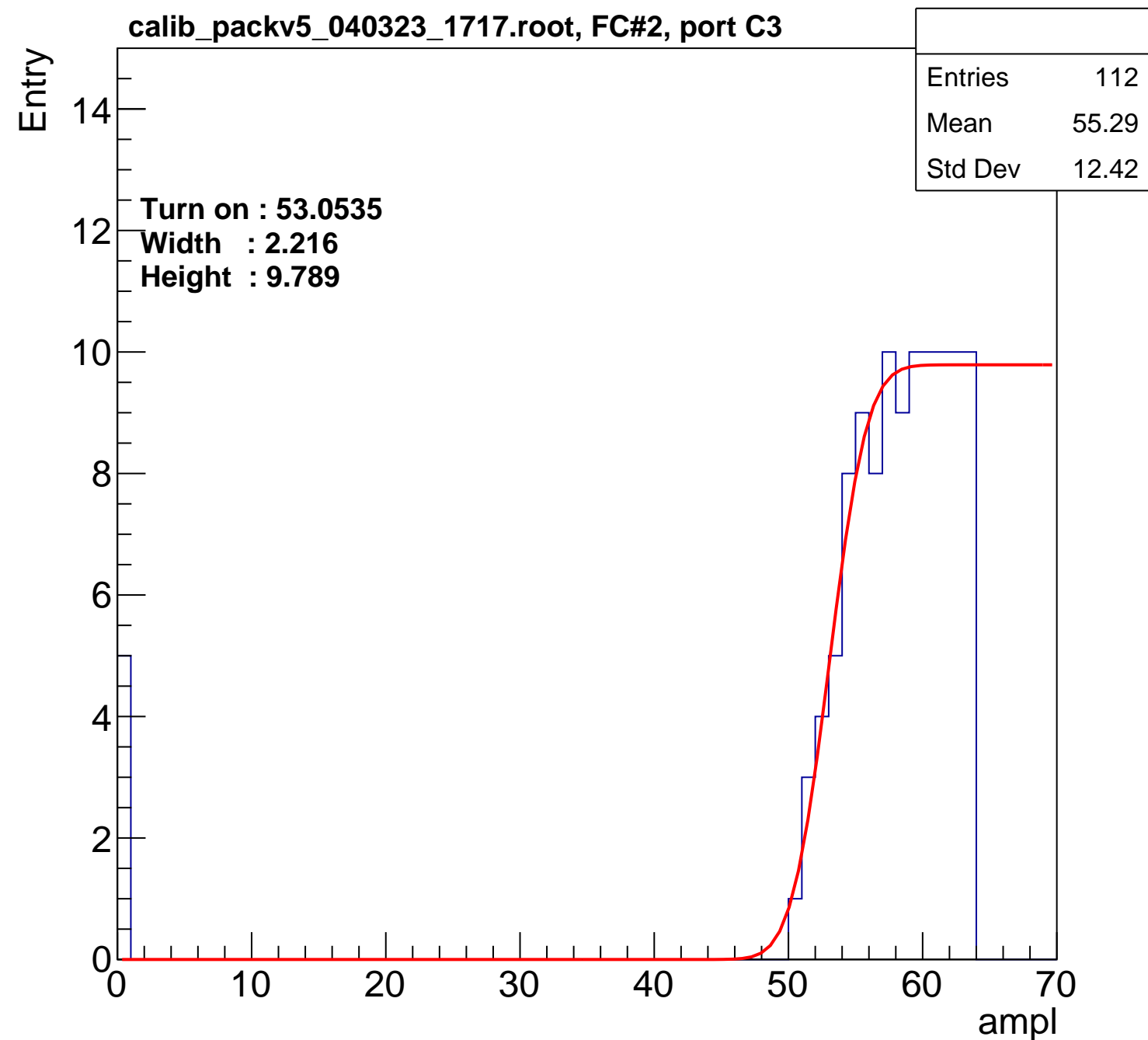
Height : 9.789

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U21-ch78

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	56.72
Std Dev	6.504

Turn on : 51.9777

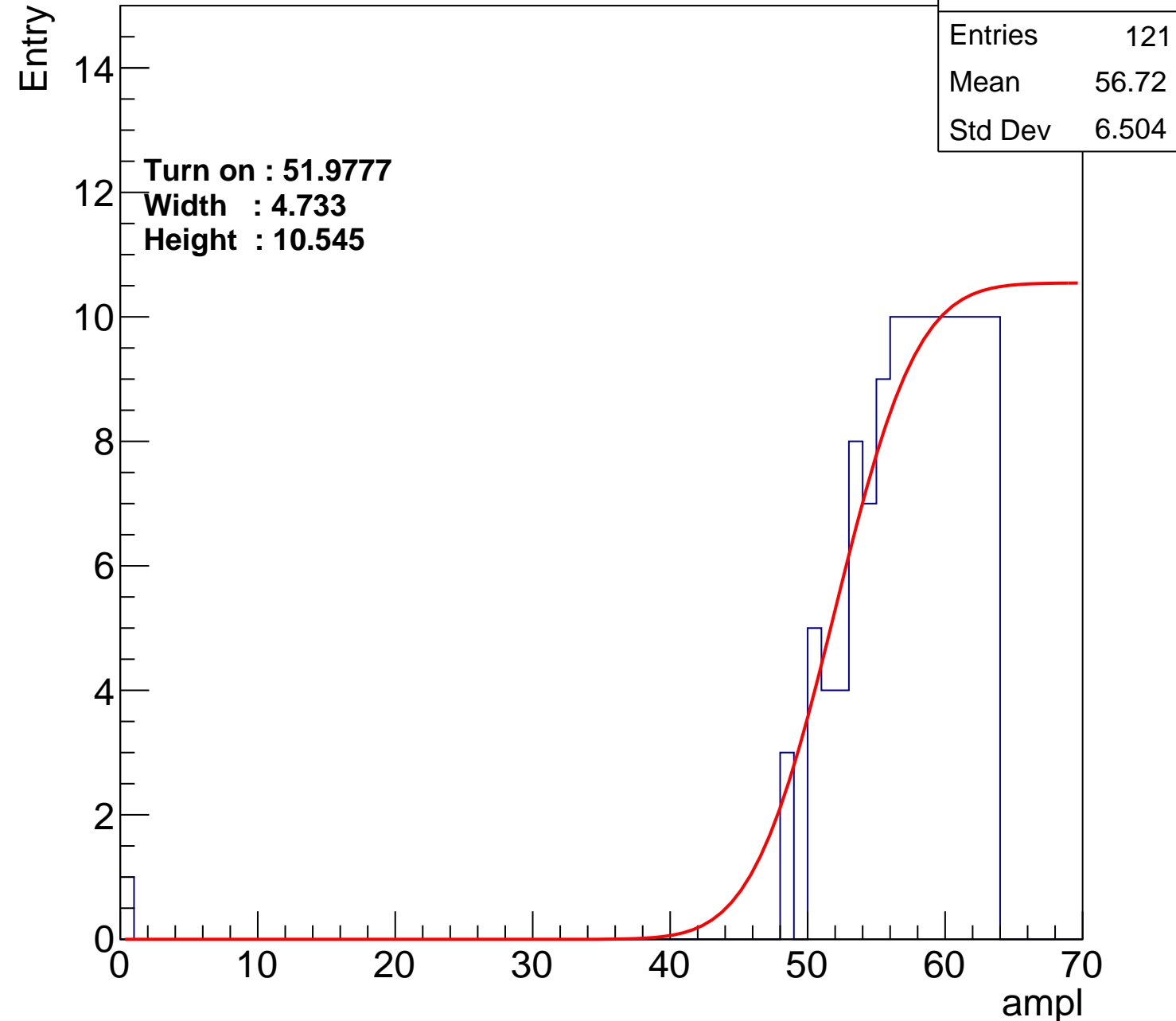
Width : 4.733

Height : 10.545

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch79

calib_packv5_040323_1717.root, FC#2, port C3

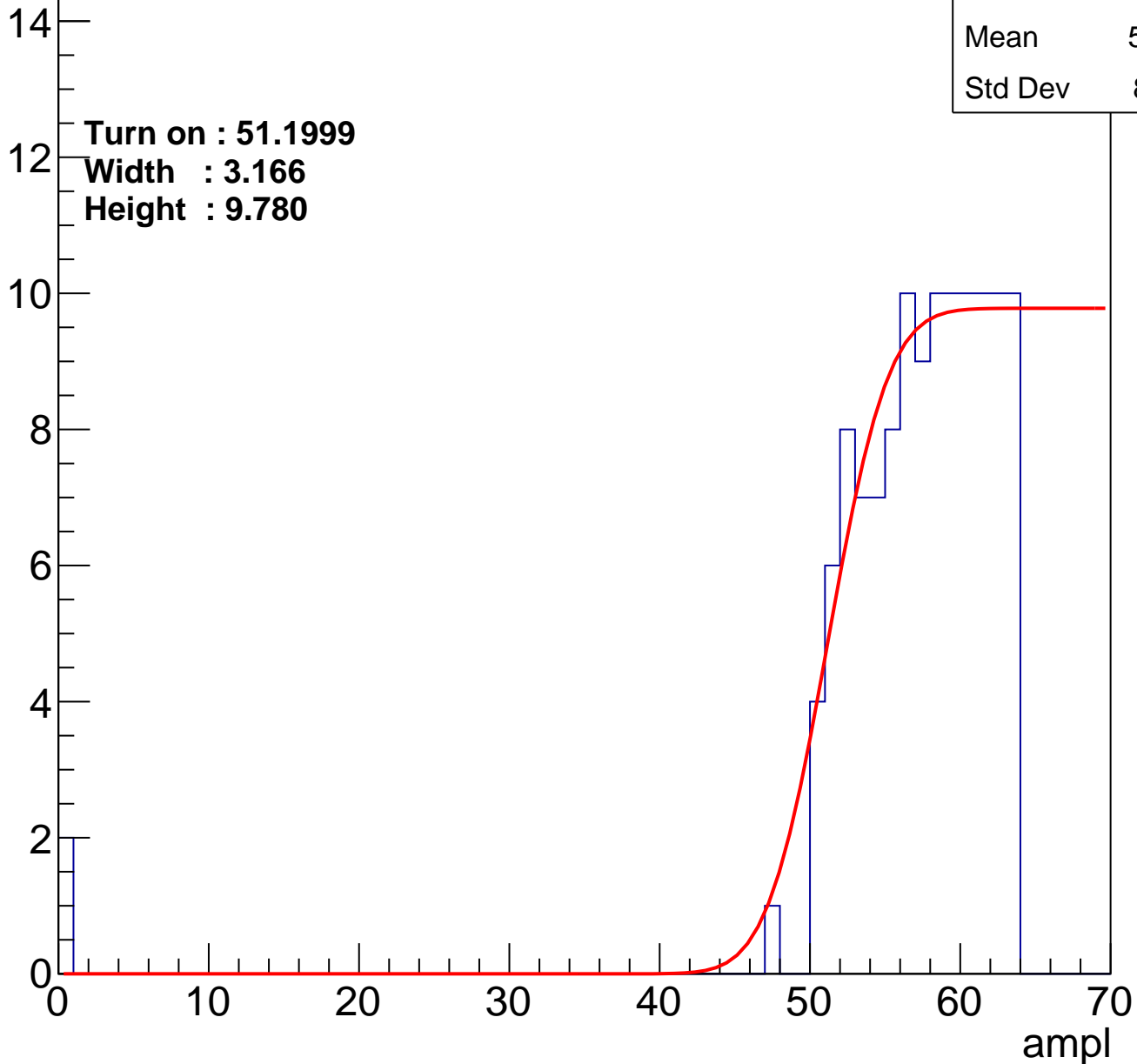
Entries	122
Mean	56.24
Std Dev	8.231

Turn on : 51.1999

Width : 3.166

Height : 9.780

Entry



B0L103S, U21-ch80

calib_packv5_040323_1717.root, FC#2, port C3

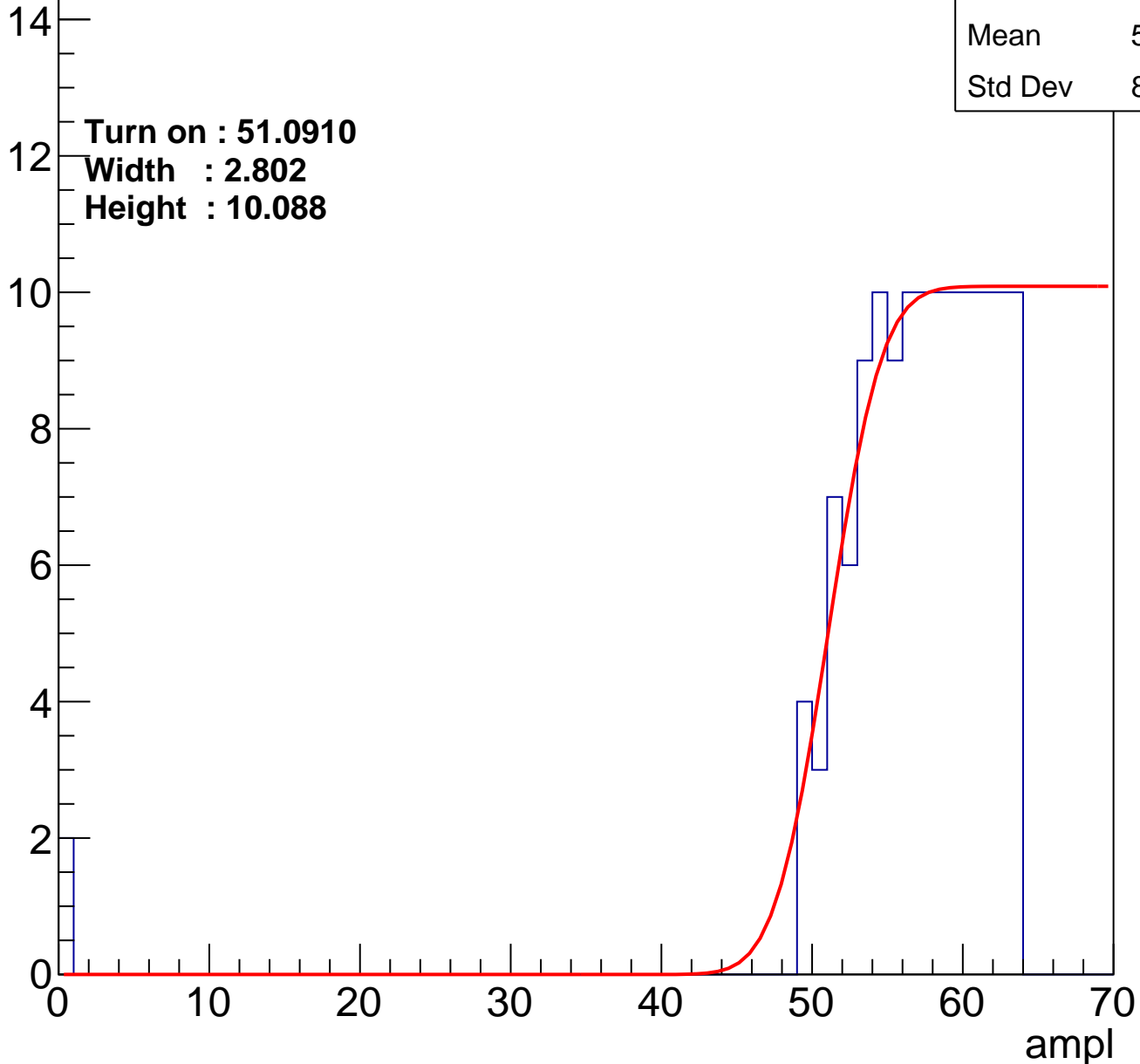
Entries	130
Mean	56.05
Std Dev	8.027

Turn on : 51.0910

Width : 2.802

Height : 10.088

Entry



B0L103S, U21-ch81

calib_packv5_040323_1717.root, FC#2, port C3

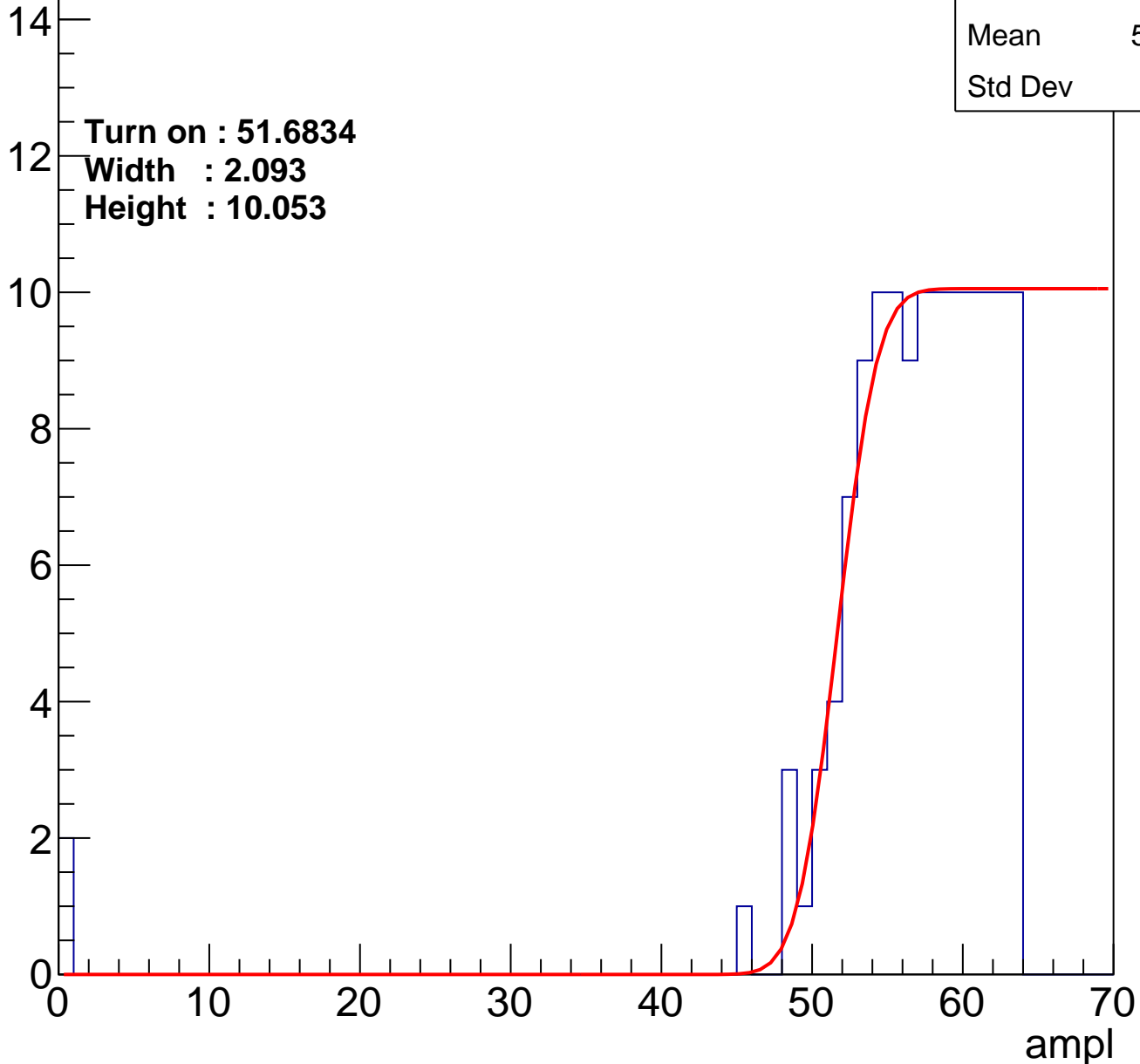
Entries	129
Mean	56.02
Std Dev	8.11

Turn on : 51.6834

Width : 2.093

Height : 10.053

Entry



B0L103S, U21-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	56.59
Std Dev	6.479

Turn on : 51.5939

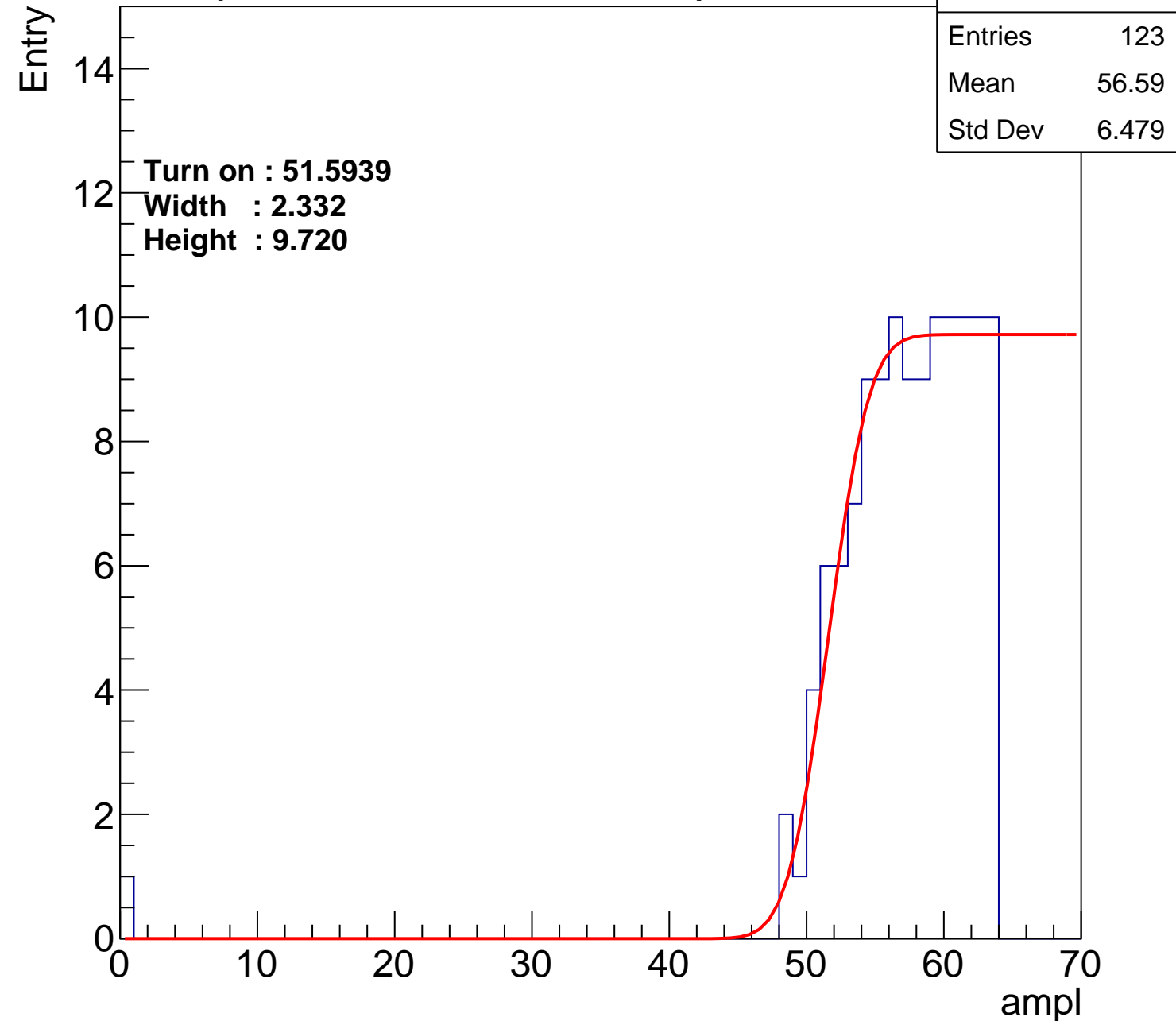
Width : 2.332

Height : 9.720

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch83

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.2
Std Dev	10.7

Turn on : 52.4560

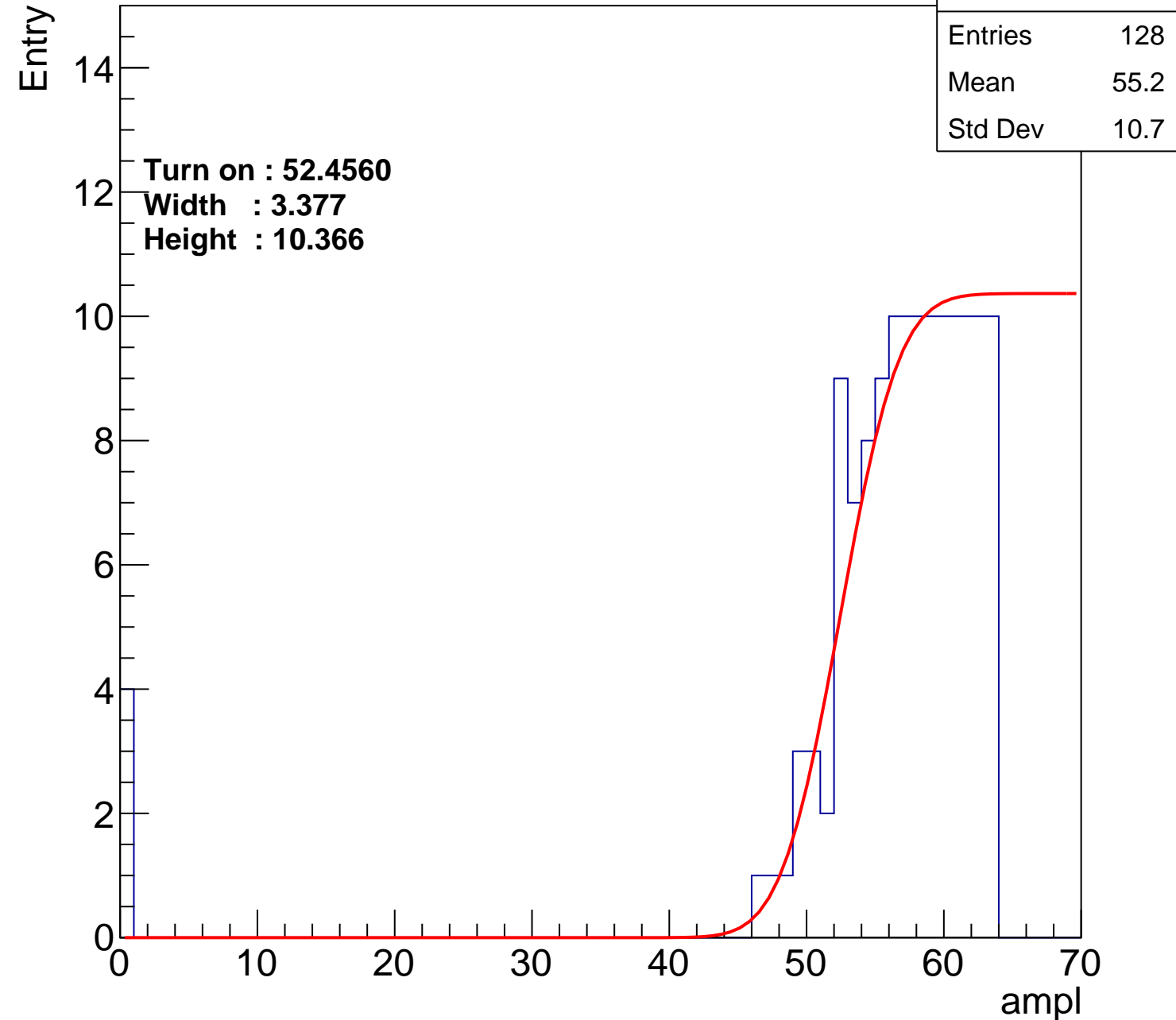
Width : 3.377

Height : 10.366

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch84

calib_packv5_040323_1717.root, FC#2, port C3

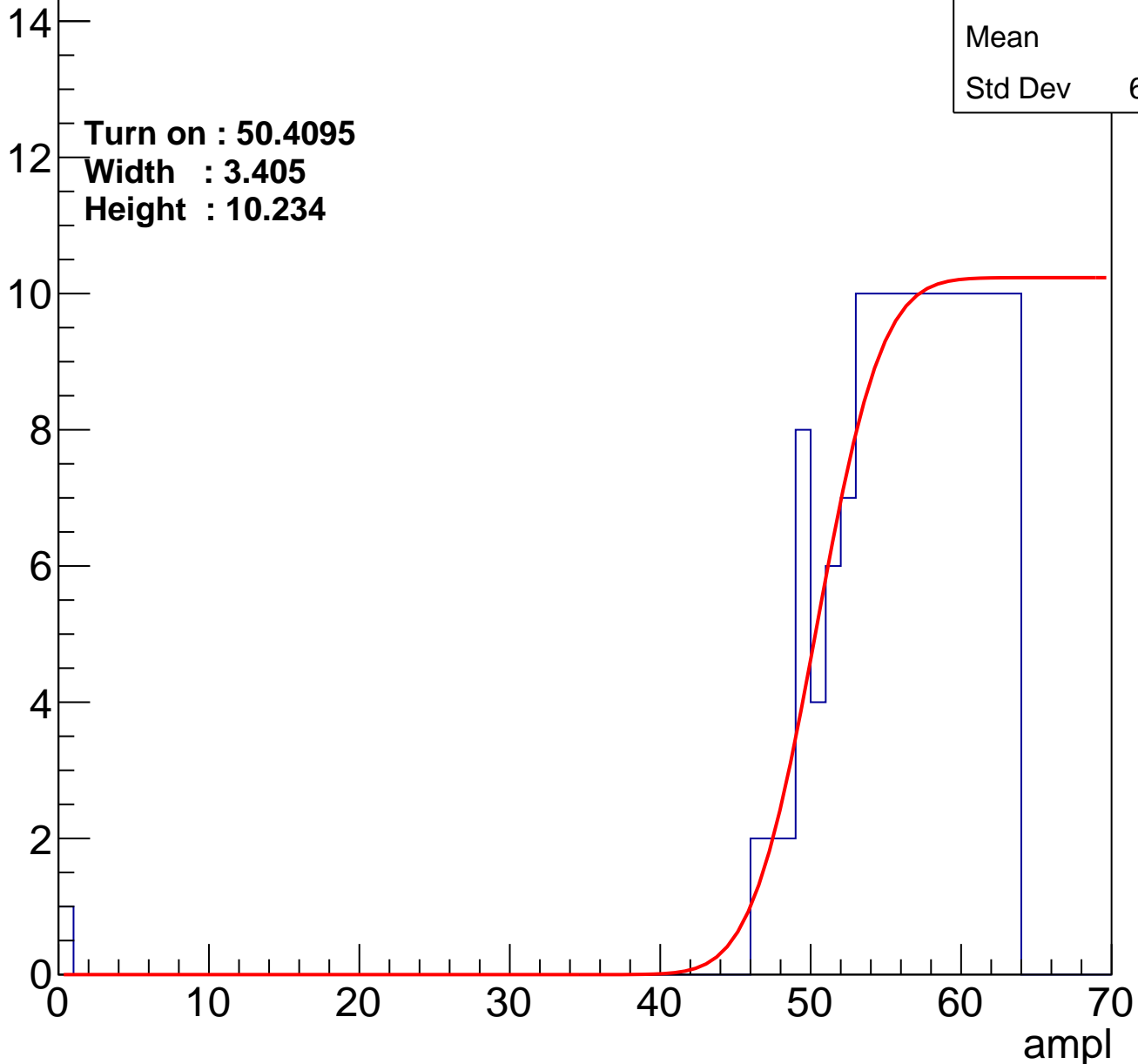
Entries	142
Mean	55.8
Std Dev	6.478

Turn on : 50.4095

Width : 3.405

Height : 10.234

Entry



B0L103S, U21-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	56.13
Std Dev	9.726

Turn on : 52.5658

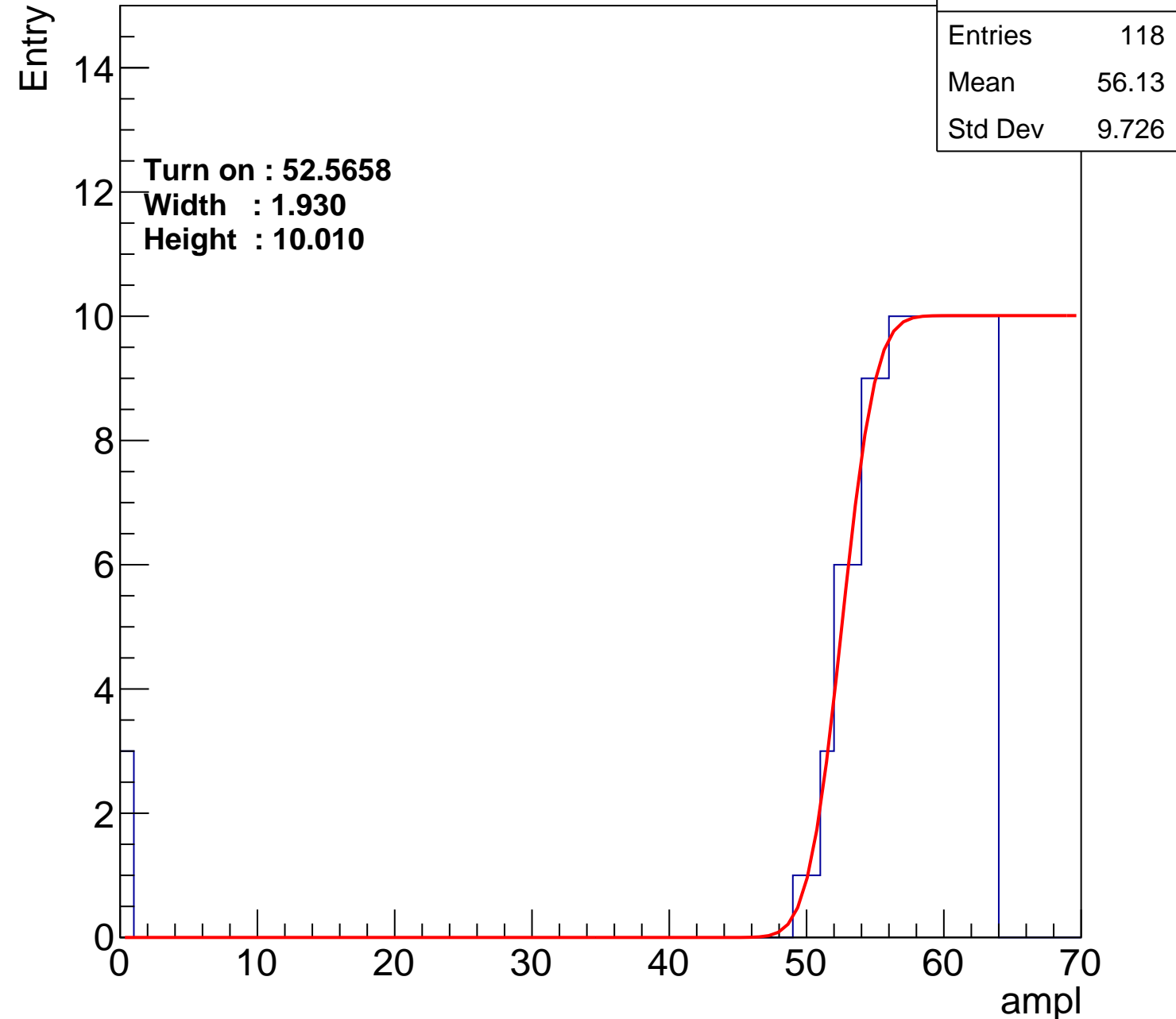
Width : 1.930

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch86

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.22
Std Dev	11.12

Turn on : 50.0310

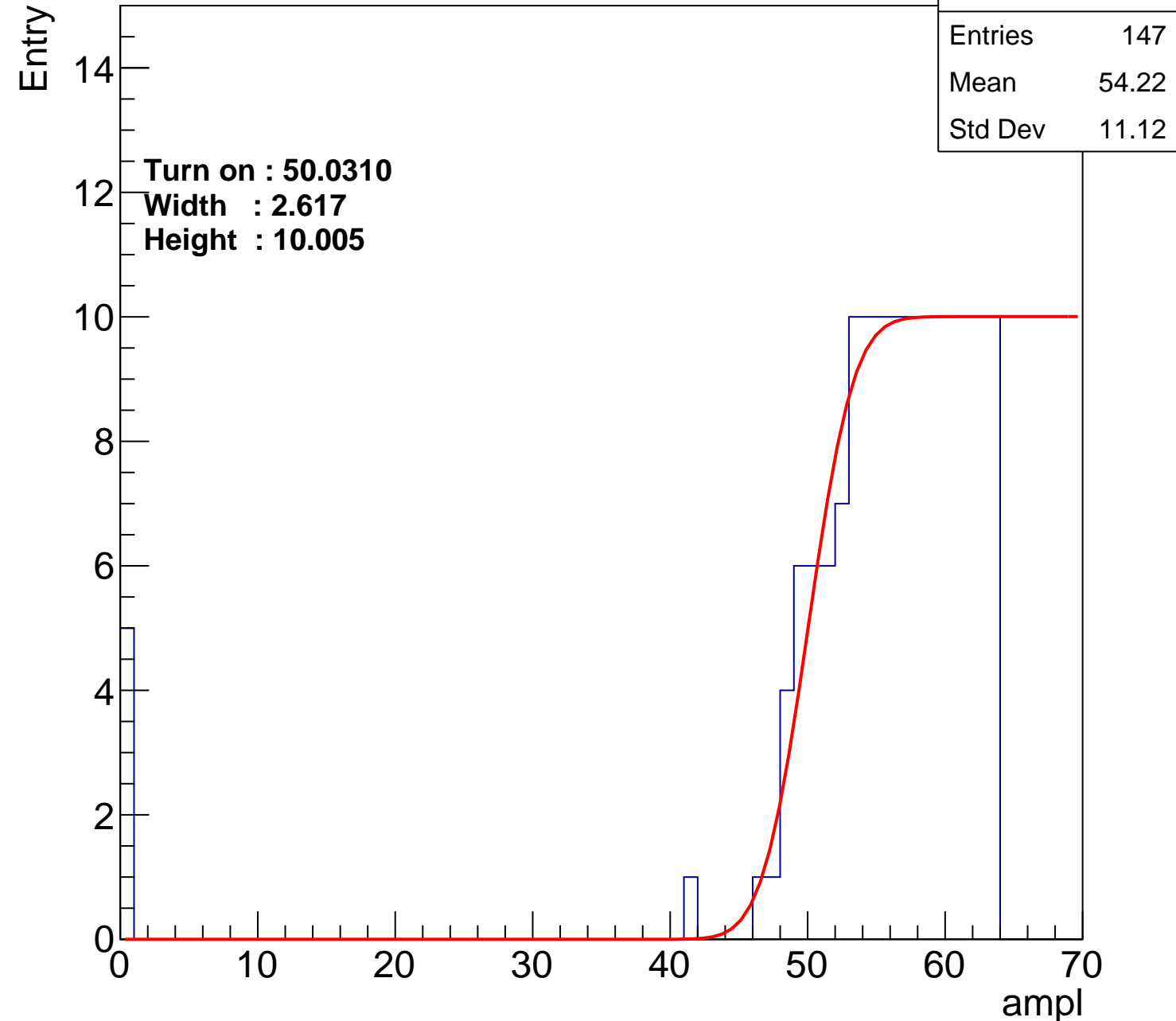
Width : 2.617

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch87

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.8
Std Dev	9.739

Turn on : 53.2398

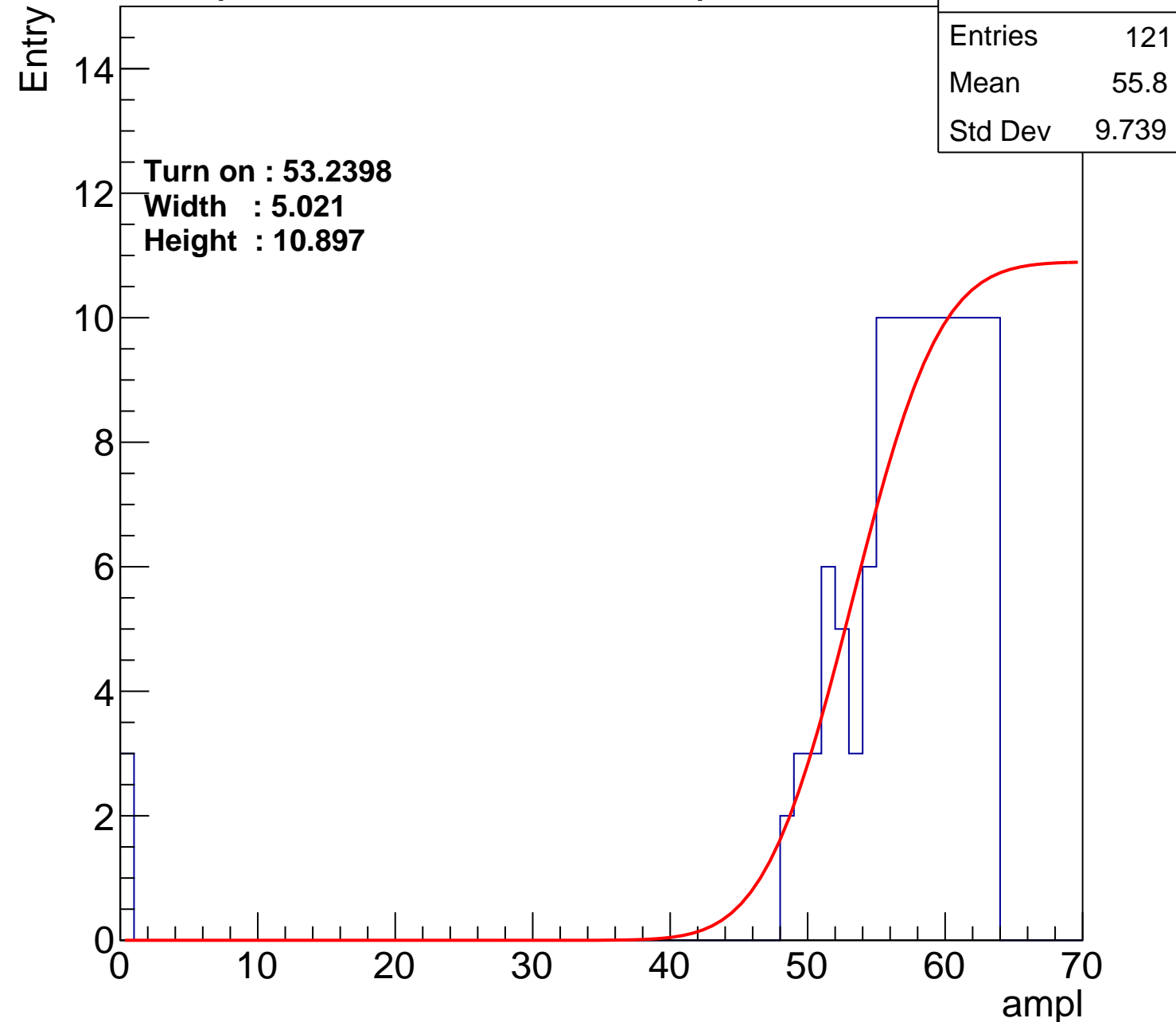
Width : 5.021

Height : 10.897

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.39
Std Dev	7.924

Turn on : 49.9585

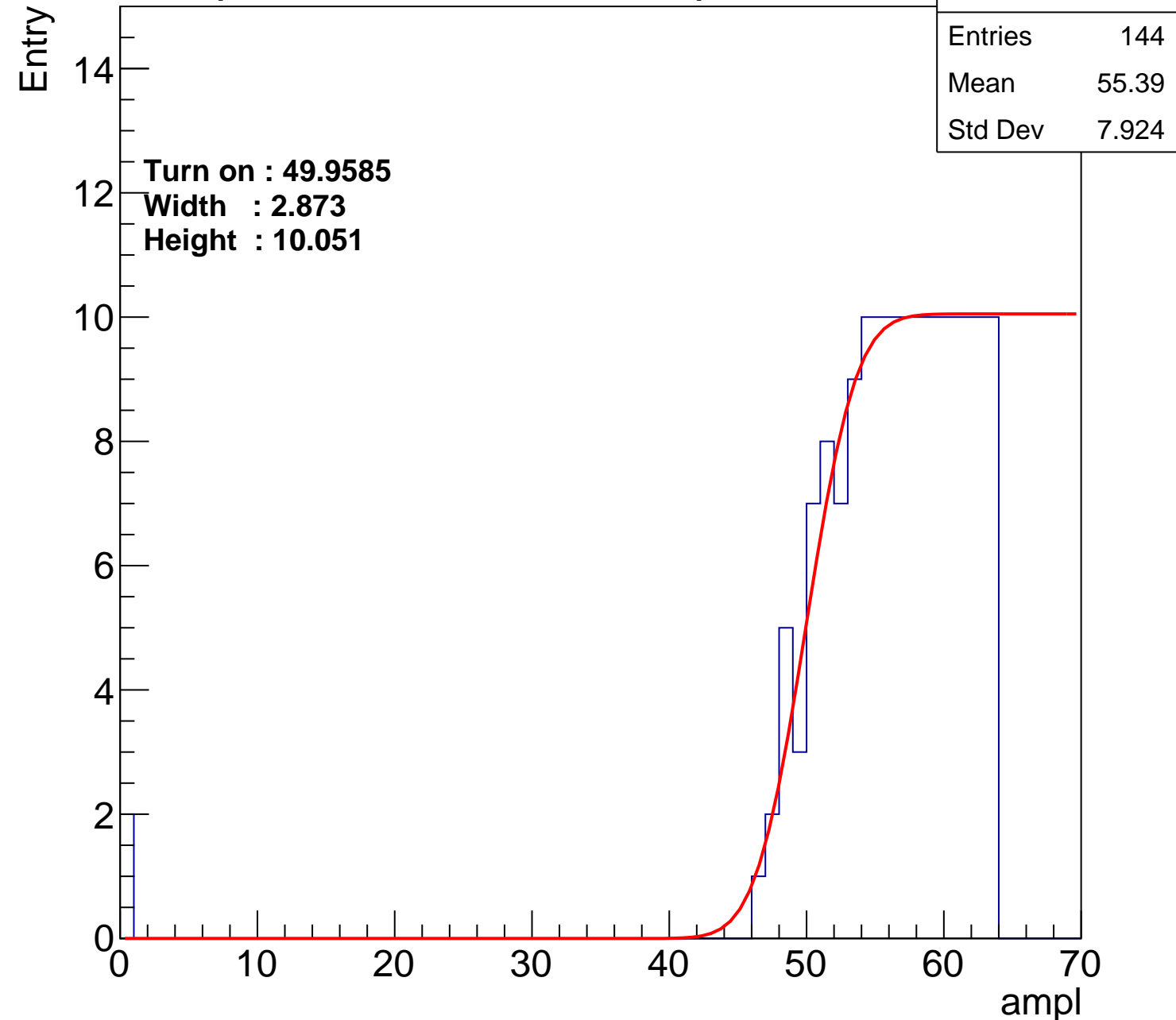
Width : 2.873

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch89

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.78
Std Dev	8.532

Turn on : 53.6738

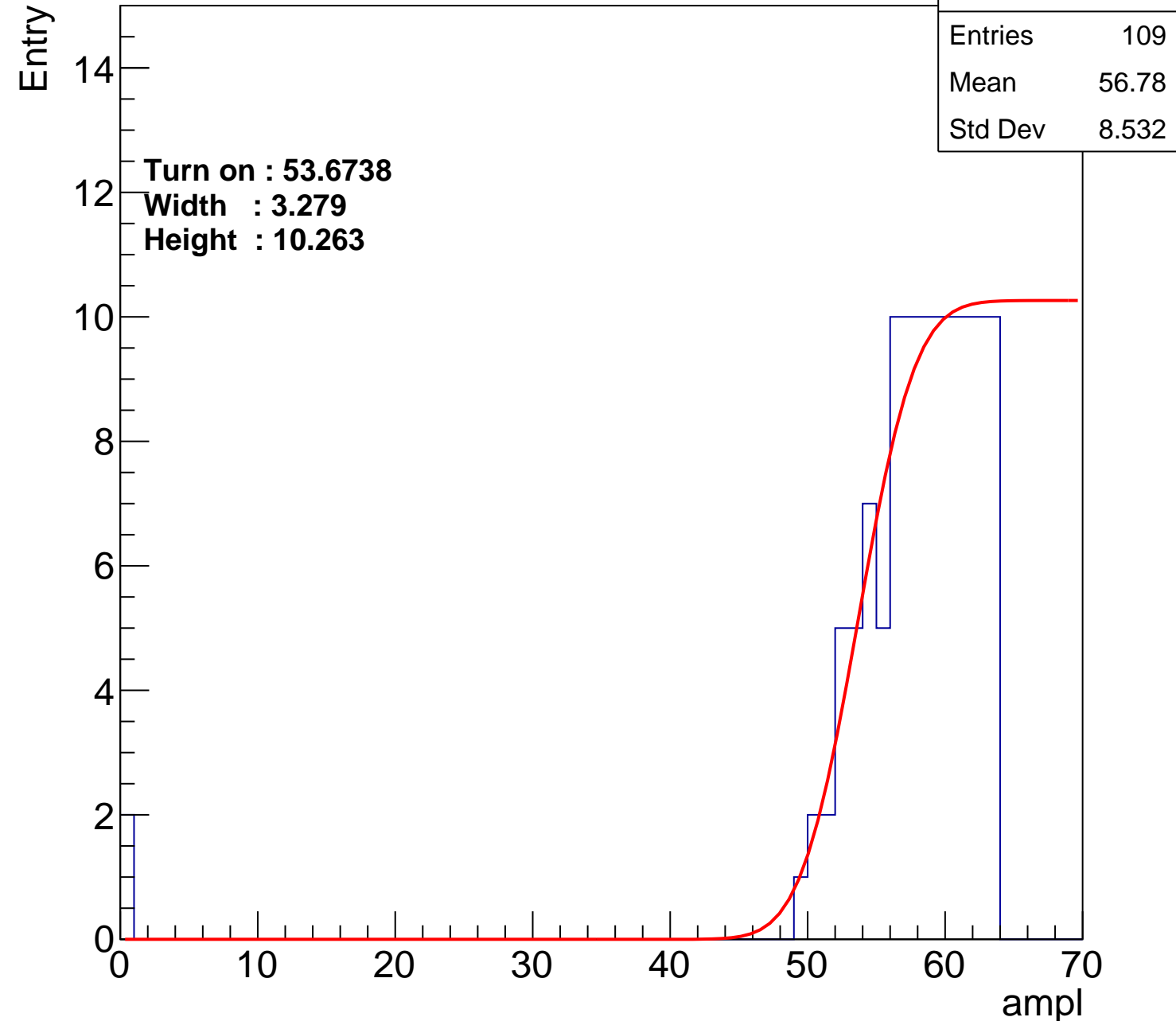
Width : 3.279

Height : 10.263

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	148
Mean	54.33
Std Dev	10.27

Turn on : 50.1878

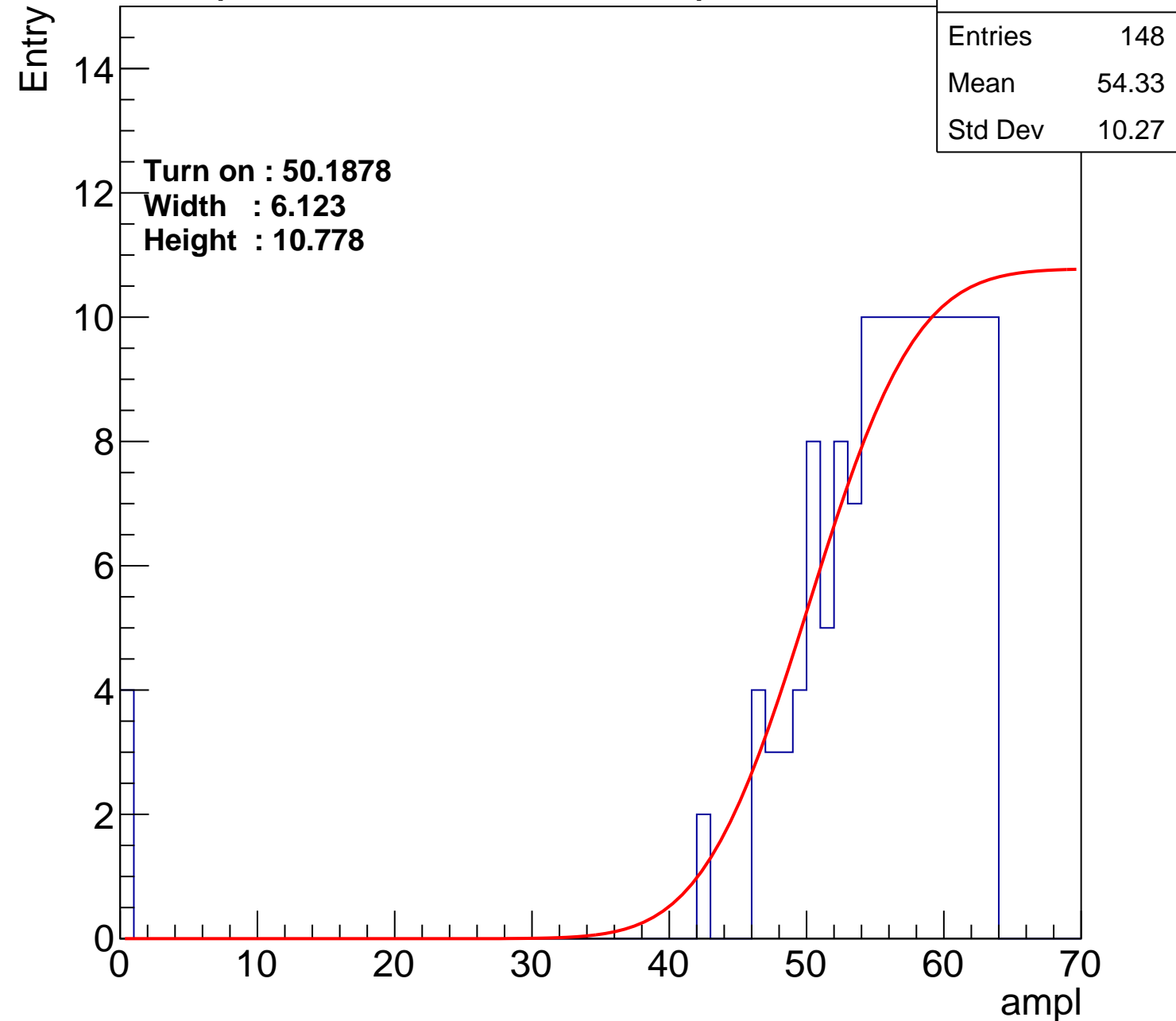
Width : 6.123

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch91

calib_packv5_040323_1717.root, FC#2, port C3

Entries	138
Mean	55.13
Std Dev	9.341

Turn on : 50.9198

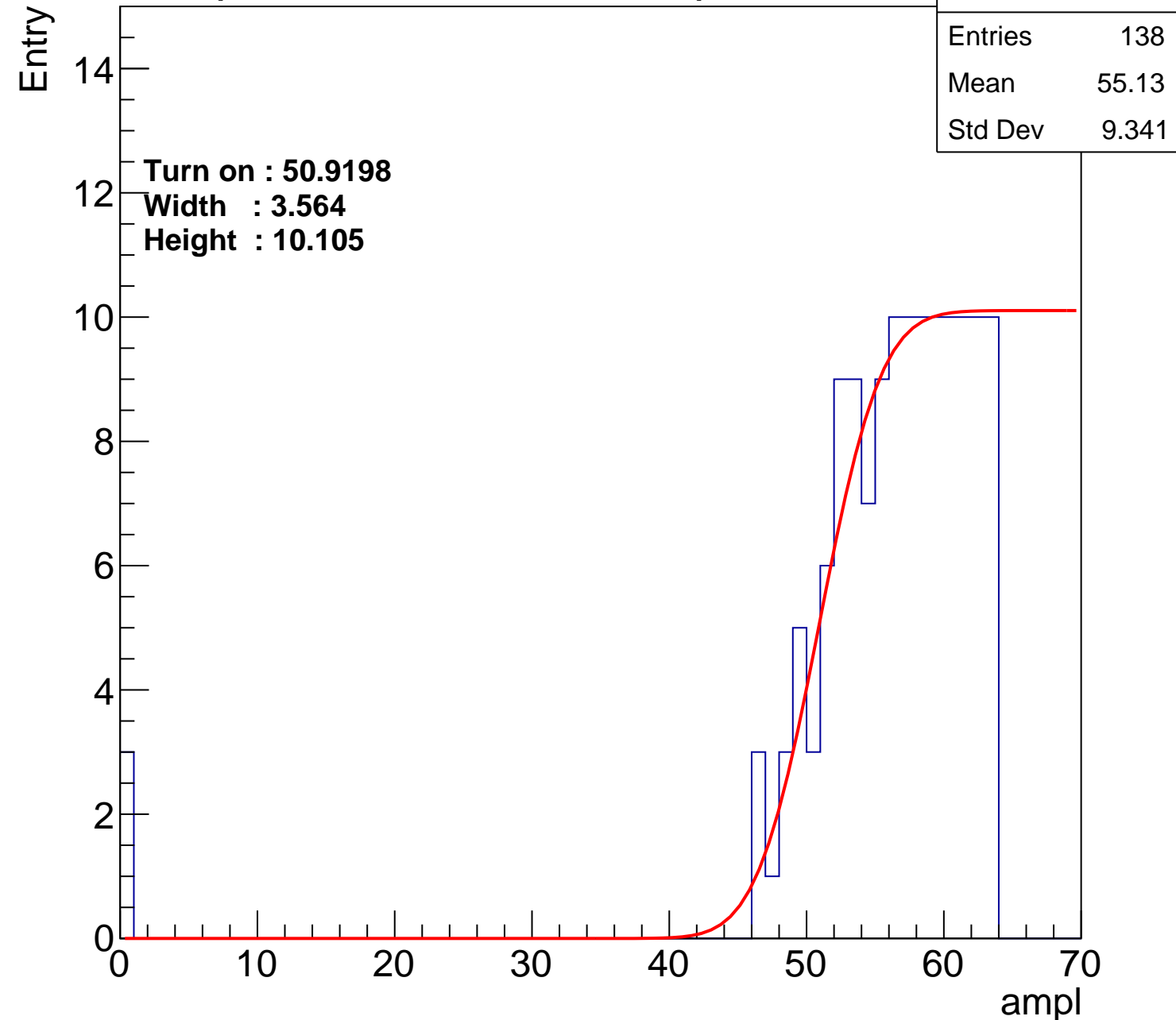
Width : 3.564

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch92

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	55.23
Std Dev	7.97

Turn on : 49.6030

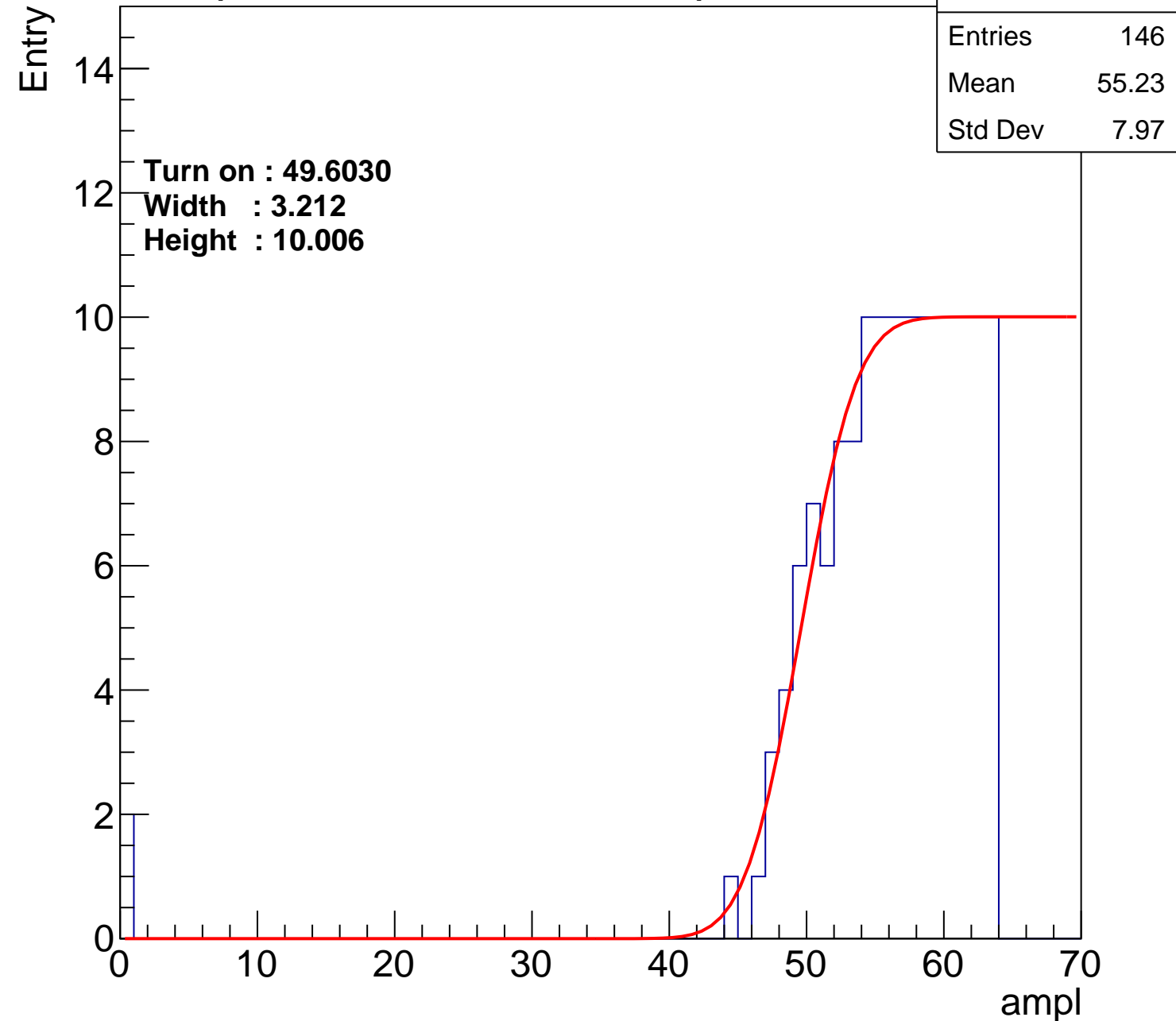
Width : 3.212

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	152
Mean	54.99
Std Dev	7.923

Turn on : 49.6756

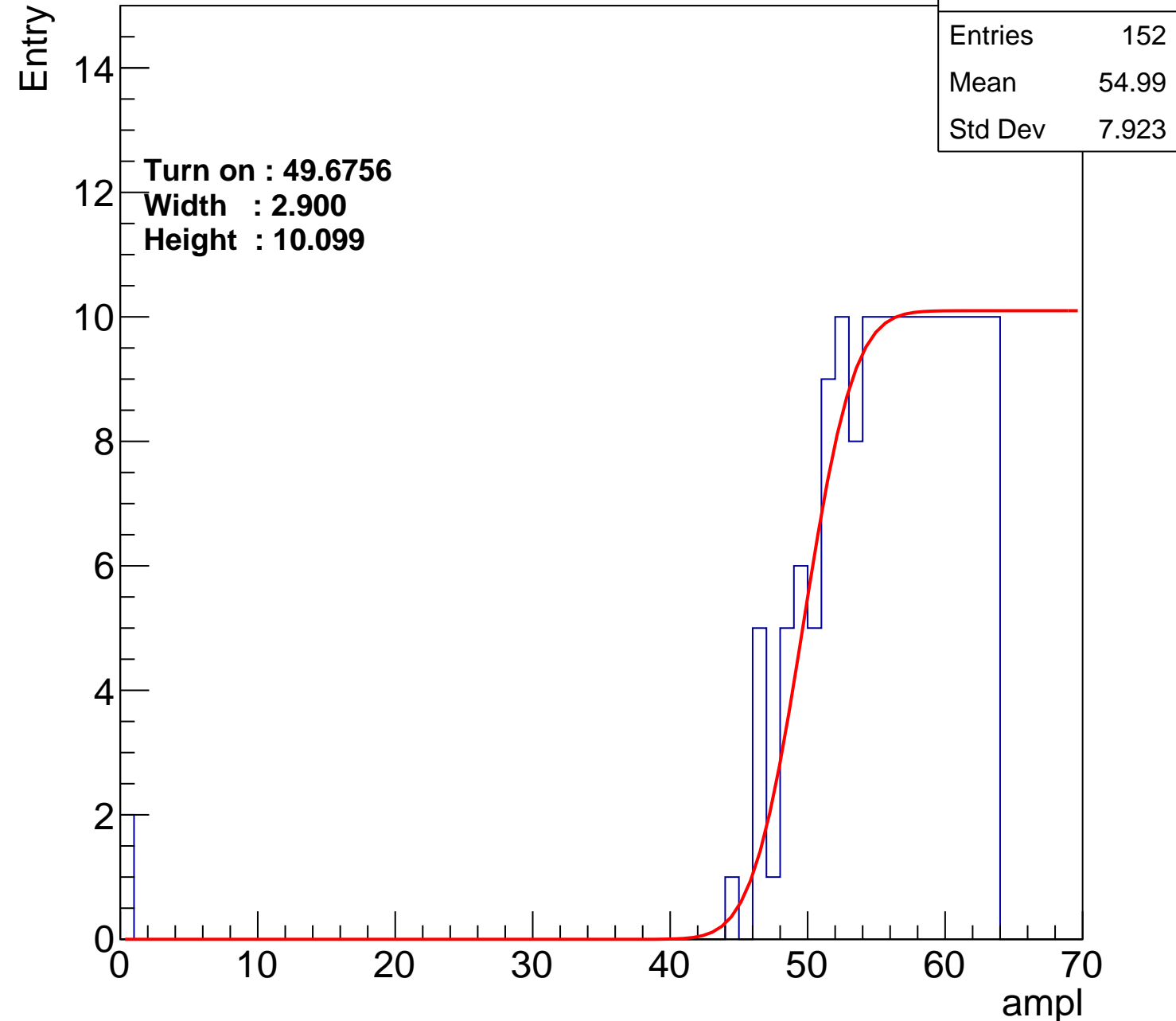
Width : 2.900

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch94

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	53.76
Std Dev	13.16

Turn on : 51.3892

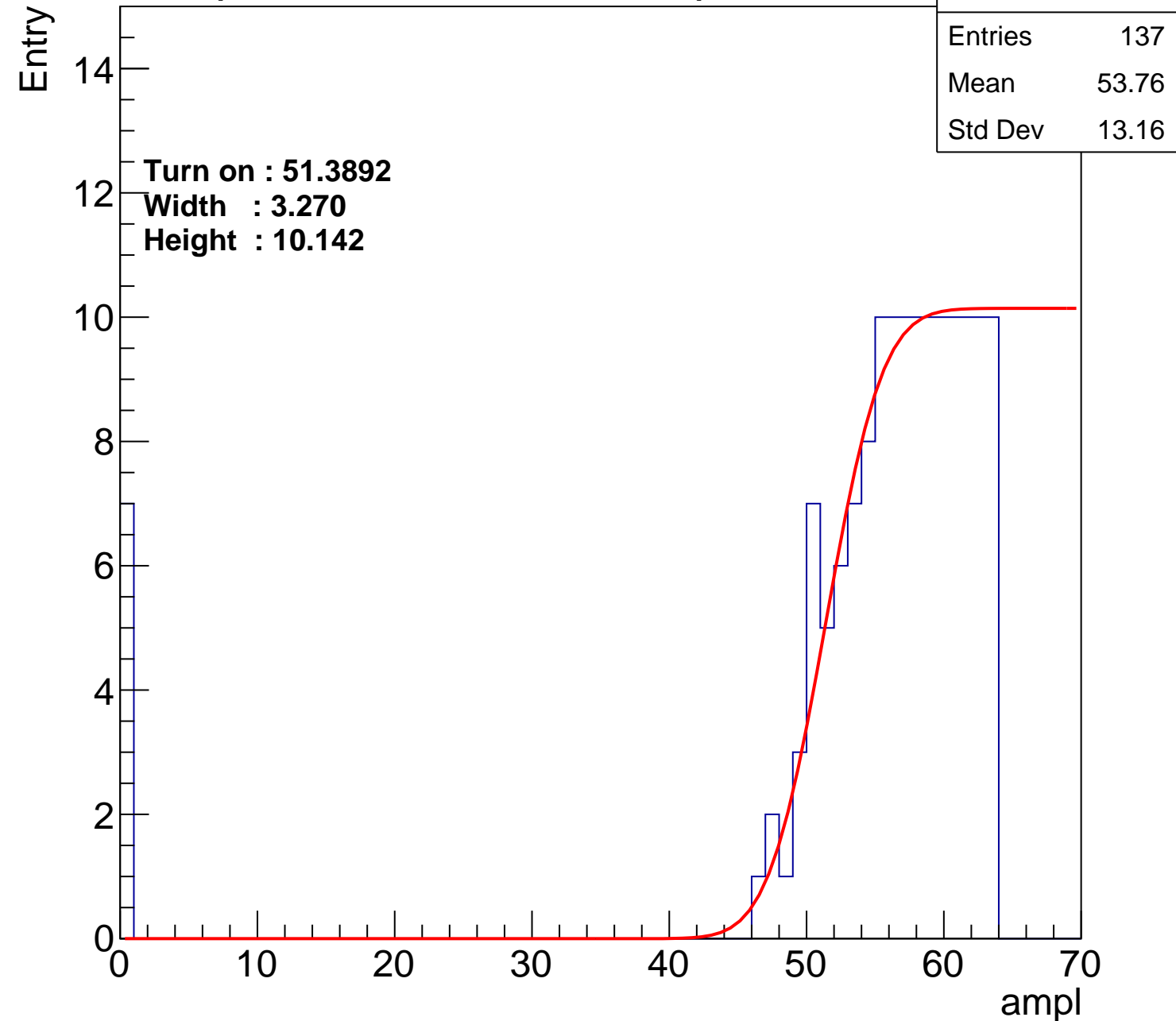
Width : 3.270

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	56.06
Std Dev	9.988

Turn on : 53.3852

Width : 3.291

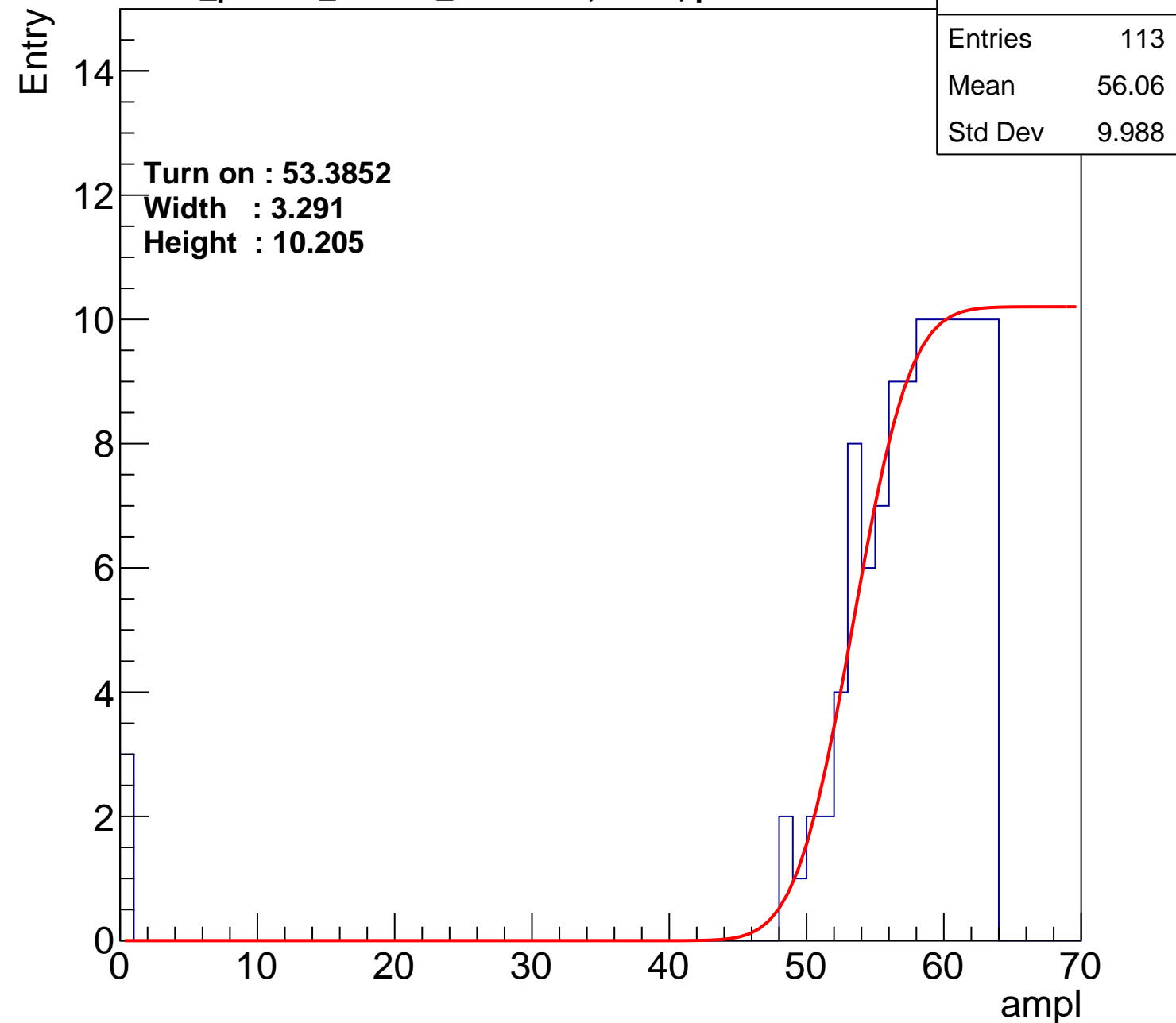
Height : 10.205

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U21-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.26
Std Dev	10.93

Turn on : 52.1249

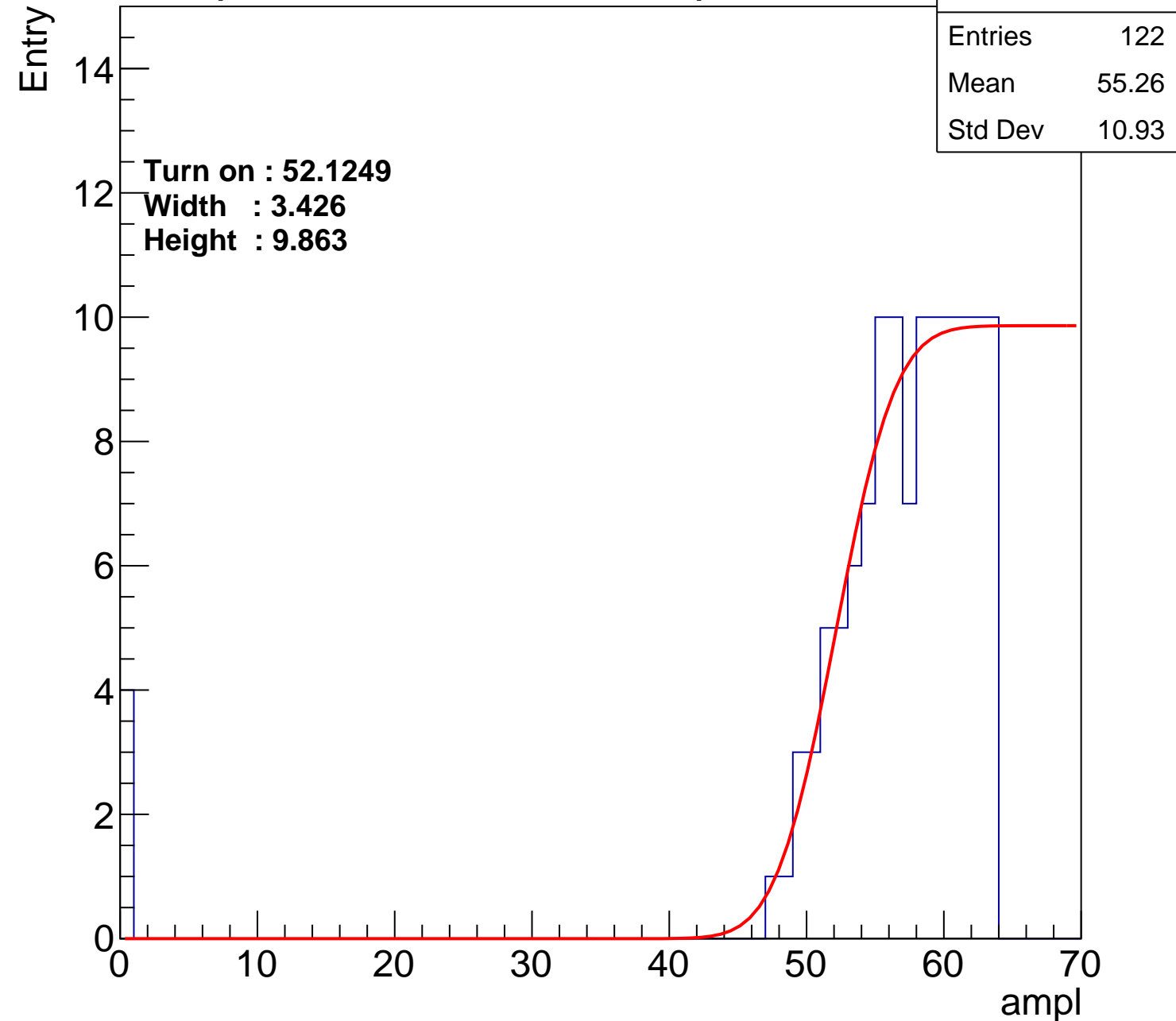
Width : 3.426

Height : 9.863

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.92
Std Dev	10.41

Turn on : 50.7042

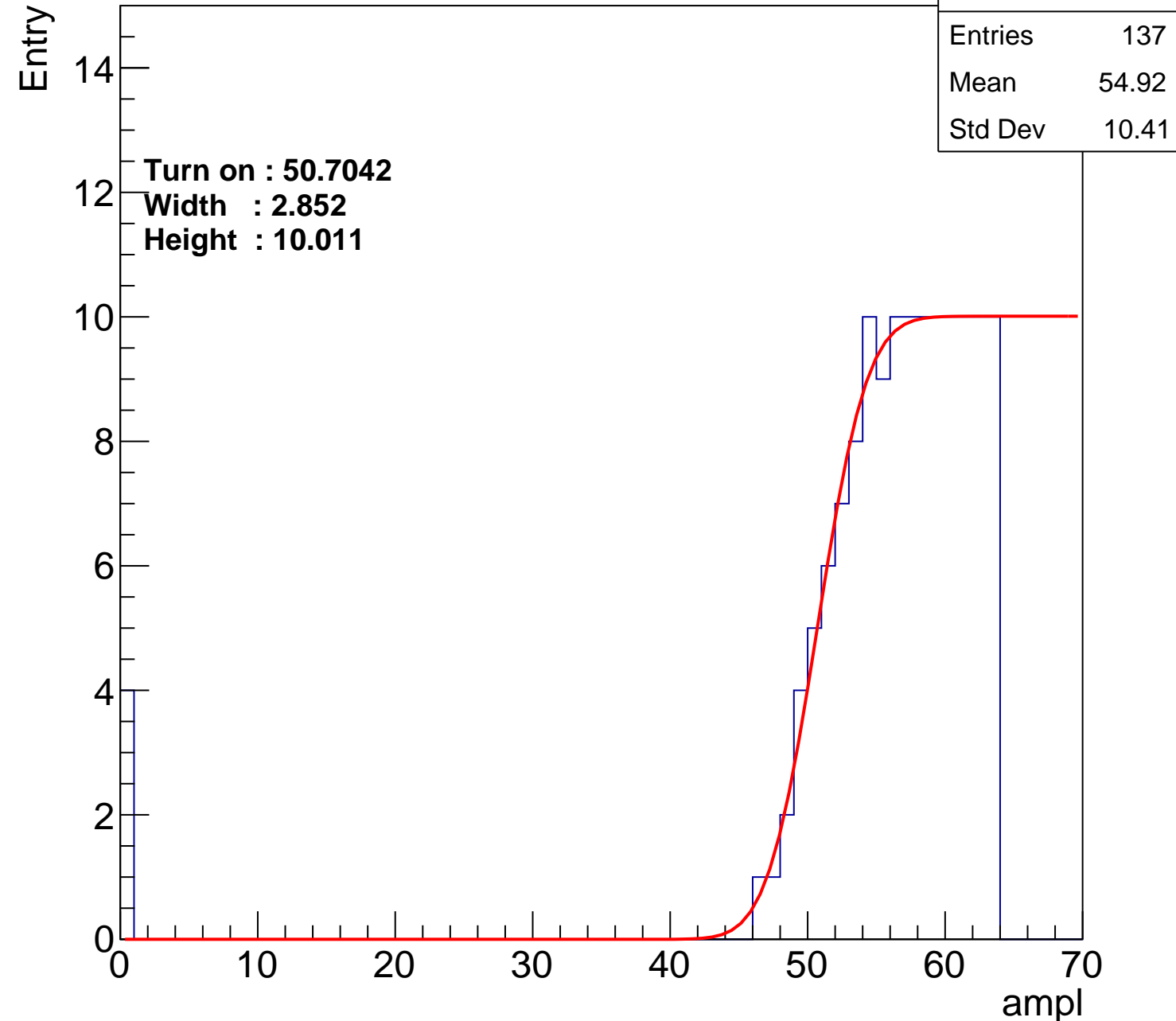
Width : 2.852

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch98

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.51
Std Dev	10.26

Turn on : 49.9596

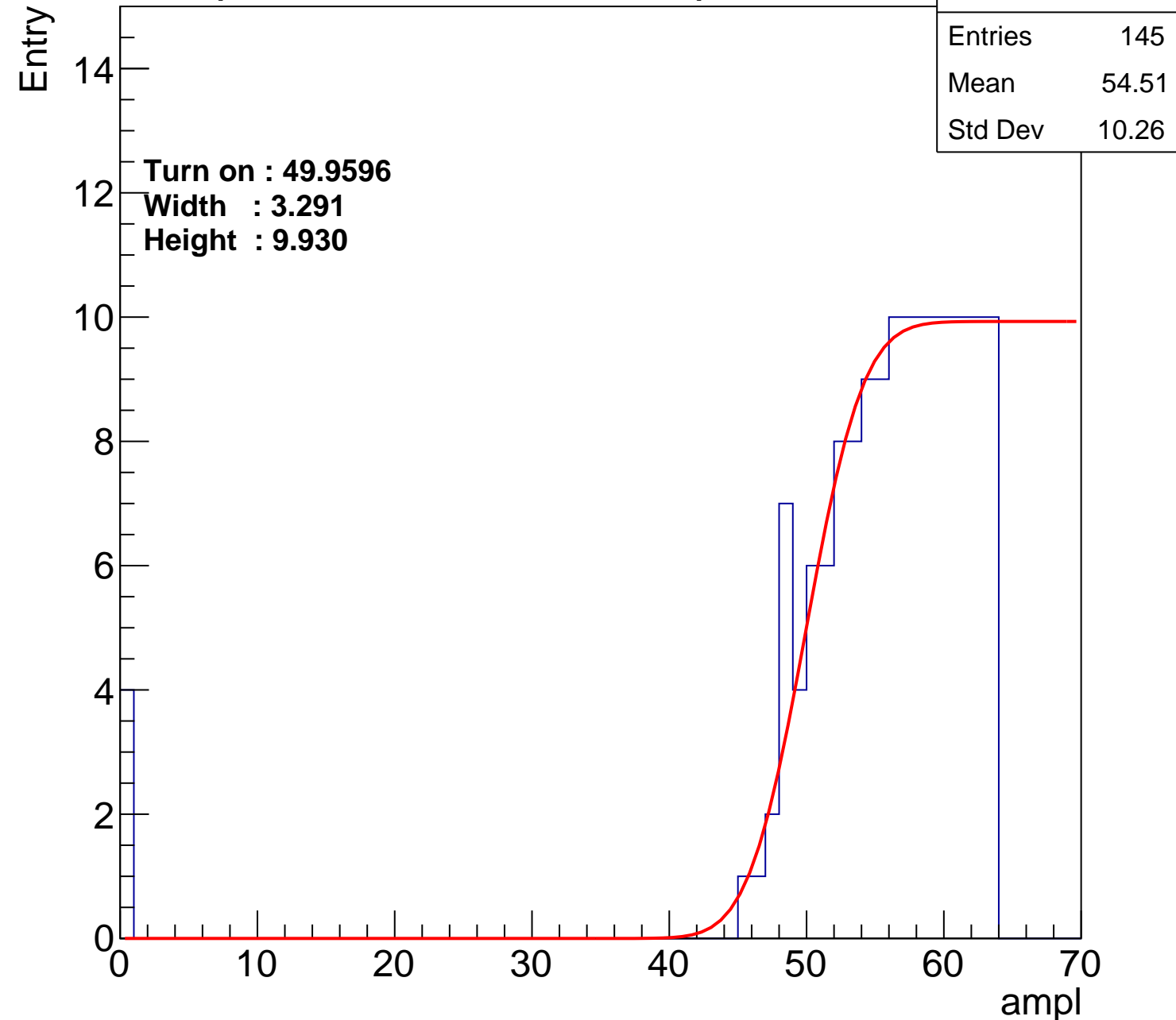
Width : 3.291

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch99

calib_packv5_040323_1717.root, FC#2, port C3

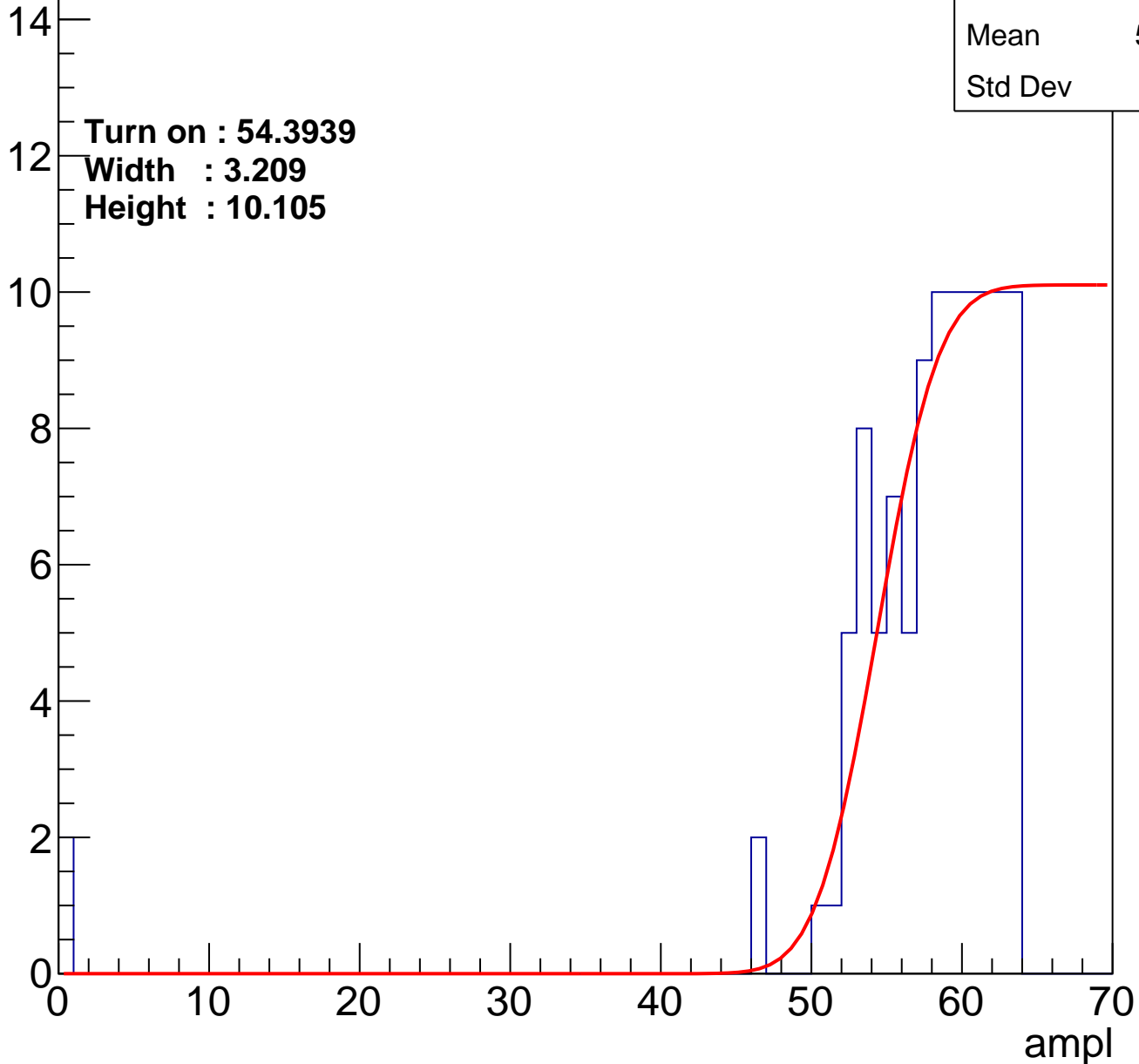
Entries	105
Mean	56.71
Std Dev	8.76

Turn on : 54.3939

Width : 3.209

Height : 10.105

Entry



B0L103S, U21-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	54.48
Std Dev	12.67

Turn on : 51.8214

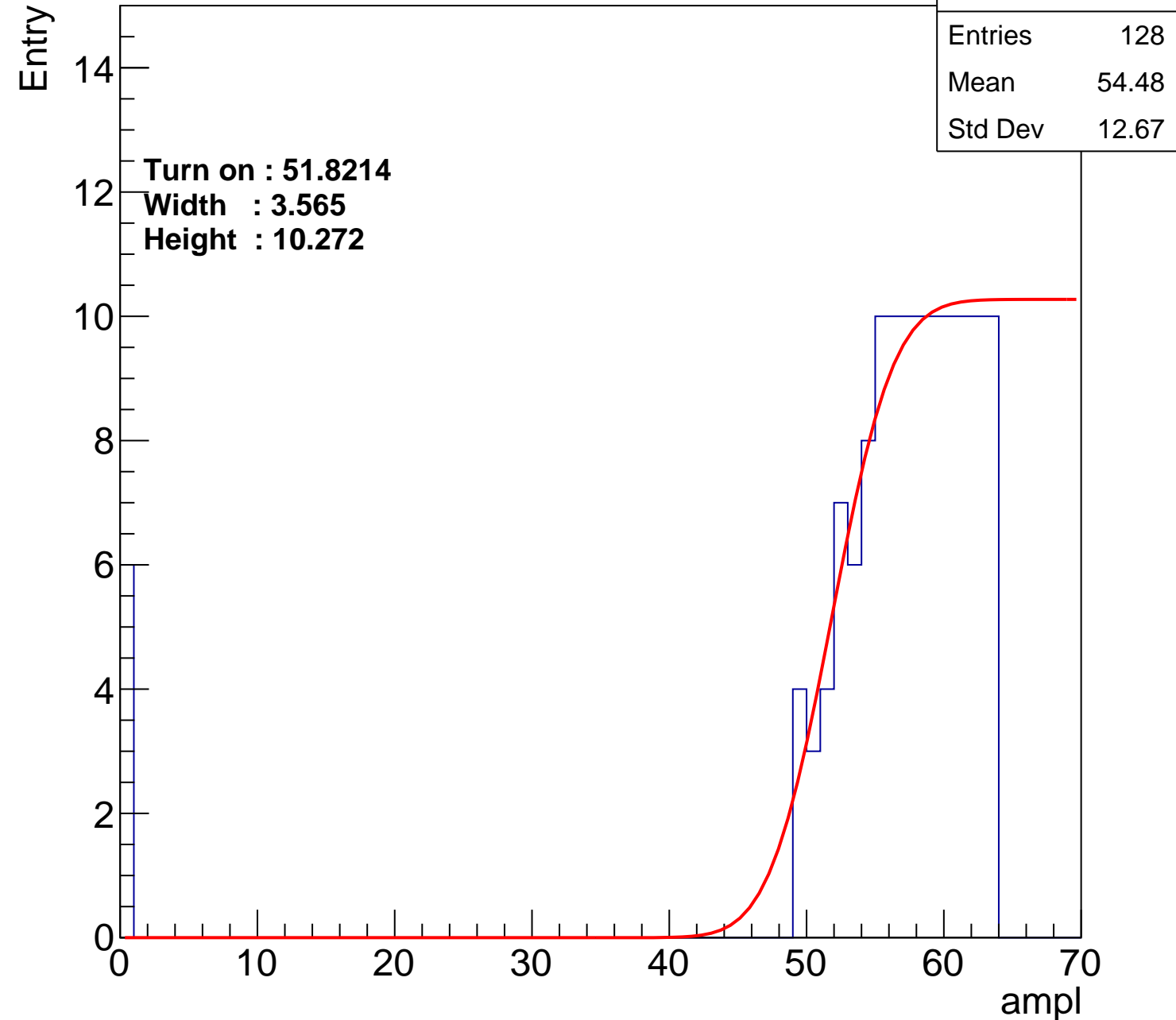
Width : 3.565

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch101

calib_packv5_040323_1717.root, FC#2, port C3

Entries	153
Mean	54.59
Std Dev	9.072

Turn on : 48.1974

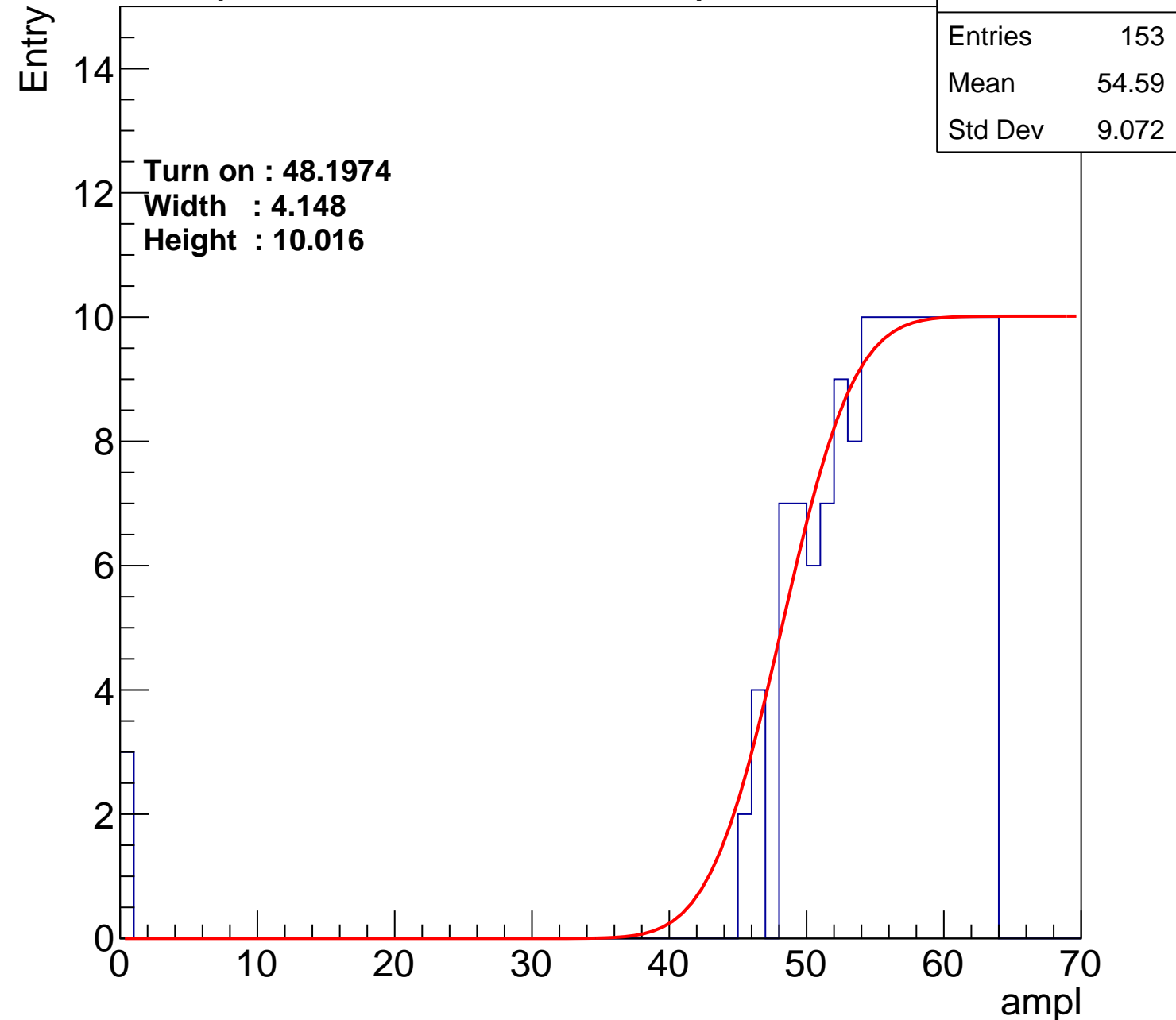
Width : 4.148

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch102

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.52
Std Dev	7.992

Turn on : 50.0748

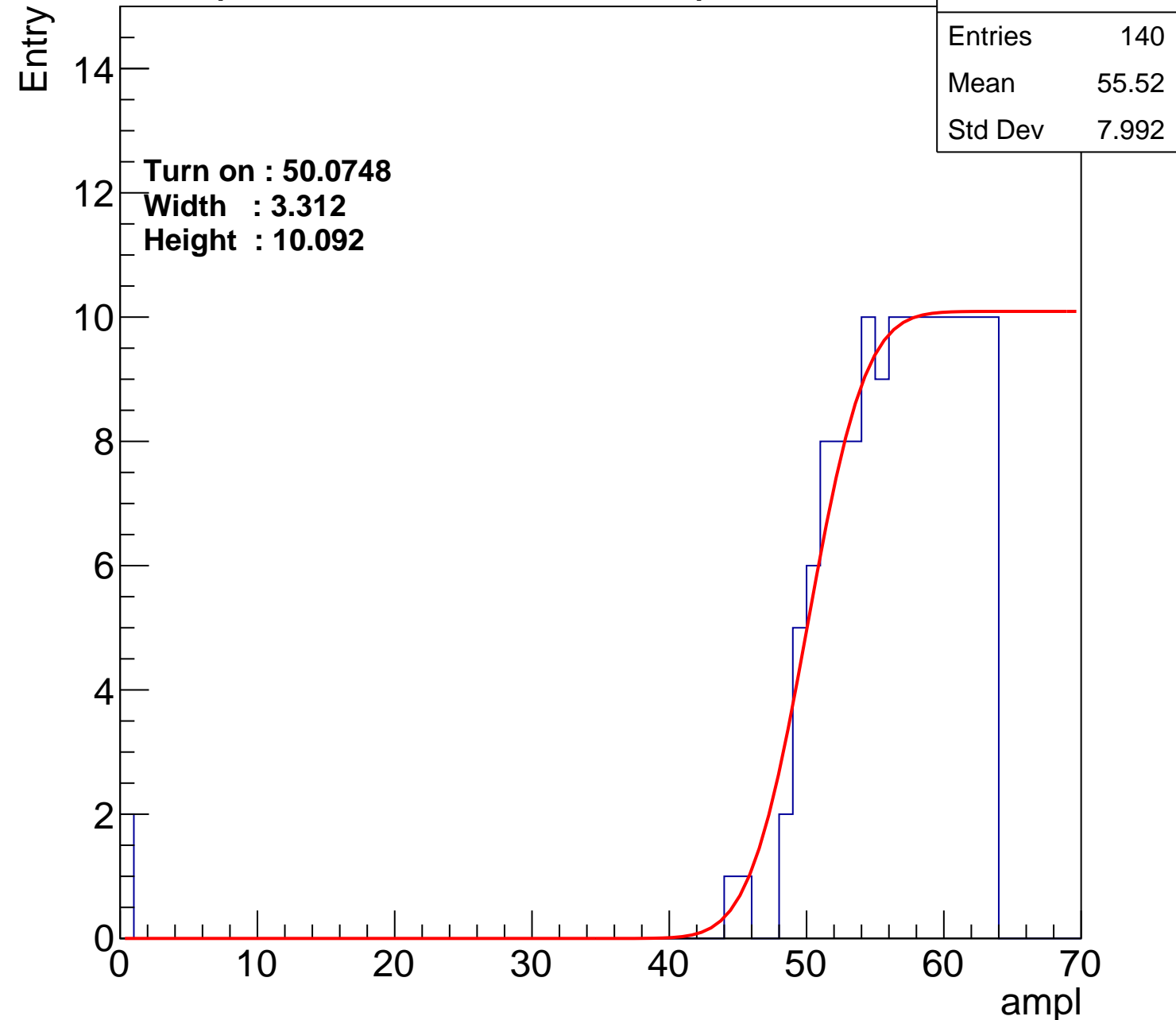
Width : 3.312

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	54.99
Std Dev	11.98

Turn on : 52.8211

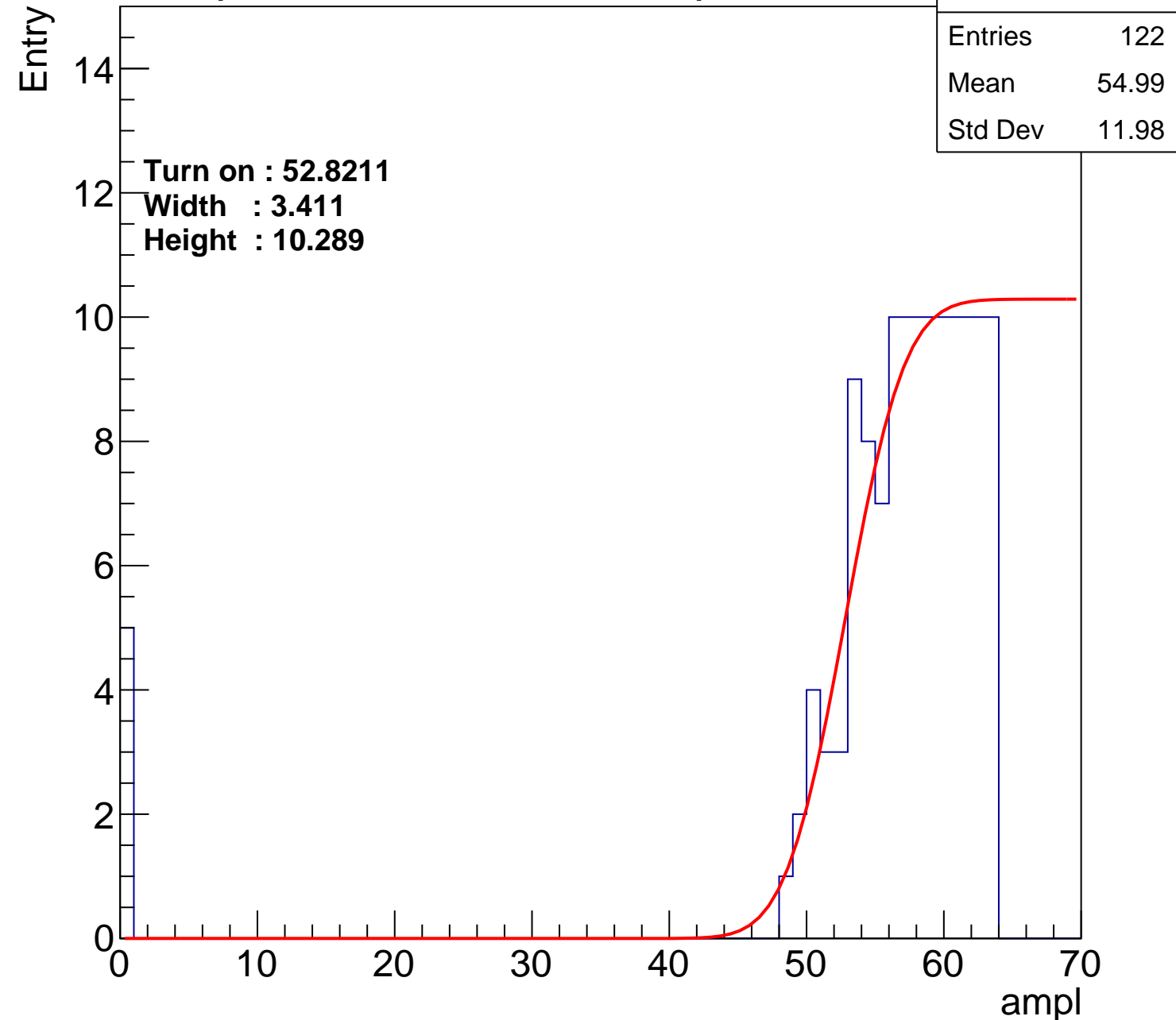
Width : 3.411

Height : 10.289

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch104

calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.34
Std Dev	7.983

Turn on : 50.1488

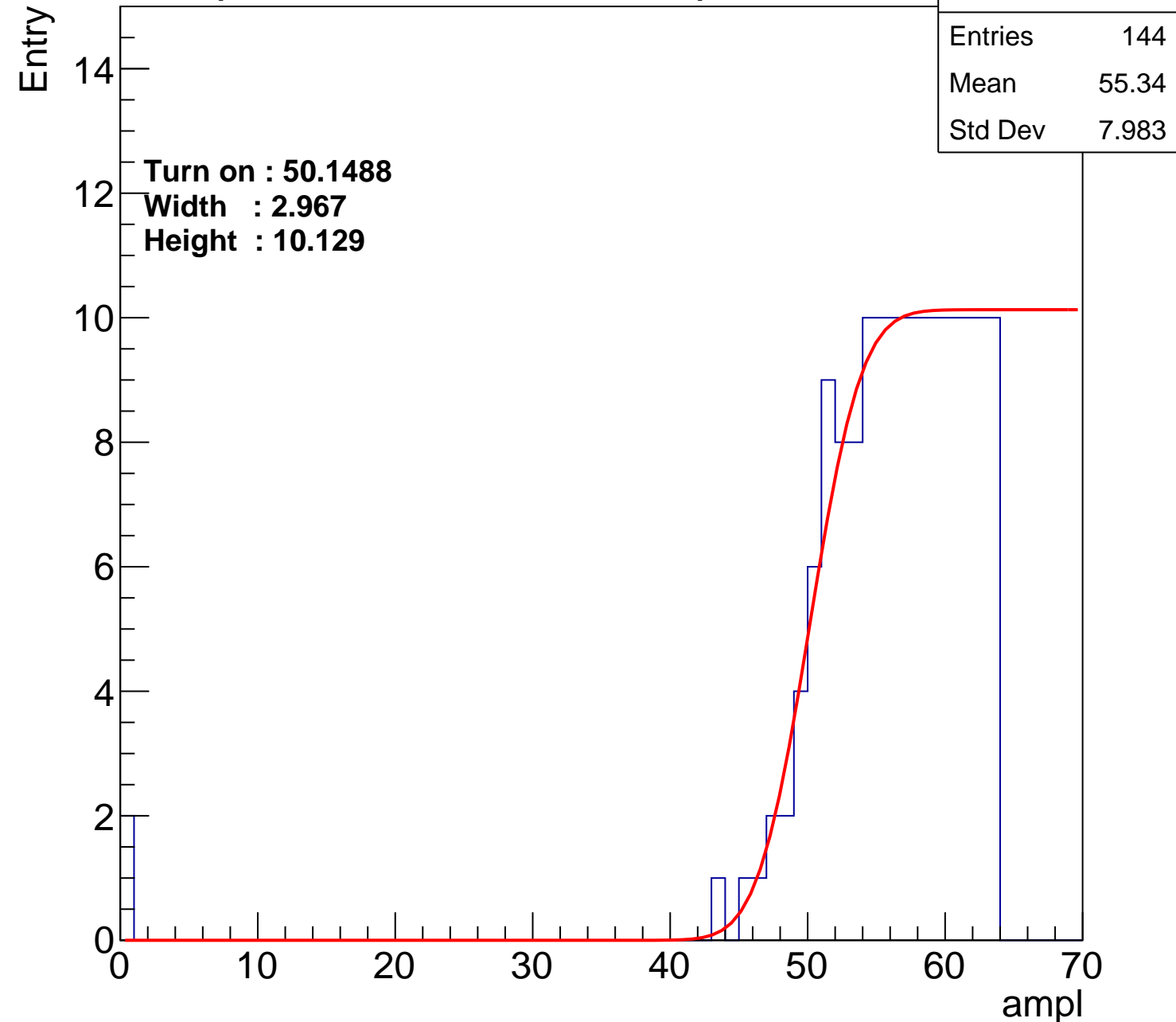
Width : 2.967

Height : 10.129

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	56.91
Std Dev	6.548

Turn on : 52.7276

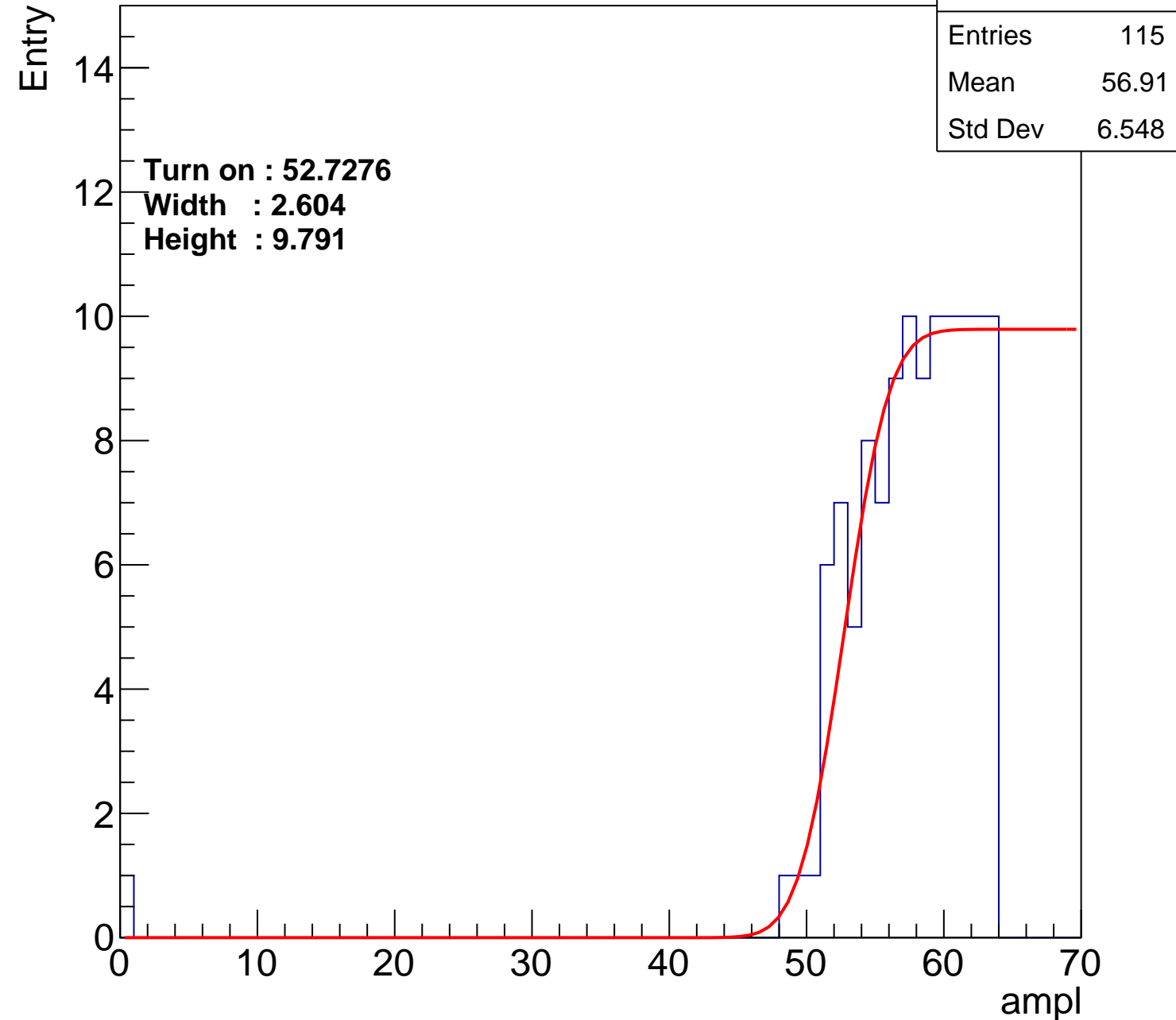
Width : 2.604

Height : 9.791

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	54.88
Std Dev	9.098

Turn on : 49.4840

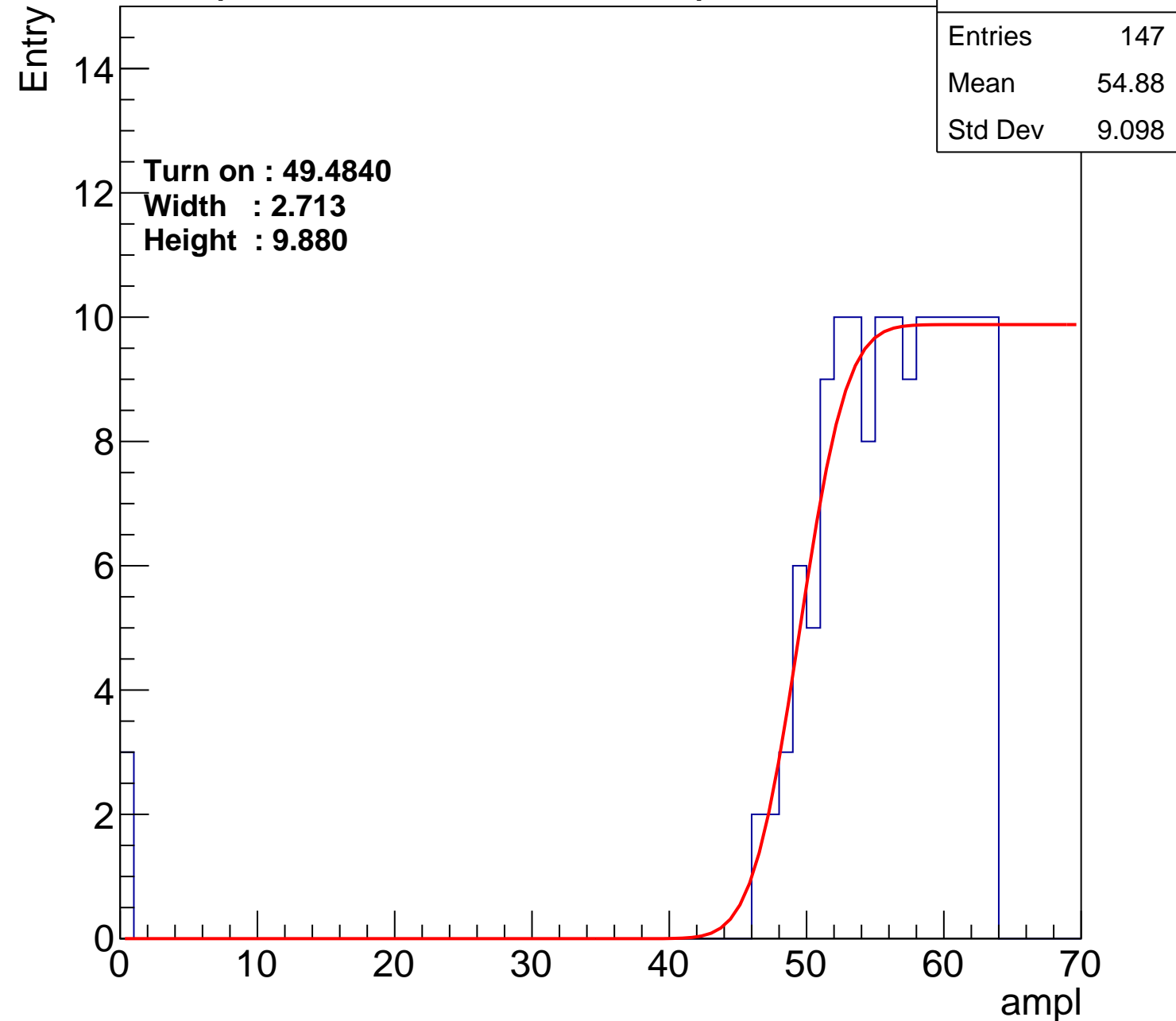
Width : 2.713

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.35
Std Dev	6.501

Turn on : 52.1884

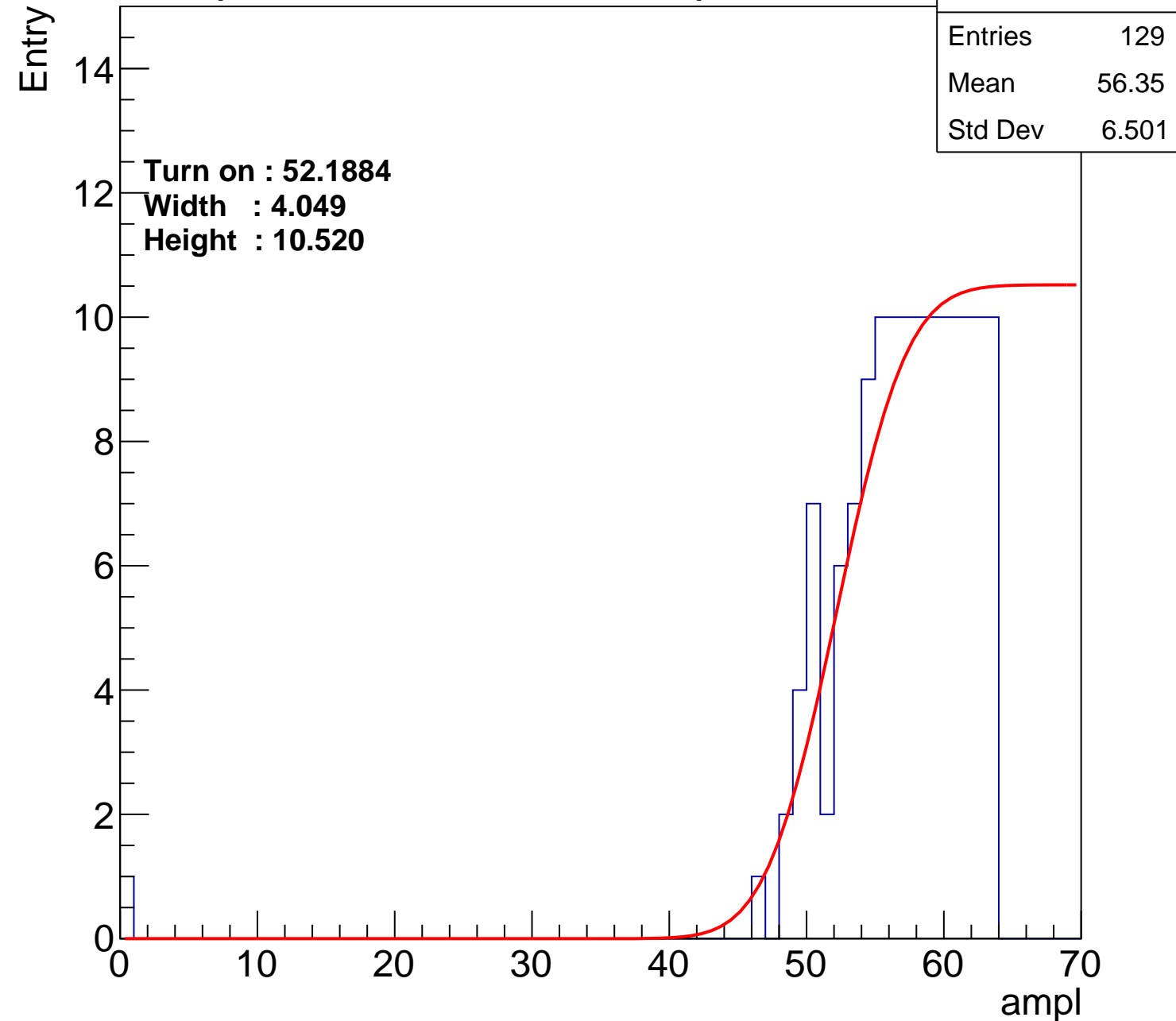
Width : 4.049

Height : 10.520

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch108

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.95
Std Dev	10.64

Turn on : 51.1459

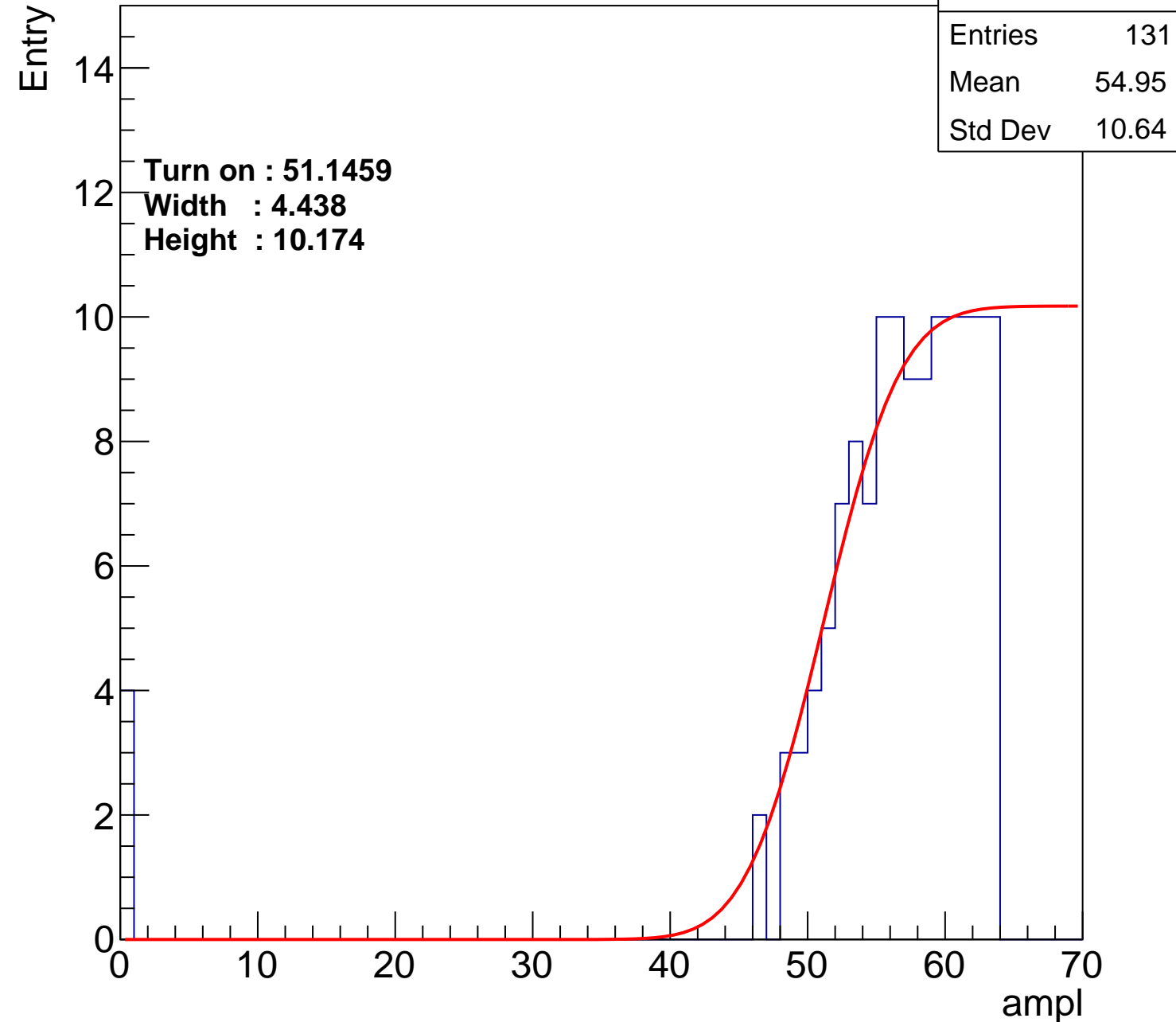
Width : 4.438

Height : 10.174

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch109

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	57.05
Std Dev	6.525

Turn on : 53.1035

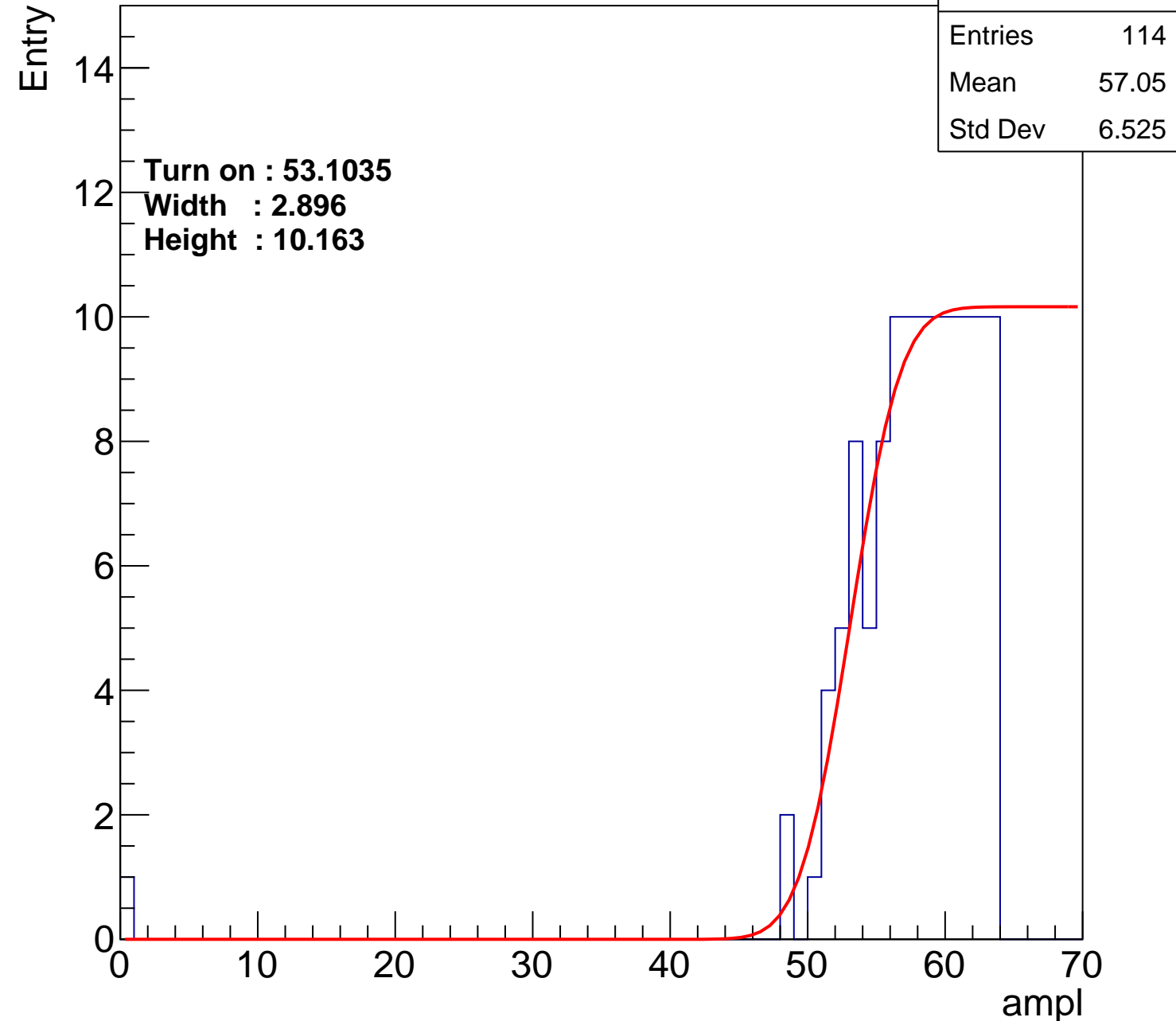
Width : 2.896

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch110

calib_packv5_040323_1717.root, FC#2, port C3

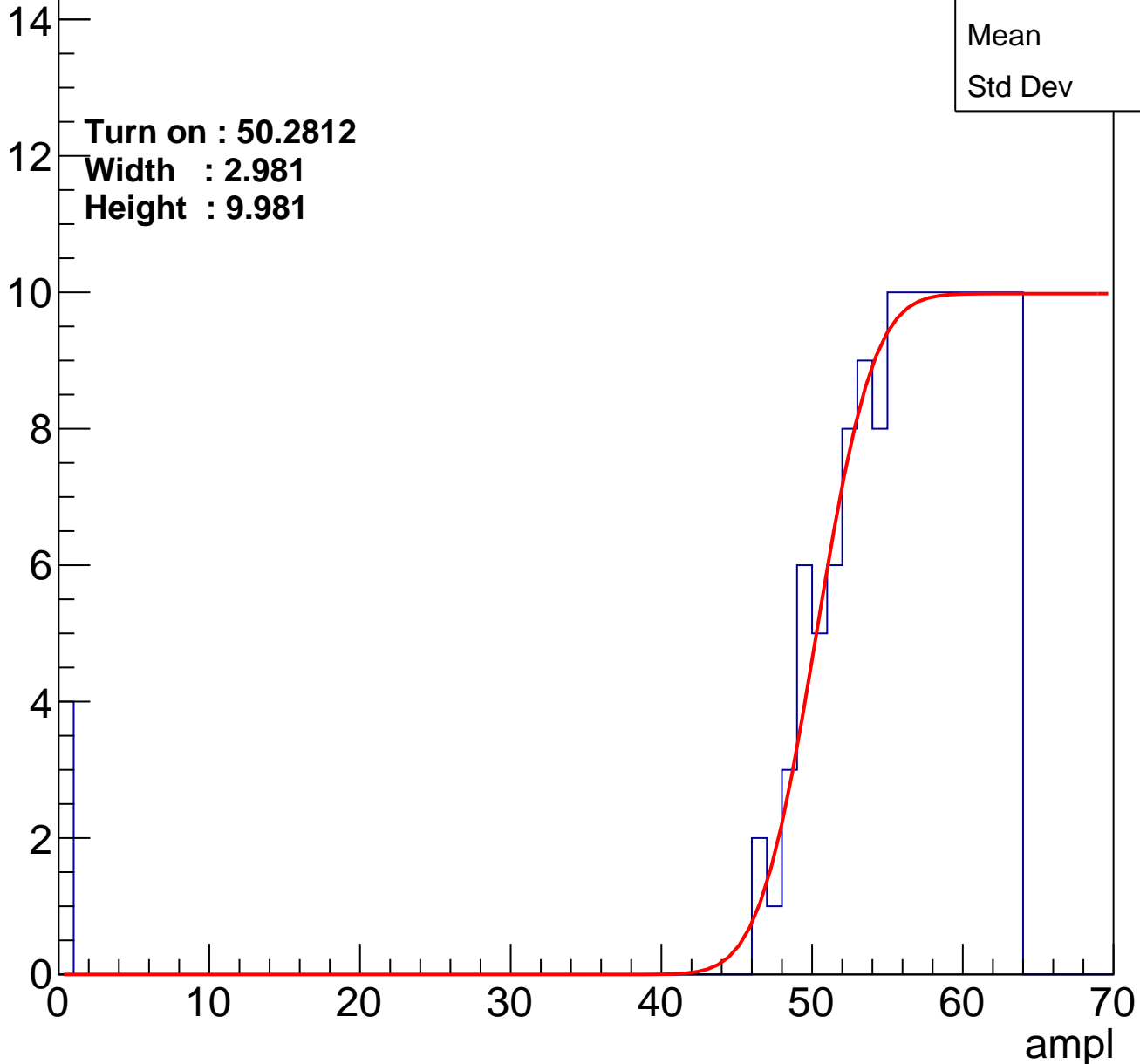
Entries	142
Mean	54.7
Std Dev	10.3

Turn on : 50.2812

Width : 2.981

Height : 9.981

Entry



B0L103S, U21-ch111

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55.83
Std Dev	9.793

Turn on : 52.5863

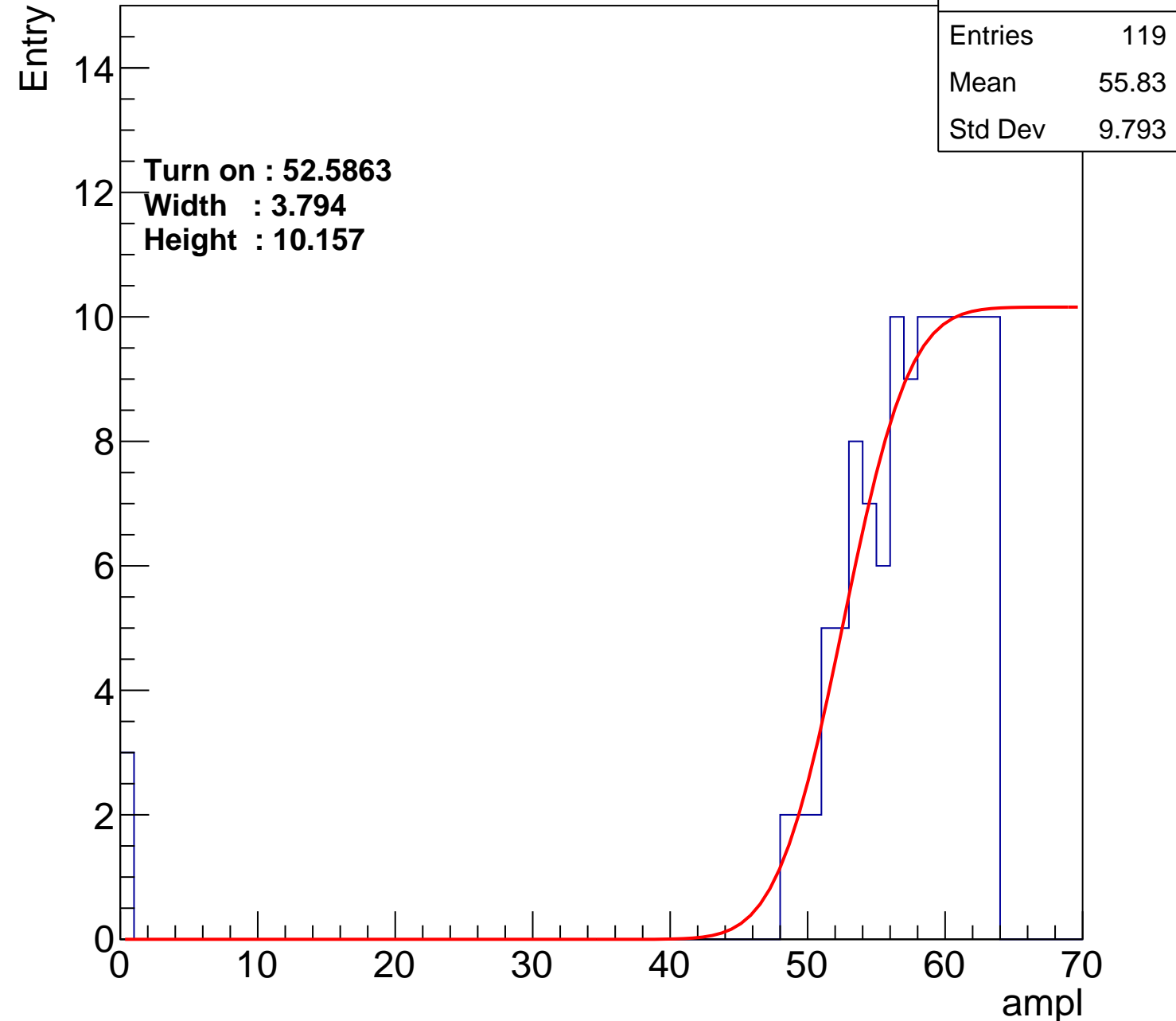
Width : 3.794

Height : 10.157

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch112

calib_packv5_040323_1717.root, FC#2, port C3

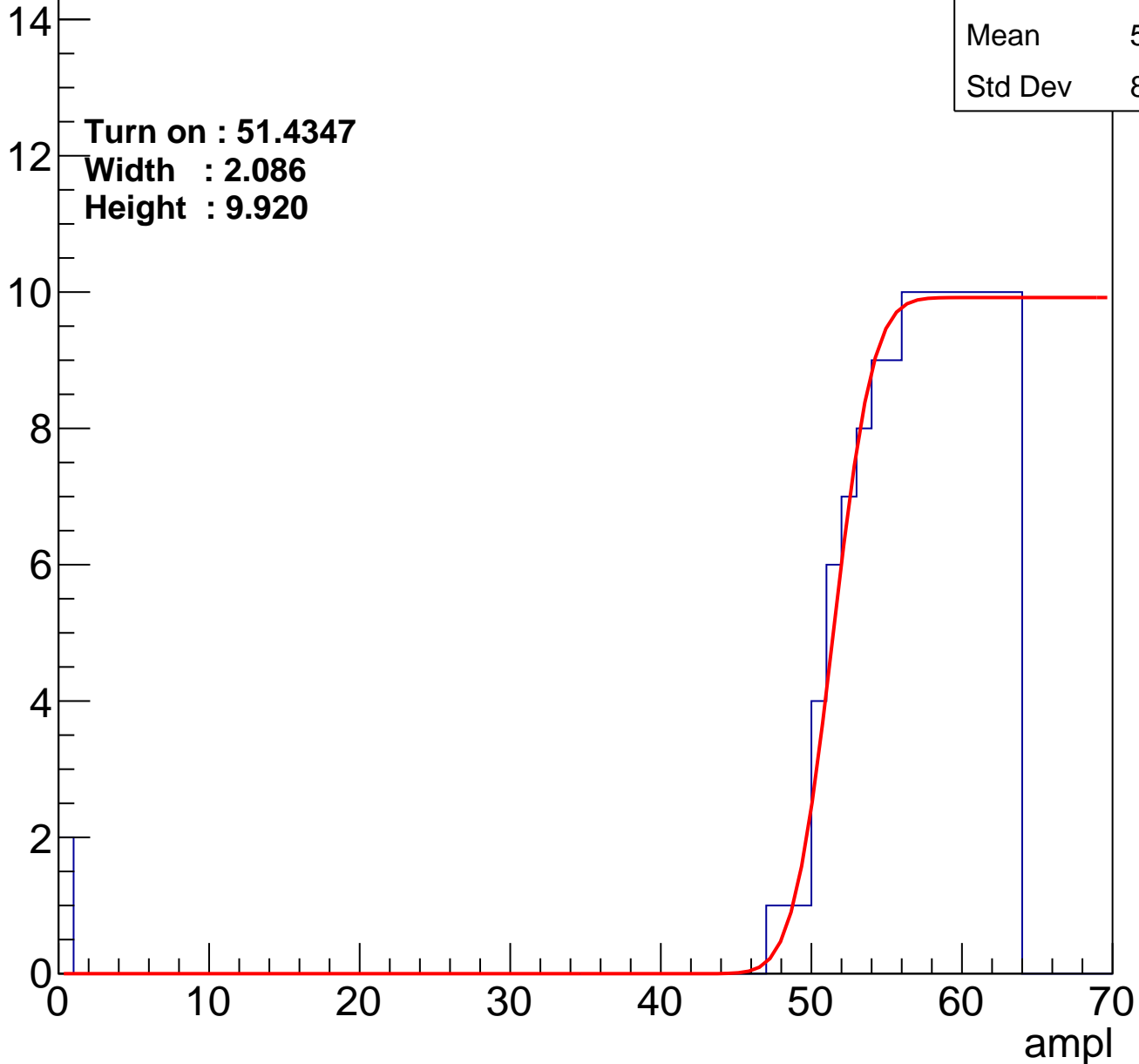
Entries	128
Mean	56.09
Std Dev	8.095

Turn on : 51.4347

Width : 2.086

Height : 9.920

Entry



B0L103S, U21-ch113

calib_packv5_040323_1717.root, FC#2, port C3

Entries	105
Mean	56.35
Std Dev	10.26

Turn on : 54.3399

Width : 2.648

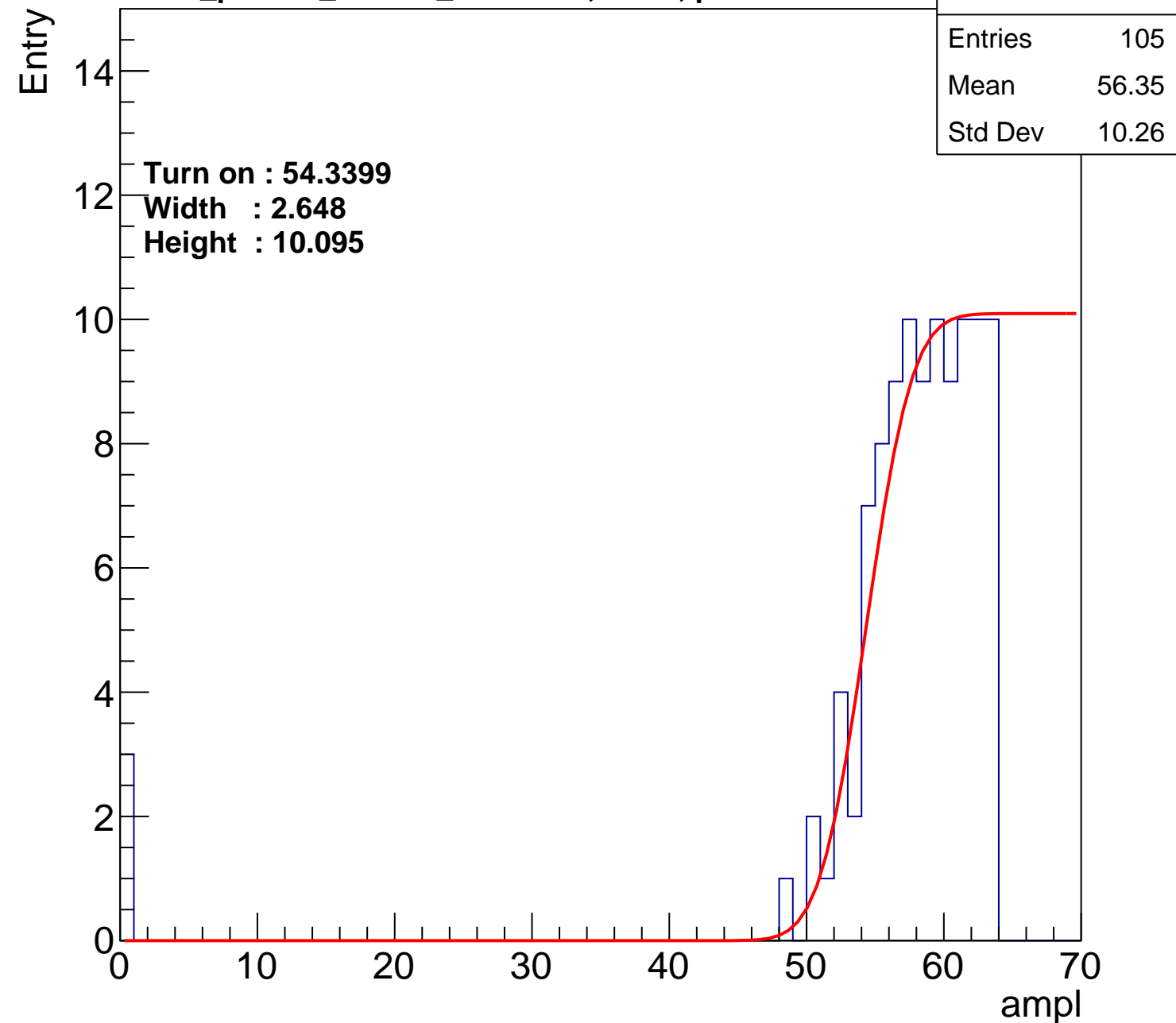
Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U21-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	57.01
Std Dev	6.567

Turn on : 53.4442

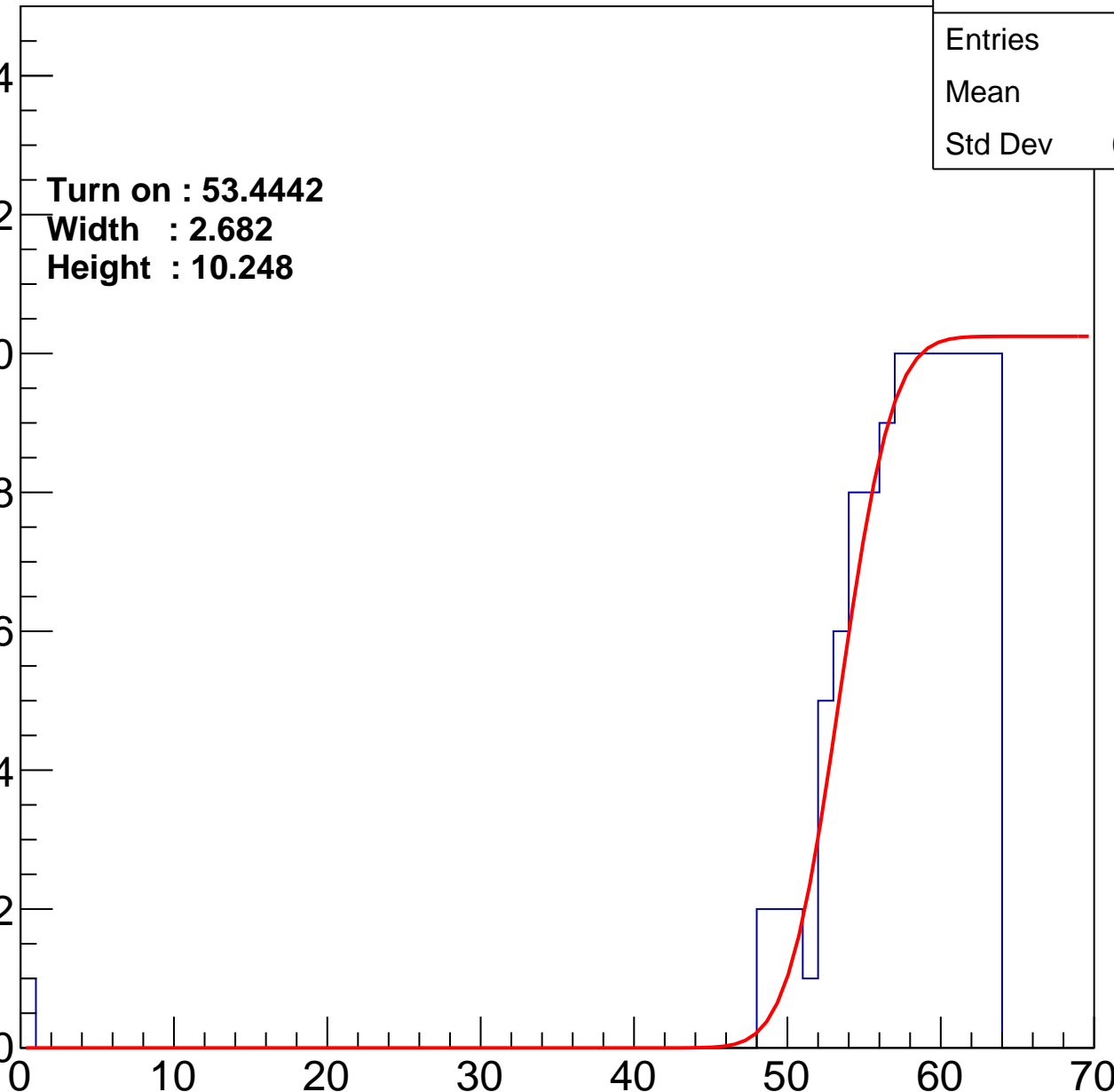
Width : 2.682

Height : 10.248

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch115

calib_packv5_040323_1717.root, FC#2, port C3

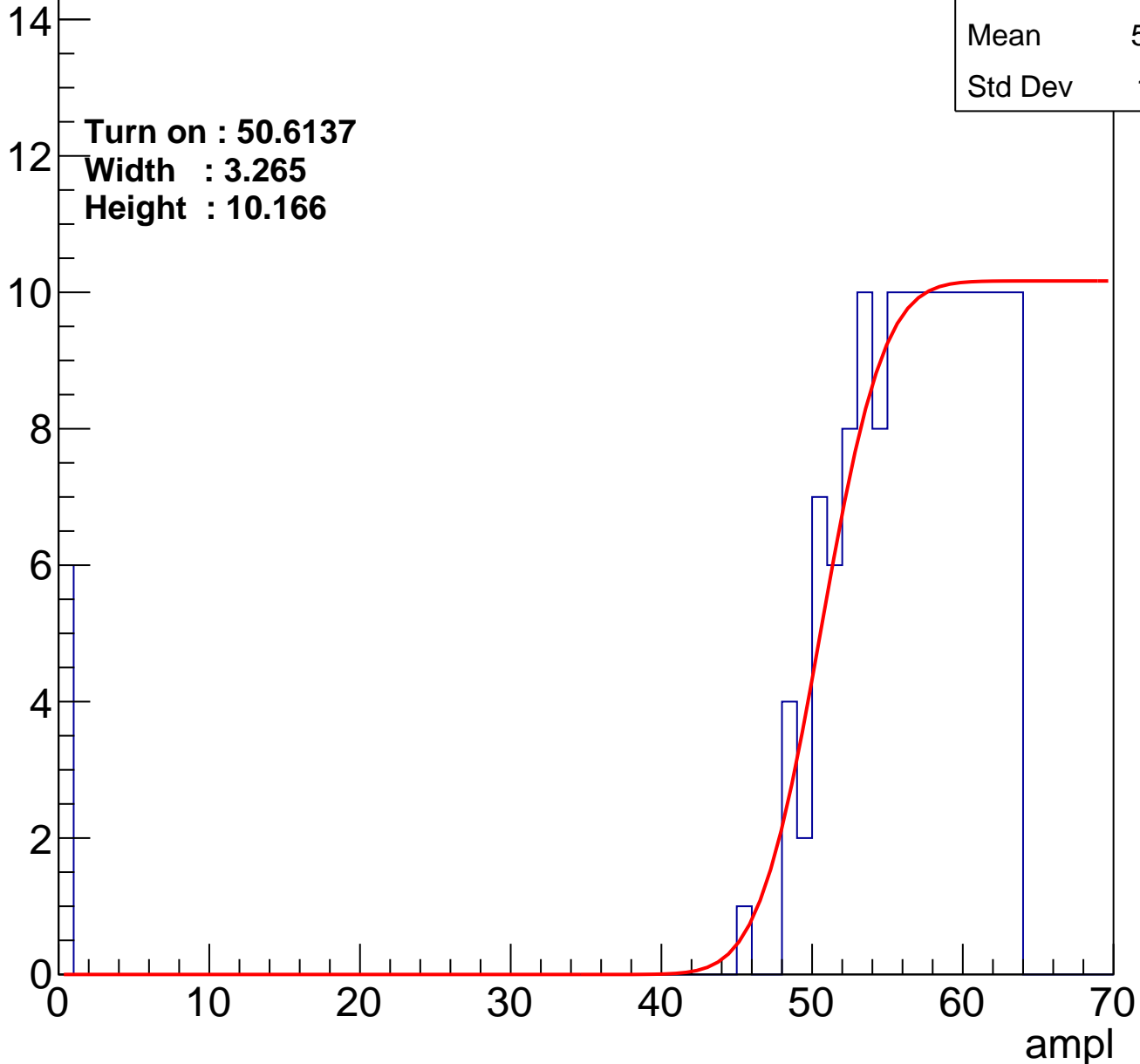
Entries	142
Mean	54.08
Std Dev	12.11

Turn on : 50.6137

Width : 3.265

Height : 10.166

Entry



B0L103S, U21-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	54.91
Std Dev	10.45

Turn on : 51.0189

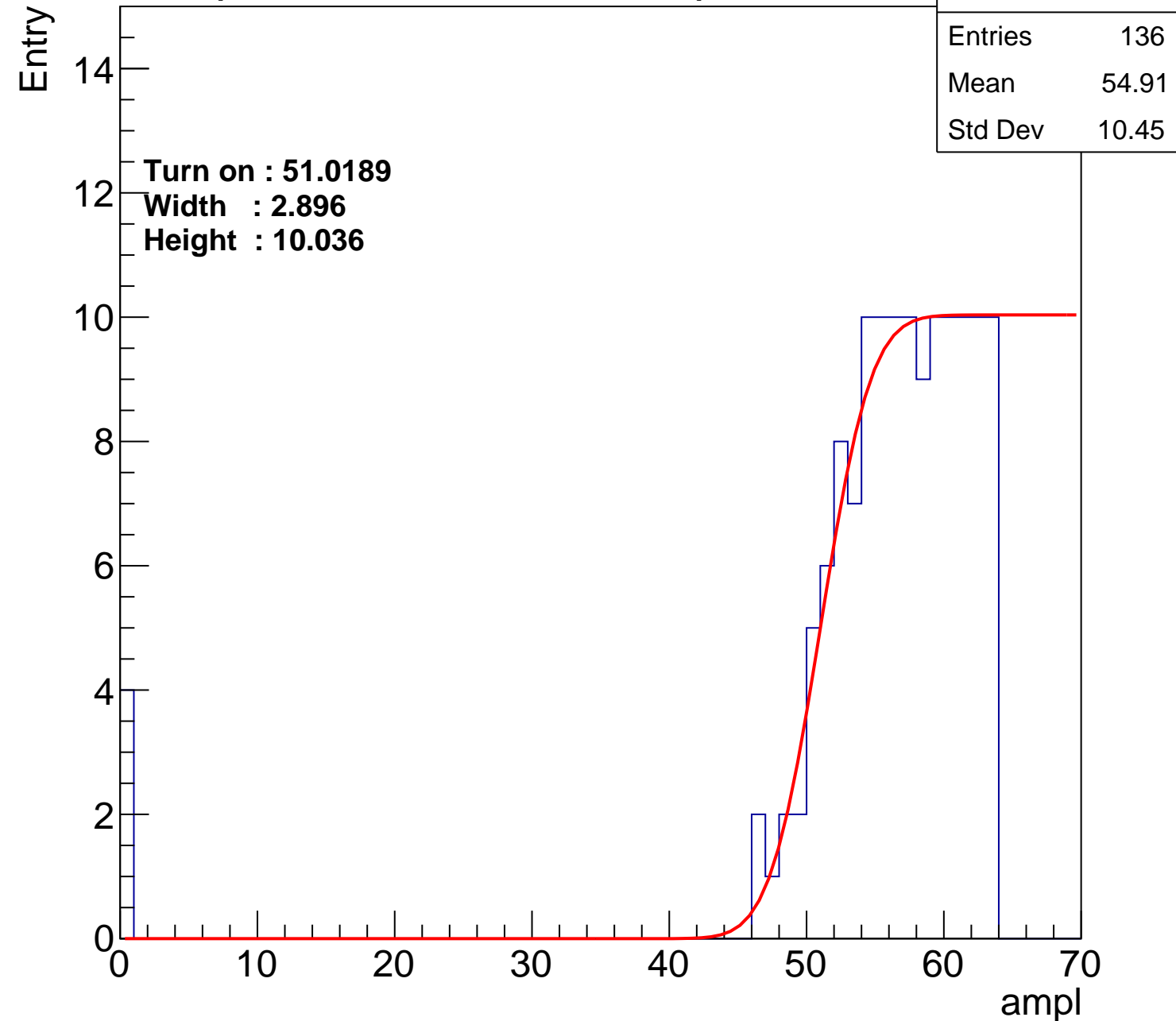
Width : 2.896

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	56.04
Std Dev	9.8

Turn on : 52.6120

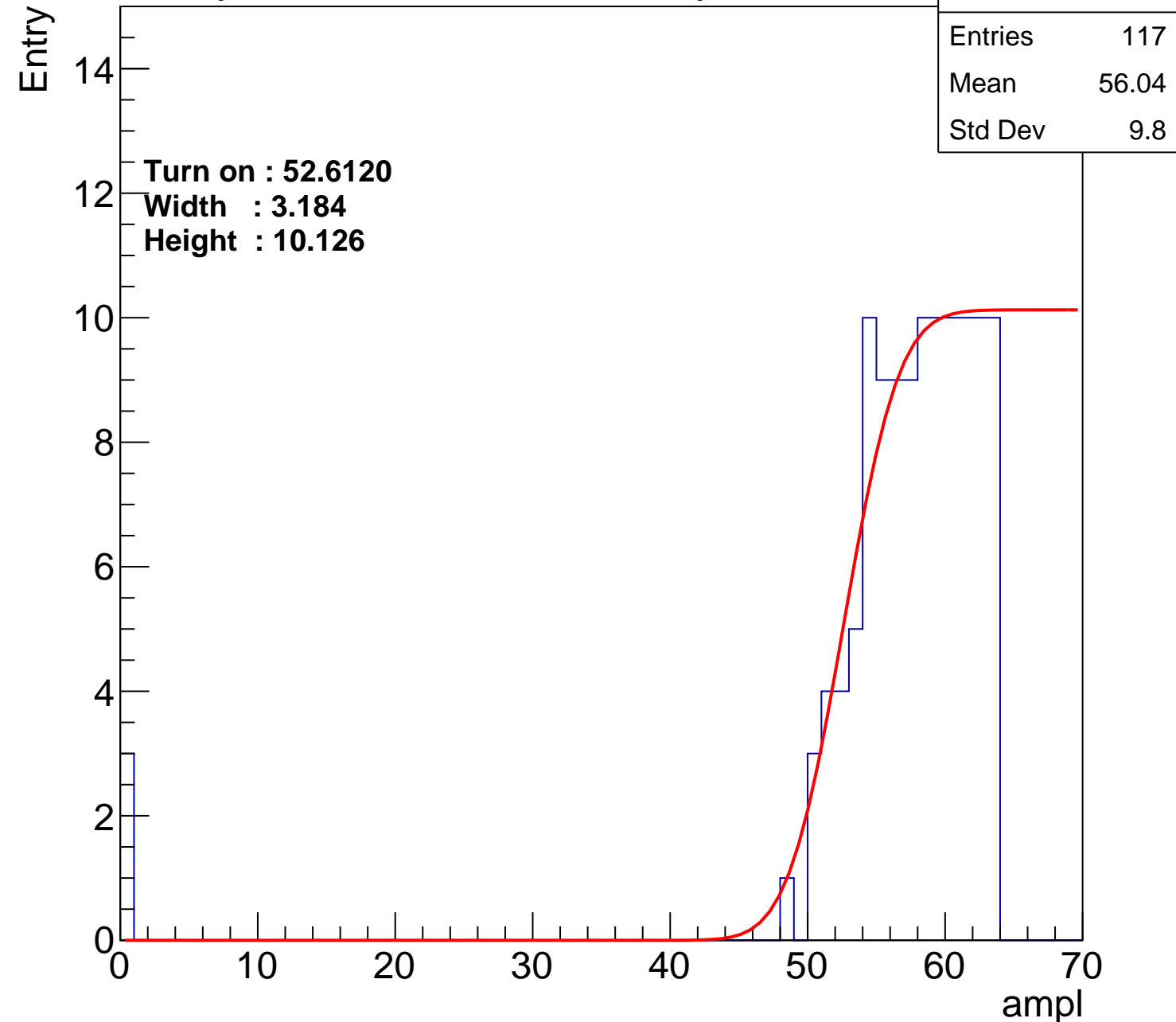
Width : 3.184

Height : 10.126

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	55.22
Std Dev	9.451

Turn on : 51.6072

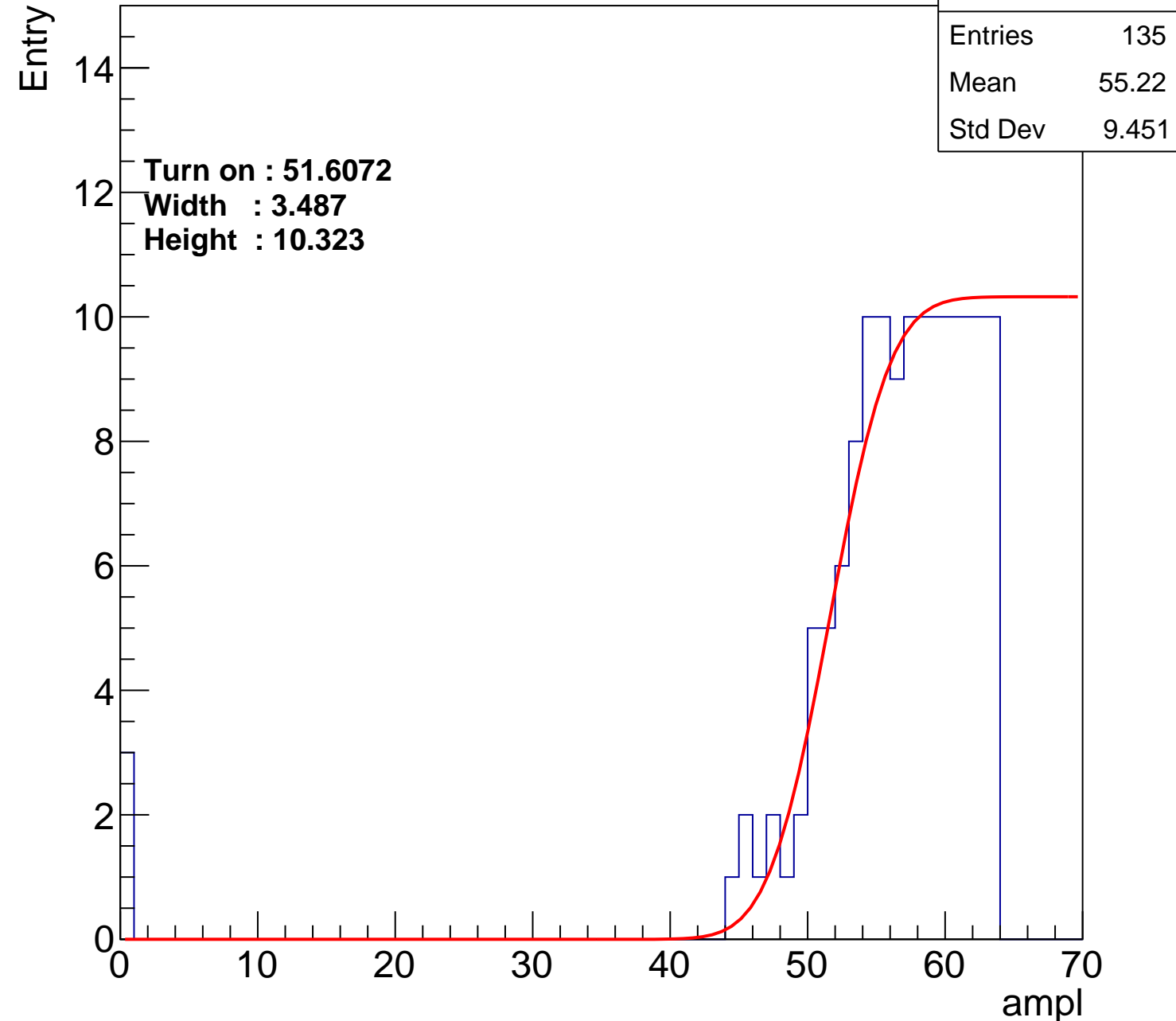
Width : 3.487

Height : 10.323

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	54.44
Std Dev	11.34

Turn on : 50.3819

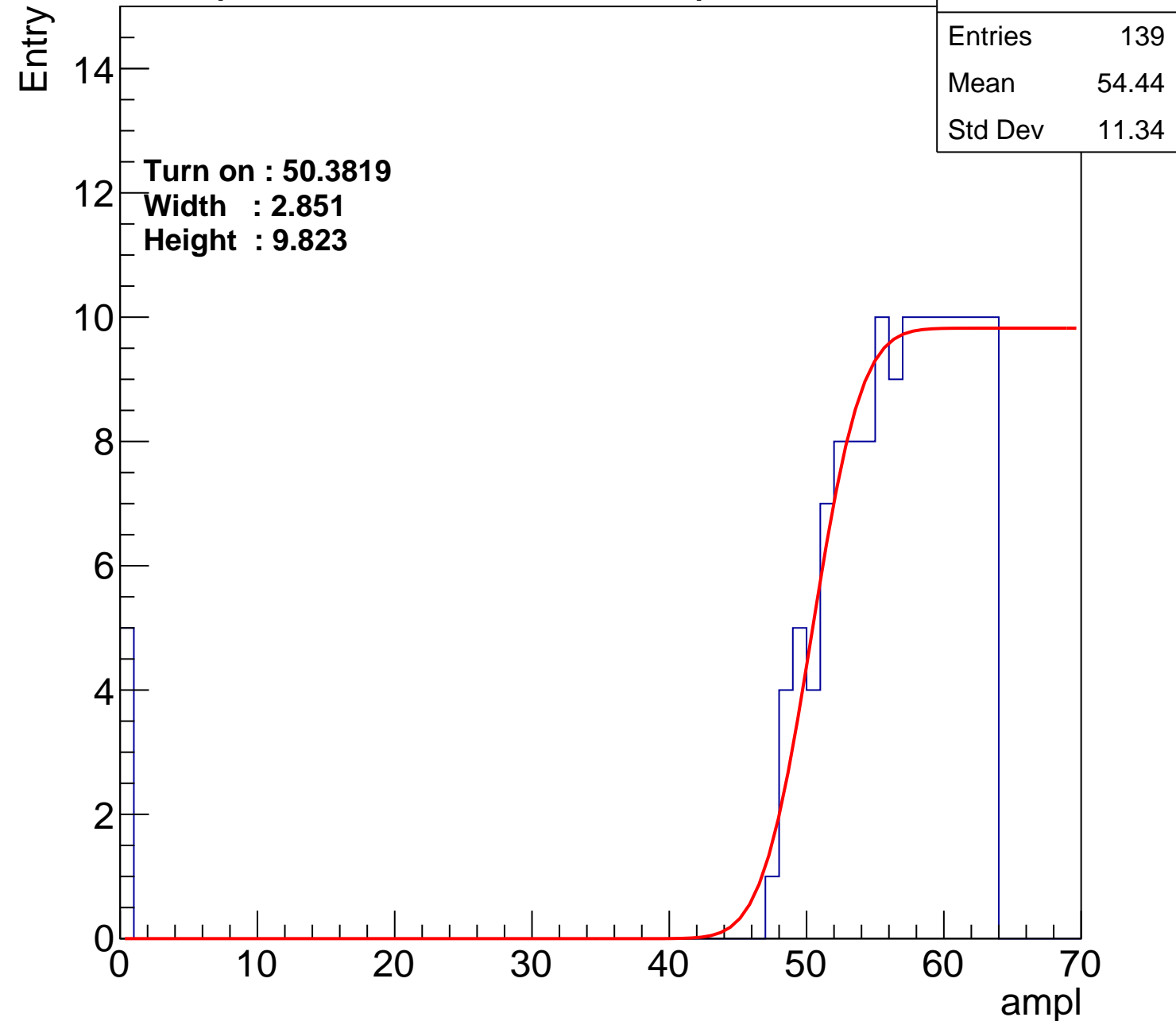
Width : 2.851

Height : 9.823

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch120

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.07
Std Dev	8.207

Turn on : 51.7638

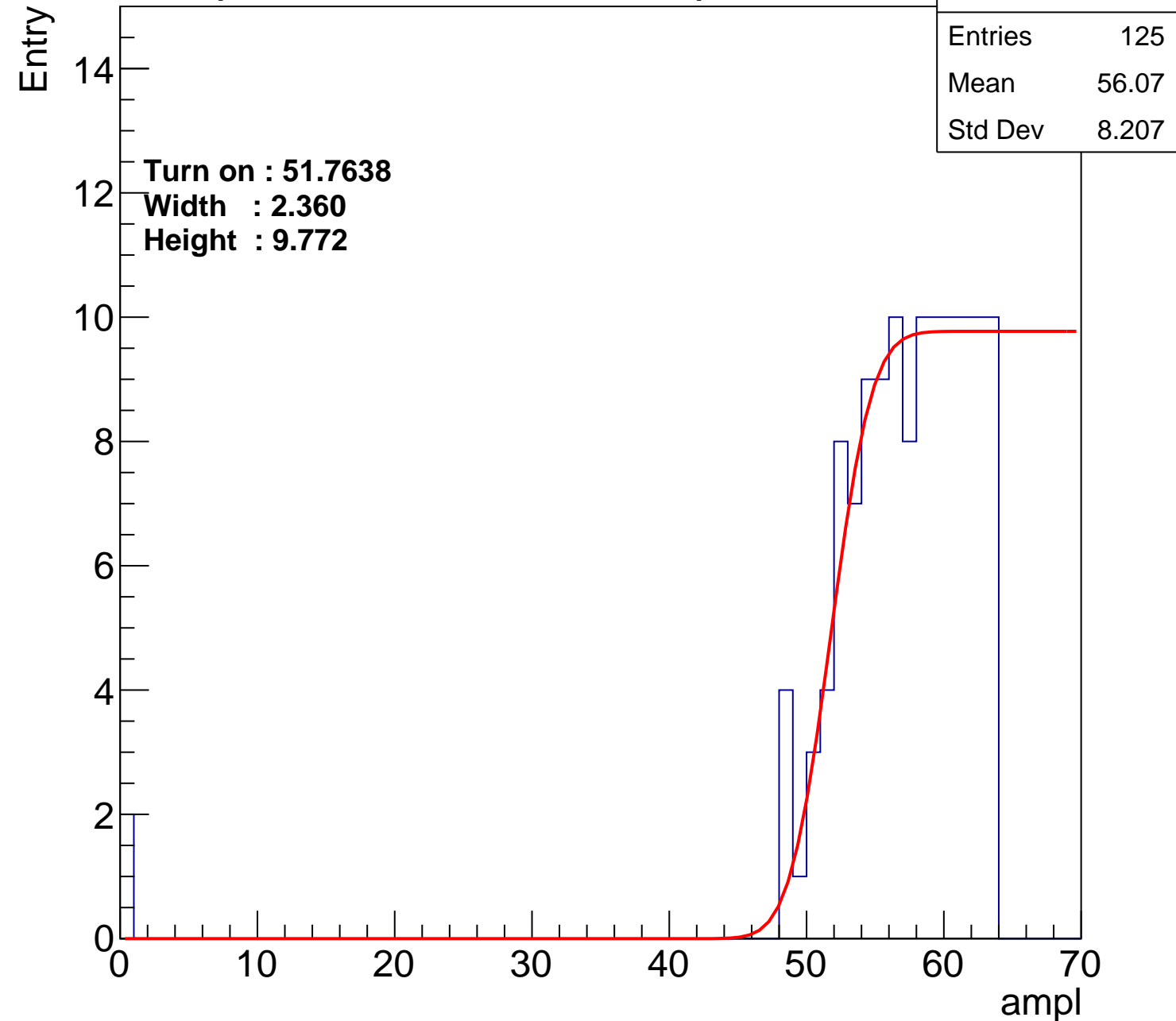
Width : 2.360

Height : 9.772

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.32
Std Dev	10.07

Turn on : 53.1584

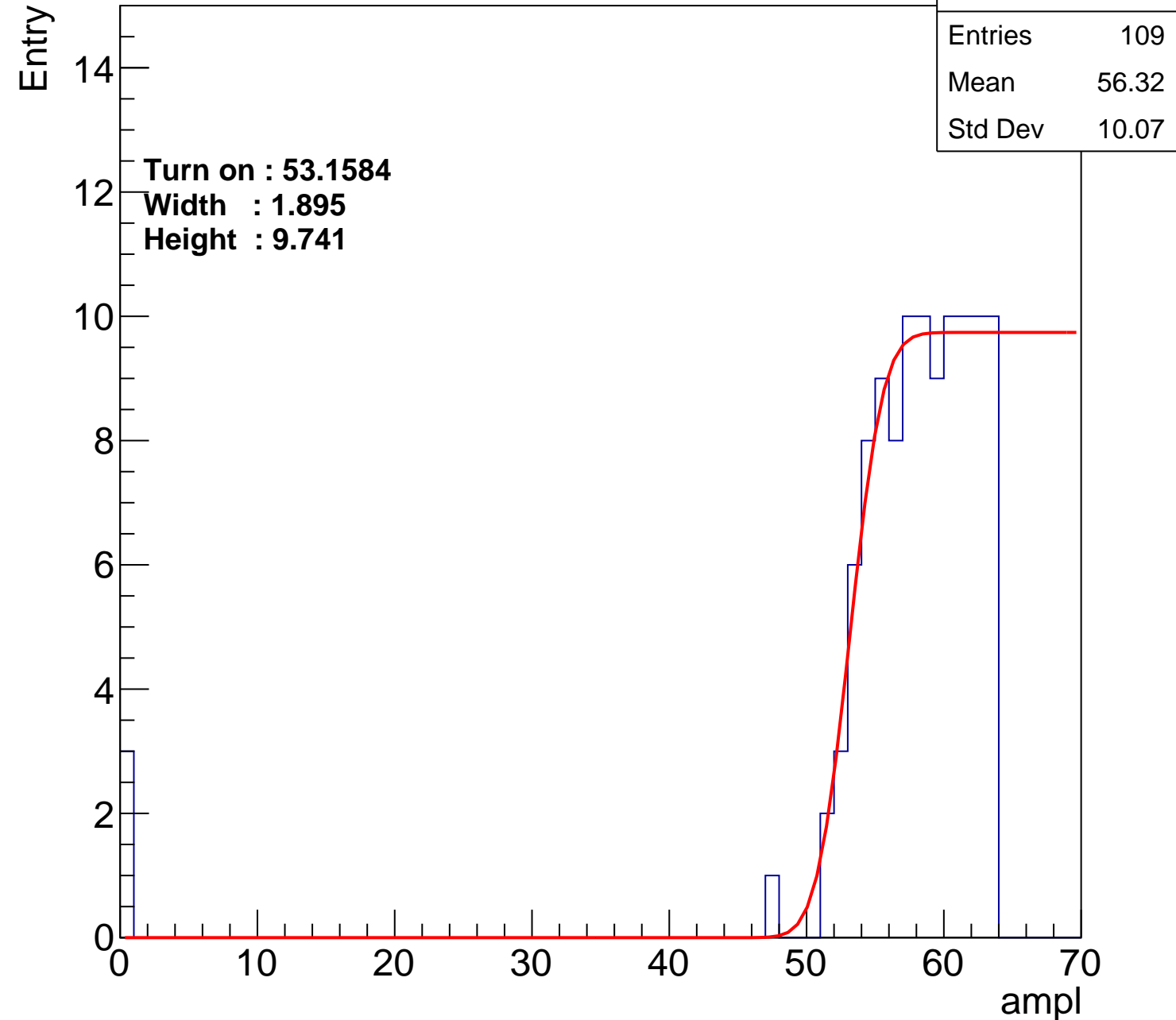
Width : 1.895

Height : 9.741

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	54.27
Std Dev	12.49

Turn on : 51.4411

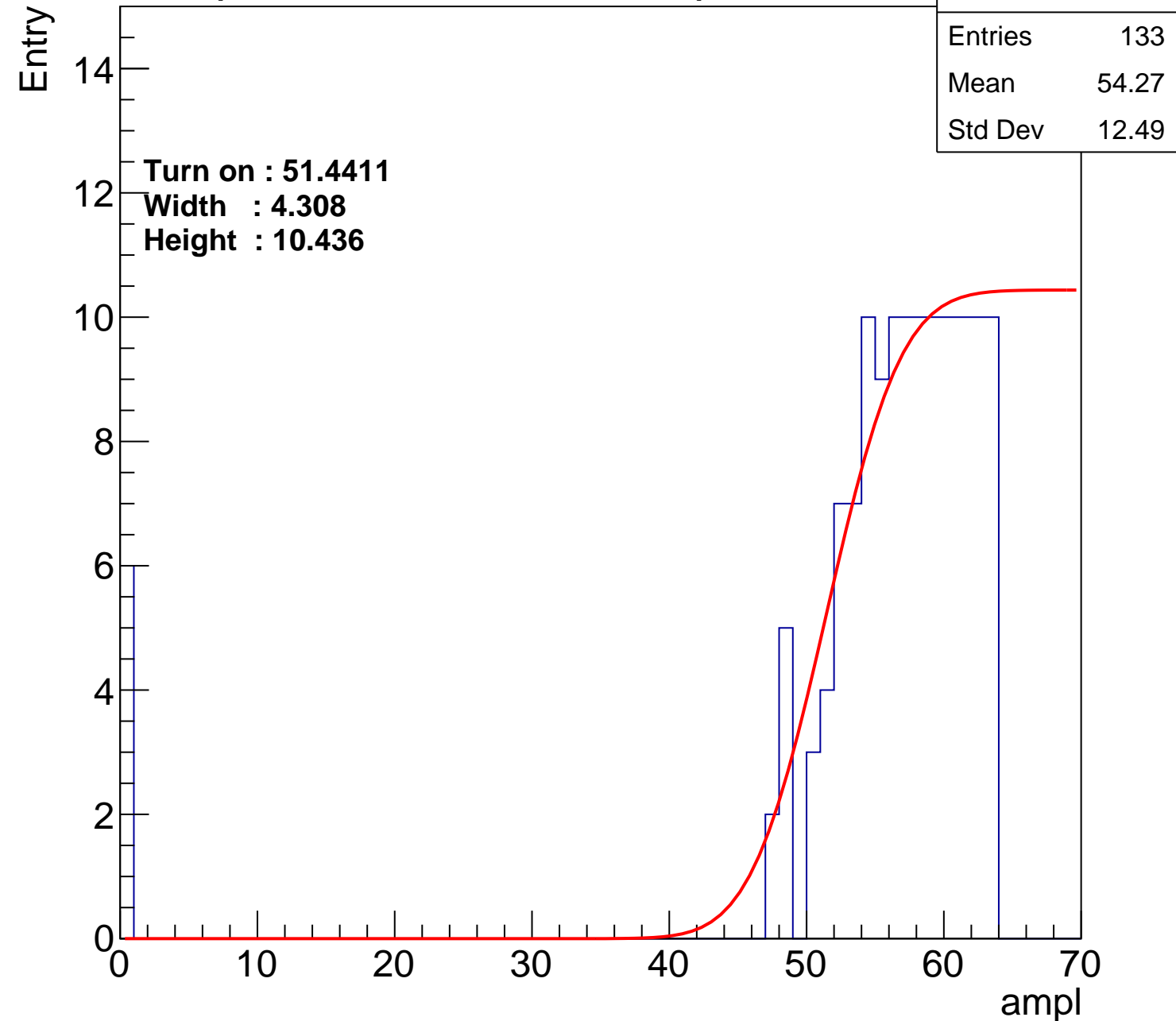
Width : 4.308

Height : 10.436

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.23
Std Dev	6.615

Turn on : 51.8014

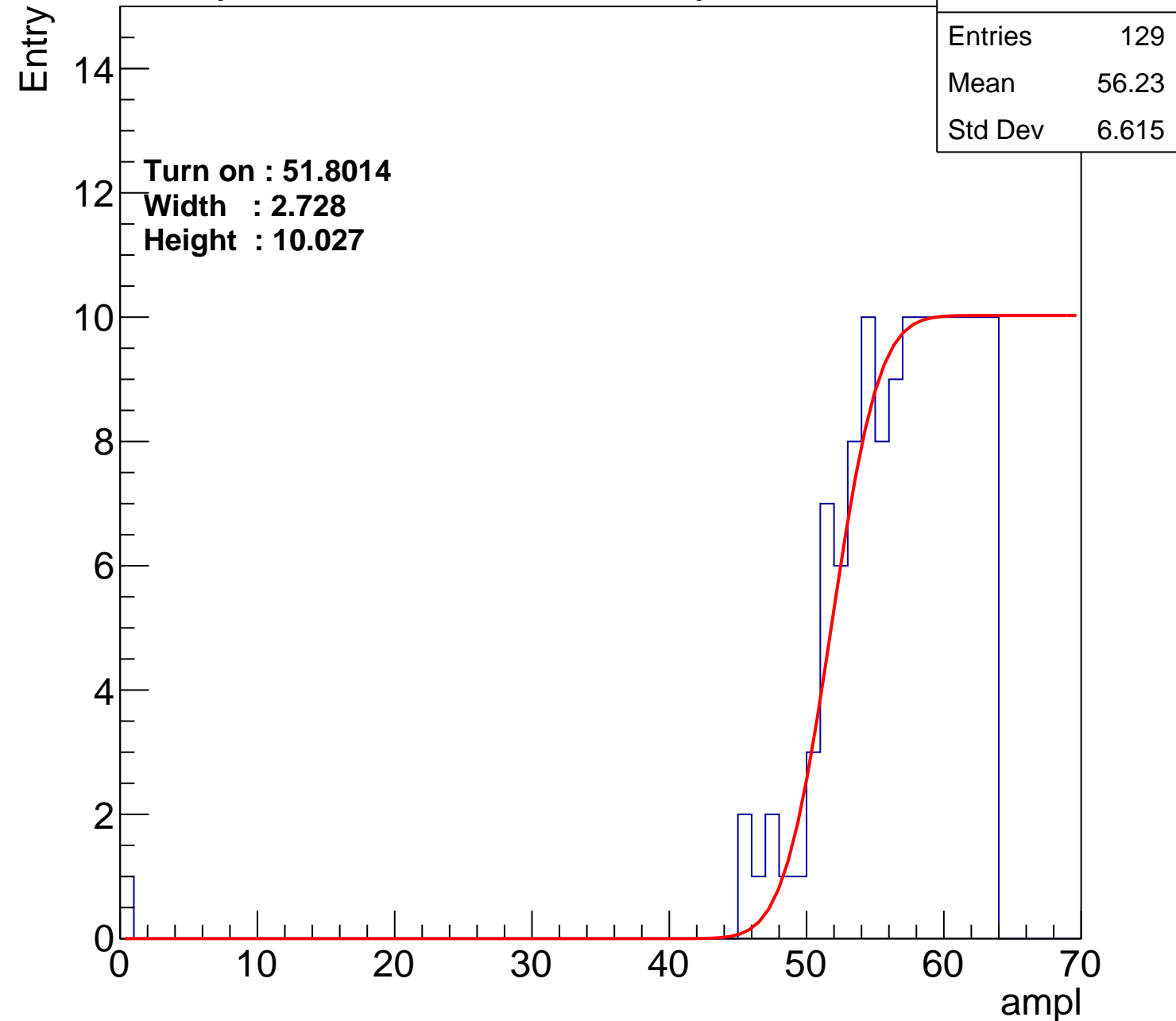
Width : 2.728

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch124

calib_packv5_040323_1717.root, FC#2, port C3

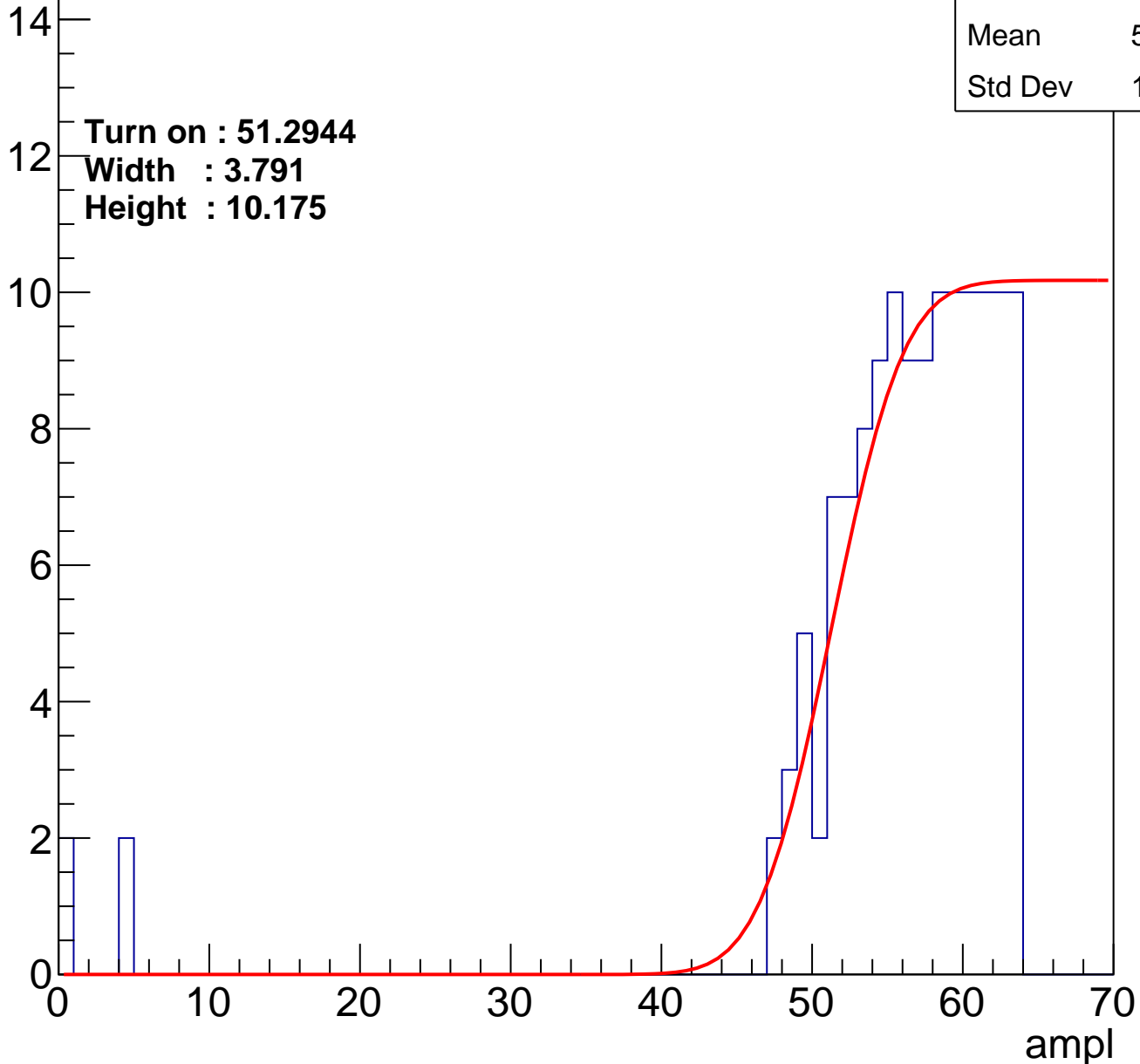
Entries	135
Mean	54.96
Std Dev	10.19

Turn on : 51.2944

Width : 3.791

Height : 10.175

Entry



B0L103S, U21-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	55.05
Std Dev	12.31

Turn on : 52.9579

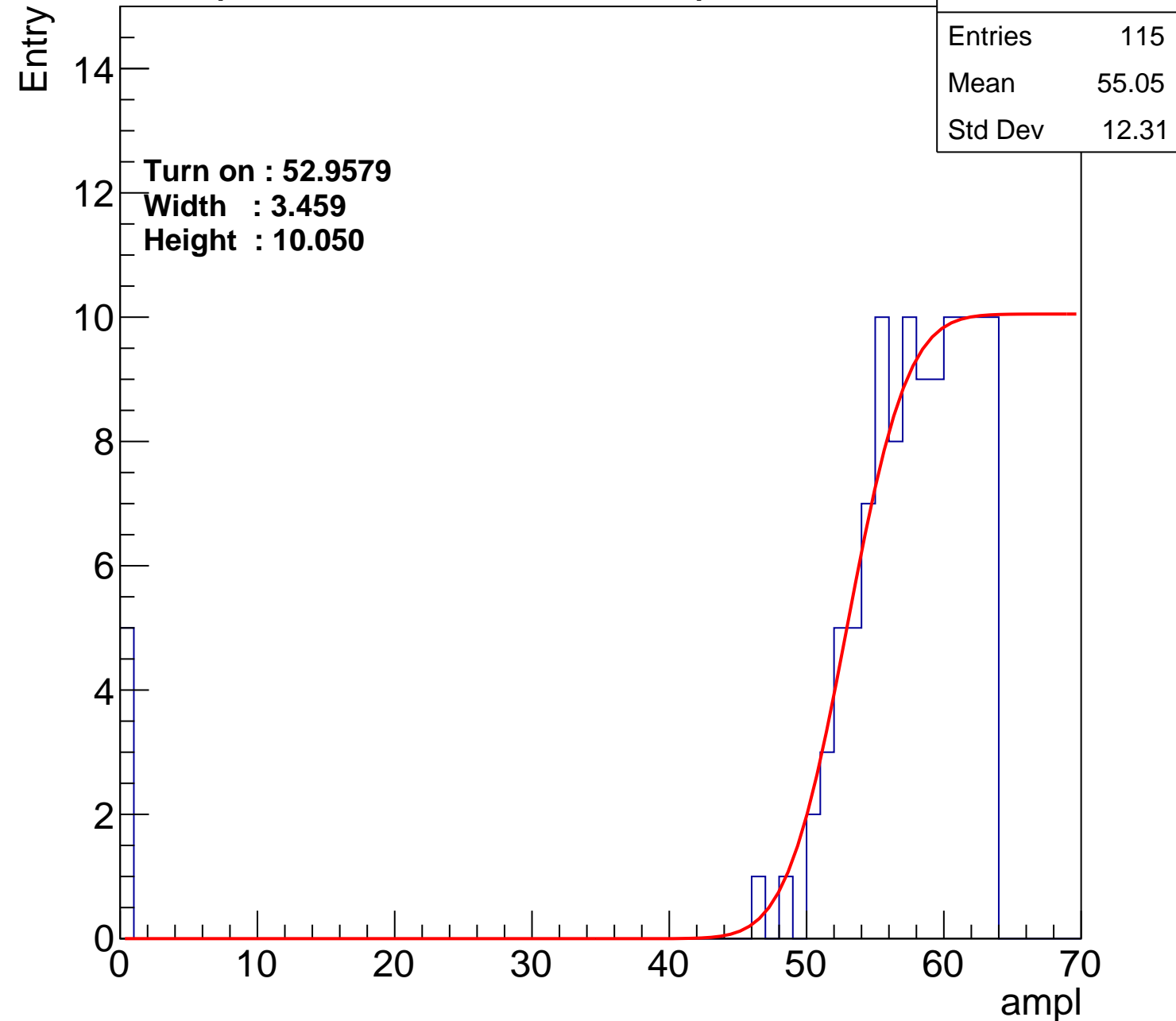
Width : 3.459

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	146
Mean	54.23
Std Dev	11.14

Turn on : 49.6744

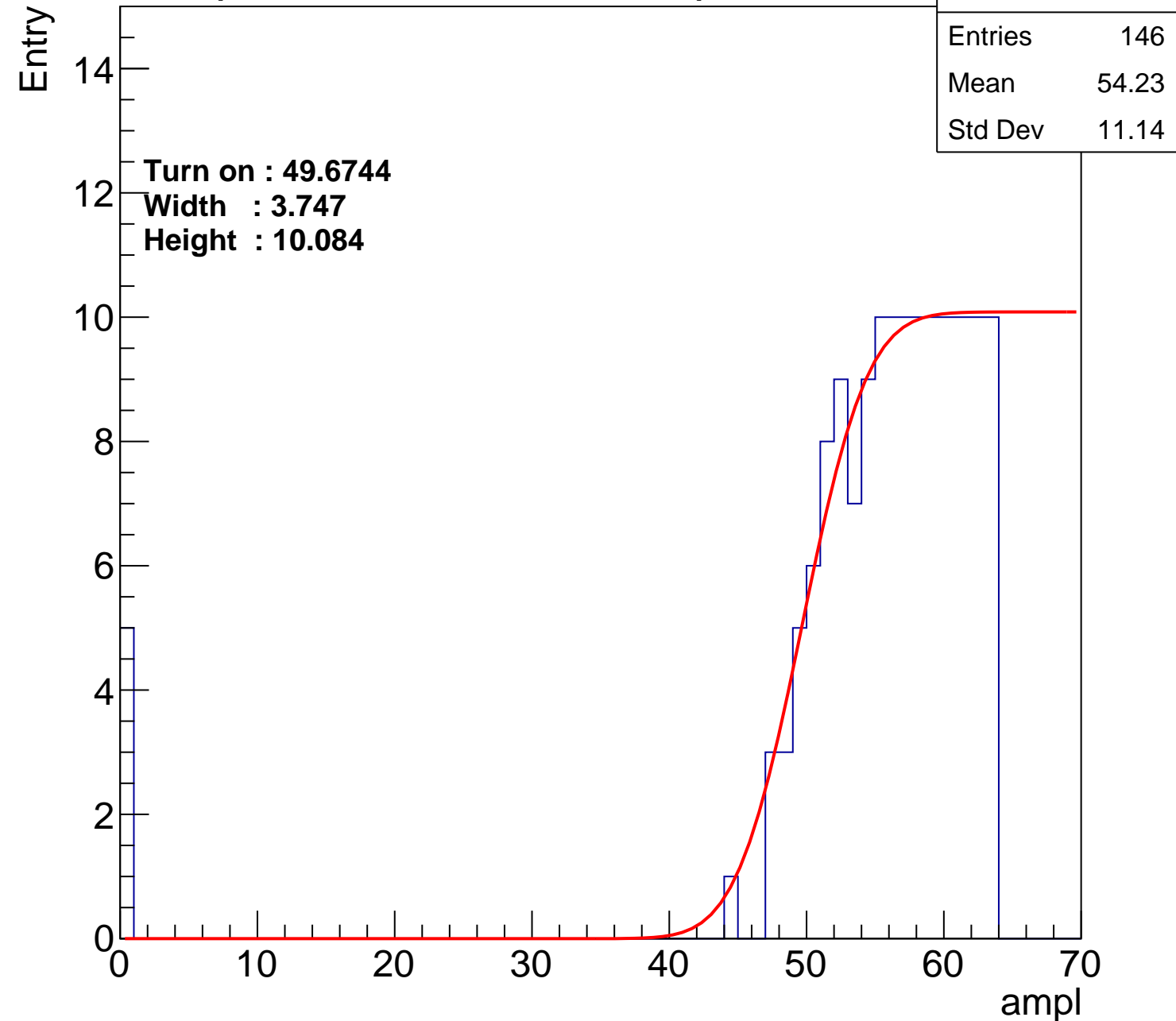
Width : 3.747

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.78
Std Dev	9.066

Turn on : 49.2631

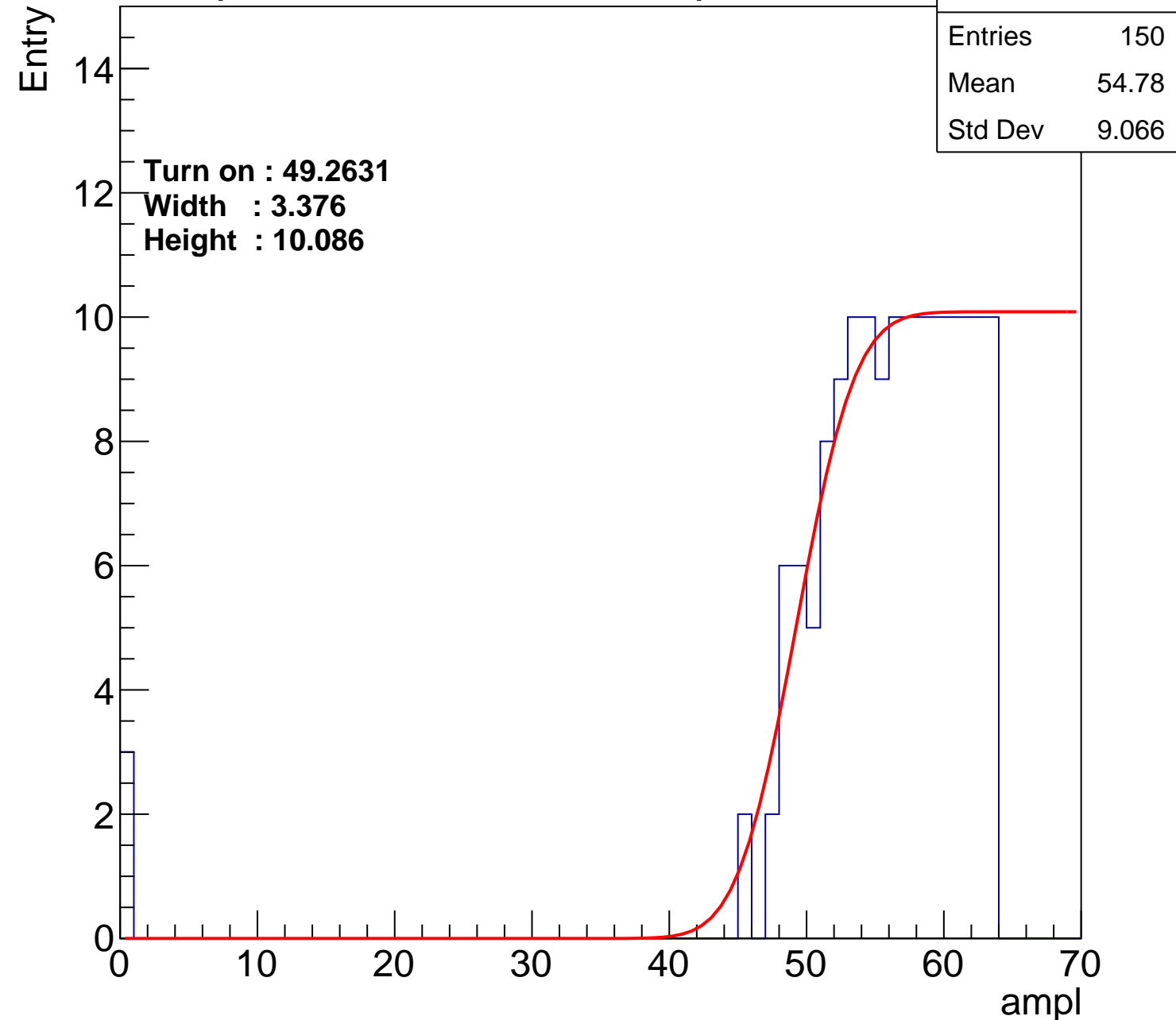
Width : 3.376

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U21-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	150
Mean	54.78
Std Dev	9.066

Turn on : 49.2631

Width : 3.376

Height : 10.086

Entry

