

B0L002S, U11-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	46.03
Std Dev	10.56

Turn on : 29.9879

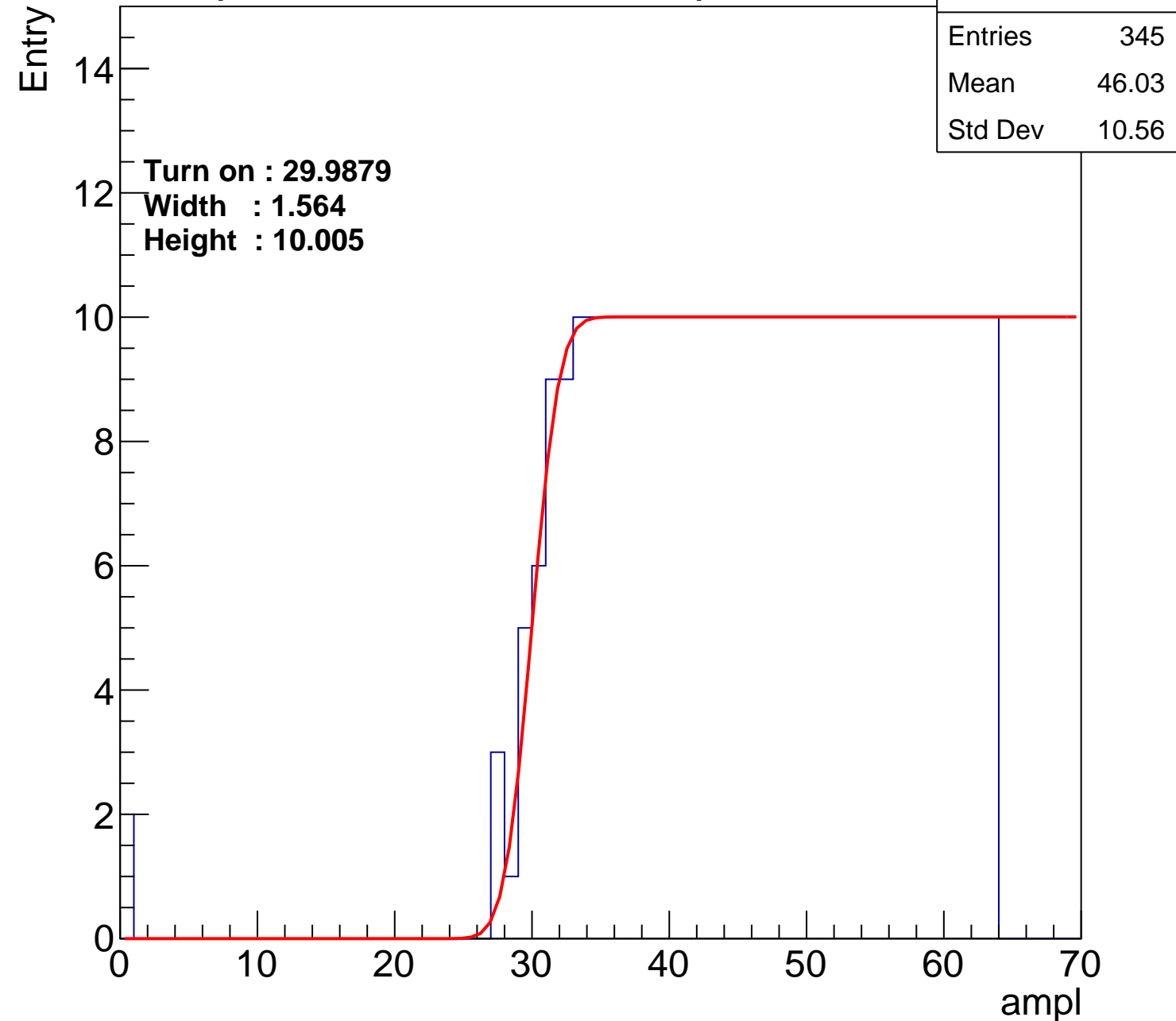
Width : 1.564

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.97
Std Dev	10.49

Turn on : 30.0005

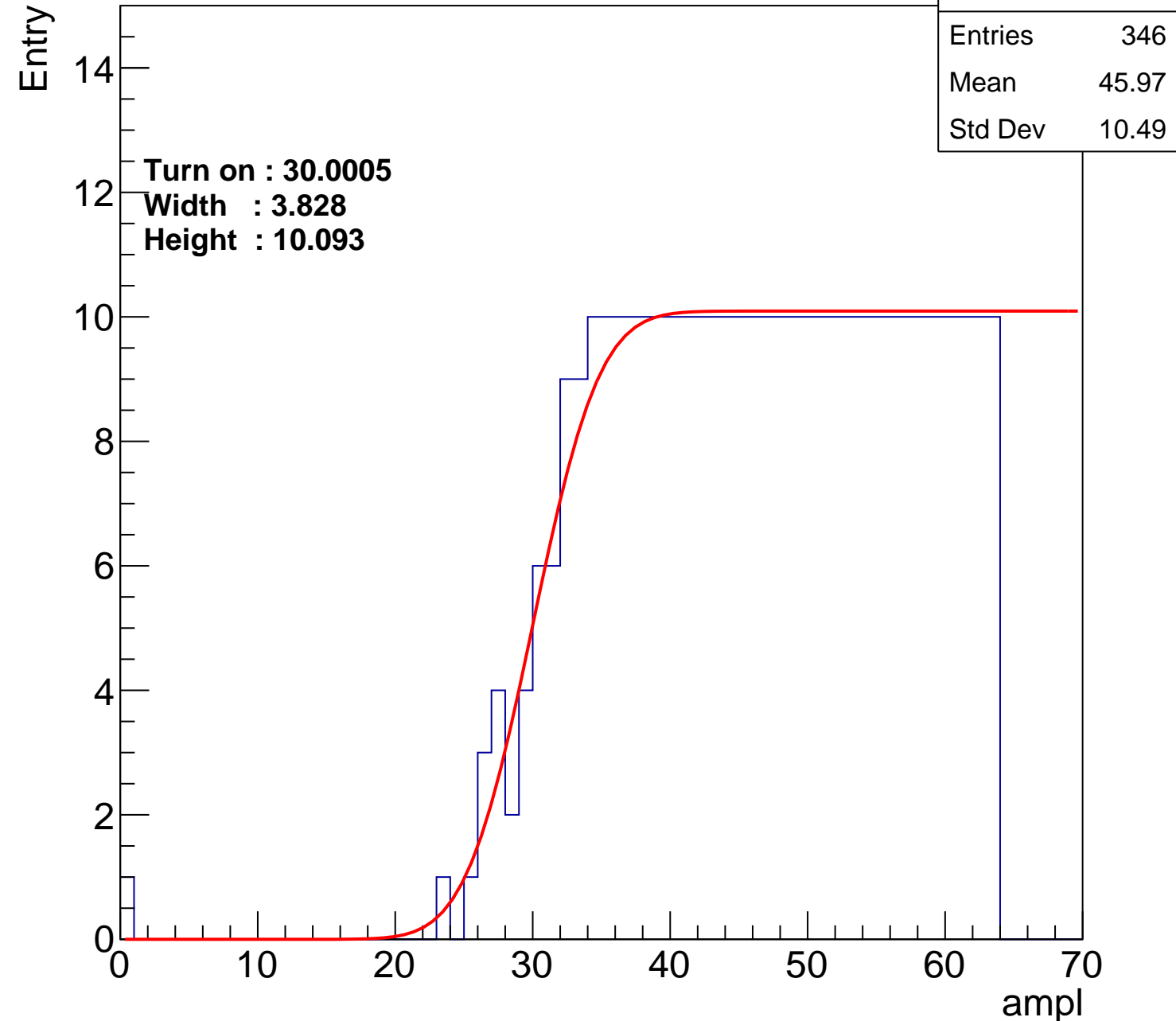
Width : 3.828

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch2

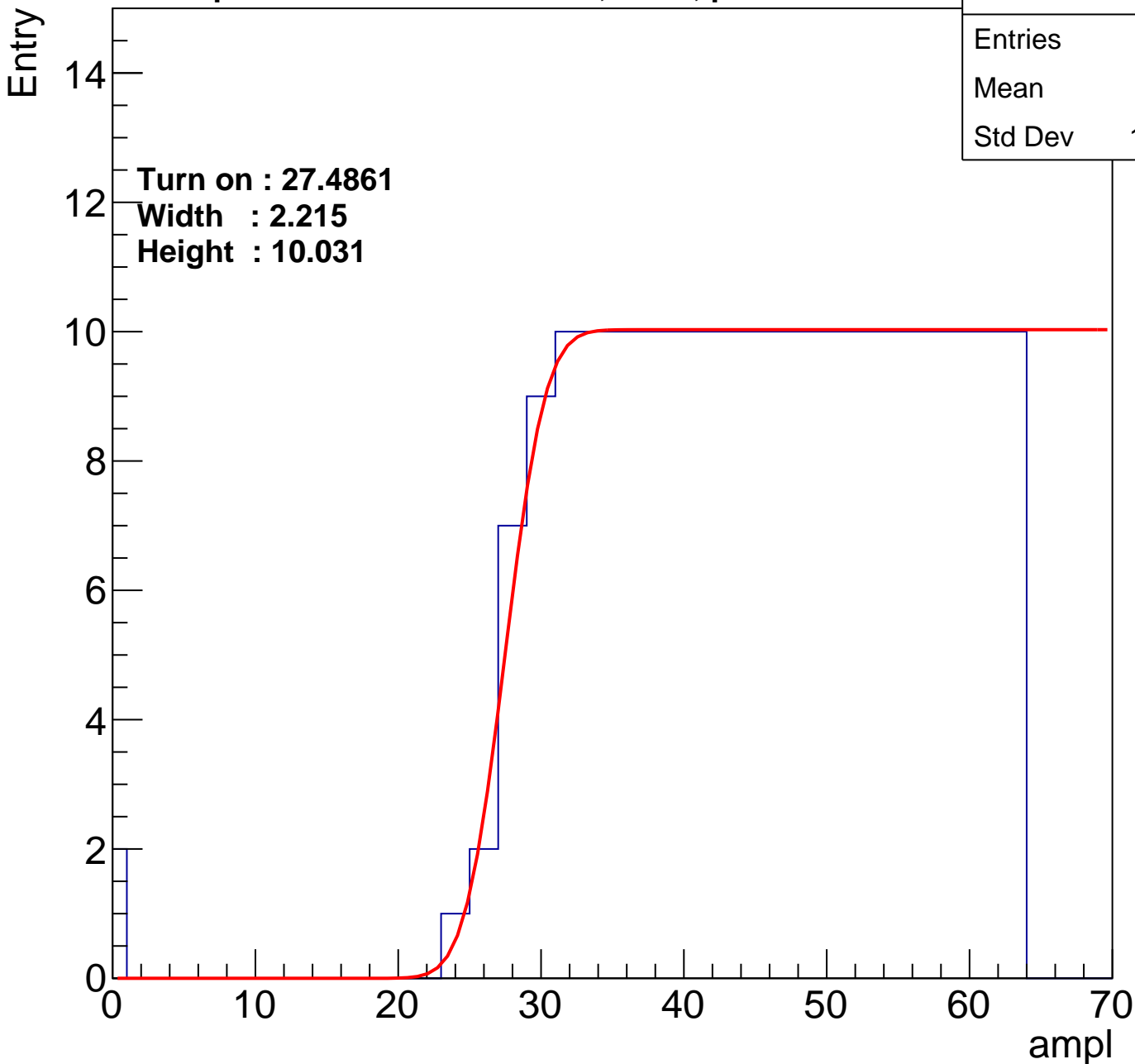
calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.4861

Width : 2.215

Height : 10.031



B0L002S, U11-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 27.5472

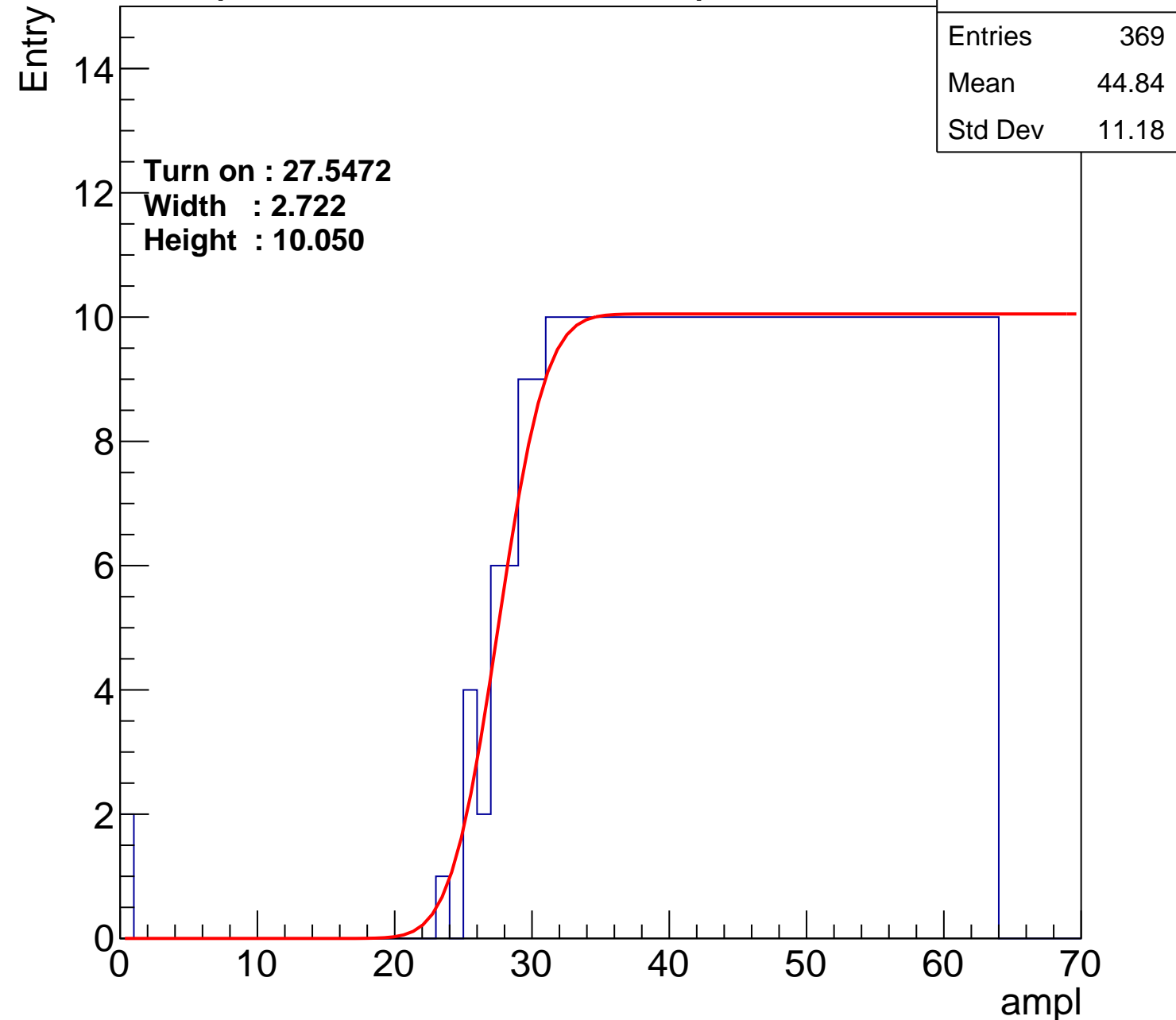
Width : 2.722

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	44.99
Std Dev	11.48

Turn on : 28.2429

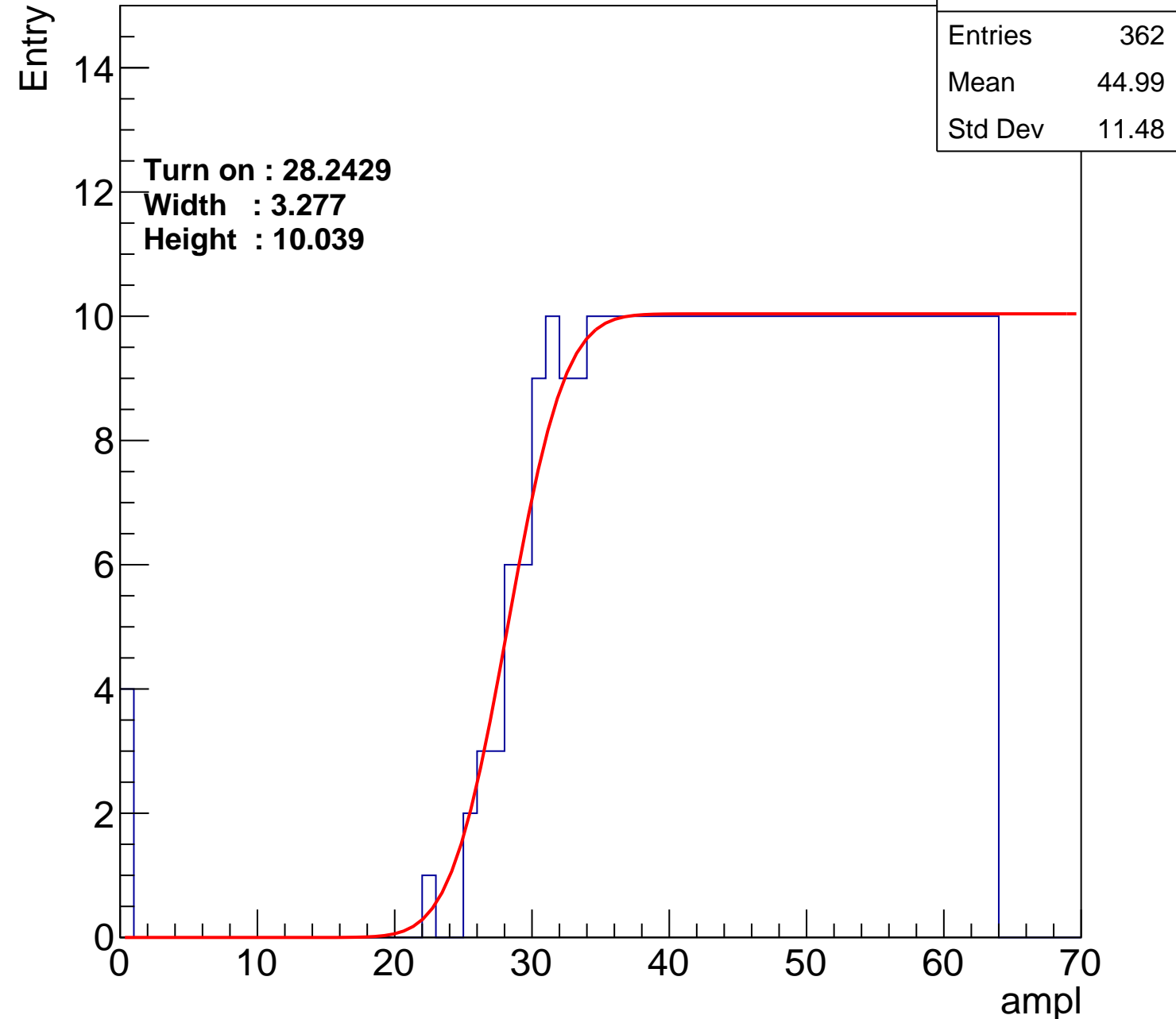
Width : 3.277

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.5
Std Dev	10.71

Turn on : 28.6577

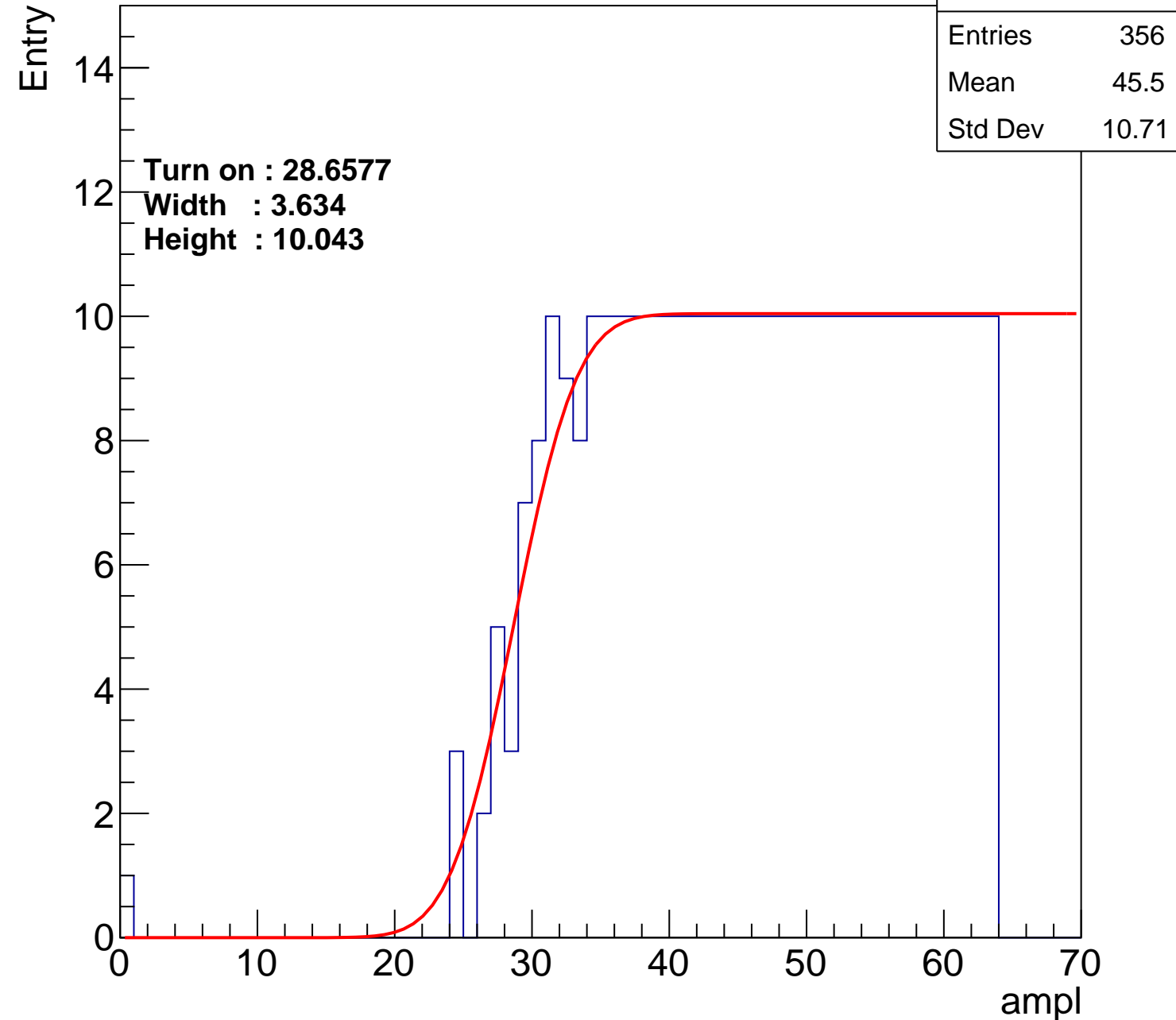
Width : 3.634

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.4255

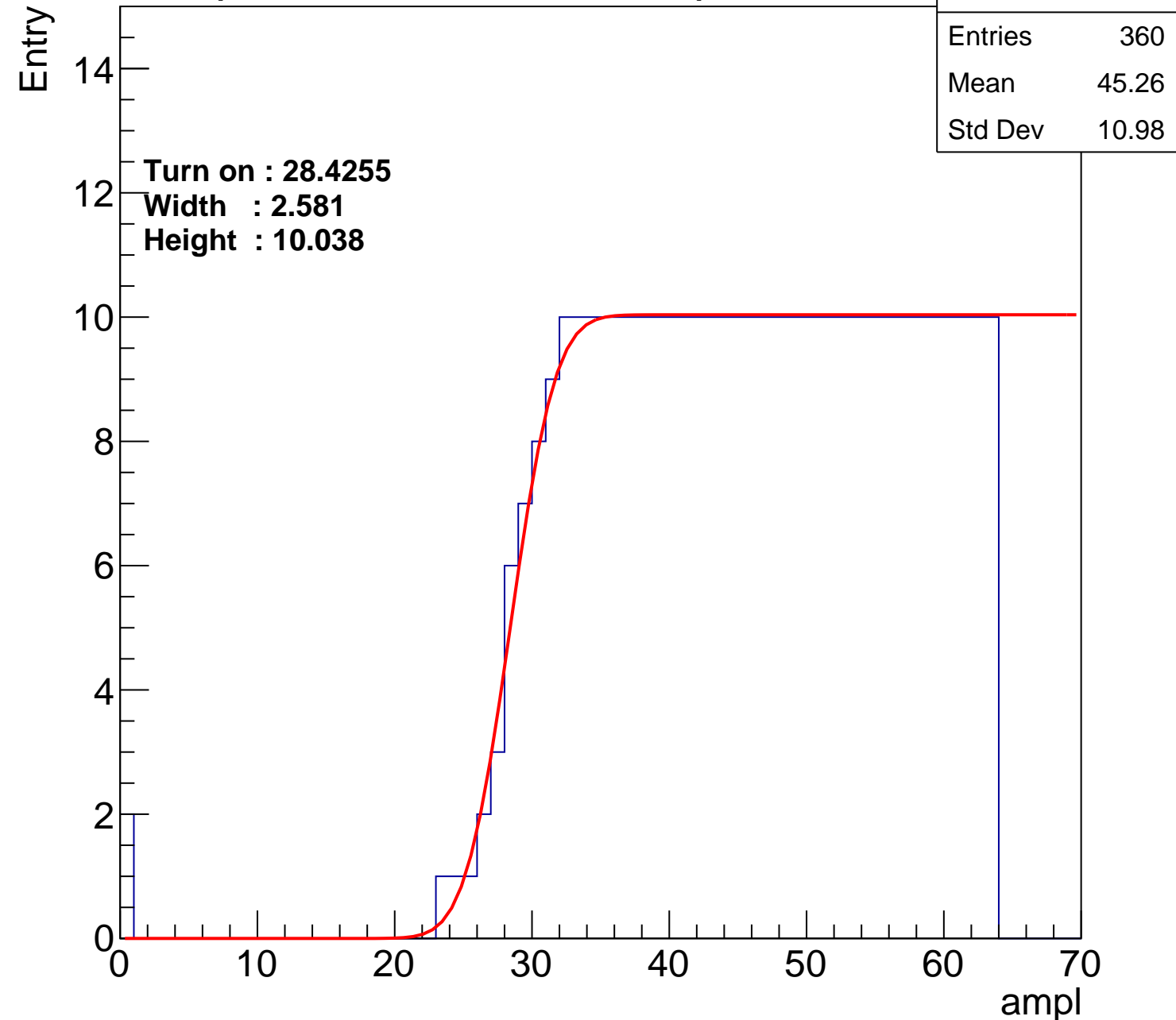
Width : 2.581

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch7

calib_packv5_042523_0143.root, FC#8, port C1

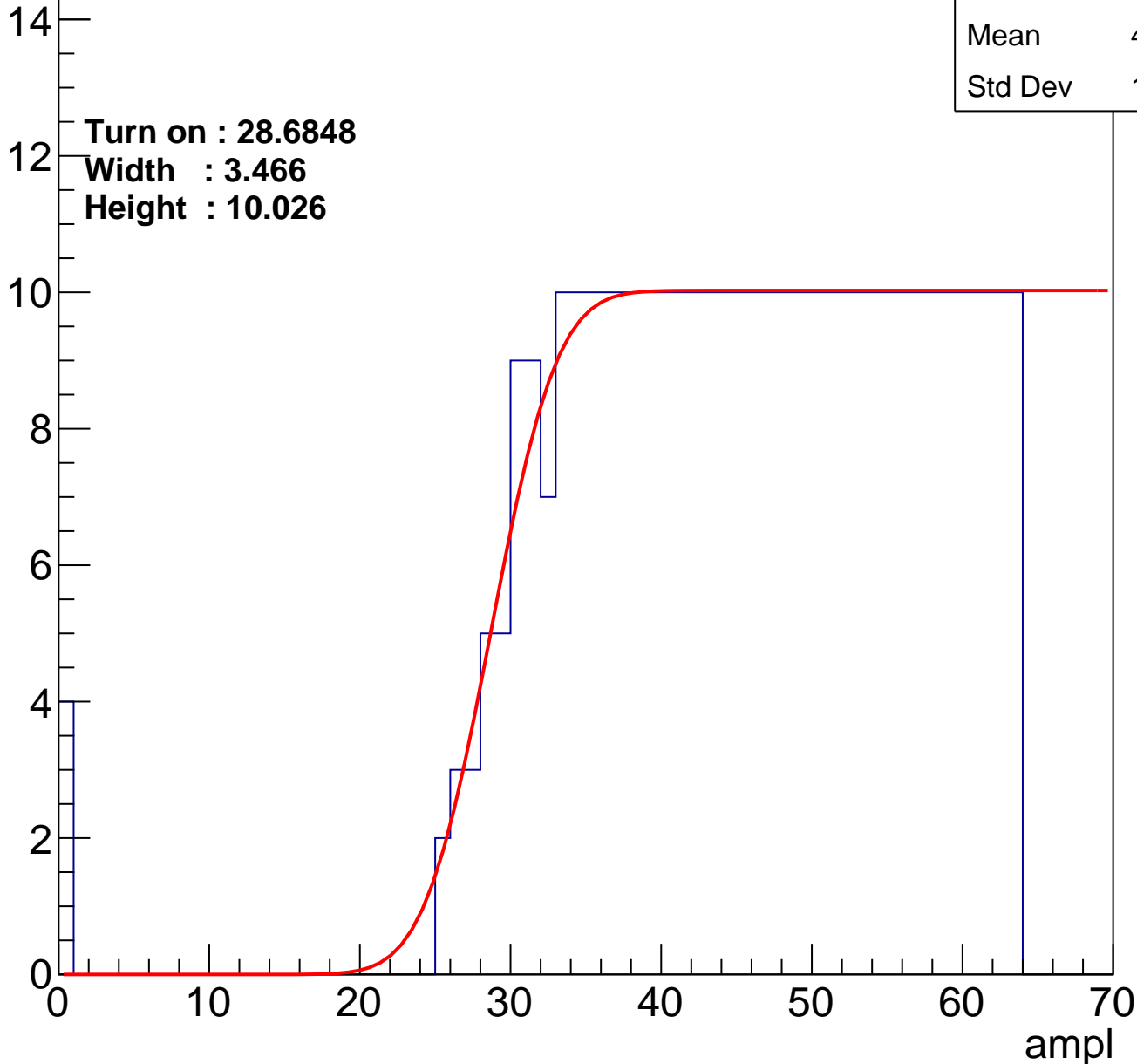
Entries	357
Mean	45.23
Std Dev	11.38

Turn on : 28.6848

Width : 3.466

Height : 10.026

Entry



B0L002S, U11-ch8

calib_packv5_042523_0143.root, FC#8, port C1

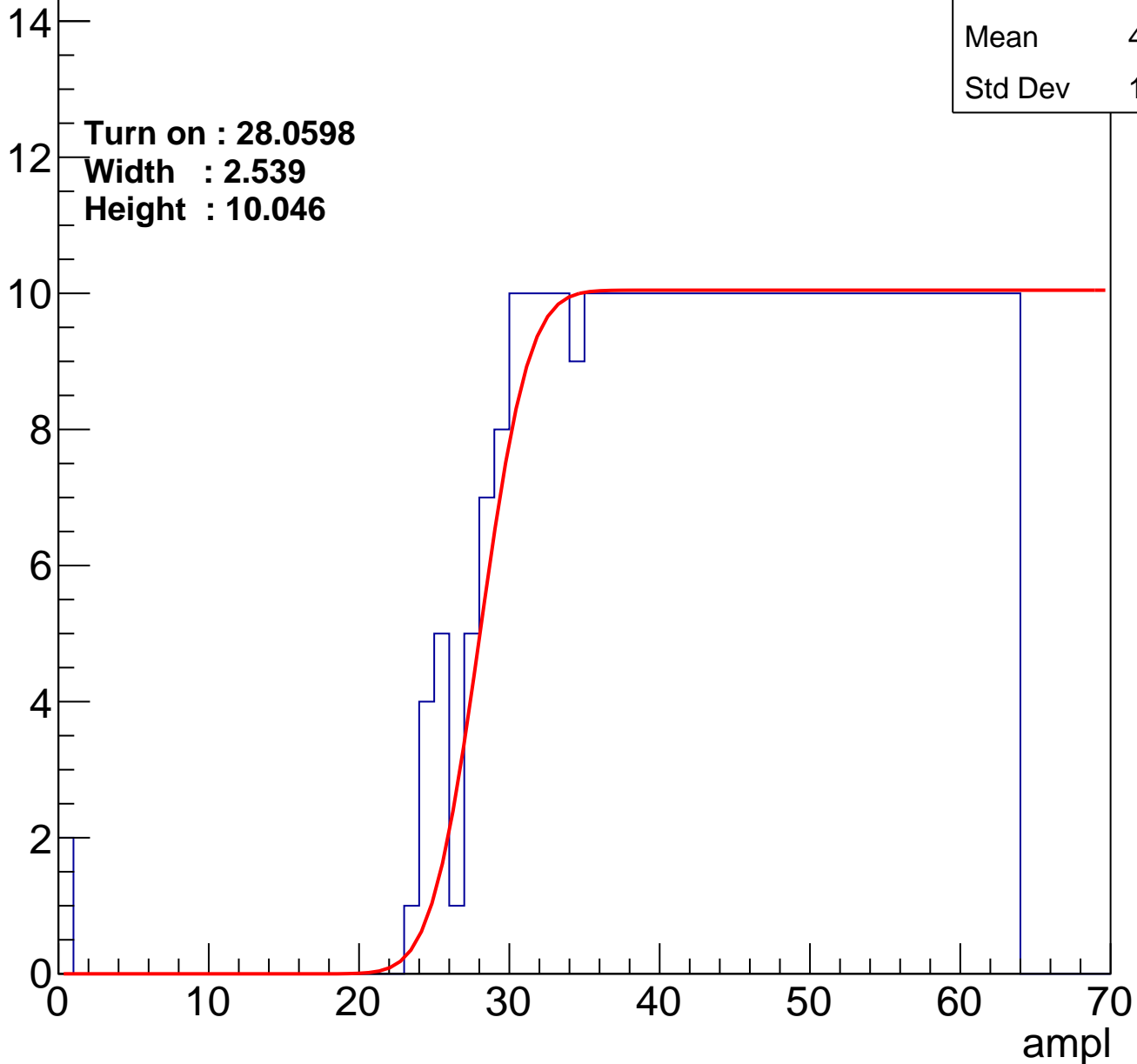
Entry

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 28.0598

Width : 2.539

Height : 10.046



B0L002S, U11-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.07
Std Dev	10.88

Turn on : 27.6605

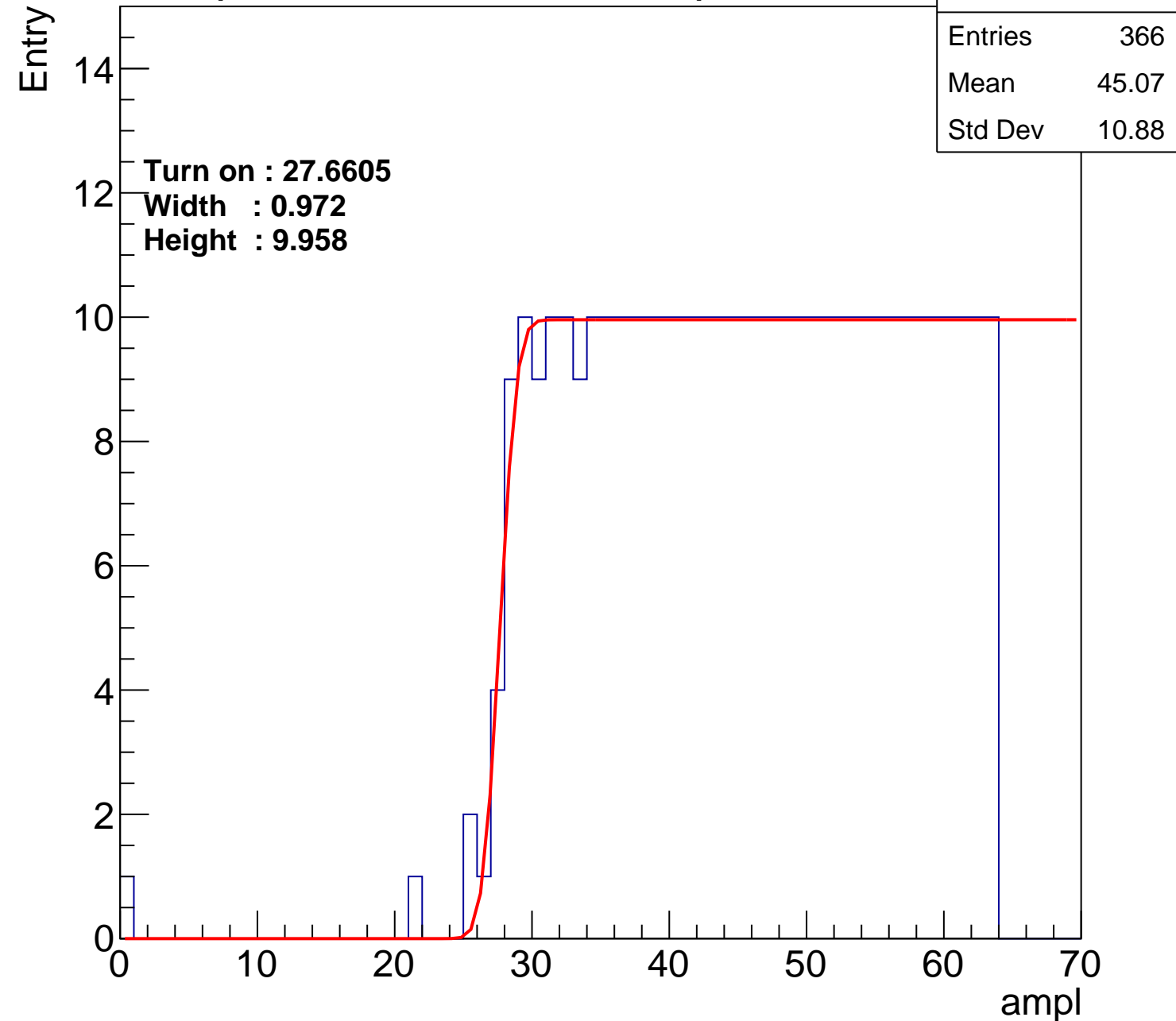
Width : 0.972

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch10

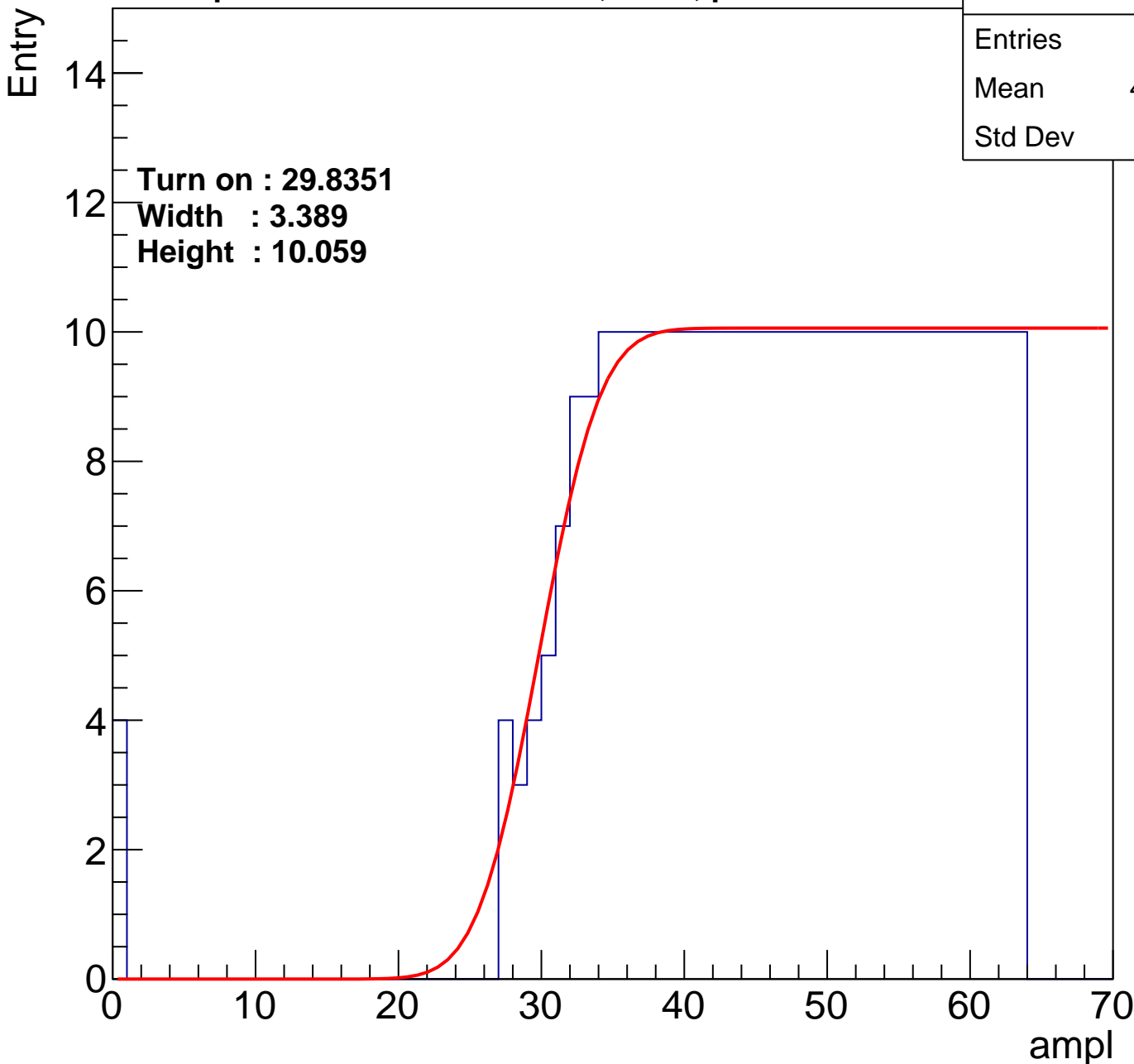
calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	45.83
Std Dev	11.1

Turn on : 29.8351

Width : 3.389

Height : 10.059



B0L002S, U11-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.89
Std Dev	10.67

Turn on : 29.6392

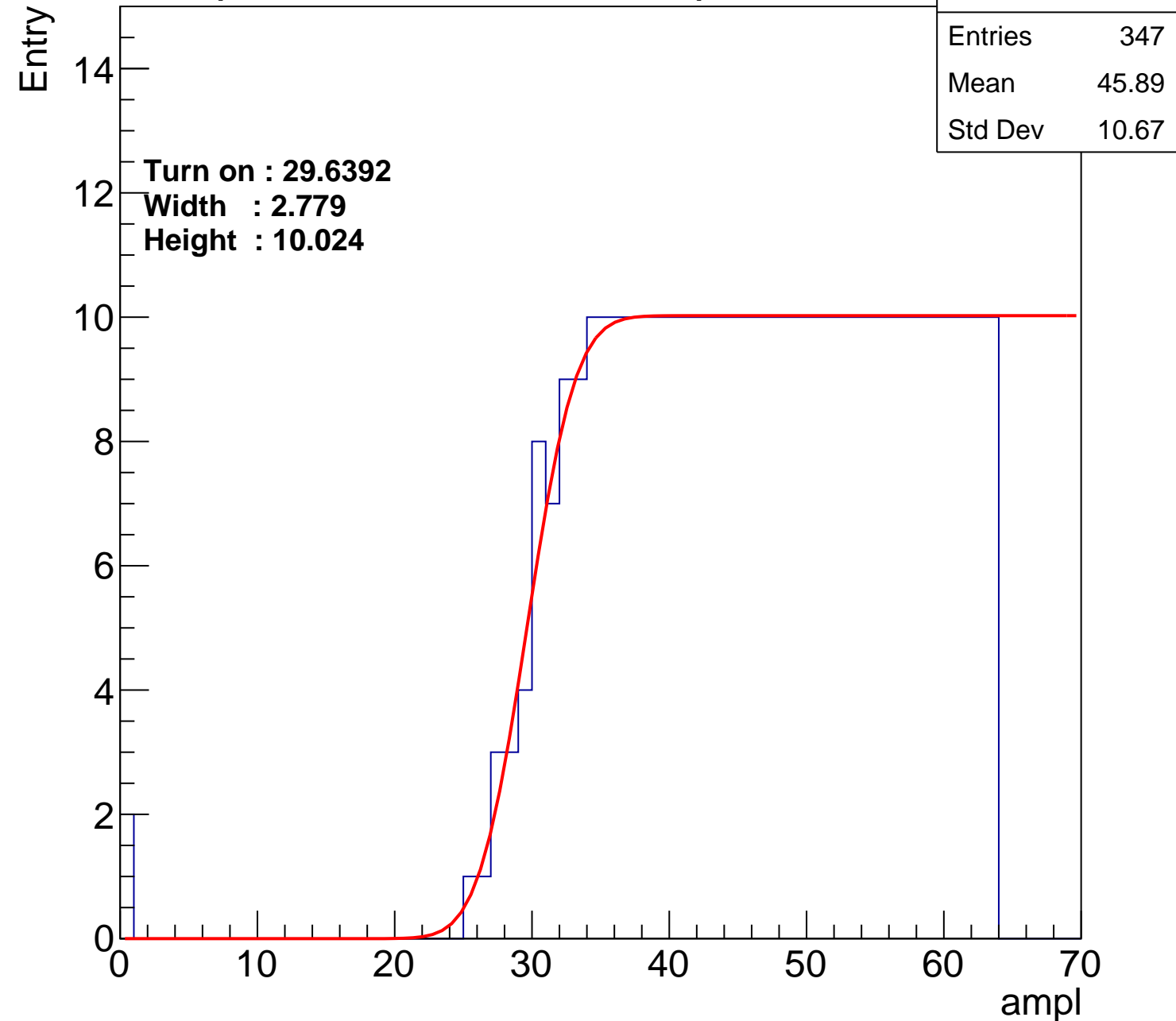
Width : 2.779

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch12

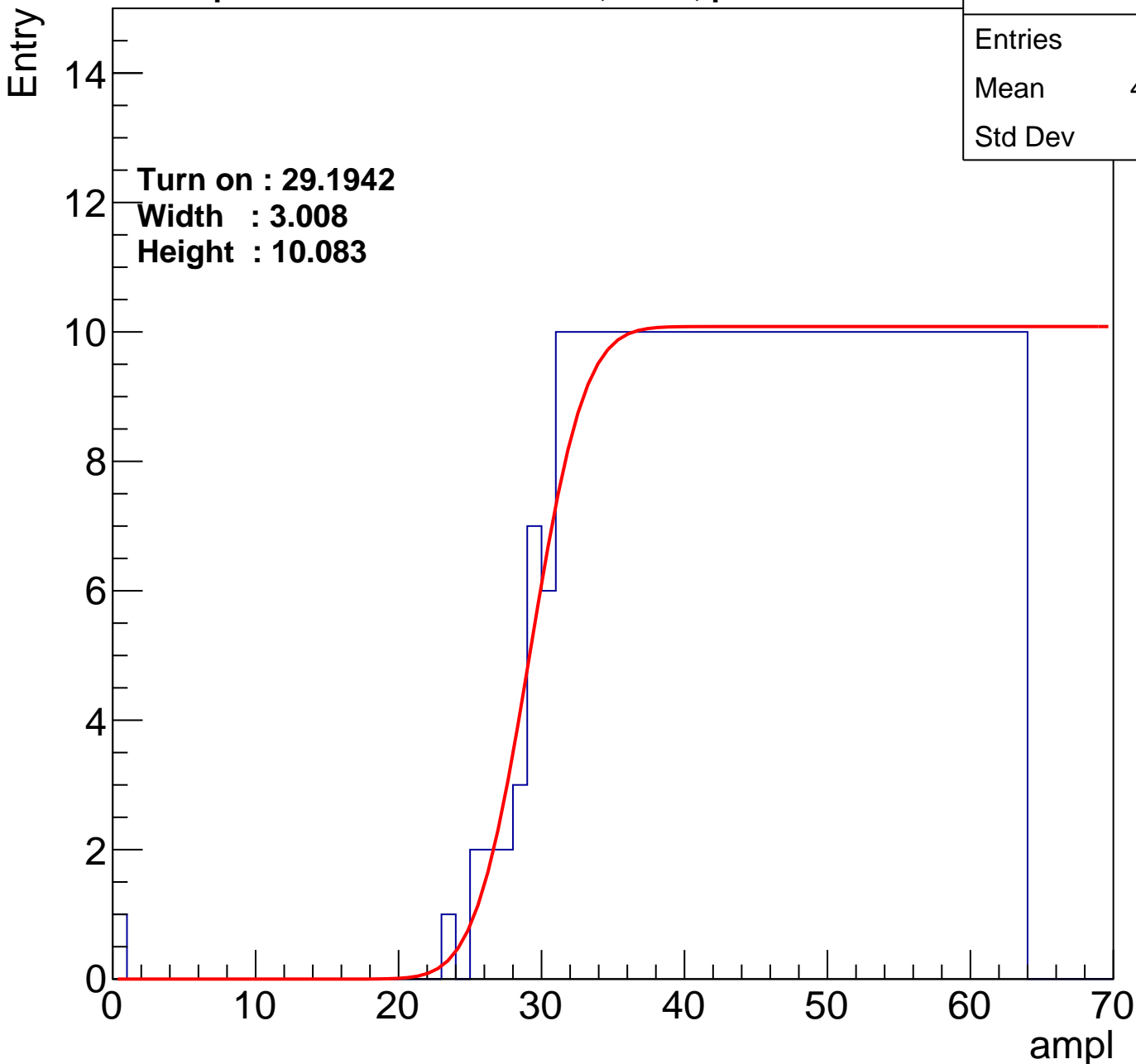
calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.64
Std Dev	10.6

Turn on : 29.1942

Width : 3.008

Height : 10.083



B0L002S, U11-ch13

calib_packv5_042523_0143.root, FC#8, port C1

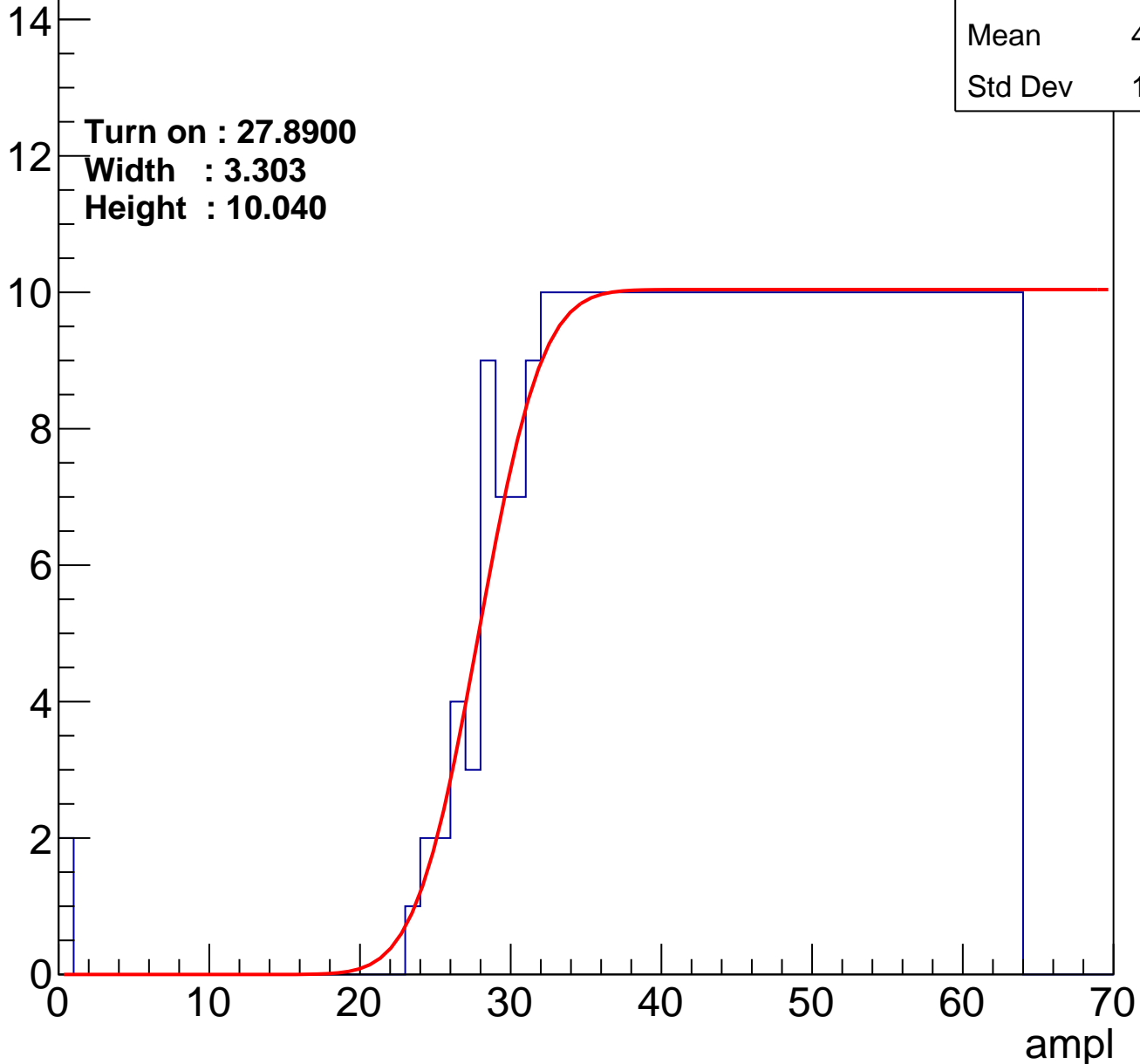
Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 27.8900

Width : 3.303

Height : 10.040

Entry



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.8717
Width : 2.495
Height : 9.978



B0L002S, U11-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	335
Mean	46.58
Std Dev	10.1

Turn on : 30.8398

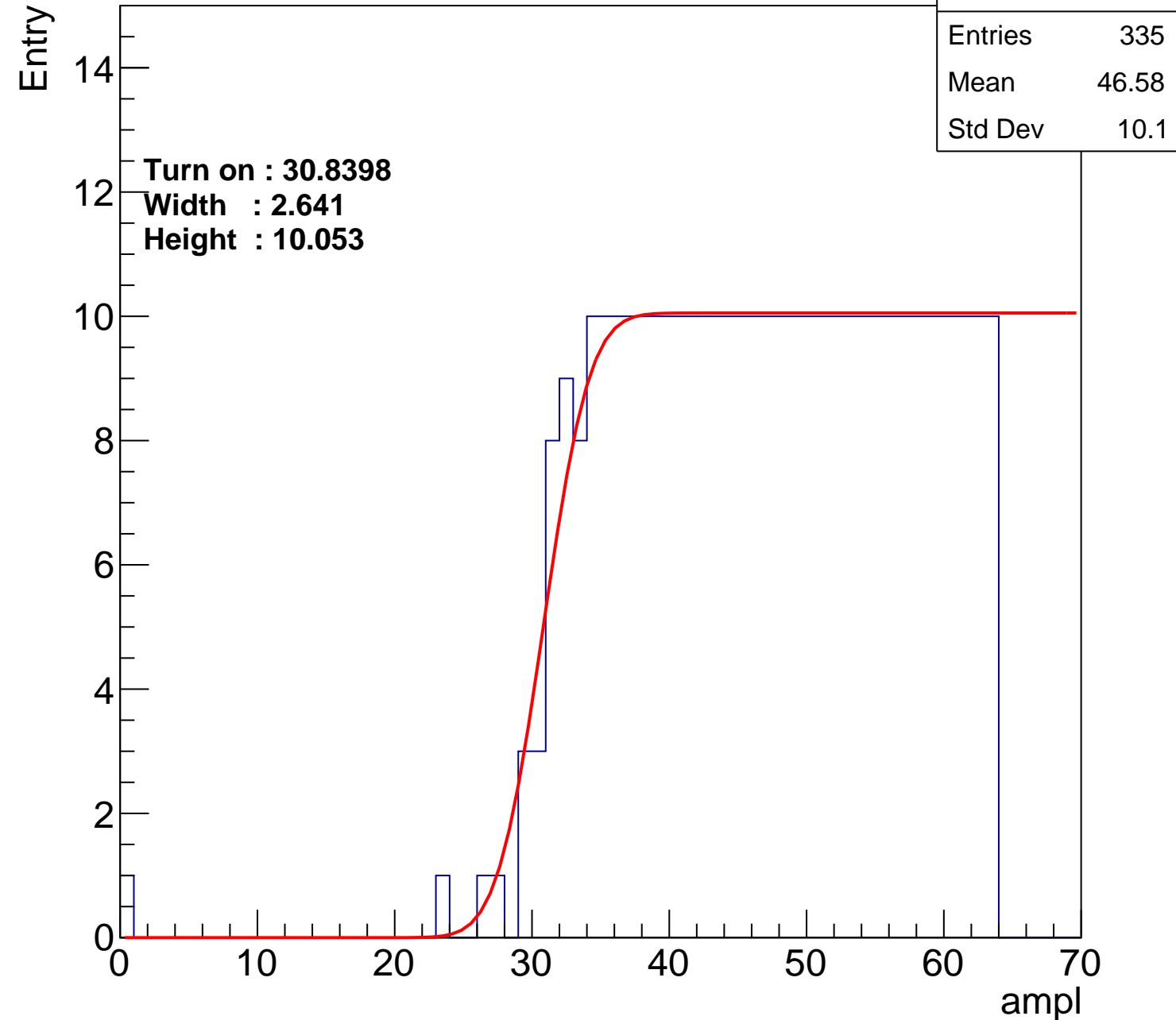
Width : 2.641

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	338
Mean	46.16
Std Dev	10.96

Turn on : 30.5183

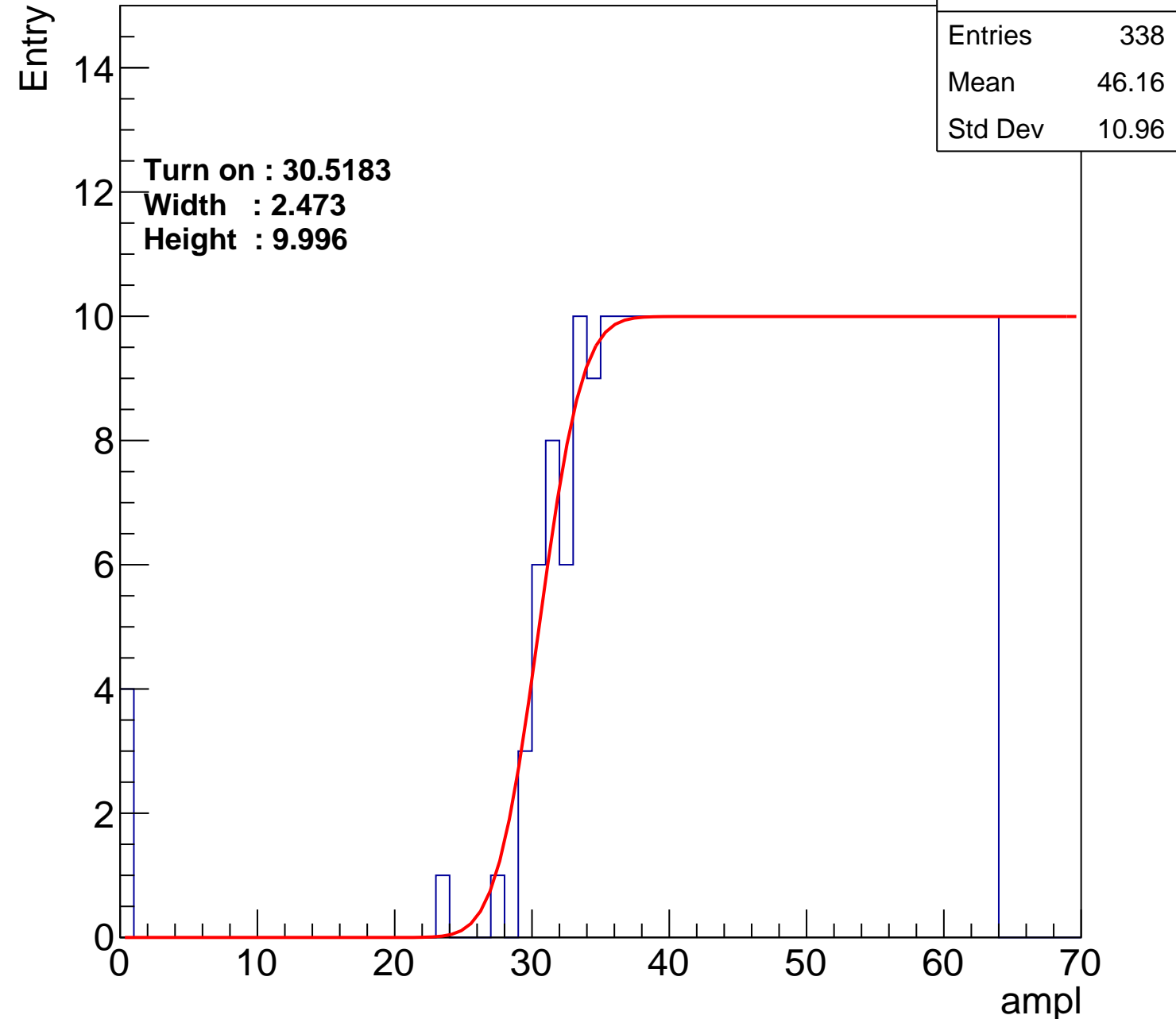
Width : 2.473

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch17

calib_packv5_042523_0143.root, FC#8, port C1

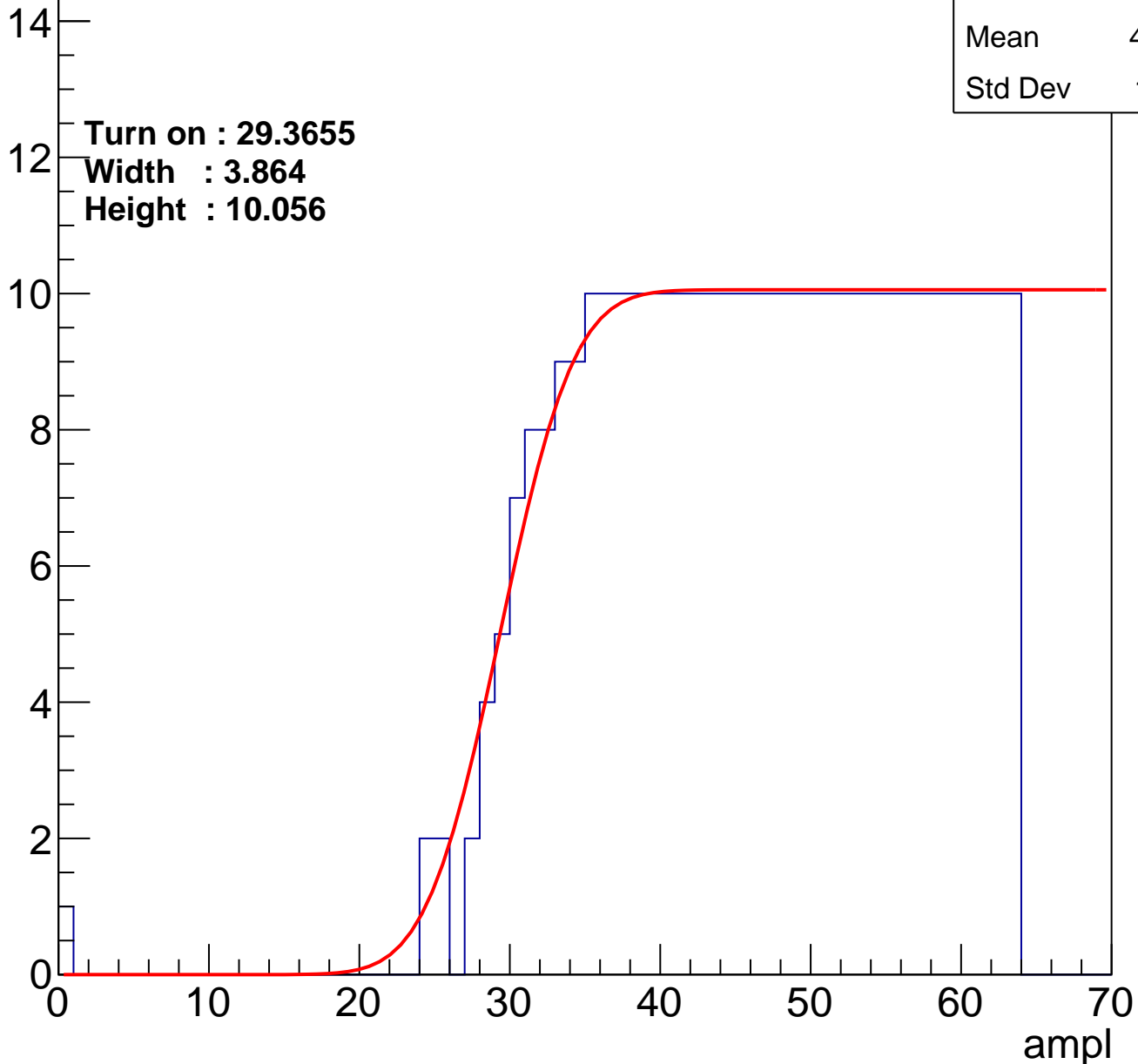
Entries	347
Mean	45.93
Std Dev	10.51

Turn on : 29.3655

Width : 3.864

Height : 10.056

Entry



B0L002S, U11-ch18

calib_packv5_042523_0143.root, FC#8, port C1

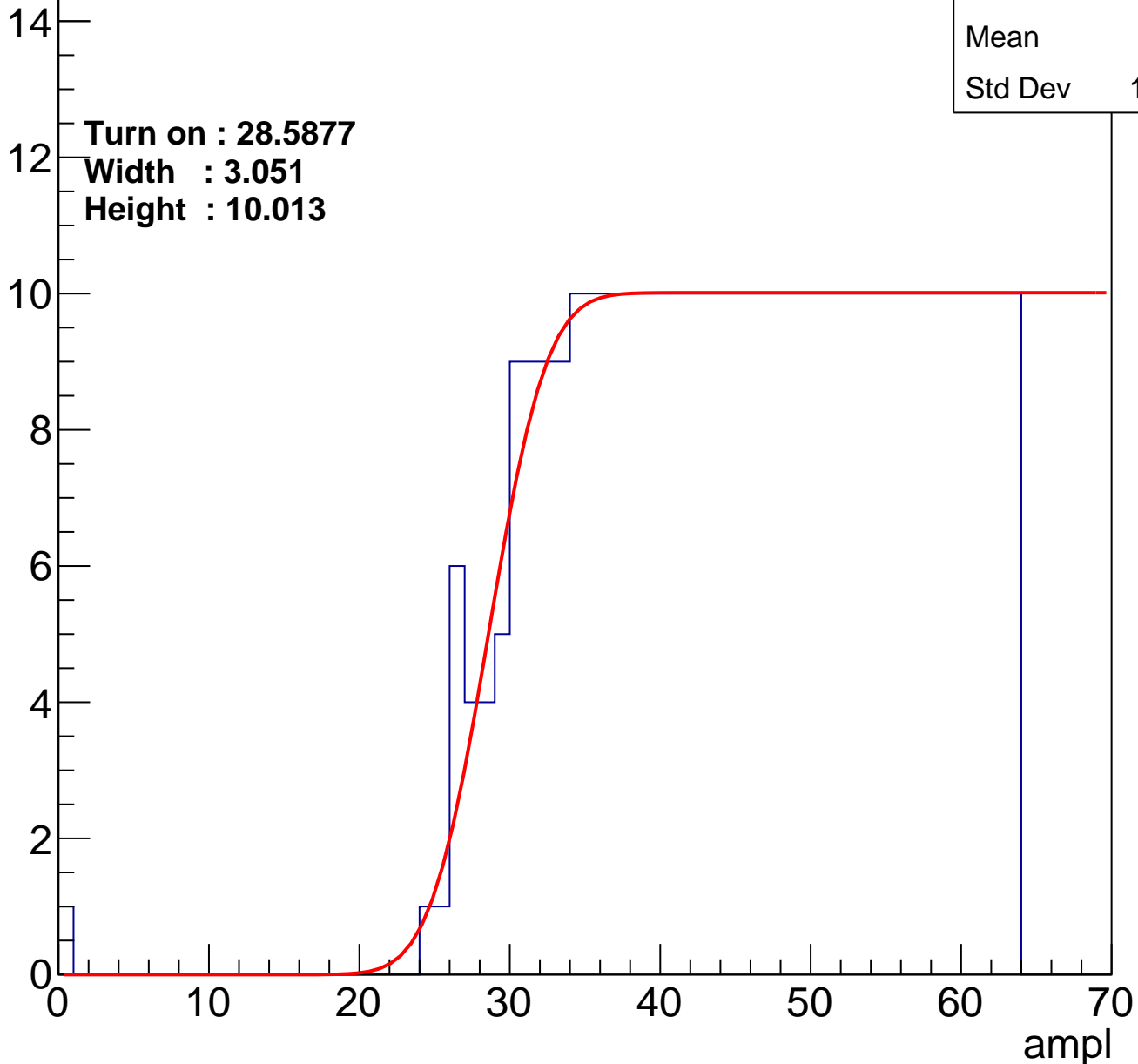
Entries	358
Mean	45.4
Std Dev	10.76

Turn on : 28.5877

Width : 3.051

Height : 10.013

Entry



B0L002S, U11-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.43
Std Dev	10.92

Turn on : 28.7970

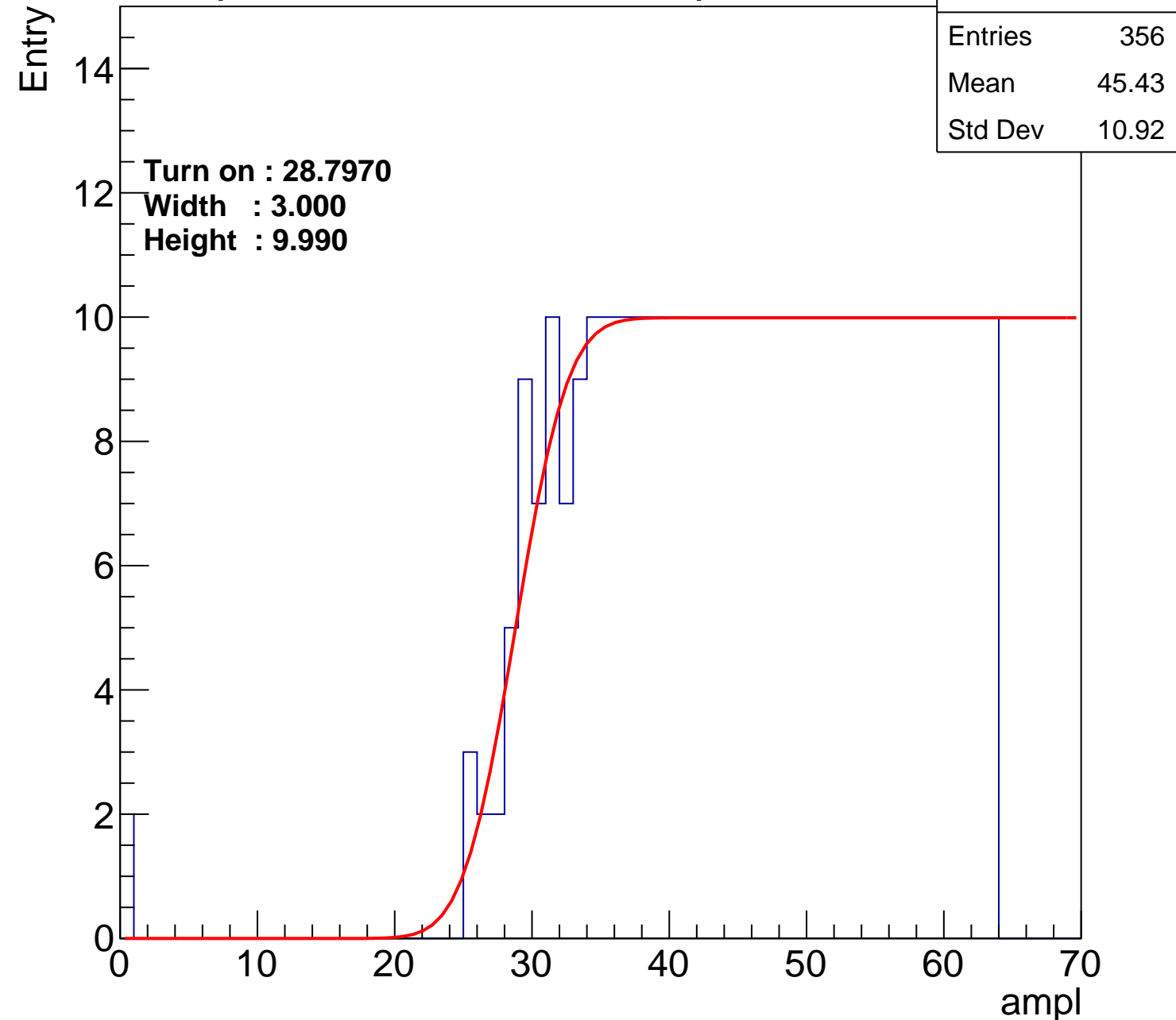
Width : 3.000

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.82
Std Dev	10.69

Turn on : 29.4504

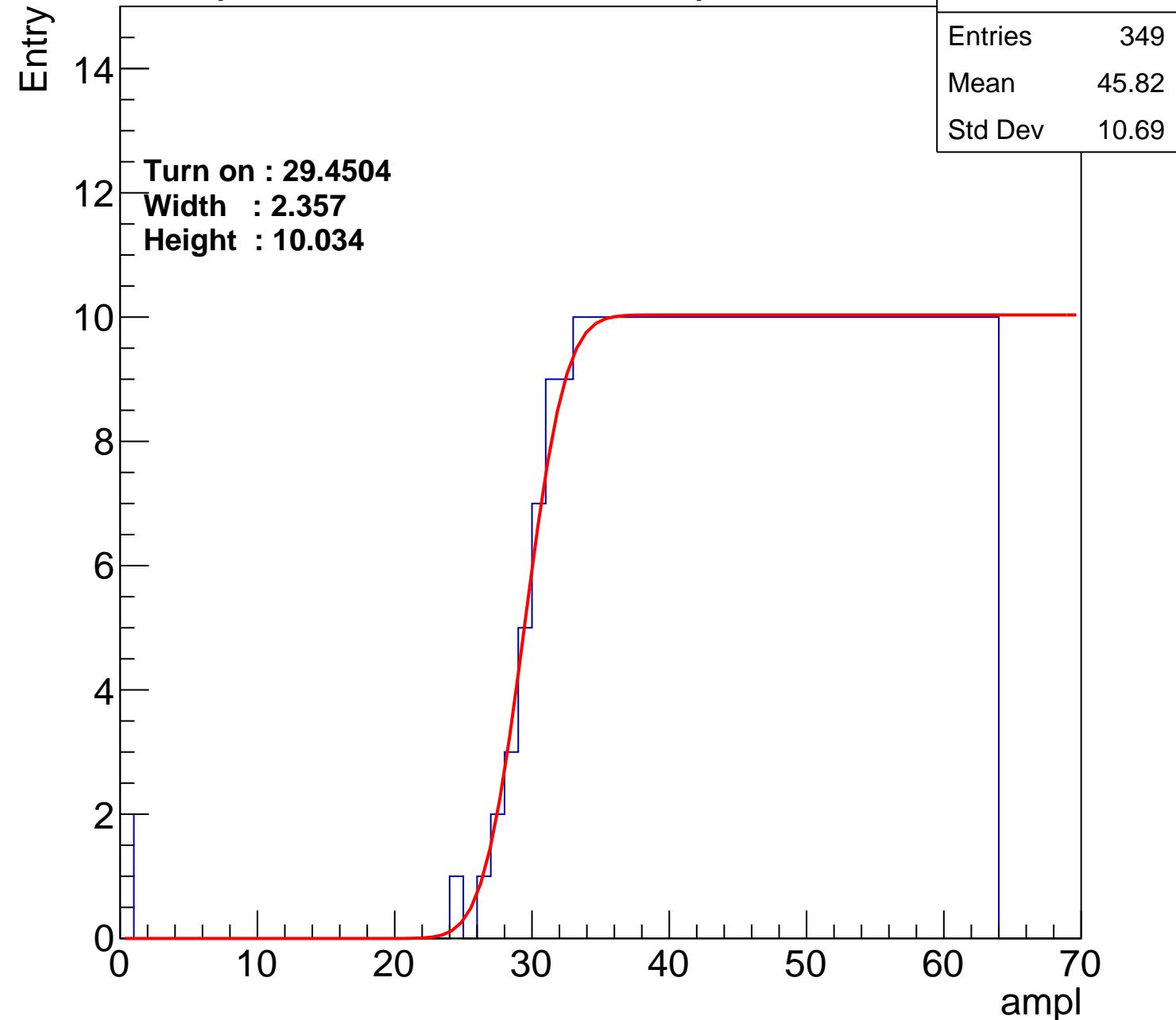
Width : 2.357

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.55
Std Dev	10.69

Turn on : 28.8143

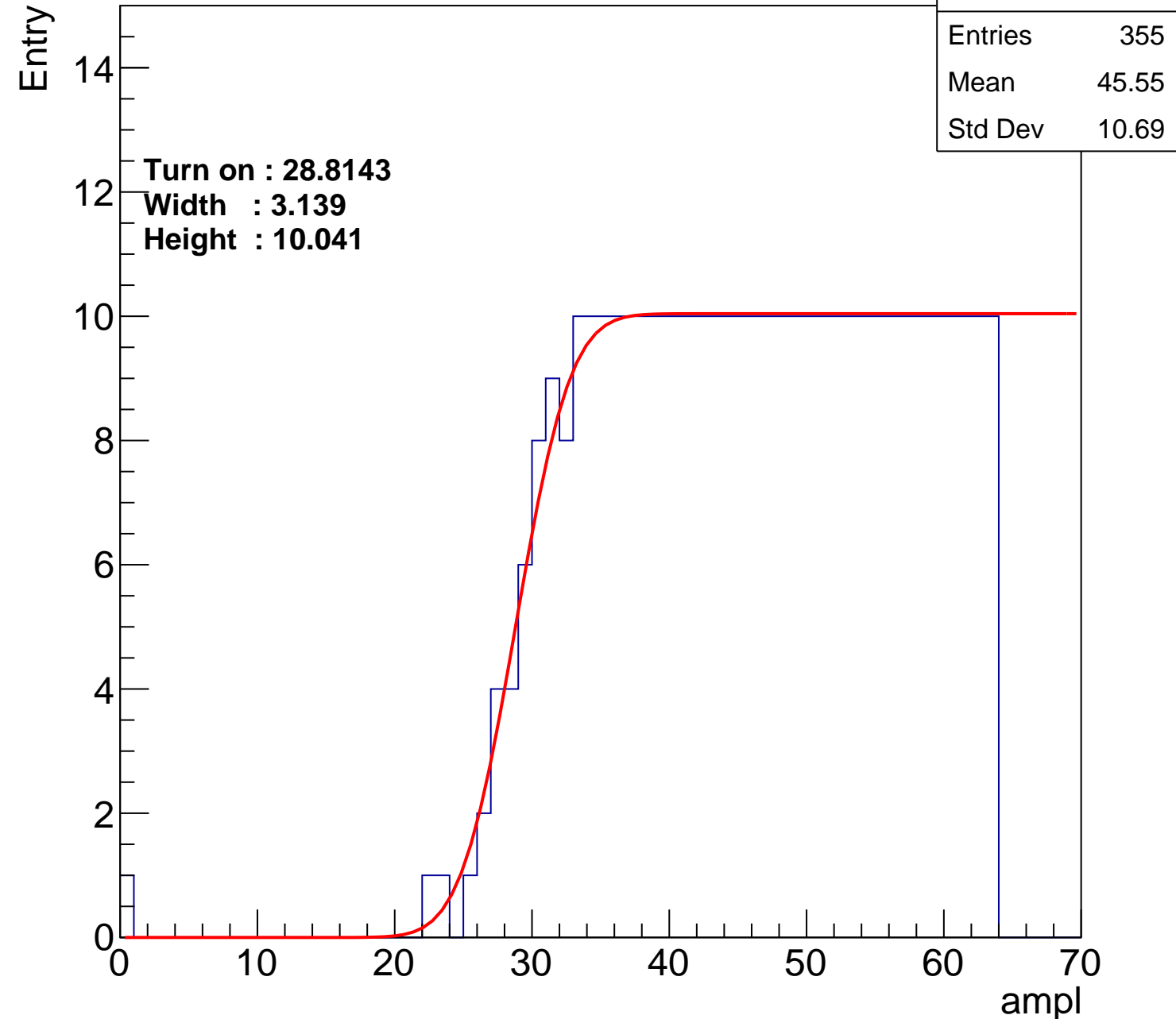
Width : 3.139

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch22

calib_packv5_042523_0143.root, FC#8, port C1

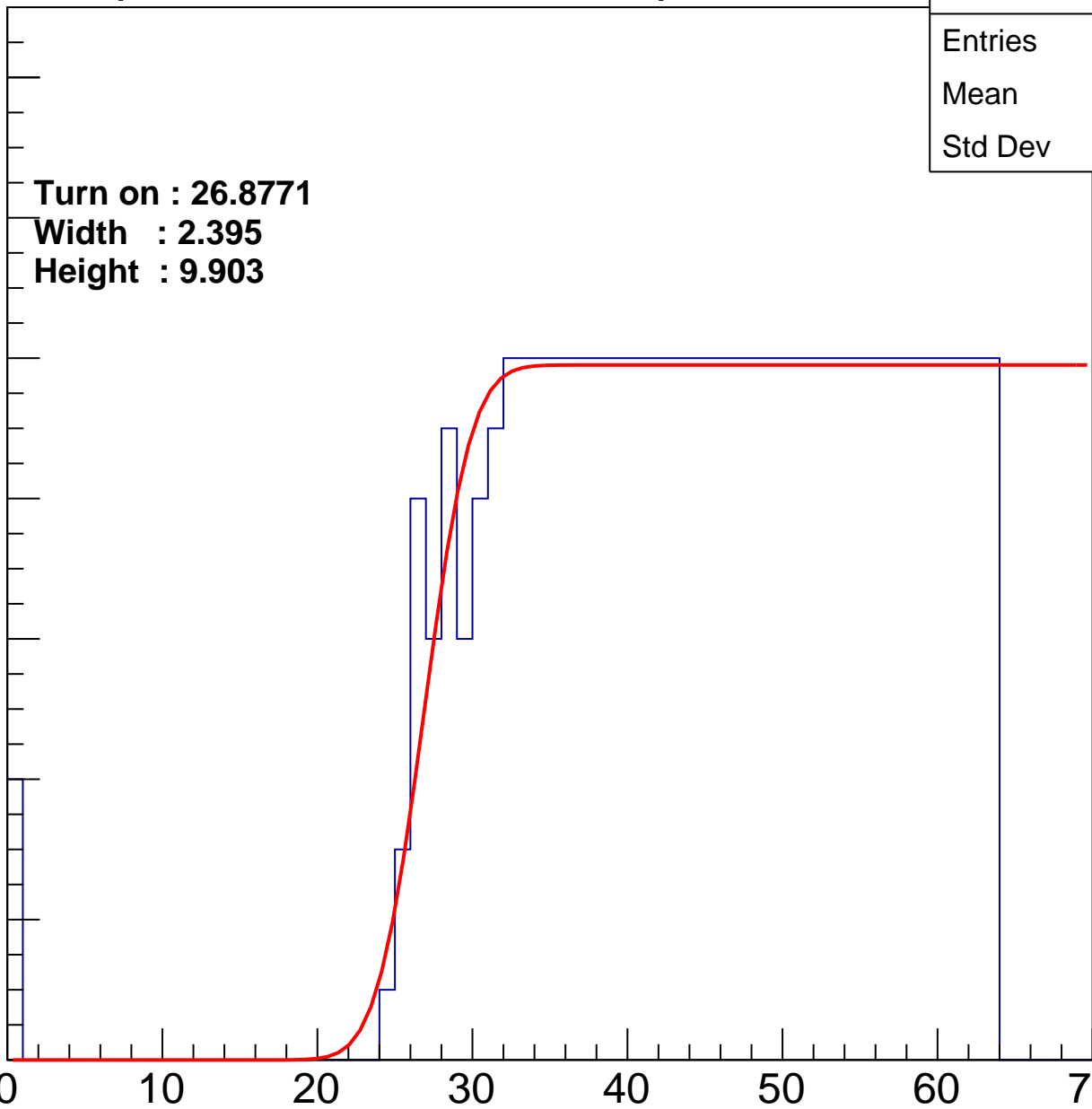
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8771
Width : 2.395
Height : 9.903

Entries	374
Mean	44.42
Std Dev	11.73

ampl



B0L002S, U11-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.96
Std Dev	10.95

Turn on : 27.4614

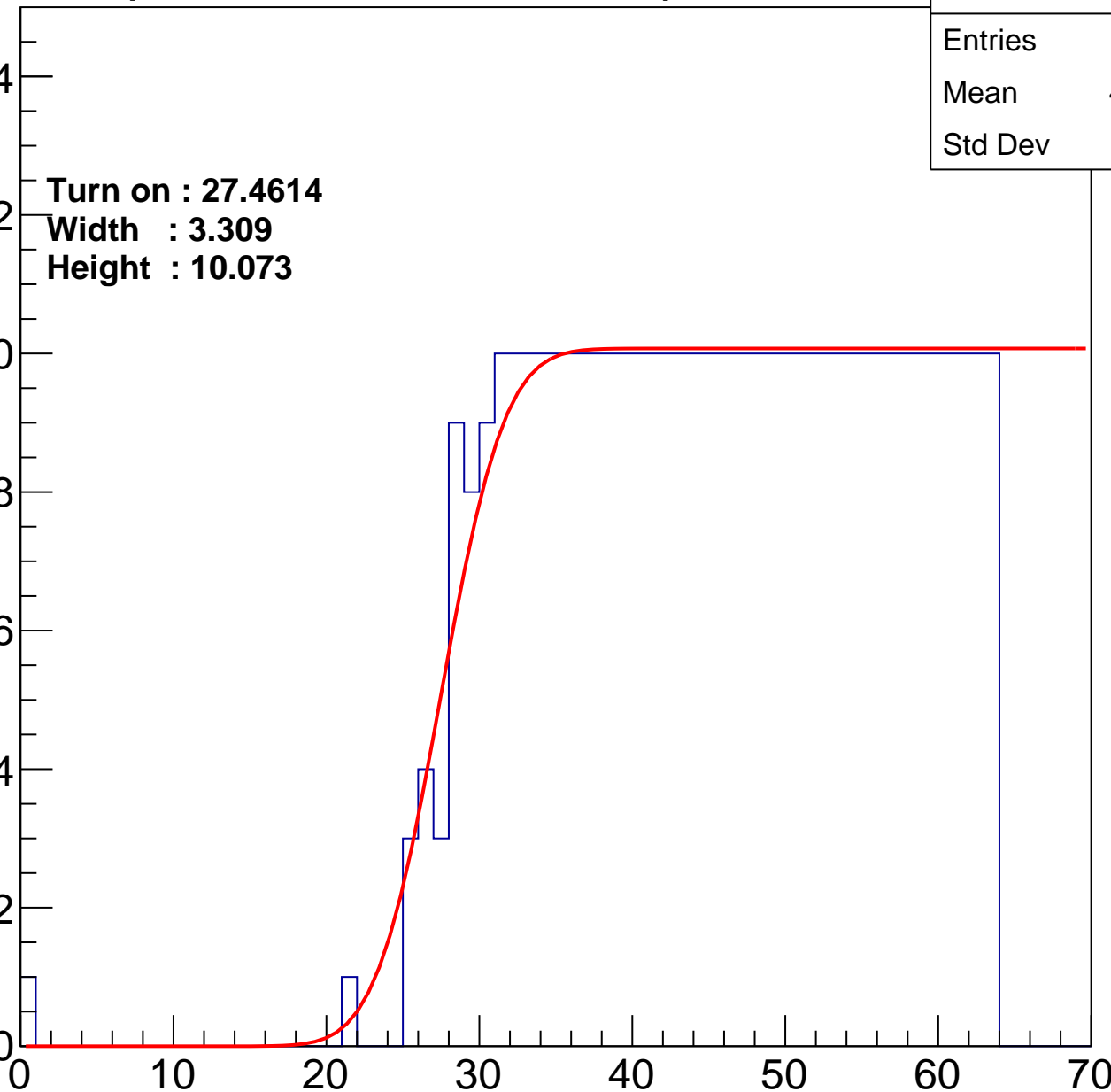
Width : 3.309

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.7396
Width : 2.246
Height : 9.963



B0L002S, U11-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.24
Std Dev	11.01

Turn on : 28.1422

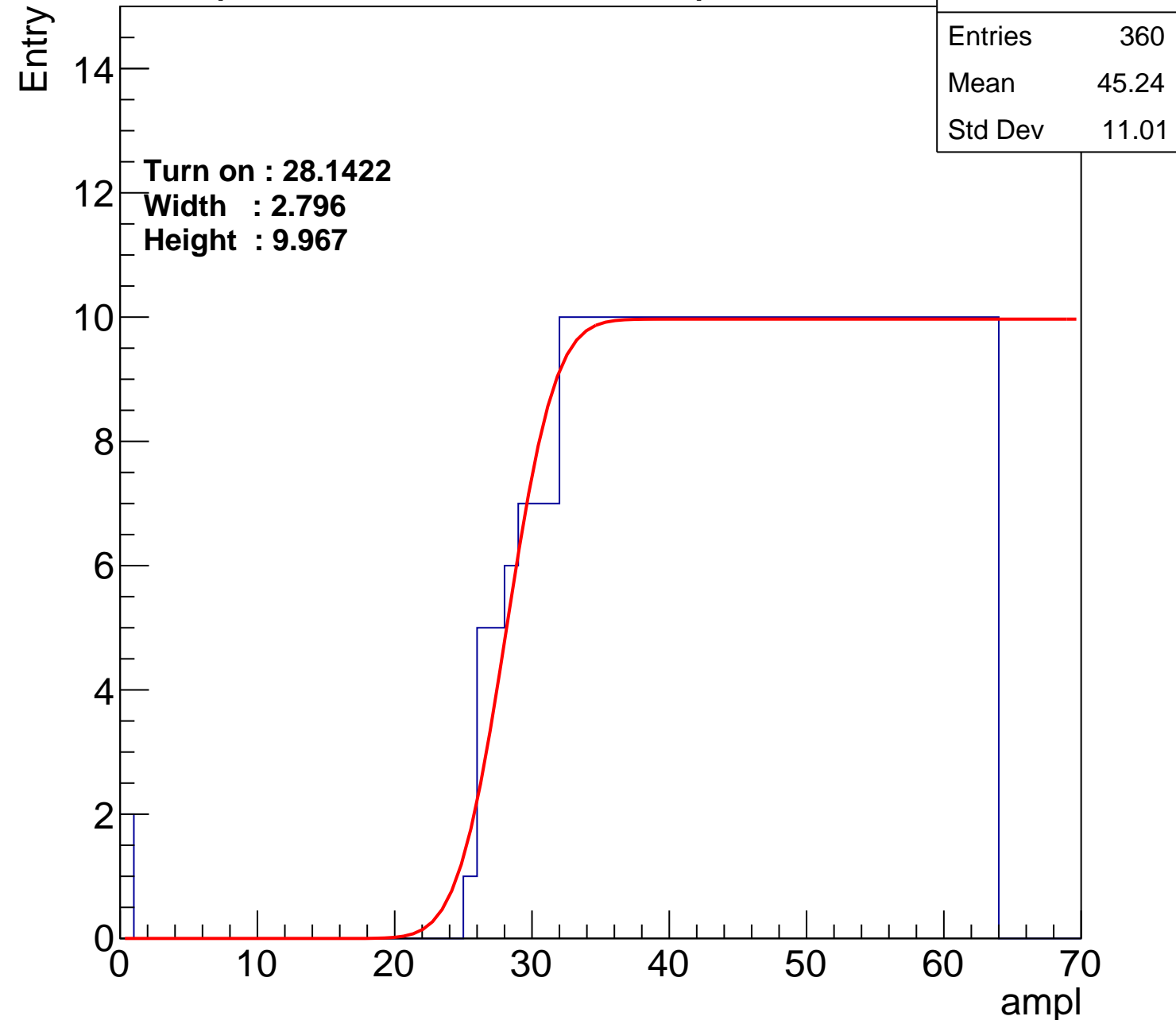
Width : 2.796

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	45.96
Std Dev	10.84

Turn on : 29.7882

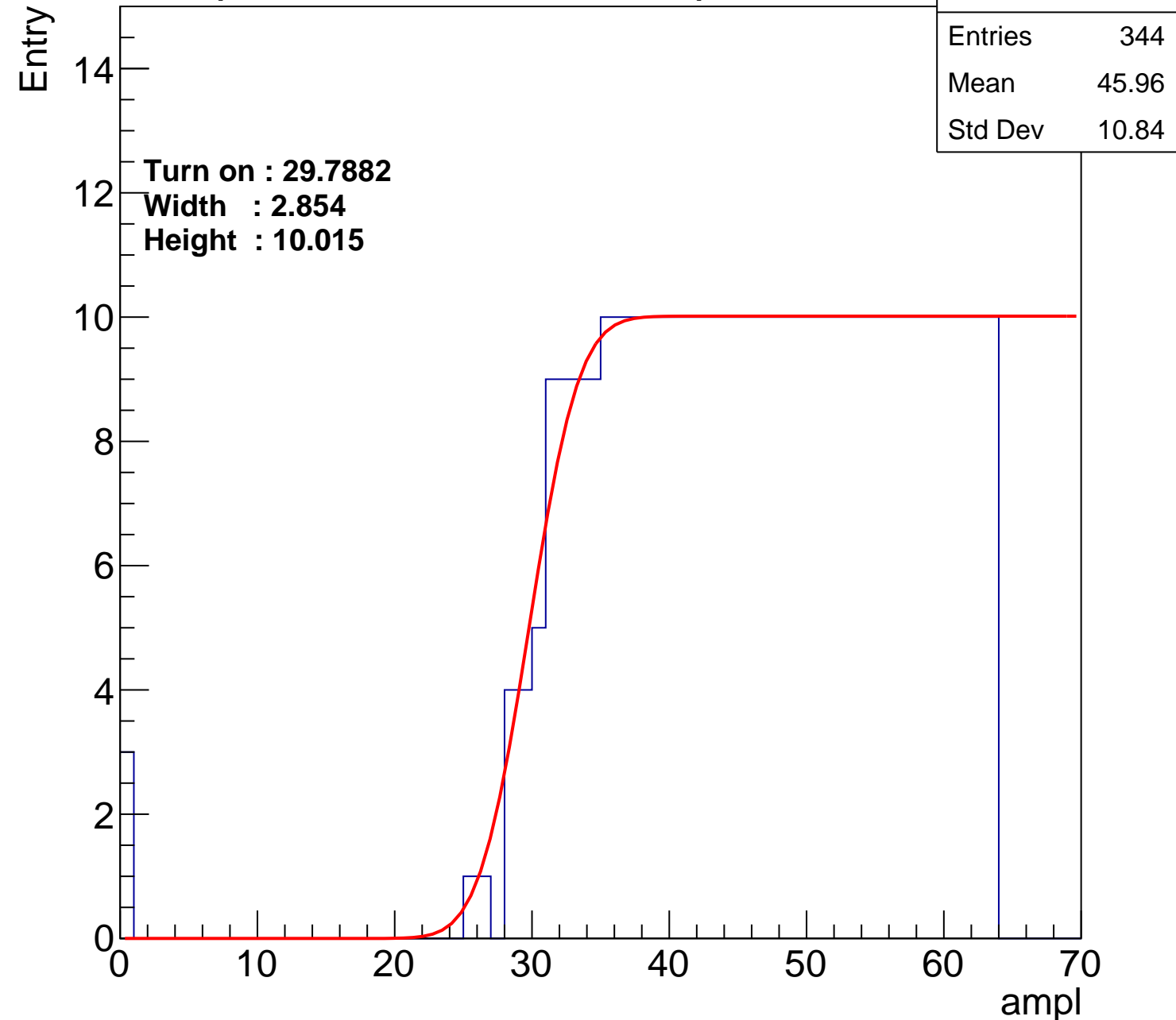
Width : 2.854

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.92
Std Dev	10.41

Turn on : 29.2130

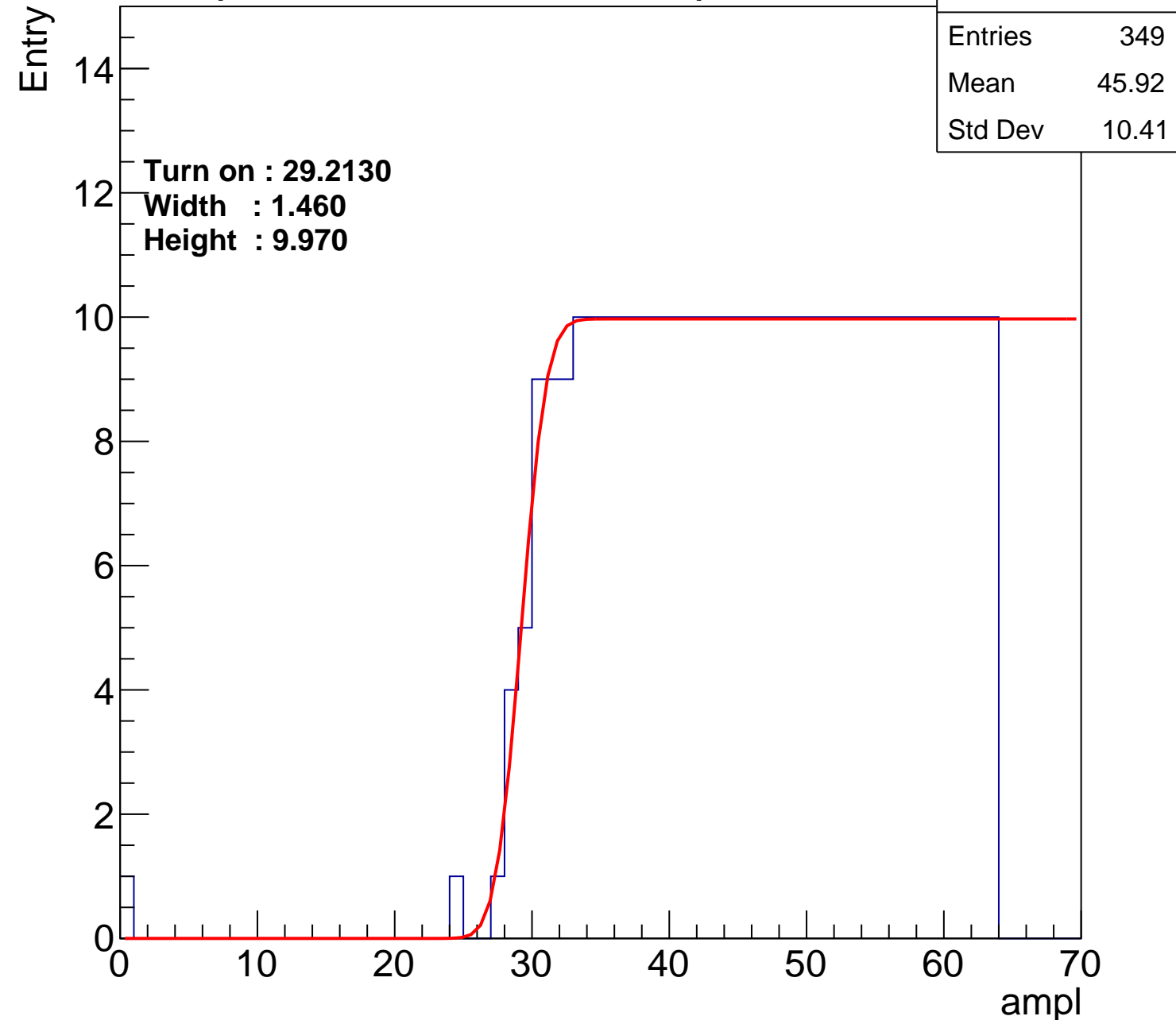
Width : 1.460

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.88
Std Dev	11.02

Turn on : 27.5073

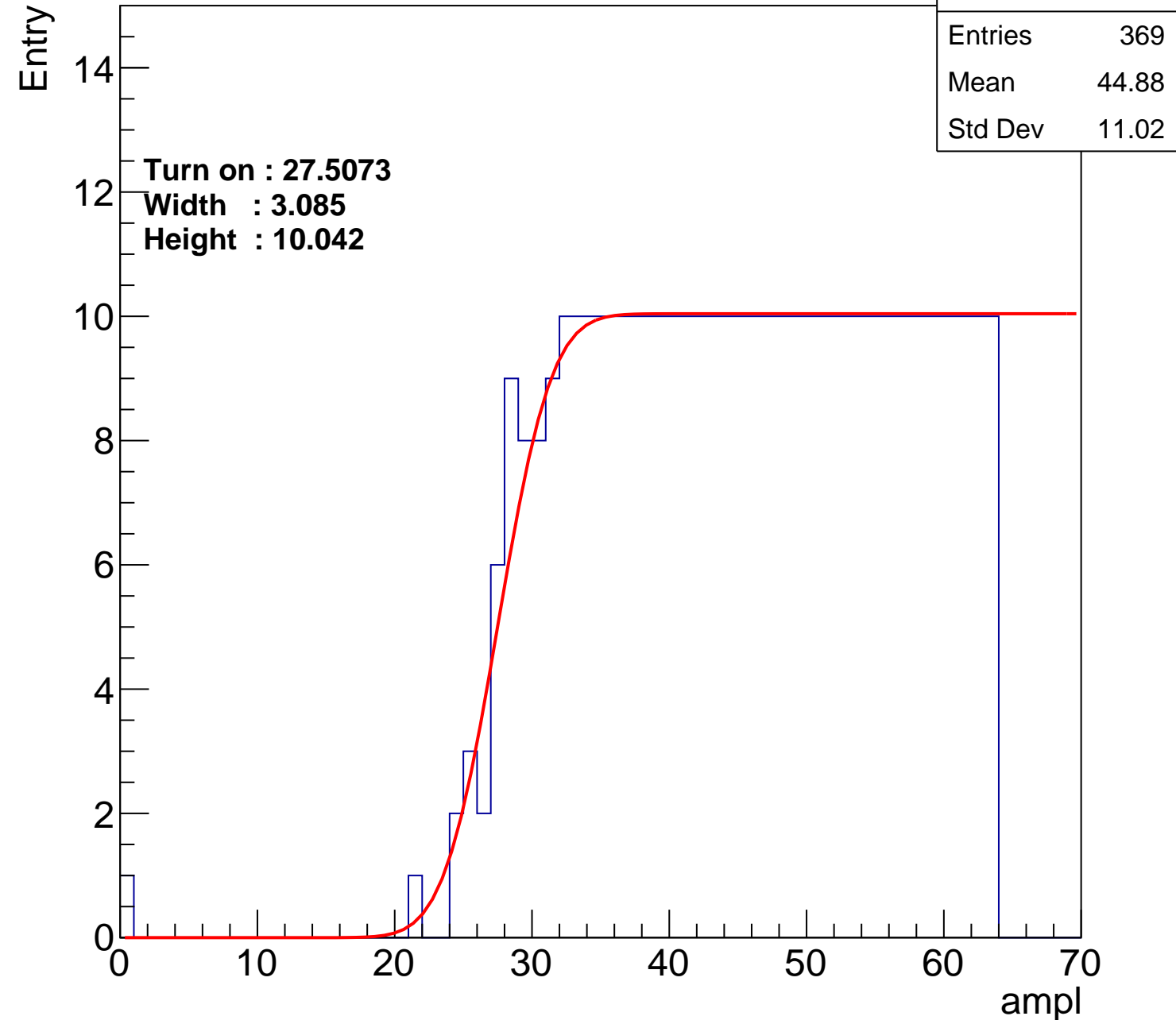
Width : 3.085

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch29

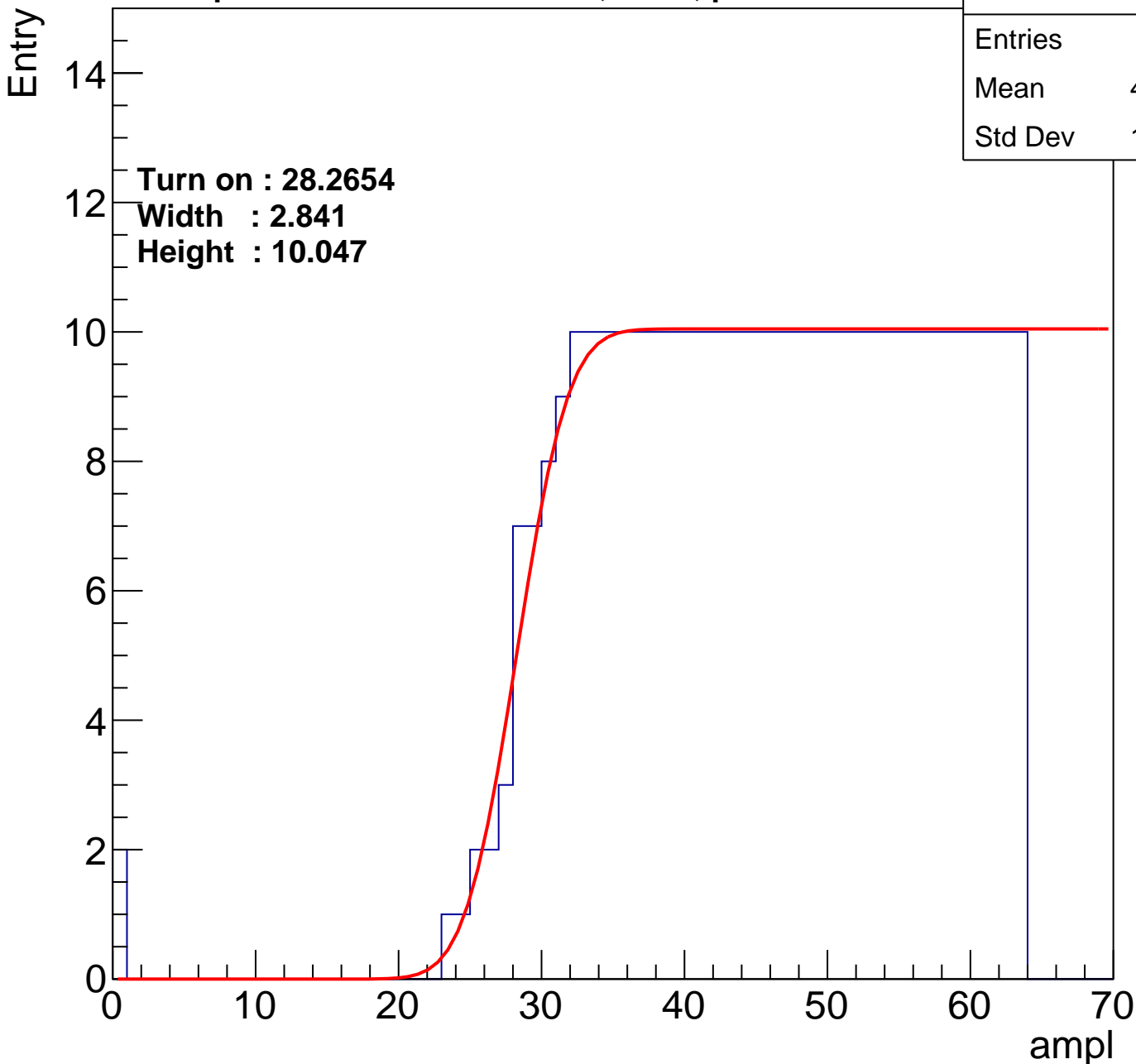
calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.16
Std Dev	11.04

Turn on : 28.2654

Width : 2.841

Height : 10.047



B0L002S, U11-ch30

calib_packv5_042523_0143.root, FC#8, port C1

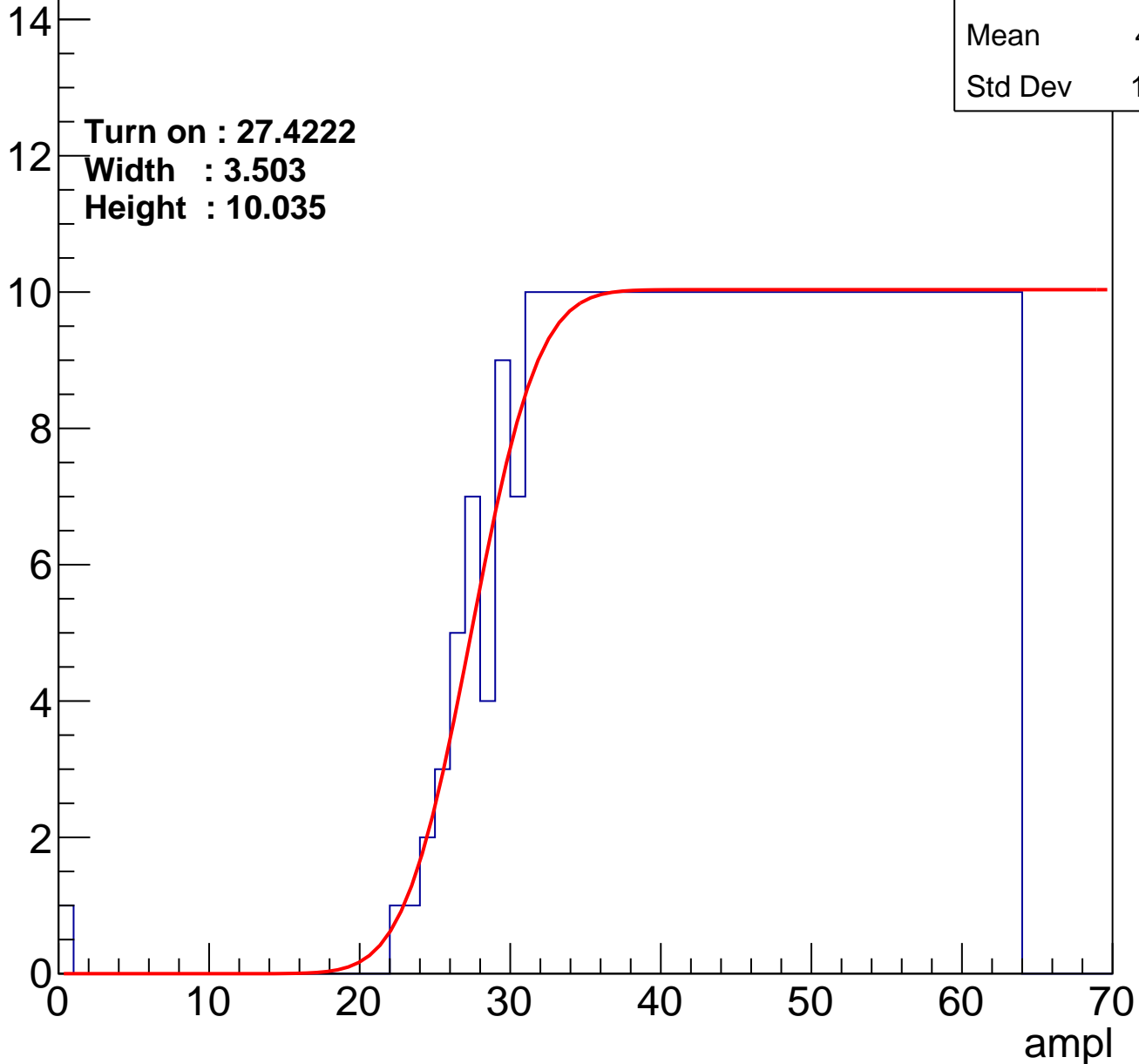
Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.4222

Width : 3.503

Height : 10.035

Entry



B0L002S, U11-ch31

calib_packv5_042523_0143.root, FC#8, port C1

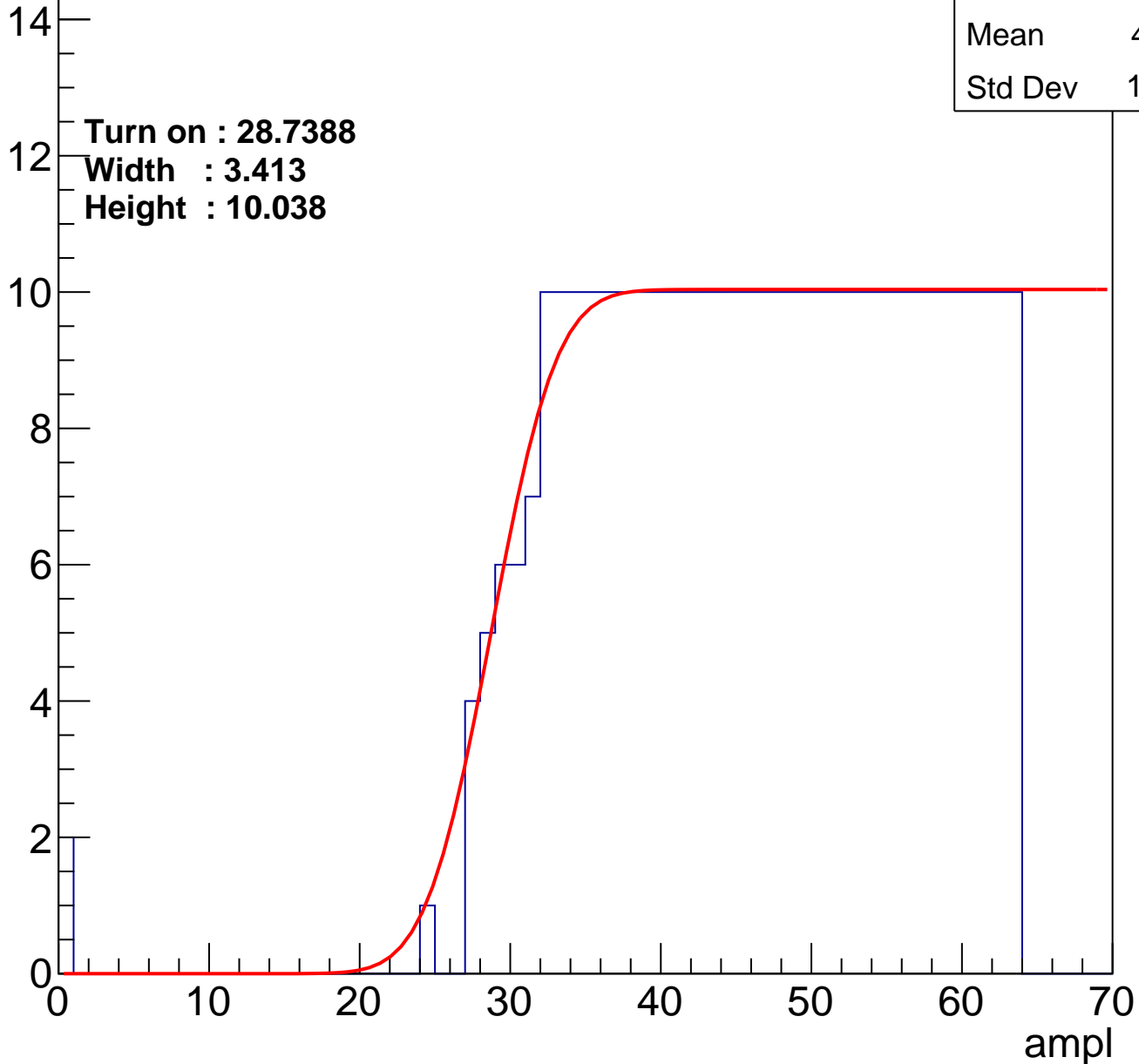
Entries	351
Mean	45.71
Std Dev	10.75

Turn on : 28.7388

Width : 3.413

Height : 10.038

Entry



B0L002S, U11-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.2405

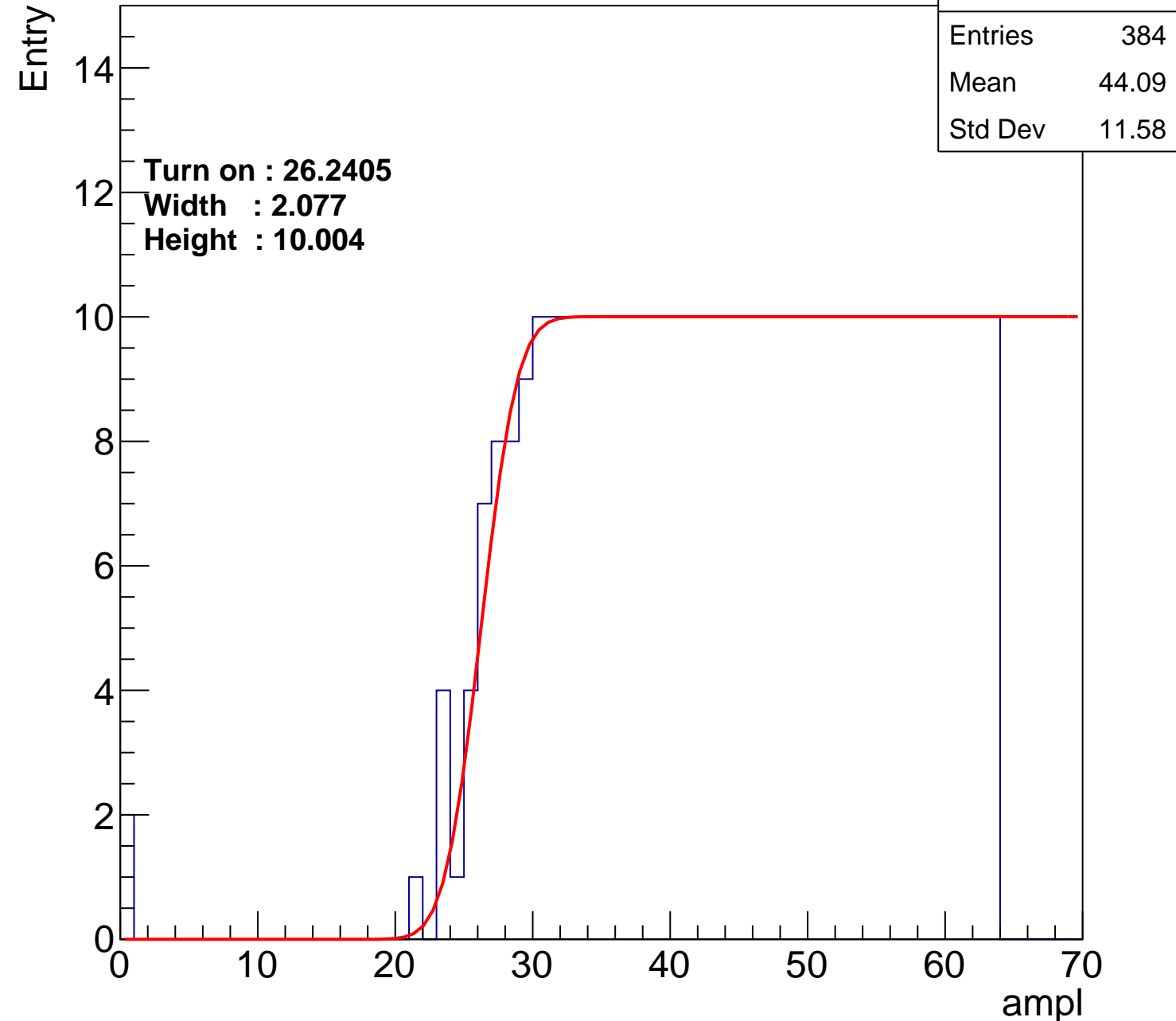
Width : 2.077

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.62
Std Dev	10.79

Turn on : 29.8469

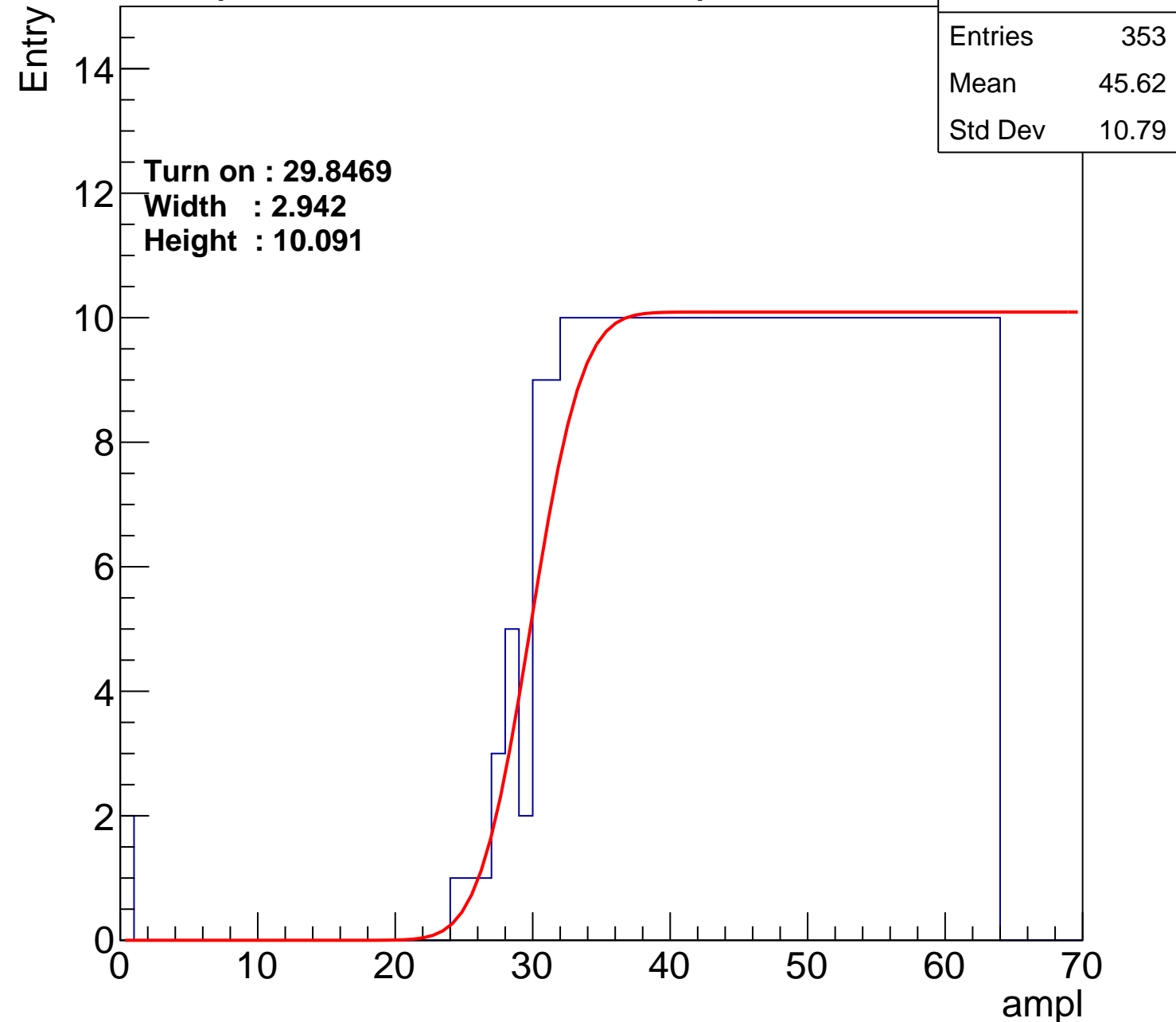
Width : 2.942

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.56
Std Dev	11.04

Turn on : 29.3497

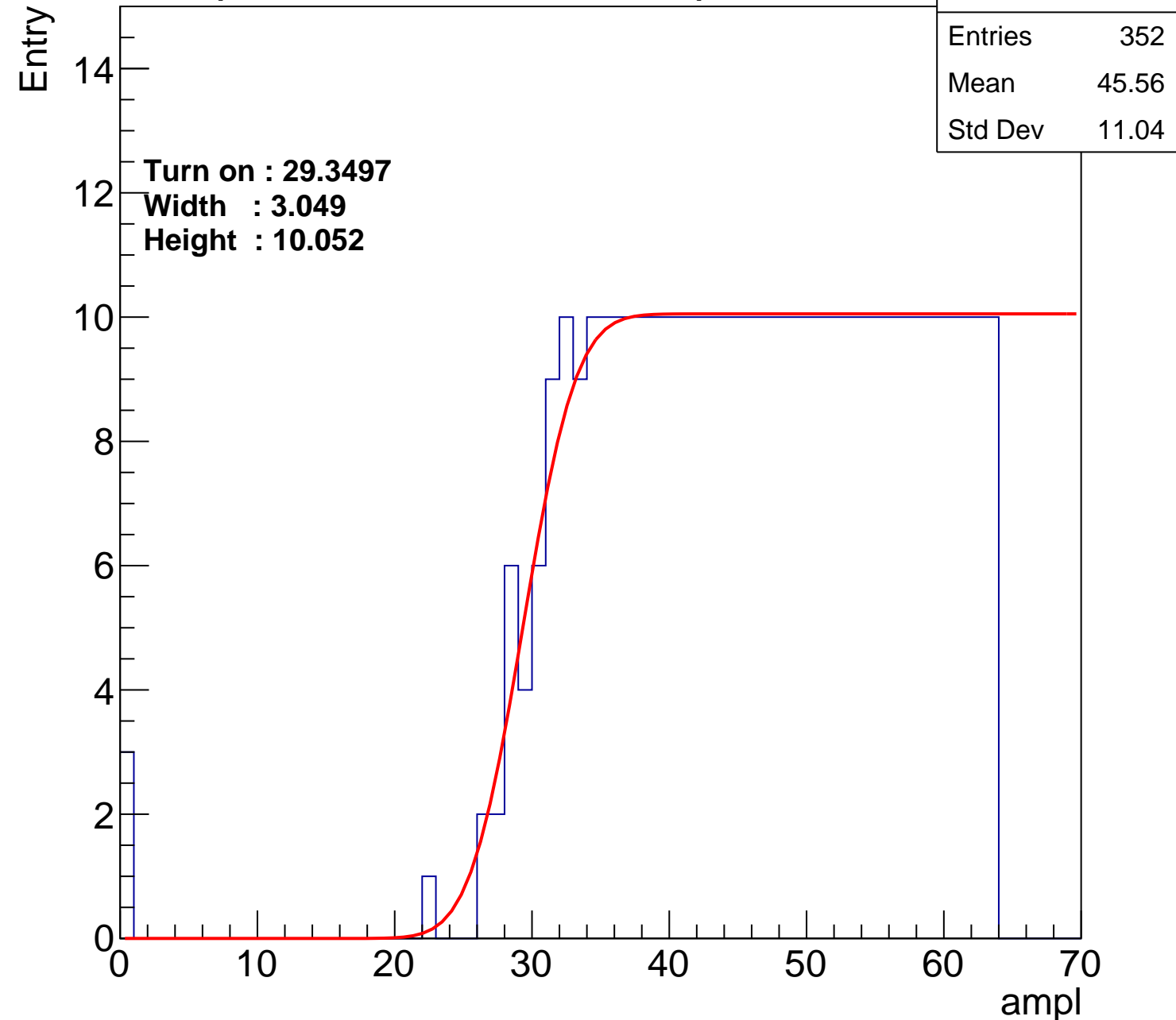
Width : 3.049

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch35

calib_packv5_042523_0143.root, FC#8, port C1

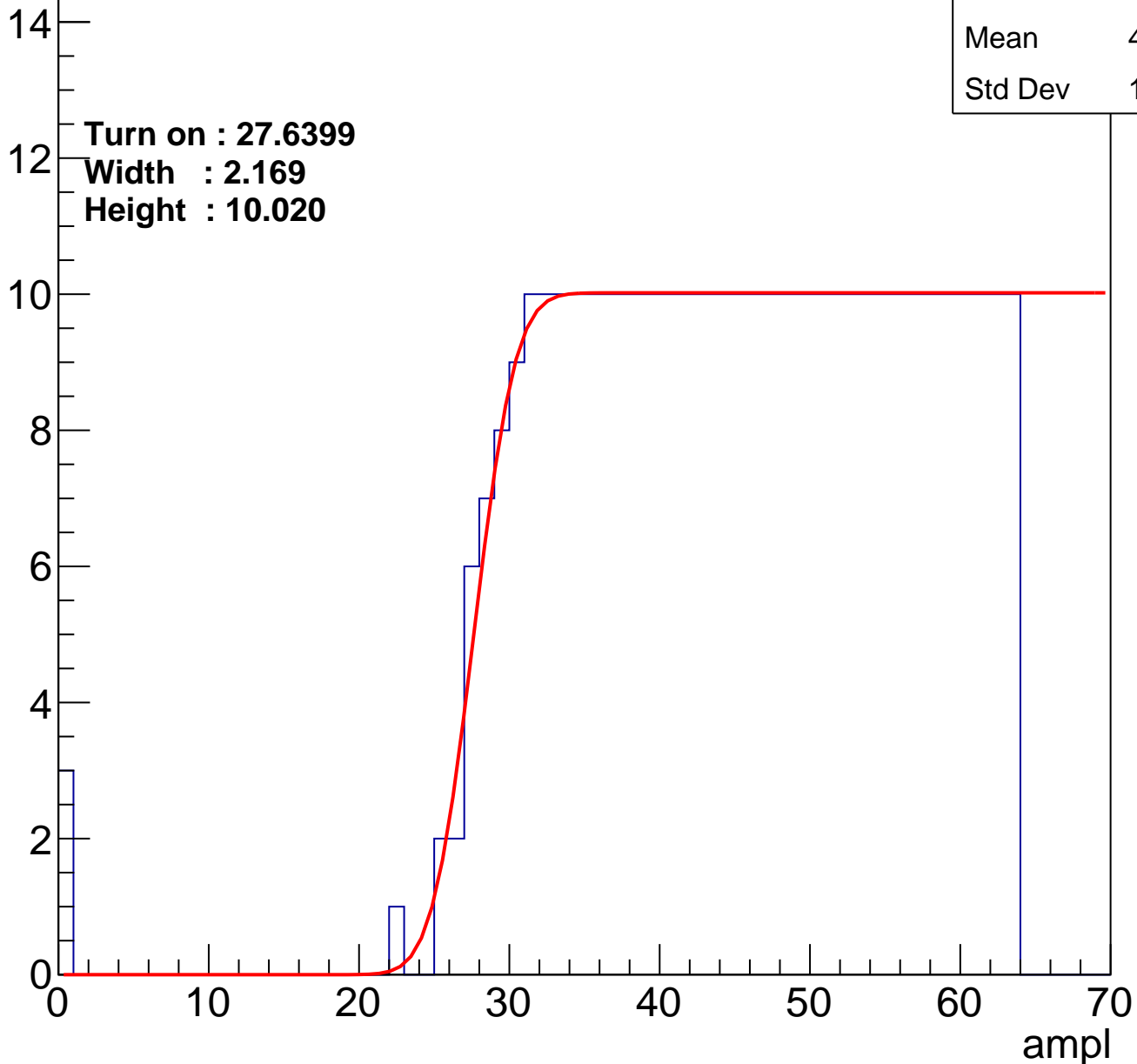
Entries	368
Mean	44.82
Std Dev	11.35

Turn on : 27.6399

Width : 2.169

Height : 10.020

Entry



B0L002S, U11-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.47
Std Dev	10.86

Turn on : 29.0100

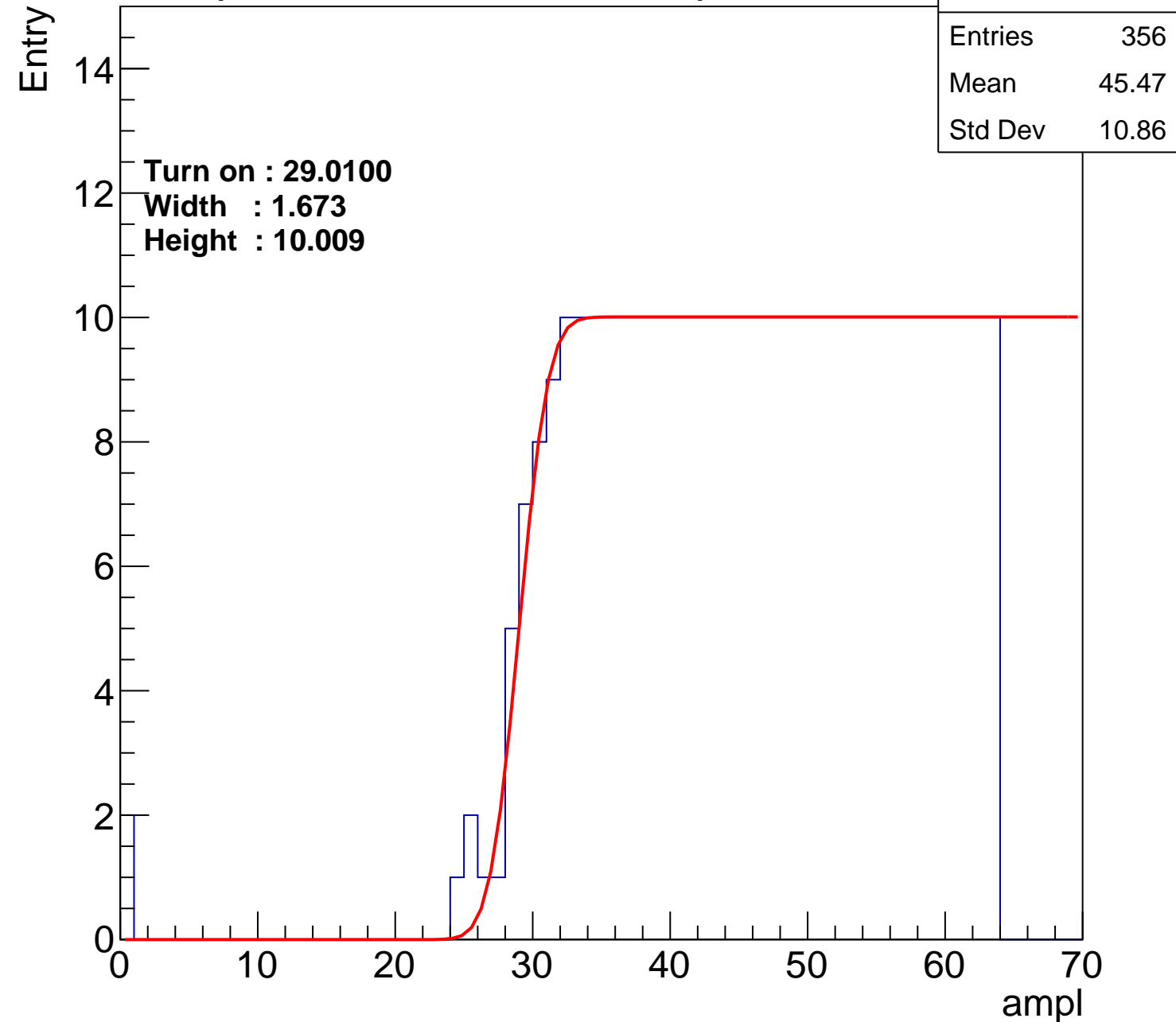
Width : 1.673

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.3895

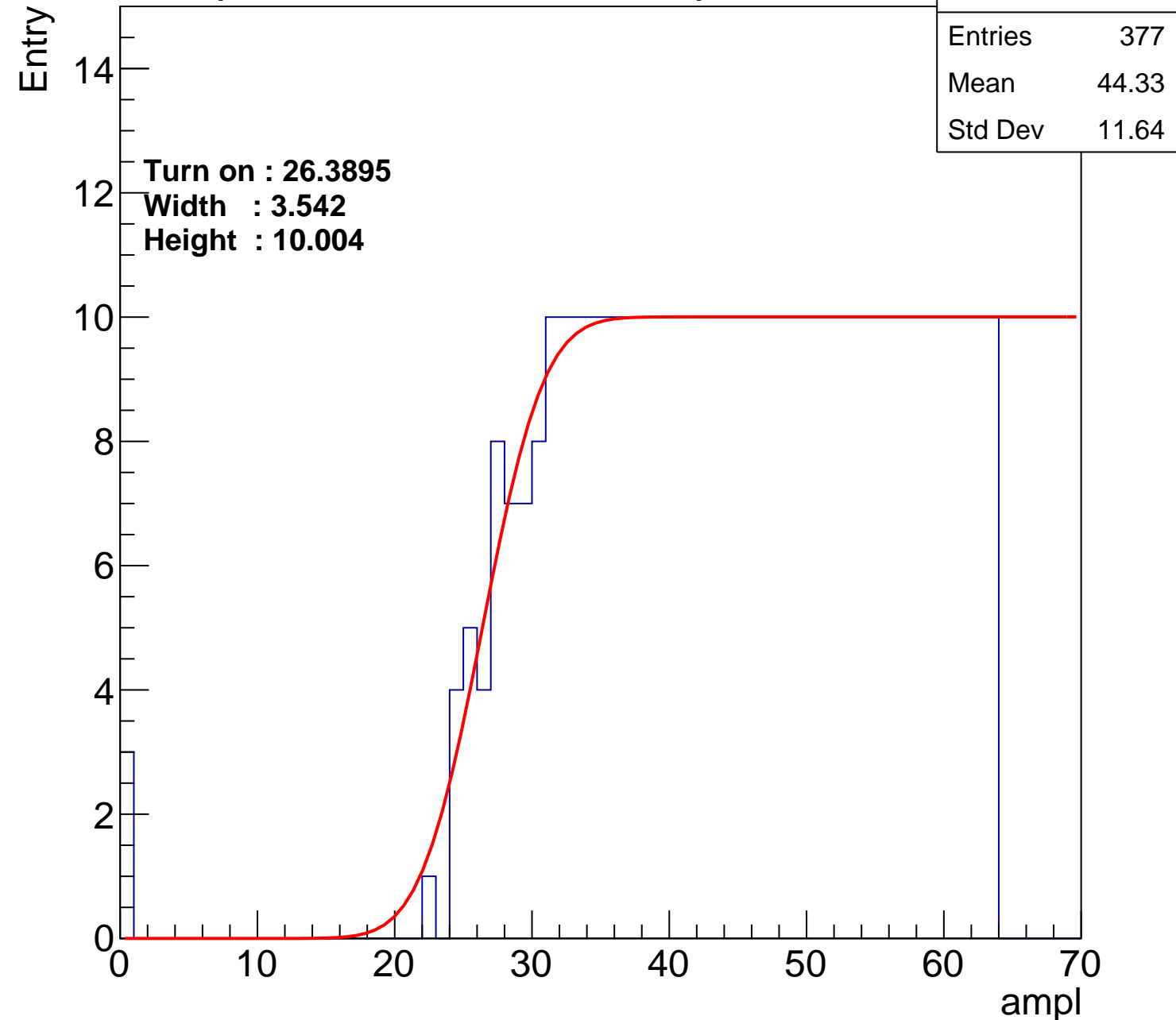
Width : 3.542

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.88
Std Dev	10.68

Turn on : 29.8280

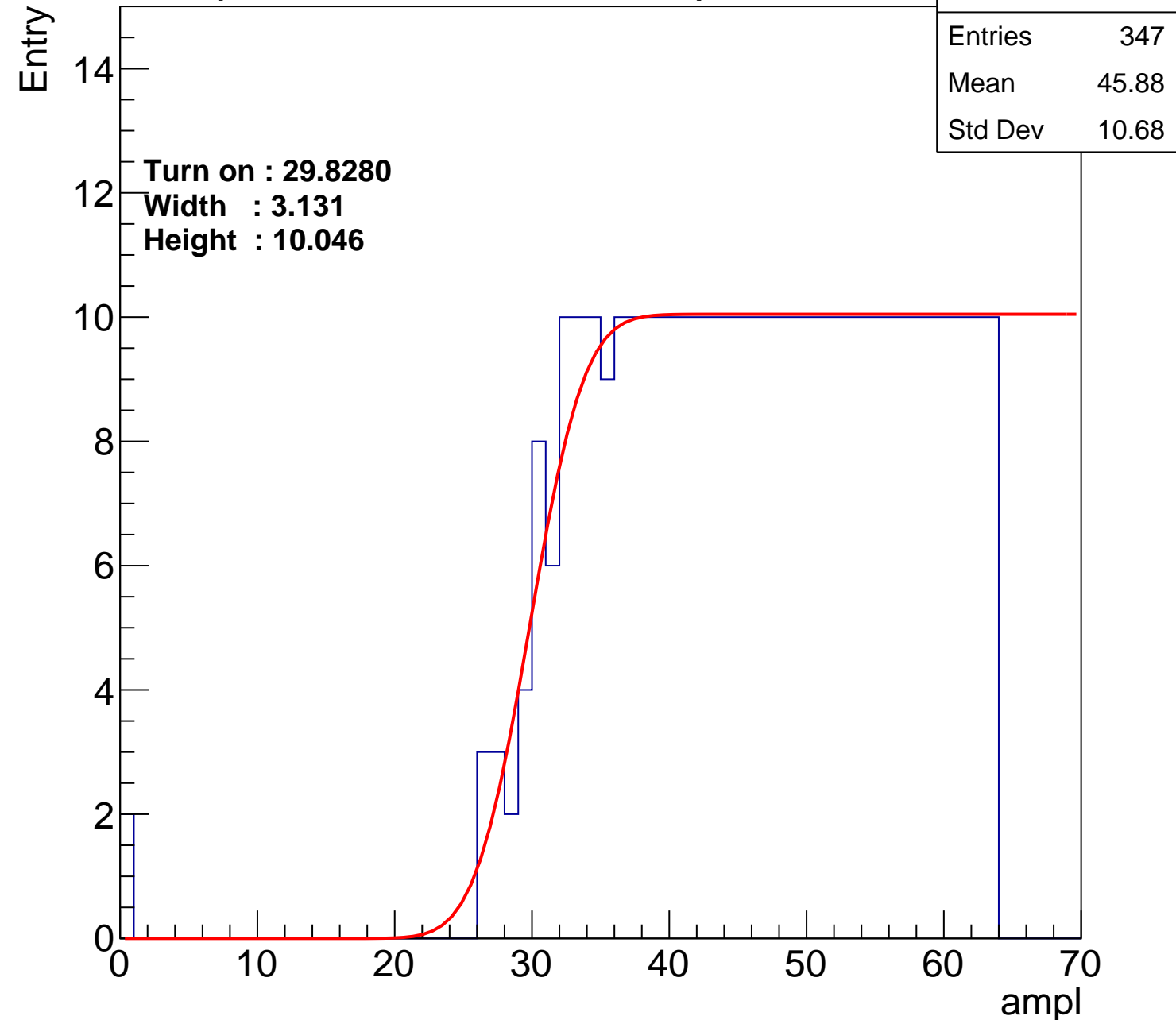
Width : 3.131

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch39

calib_packv5_042523_0143.root, FC#8, port C1

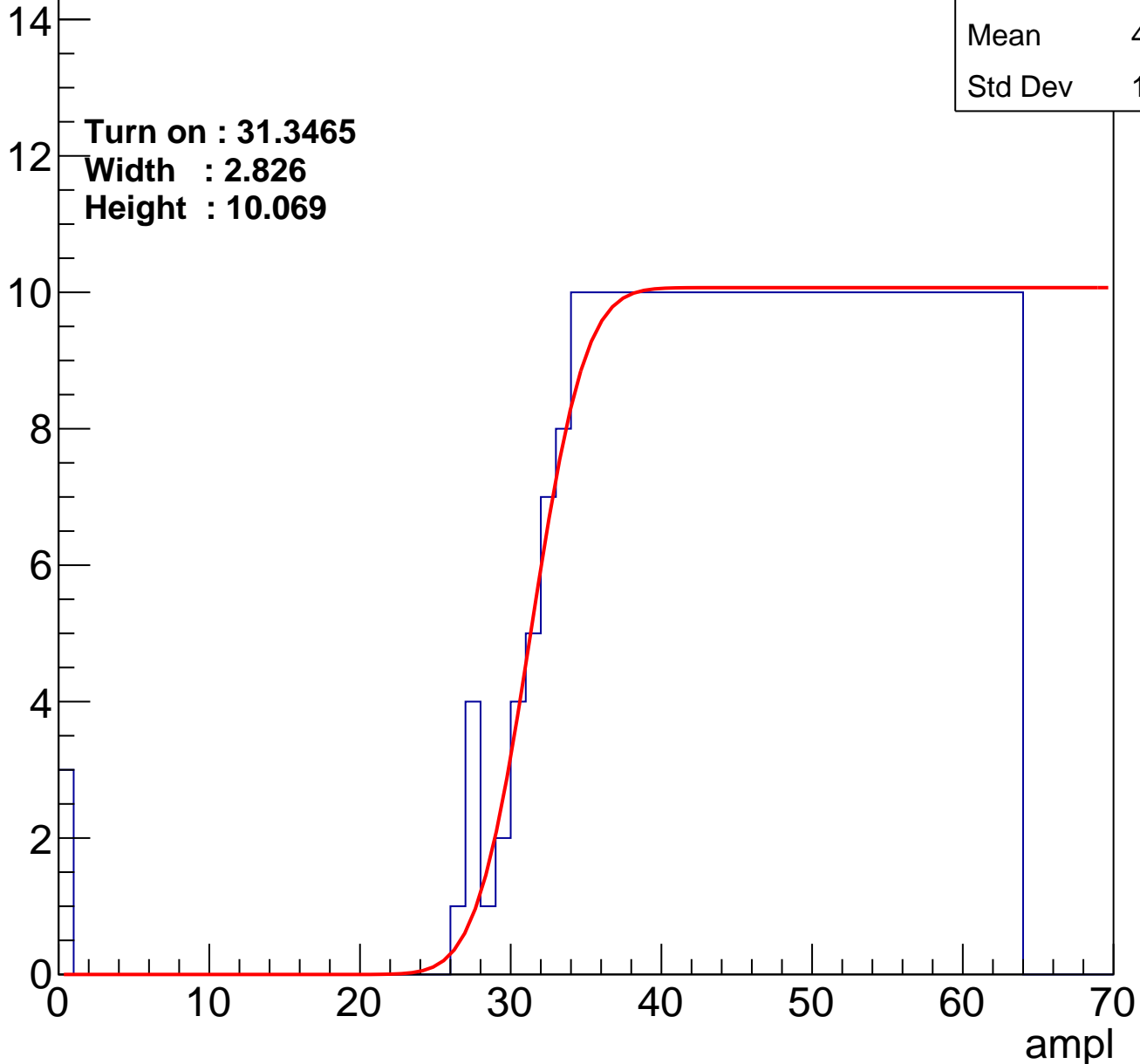
Entries	335
Mean	46.37
Std Dev	10.69

Turn on : 31.3465

Width : 2.826

Height : 10.069

Entry

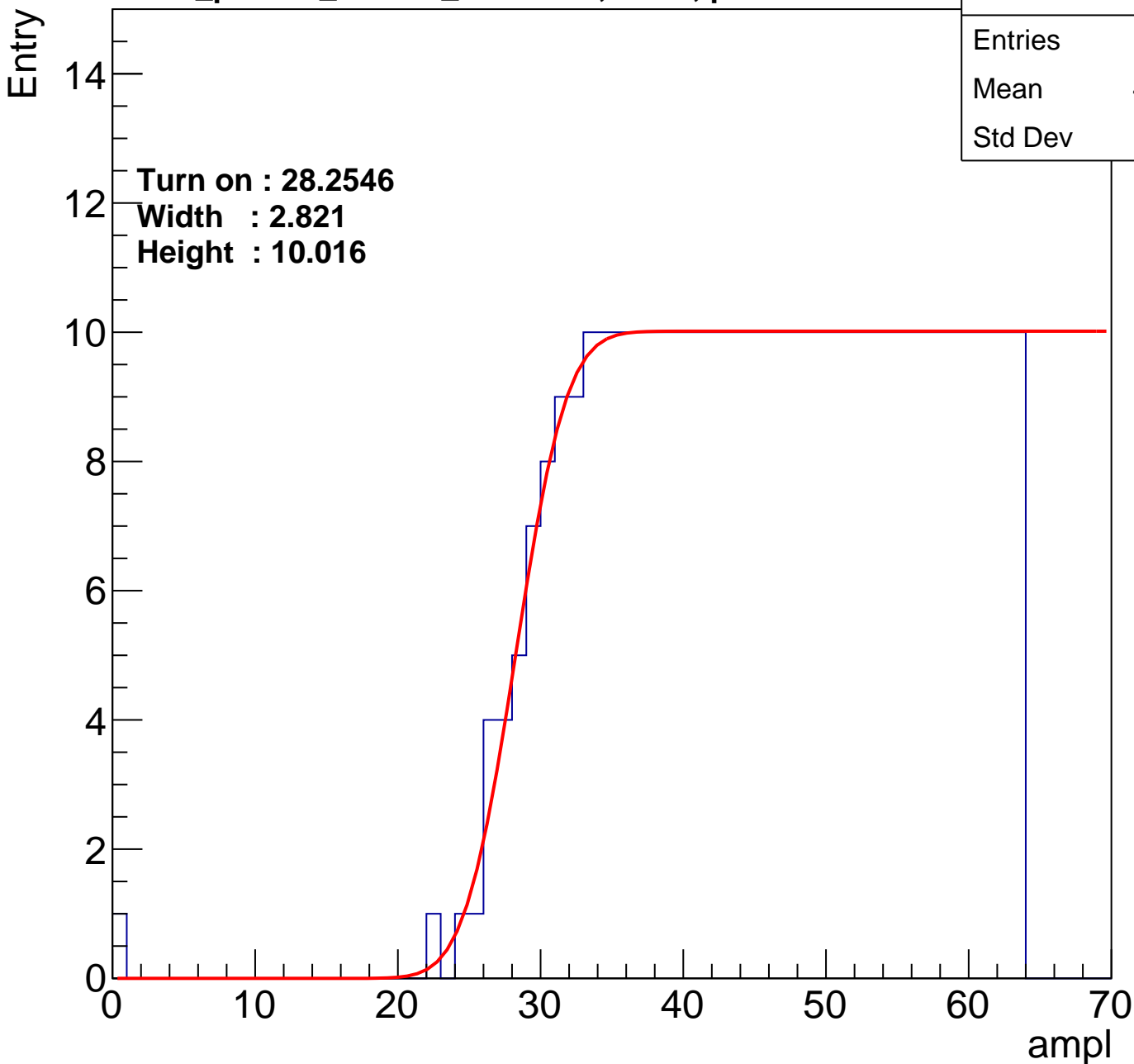


calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.31
Std Dev	10.8

Height : 10.016



B0L002S, U11-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.32
Std Dev	11.15

Turn on : 28.8582

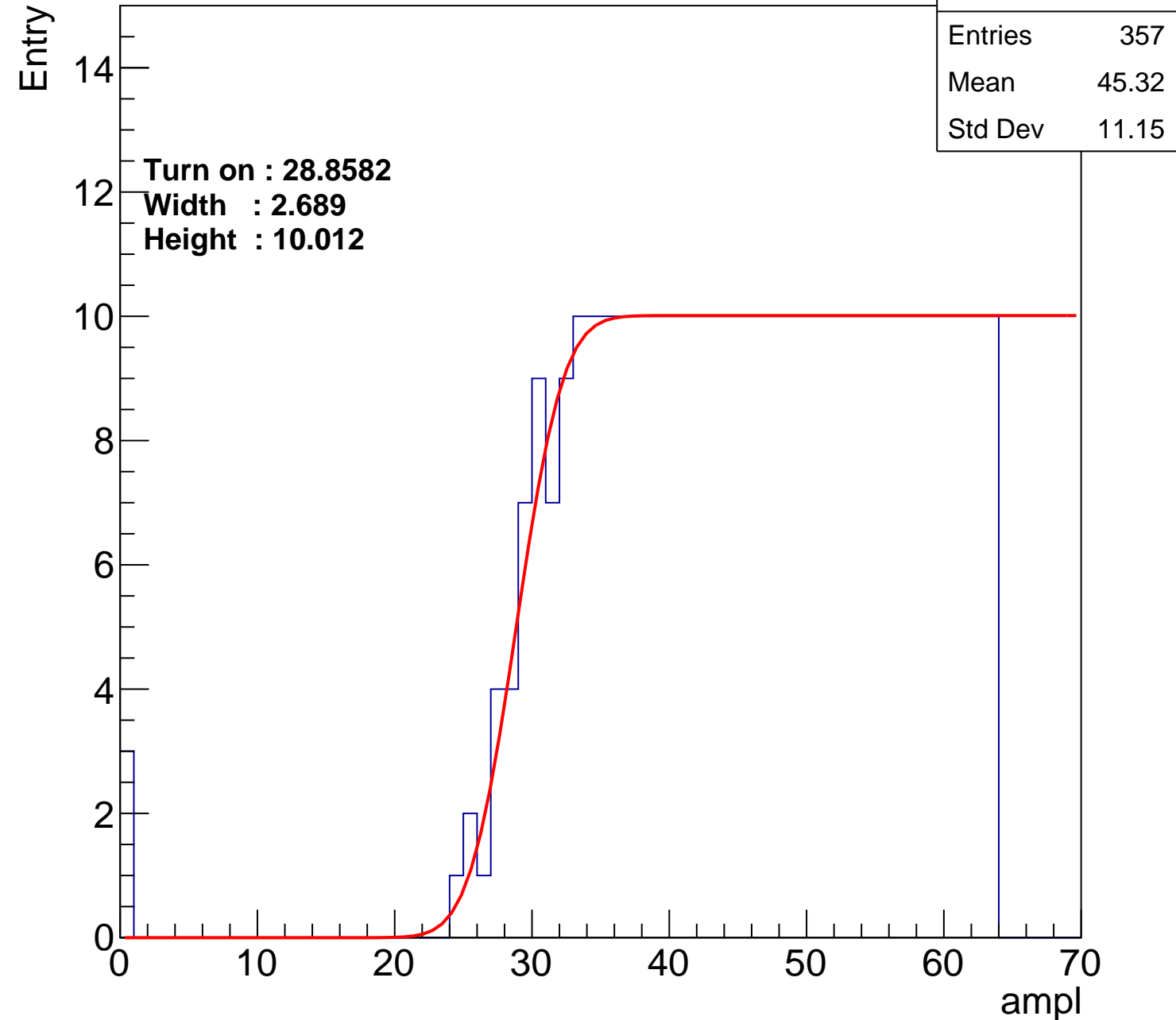
Width : 2.689

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.7
Std Dev	11.24

Turn on : 27.4787

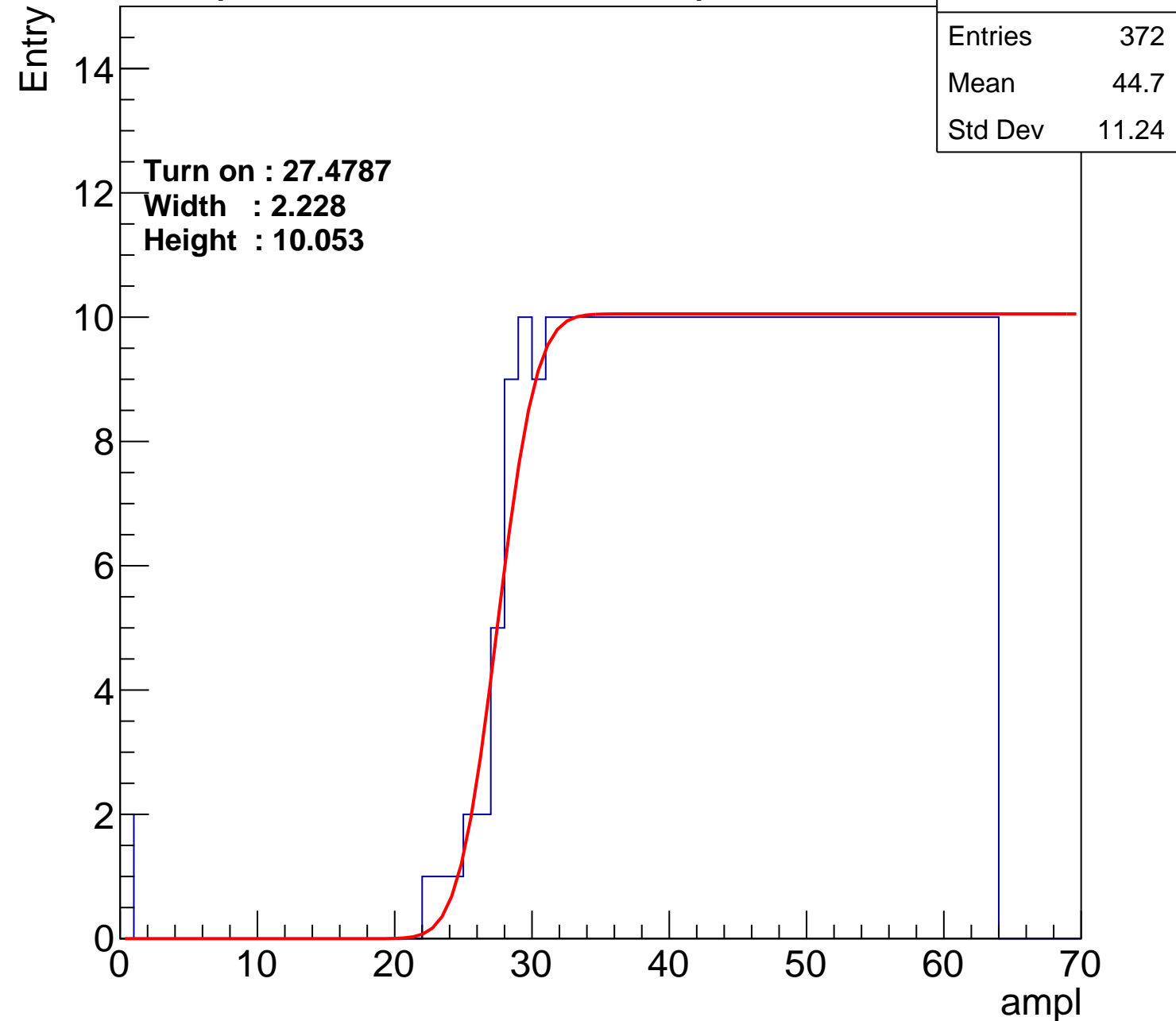
Width : 2.228

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch43

calib_packv5_042523_0143.root, FC#8, port C1

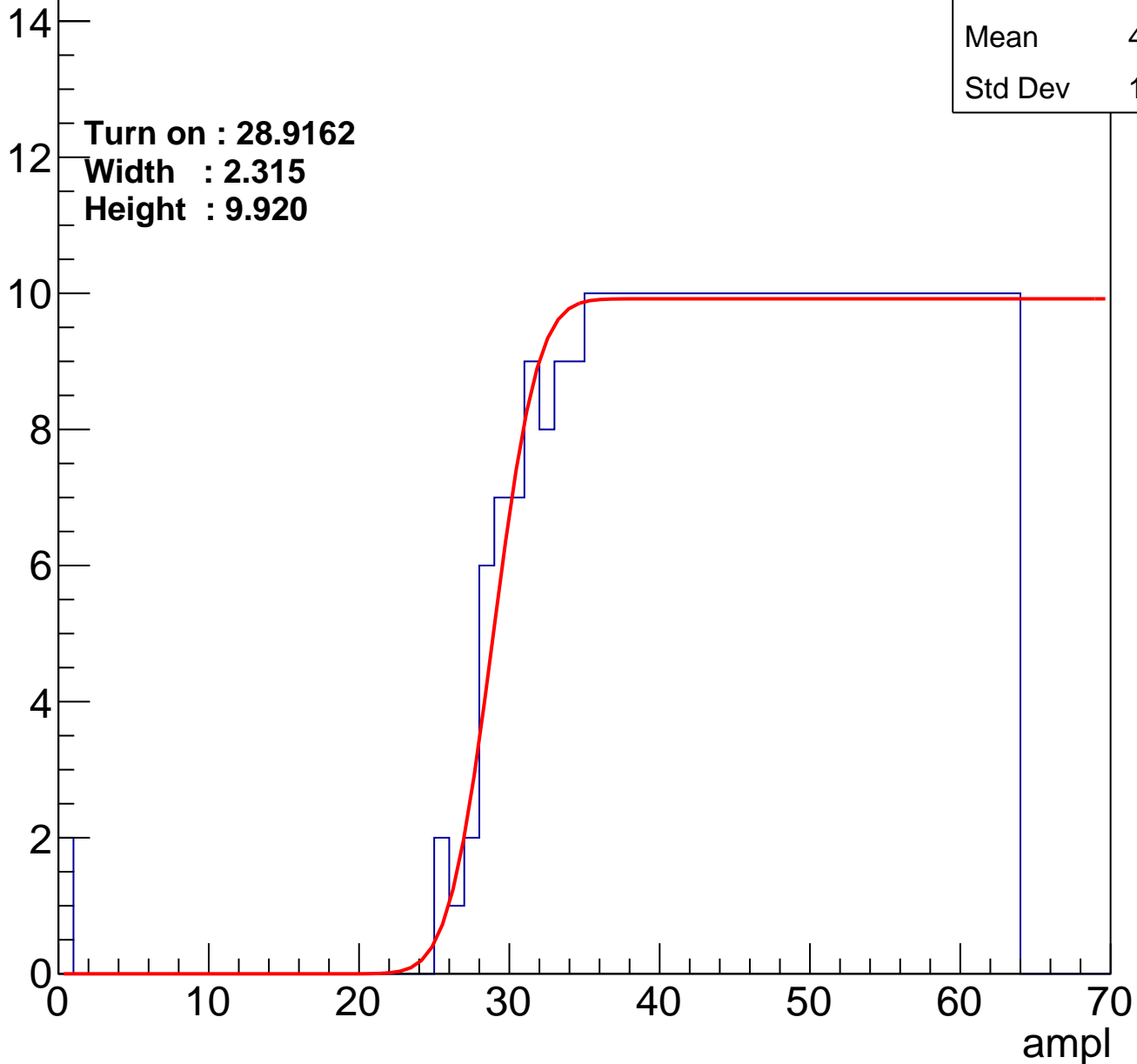
Entries	352
Mean	45.62
Std Dev	10.83

Turn on : 28.9162

Width : 2.315

Height : 9.920

Entry



B0L002S, U11-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.14
Std Dev	11.01

Turn on : 28.2920

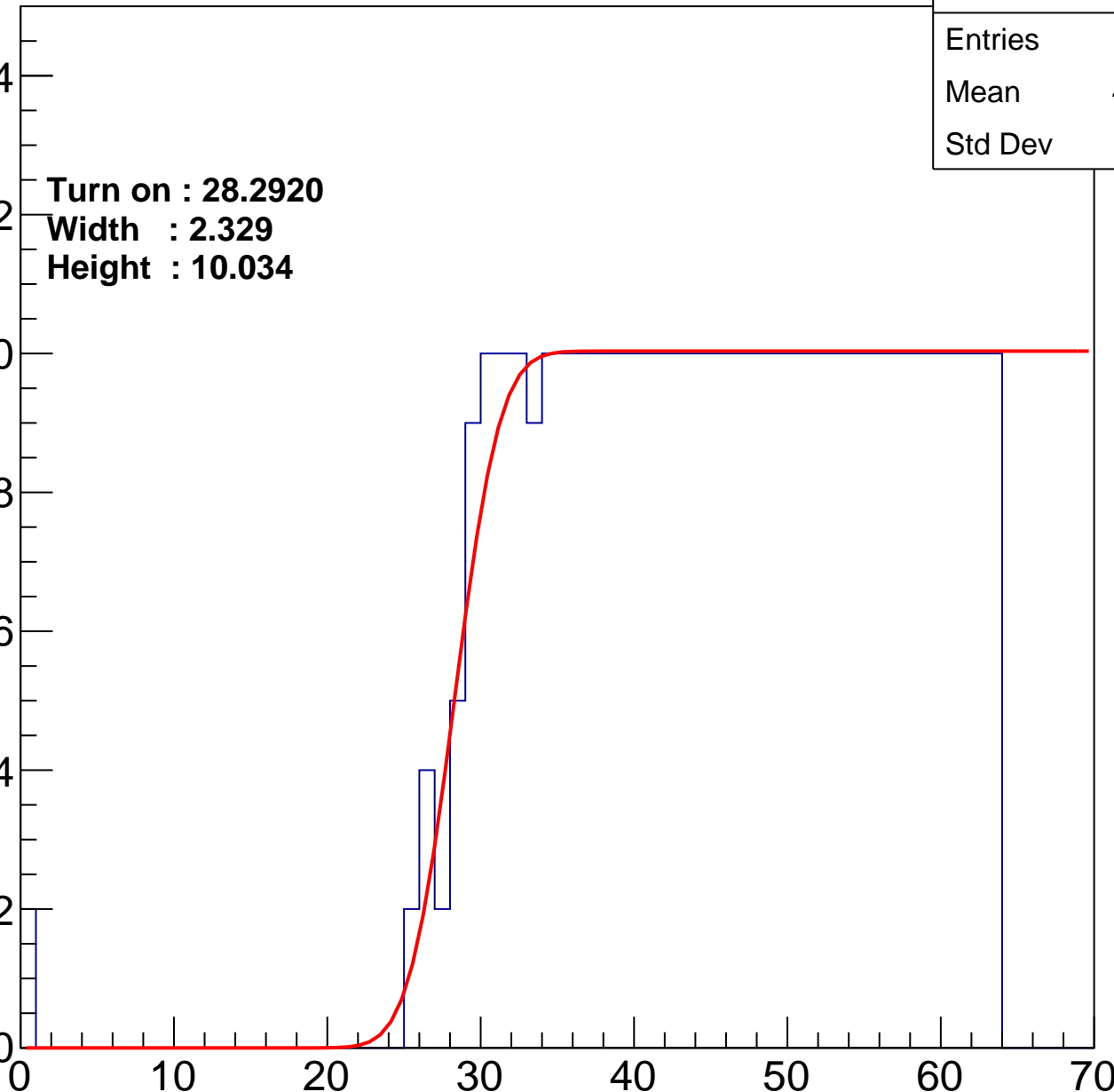
Width : 2.329

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.7837
Width : 2.543
Height : 10.027



B0L002S, U11-ch46

calib_packv5_042523_0143.root, FC#8, port C1

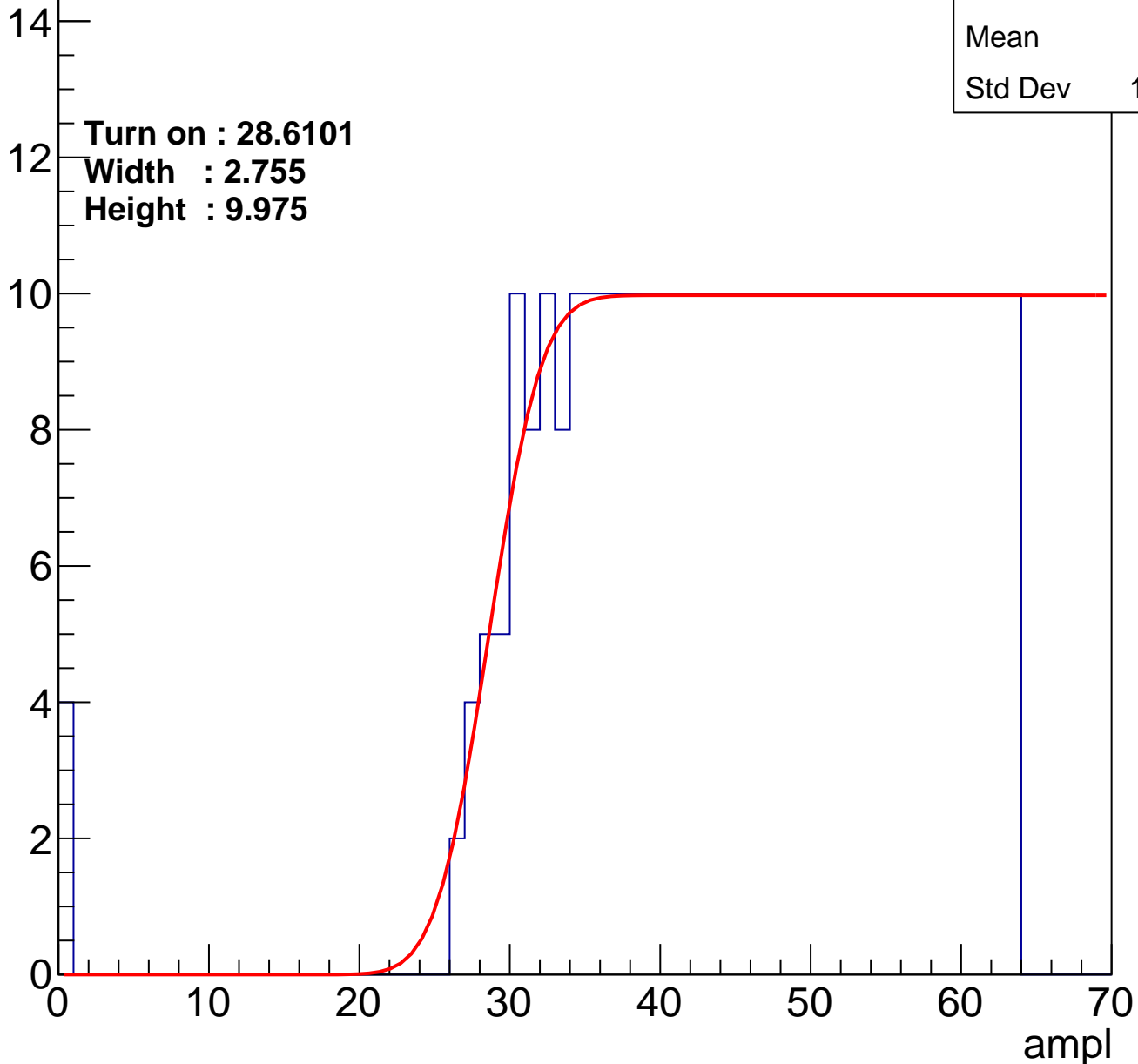
Entries	356
Mean	45.3
Std Dev	11.32

Turn on : 28.6101

Width : 2.755

Height : 9.975

Entry



B0L002S, U11-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.8606

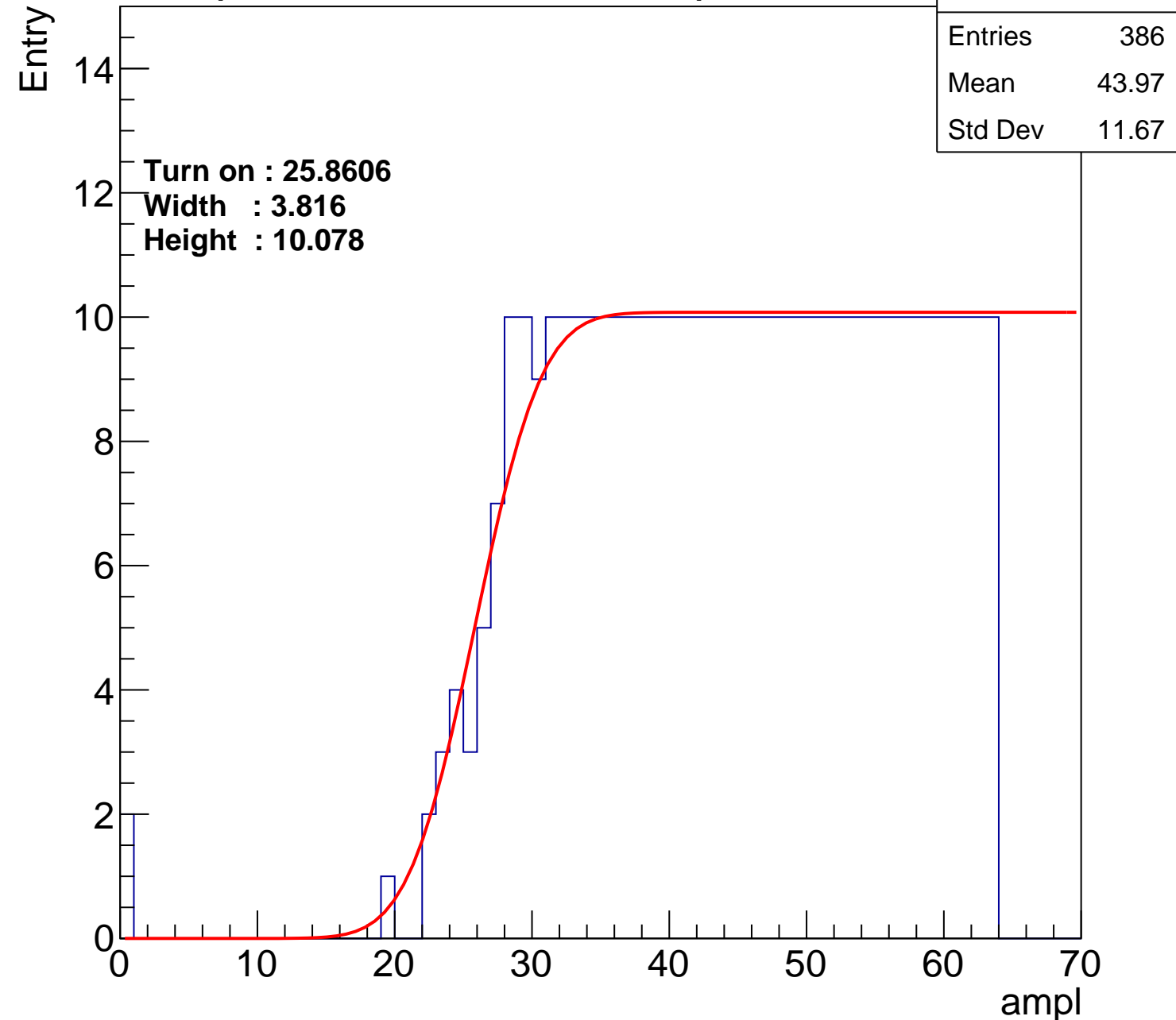
Width : 3.816

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.1
Std Dev	10.94

Turn on : 28.0193

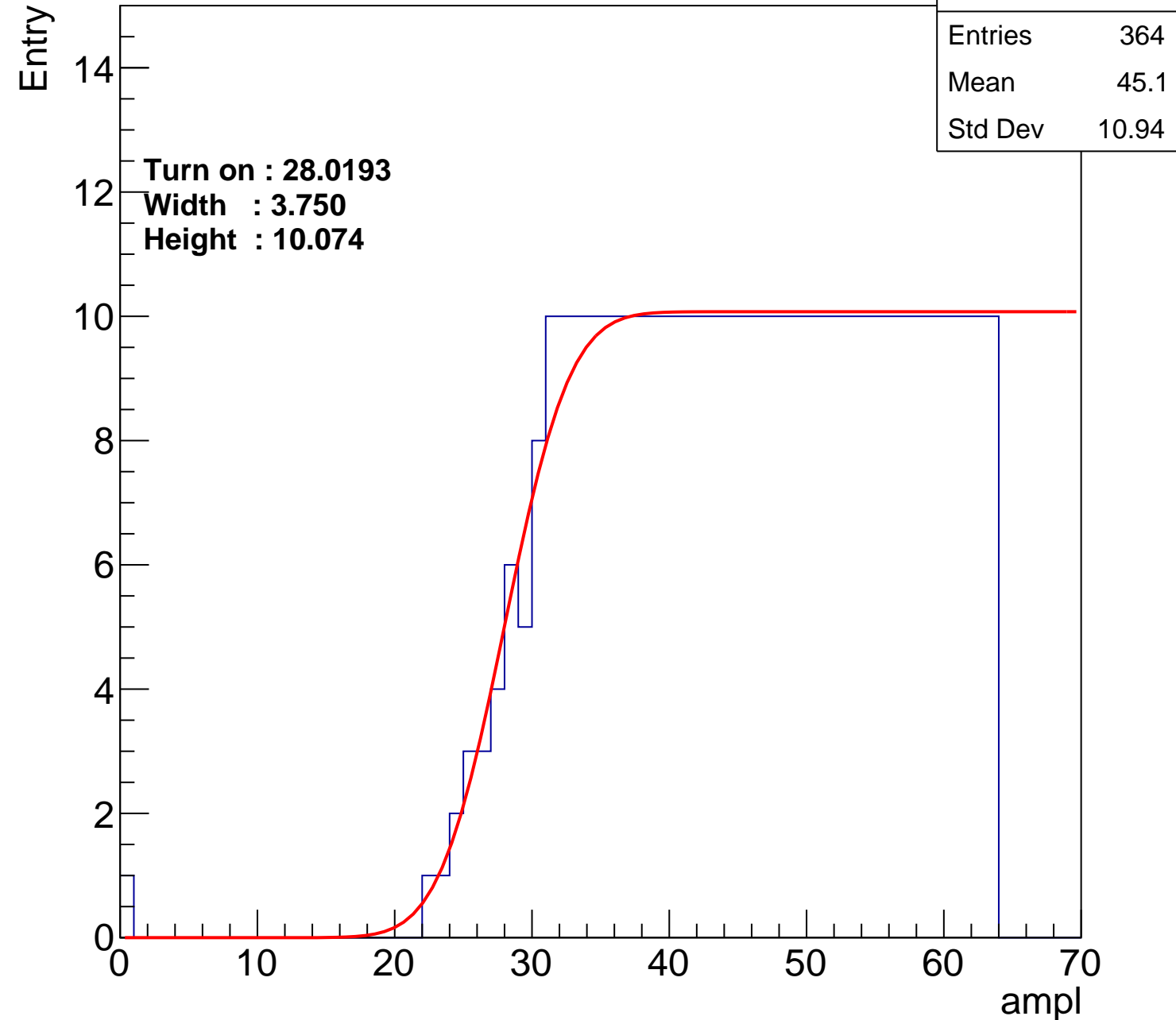
Width : 3.750

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.35
Std Dev	10.94

Turn on : 28.1770

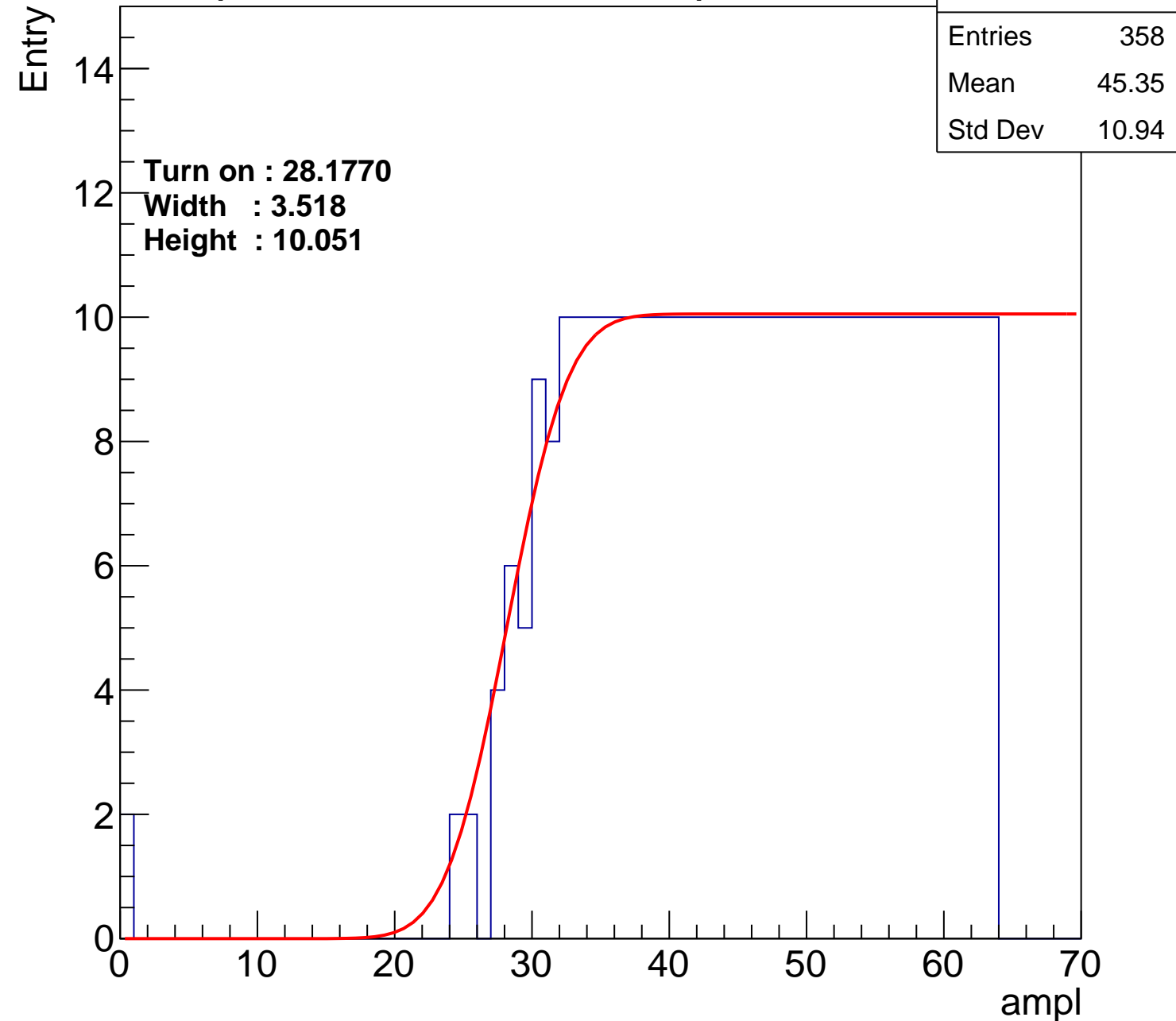
Width : 3.518

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.99
Std Dev	10.96

Turn on : 26.9432

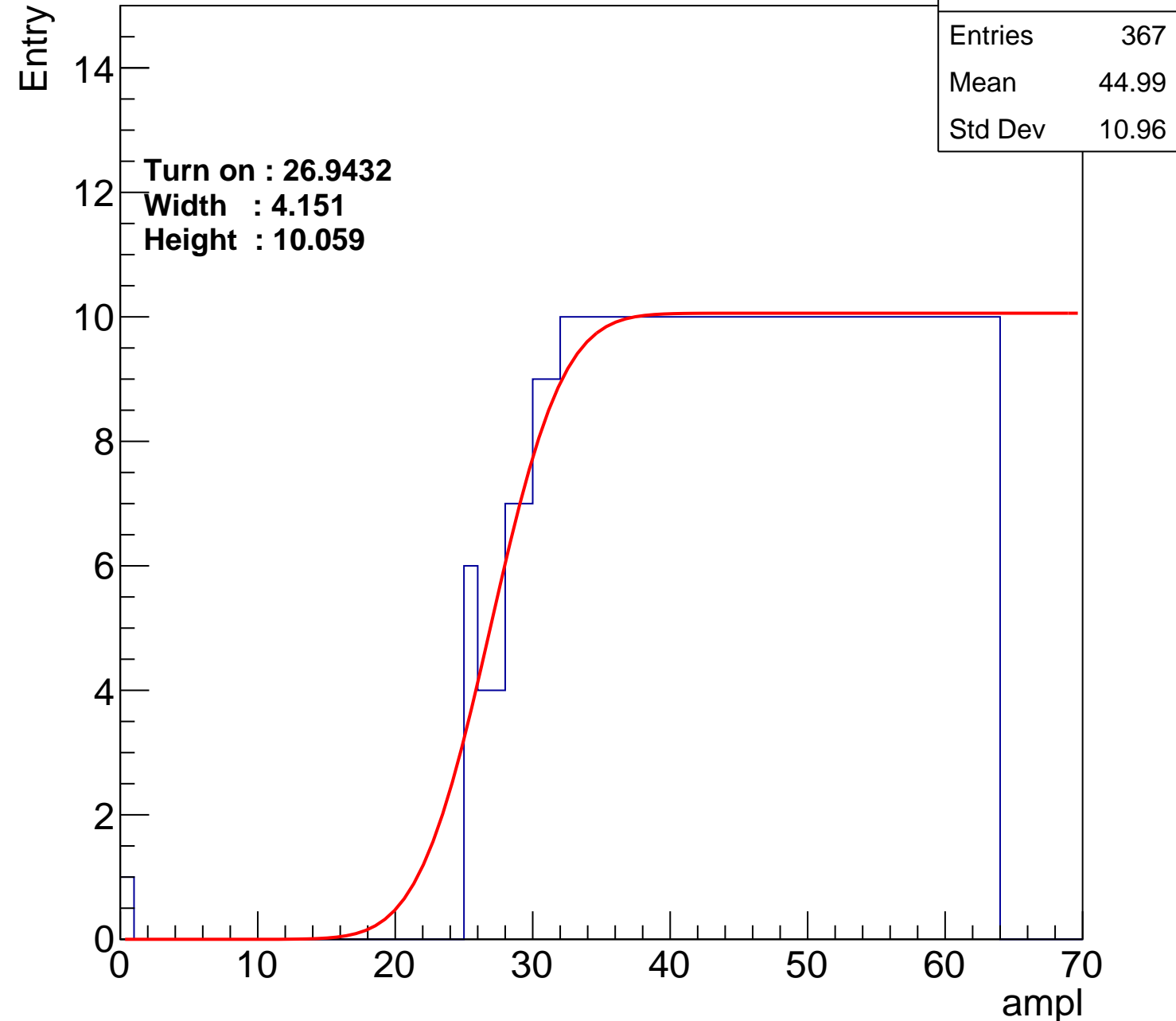
Width : 4.151

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch51

calib_packv5_042523_0143.root, FC#8, port C1

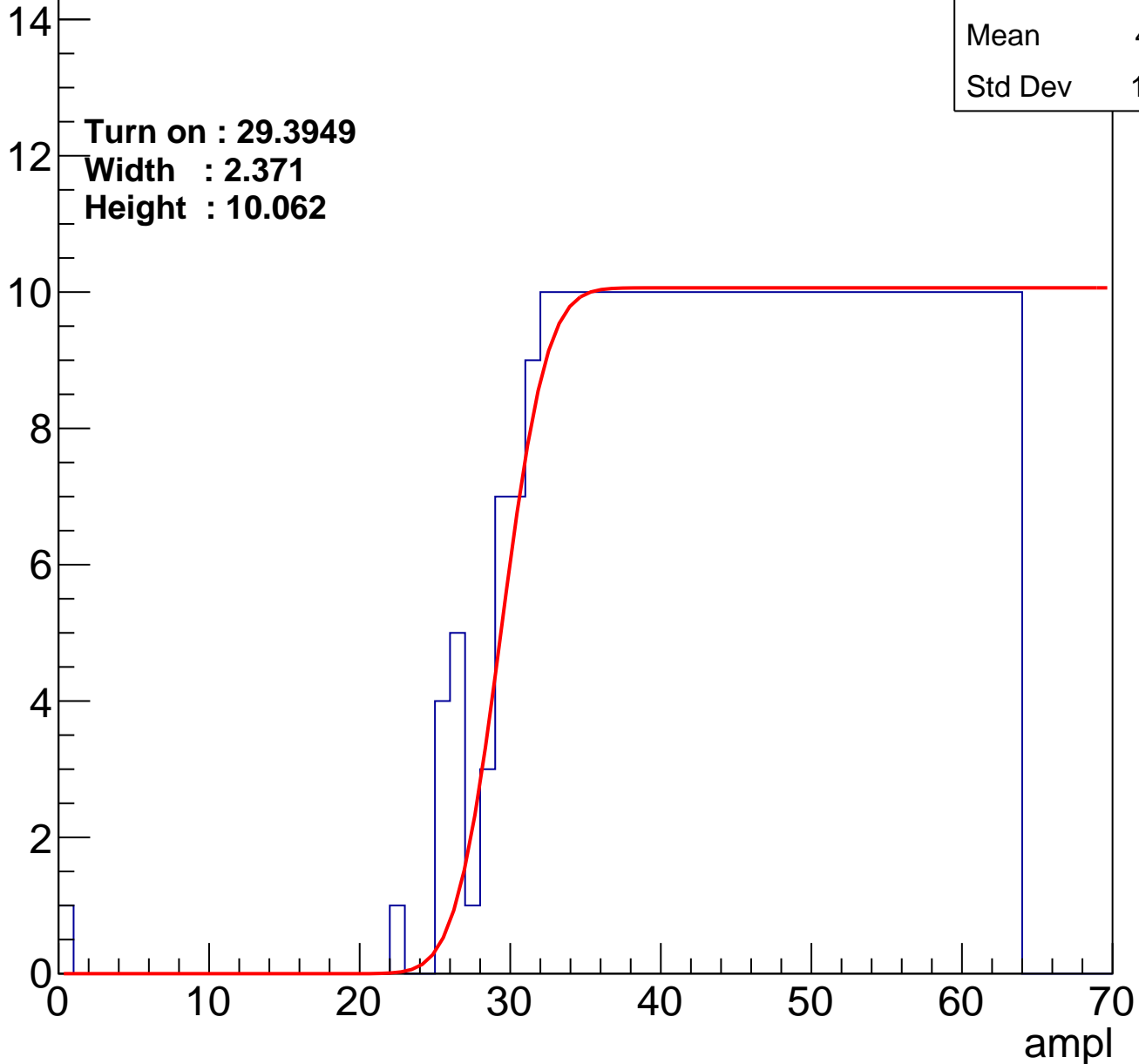
Entries	358
Mean	45.41
Std Dev	10.77

Turn on : 29.3949

Width : 2.371

Height : 10.062

Entry



B0L002S, U11-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 26.5599

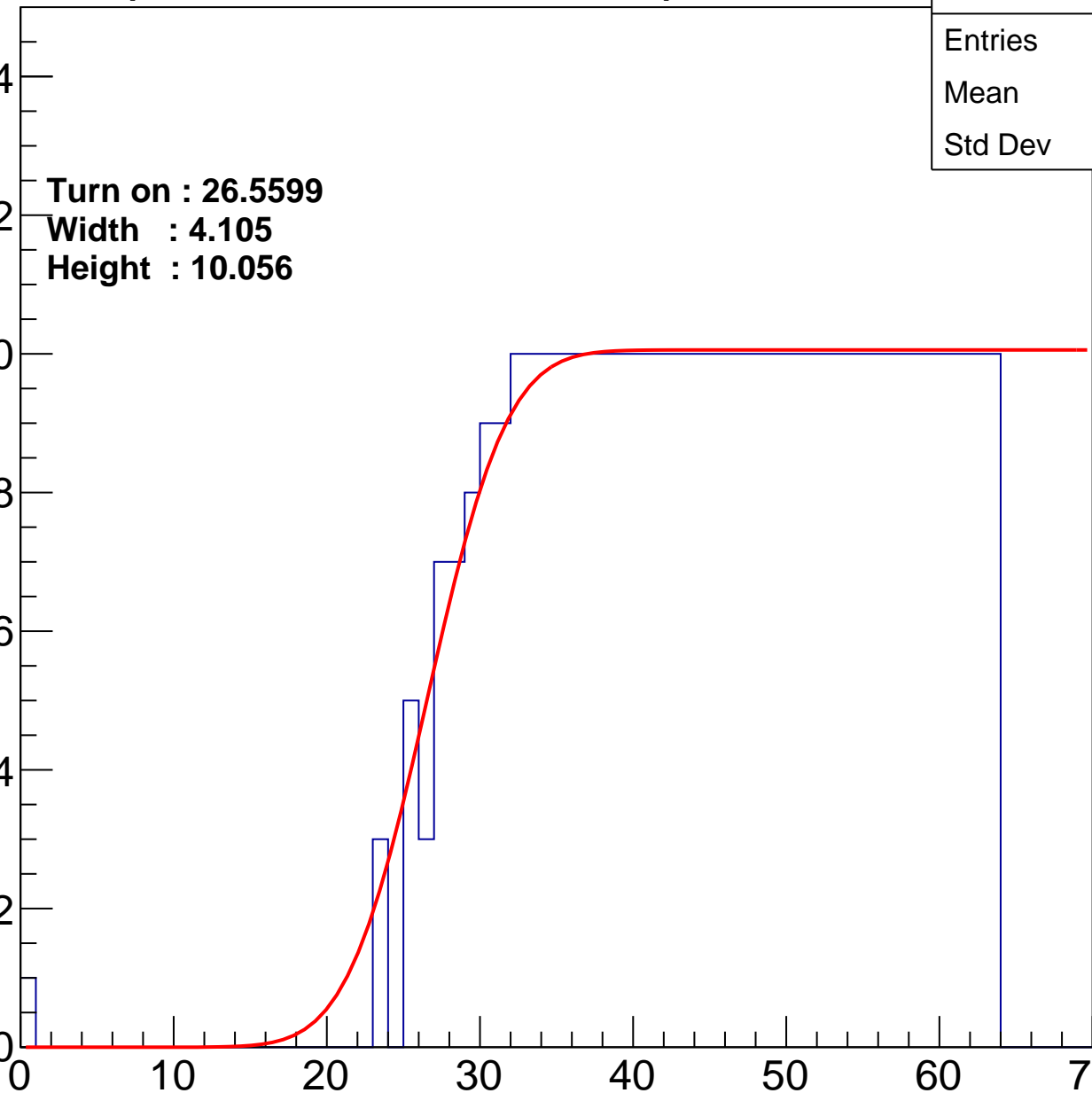
Width : 4.105

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.88
Std Dev	11.5

Turn on : 28.3377

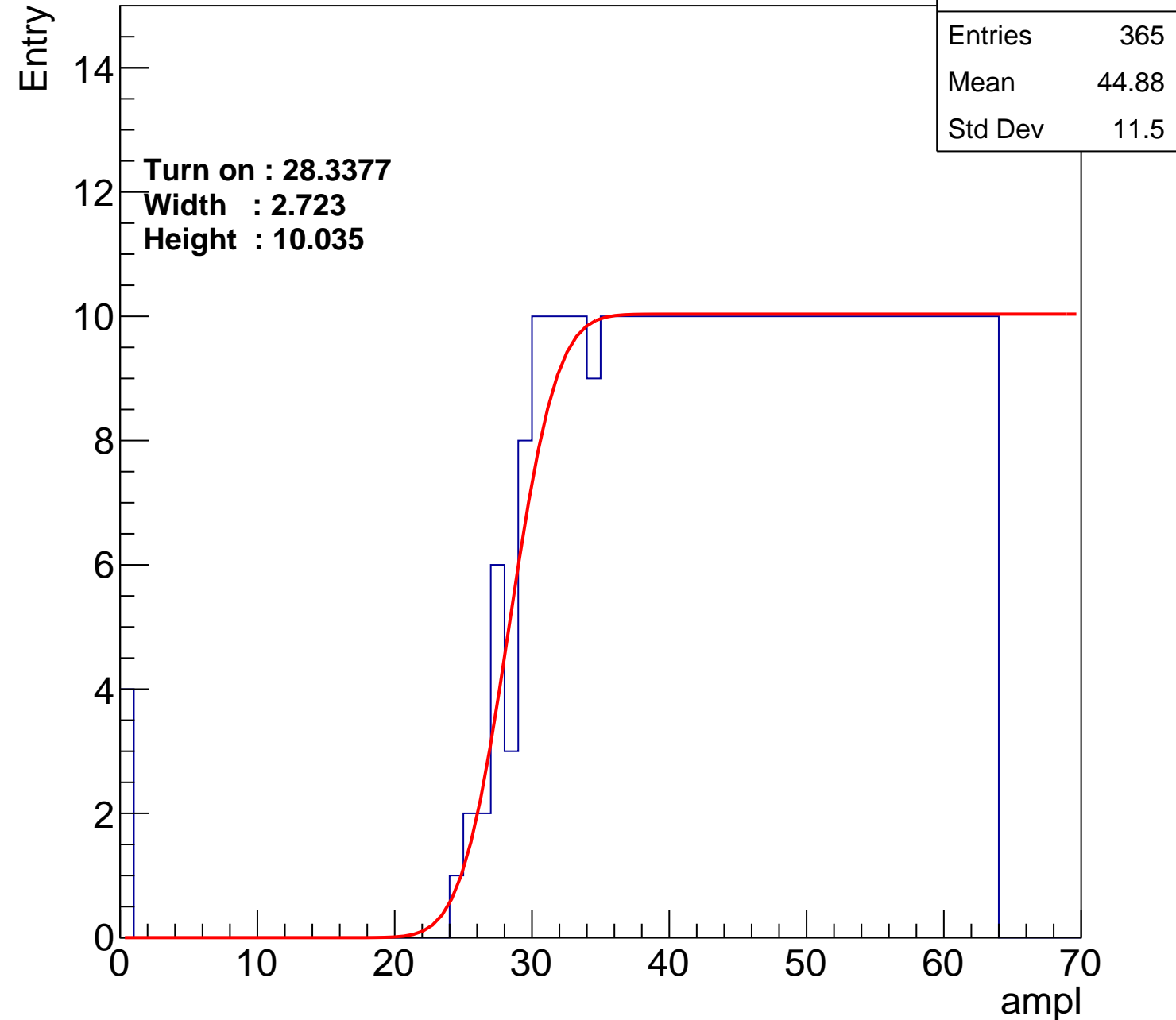
Width : 2.723

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.19
Std Dev	11.39

Turn on : 28.6519

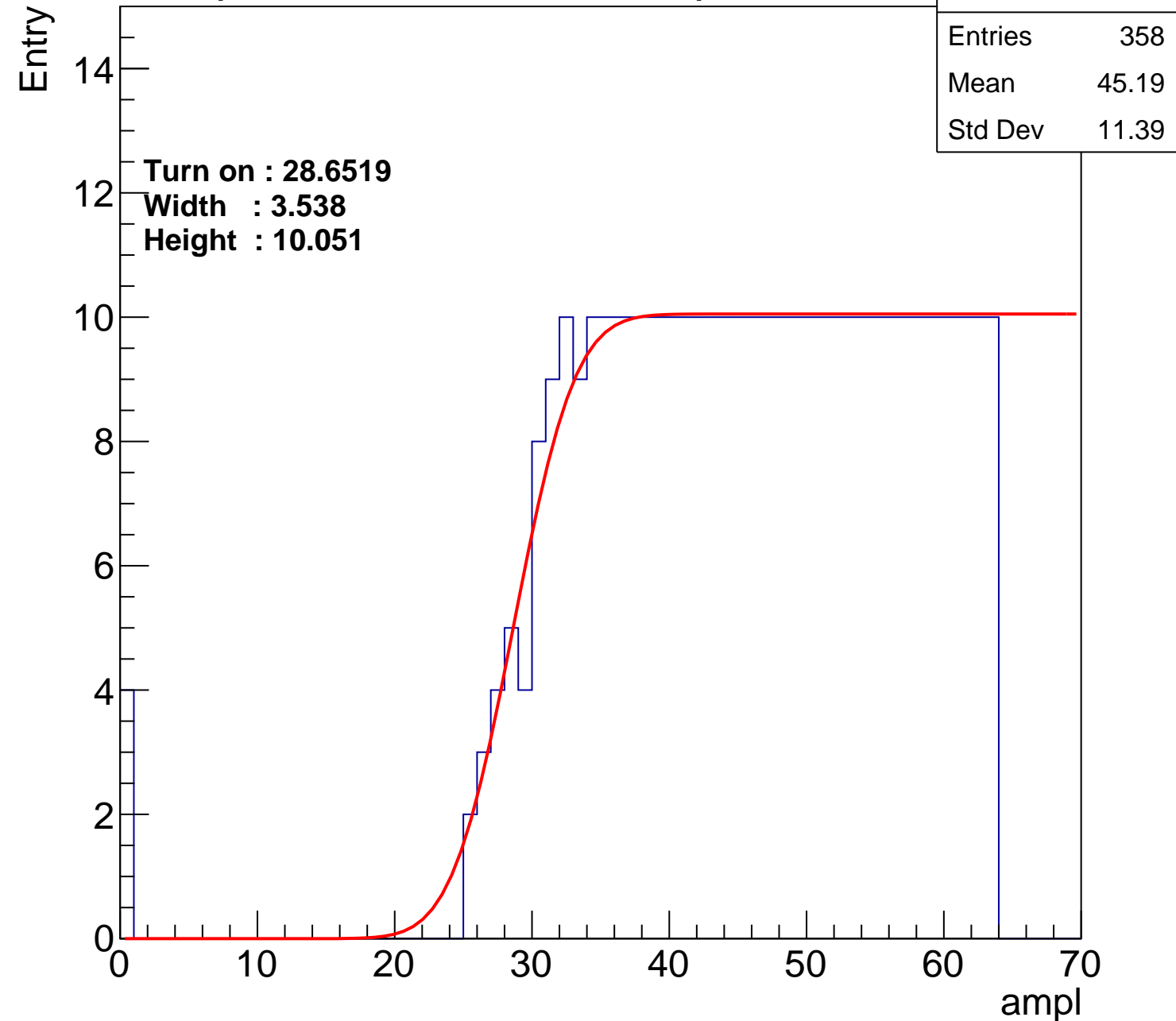
Width : 3.538

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.24
Std Dev	10.87

Turn on : 28.3175

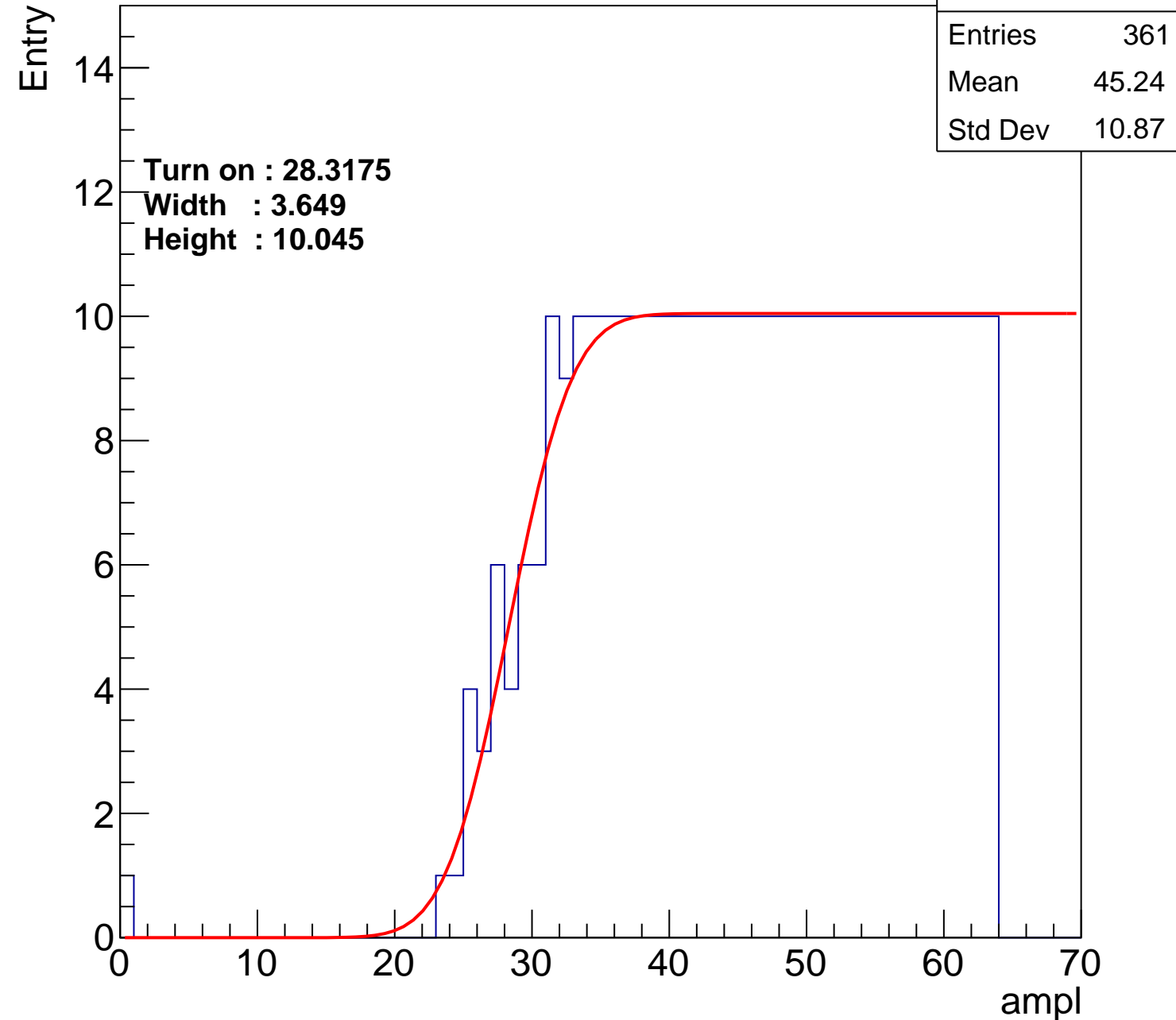
Width : 3.649

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	391
Mean	43.62
Std Dev	12.09

Turn on : 25.4786

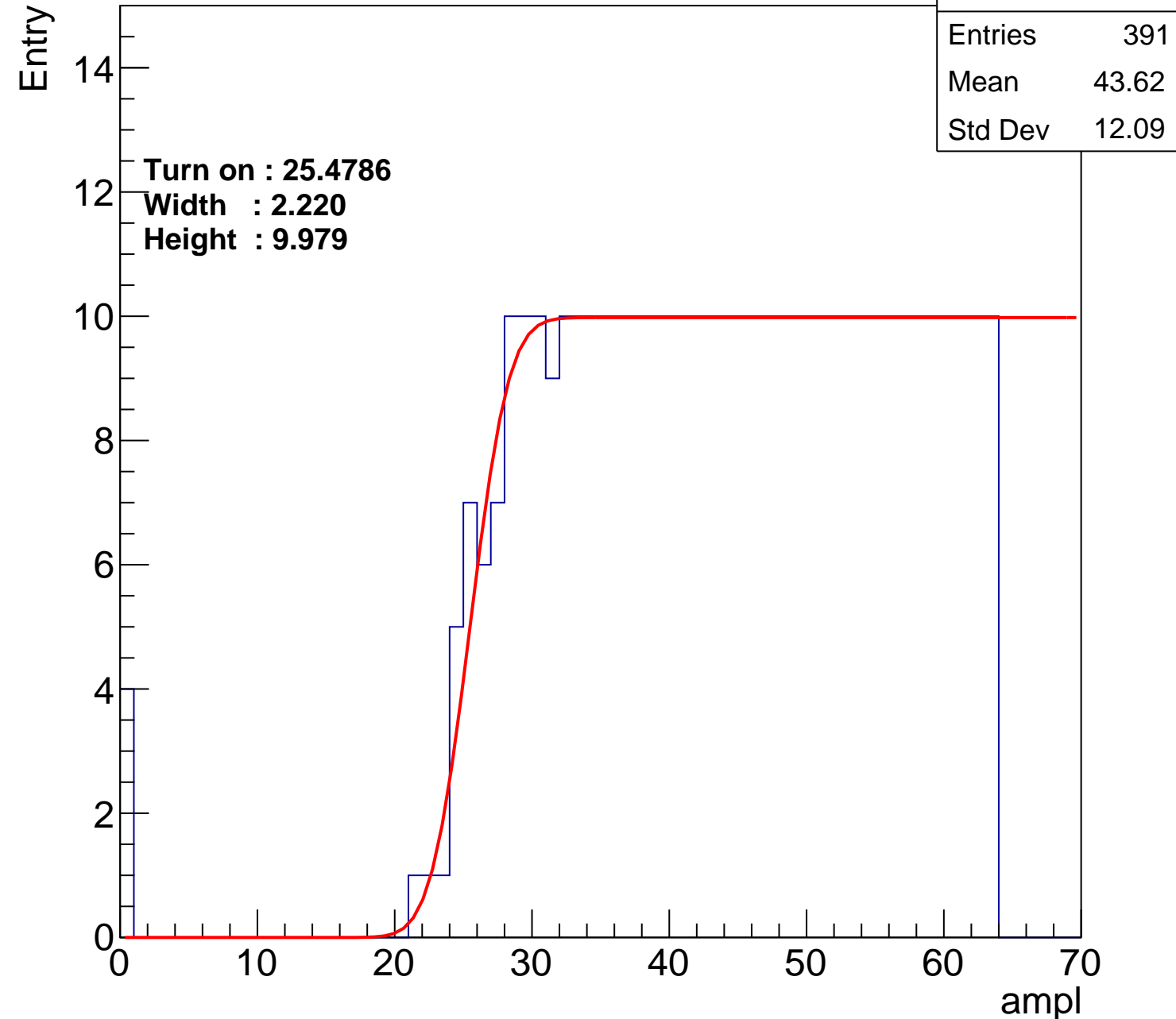
Width : 2.220

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.54
Std Dev	10.1

Mean	46.54
------	-------

Std Dev	10.1
---------	------

Width : 2.370

Height : 10.059



B0L002S, U11-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.5
Std Dev	11.53

Turn on : 26.7479

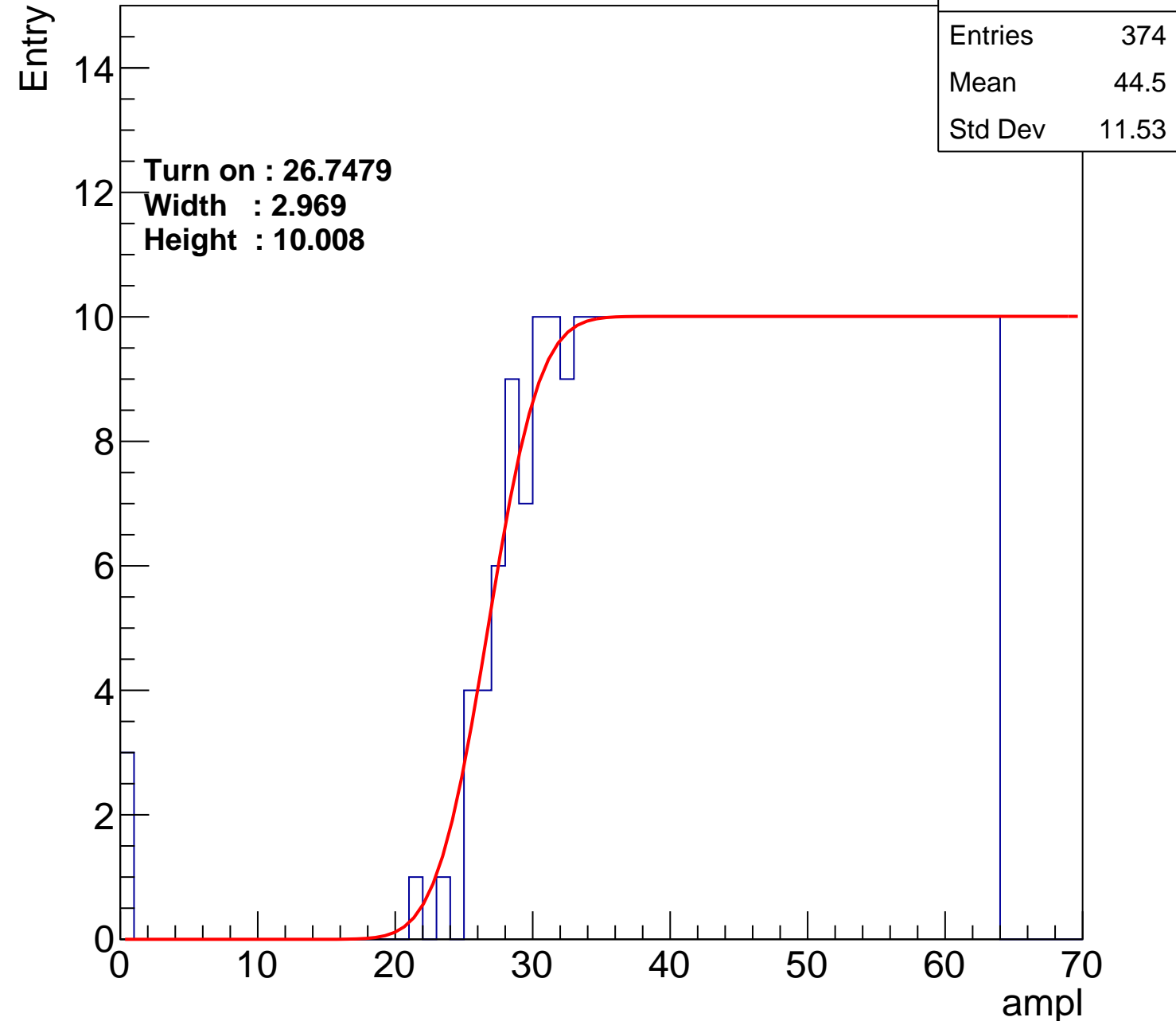
Width : 2.969

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.64
Std Dev	10.61

Turn on : 28.8656

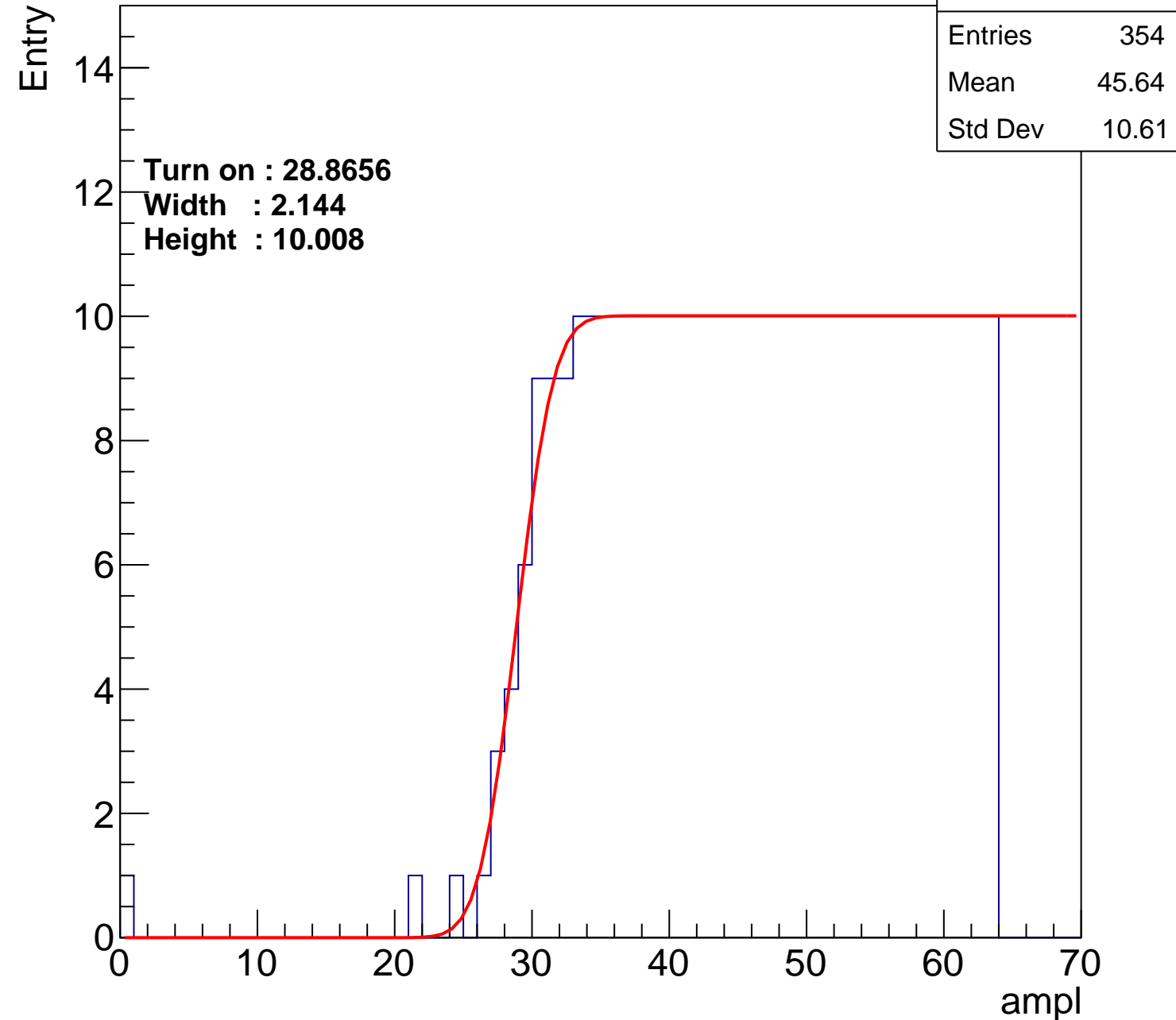
Width : 2.144

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch60

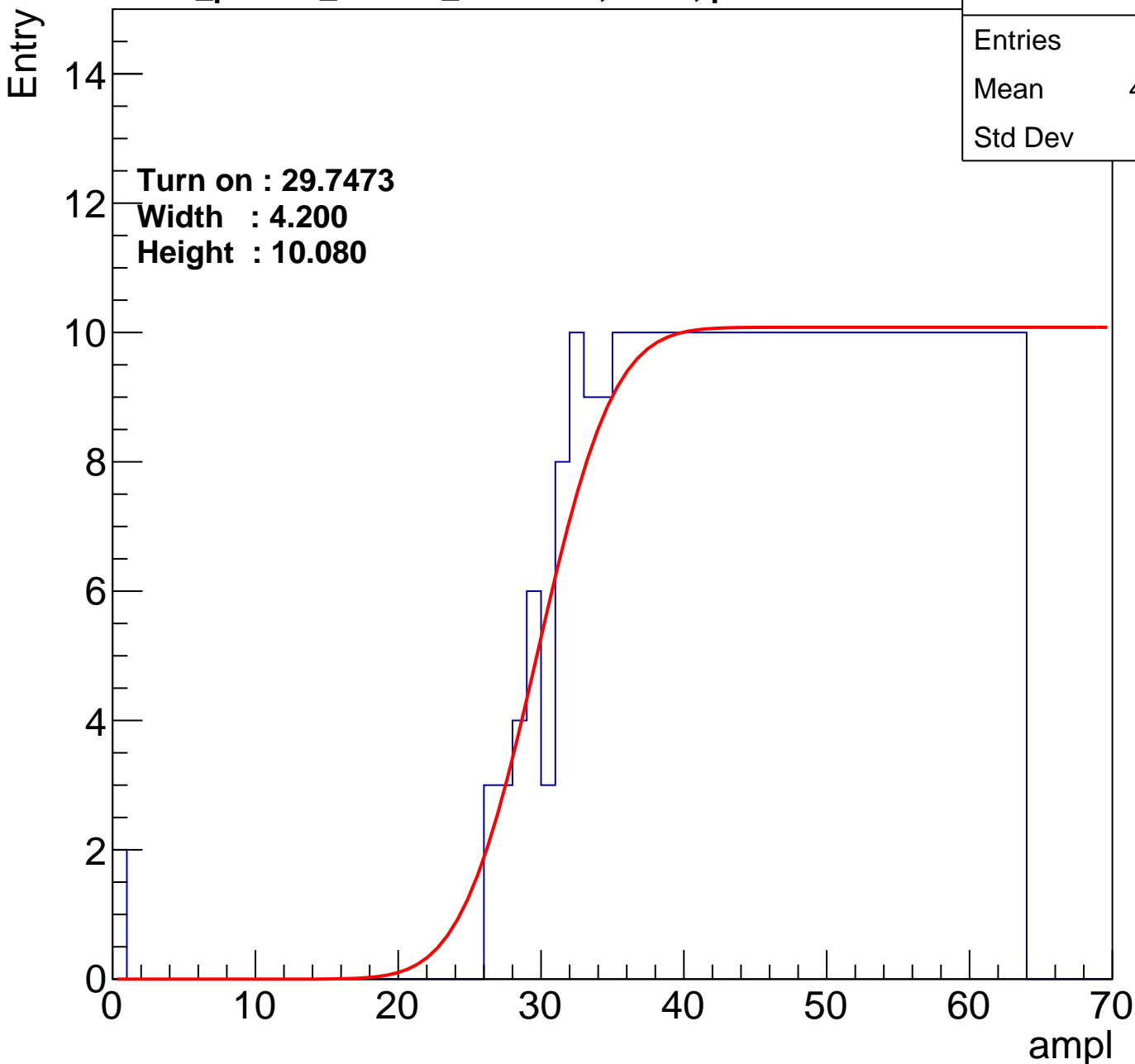
calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.87
Std Dev	10.71

Turn on : 29.7473

Width : 4.200

Height : 10.080



B0L002S, U11-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.41
Std Dev	11.11

Turn on : 29.2336

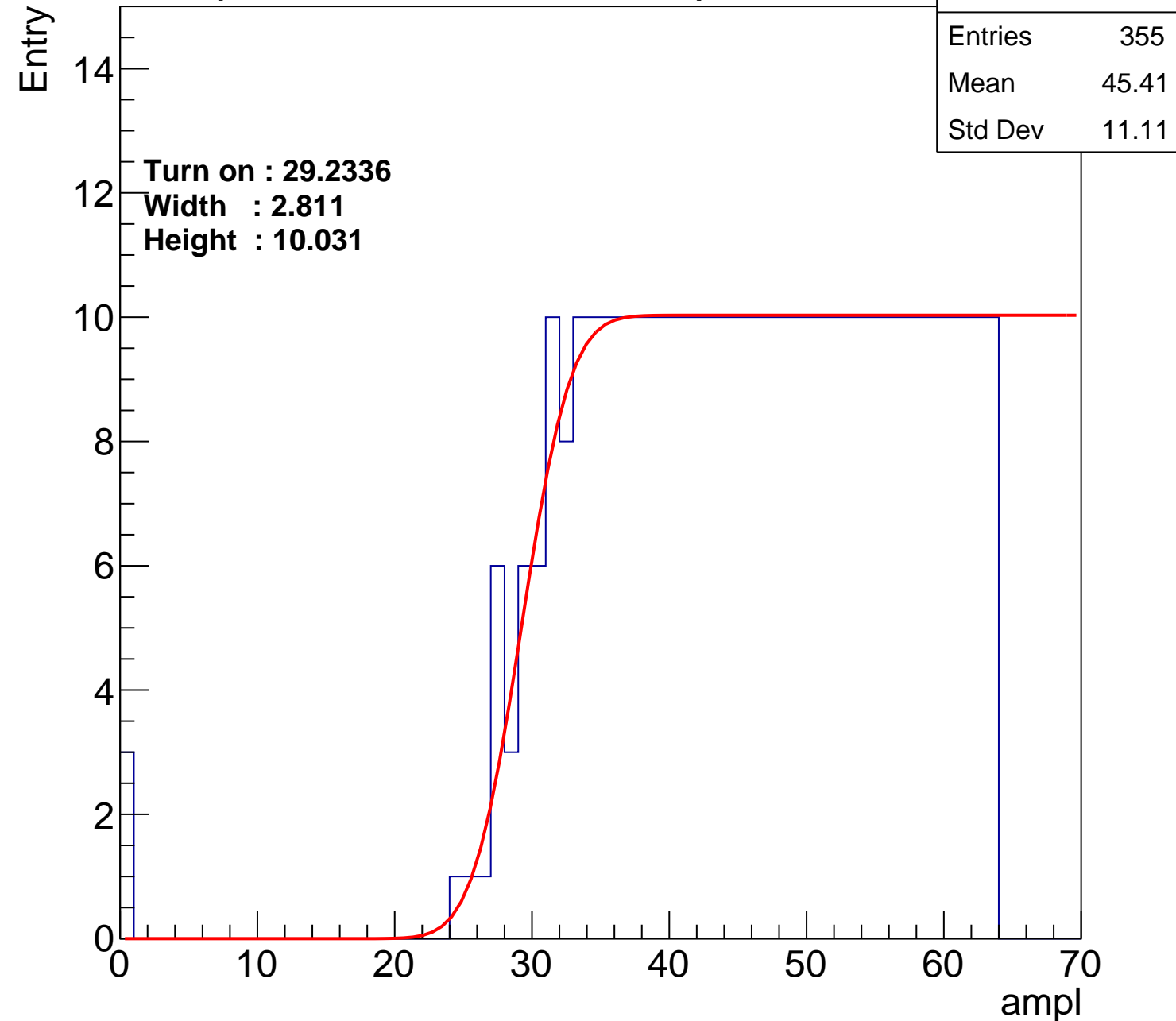
Width : 2.811

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.01
Std Dev	10.98

Turn on : 28.1617

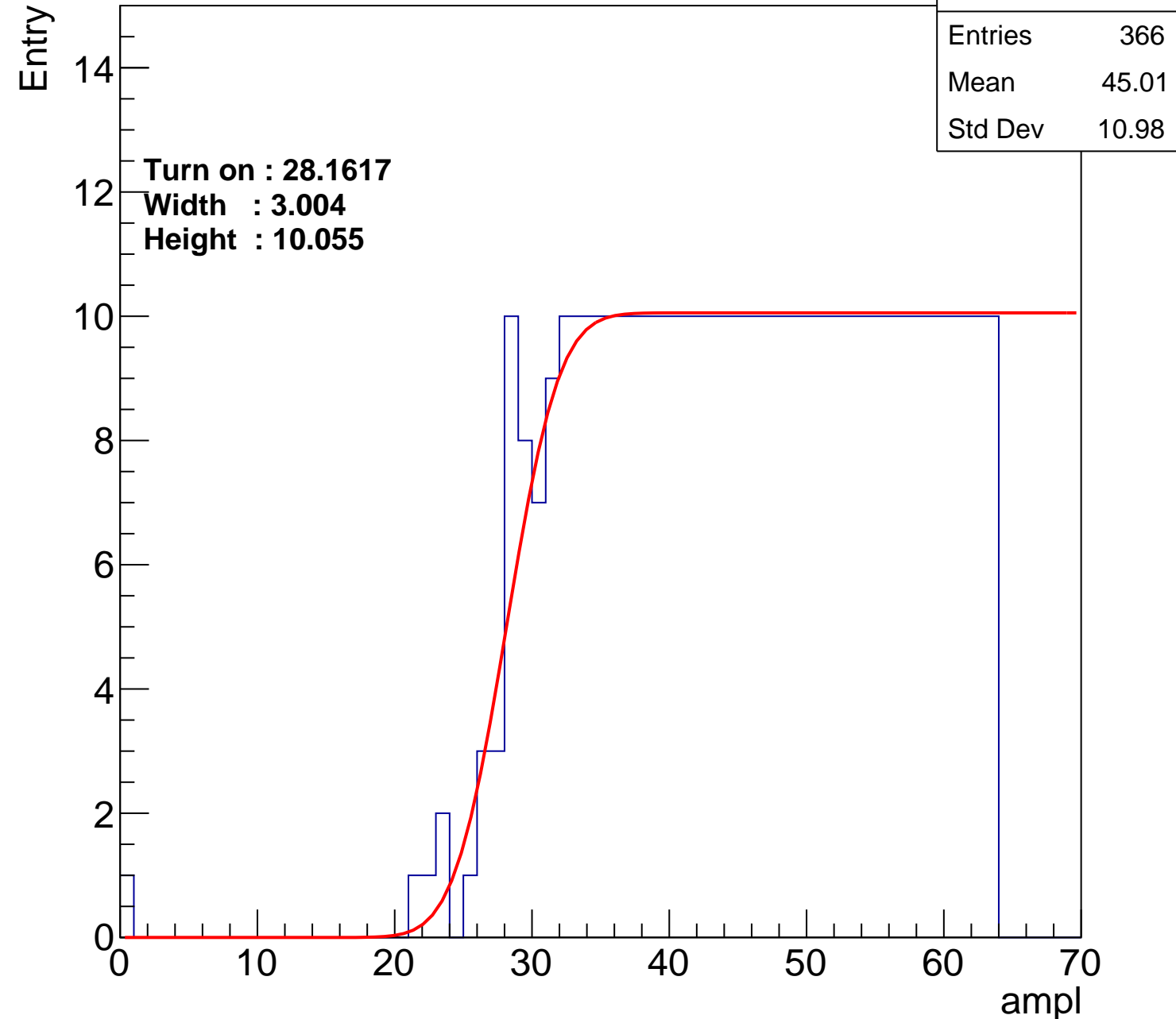
Width : 3.004

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	340
Mean	46.04
Std Dev	11.04

Turn on : 30.4717

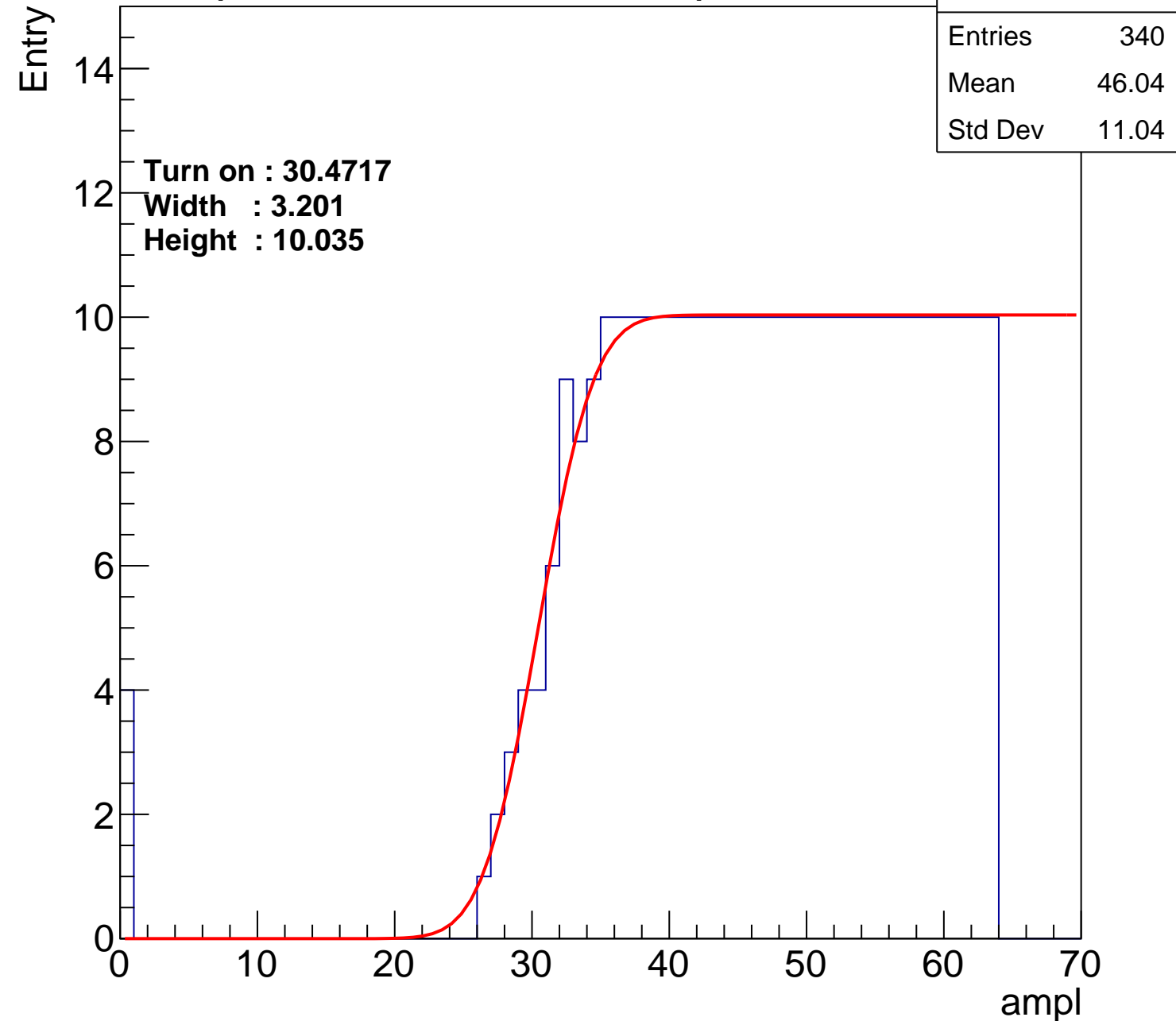
Width : 3.201

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.93
Std Dev	11

Turn on : 26.8449

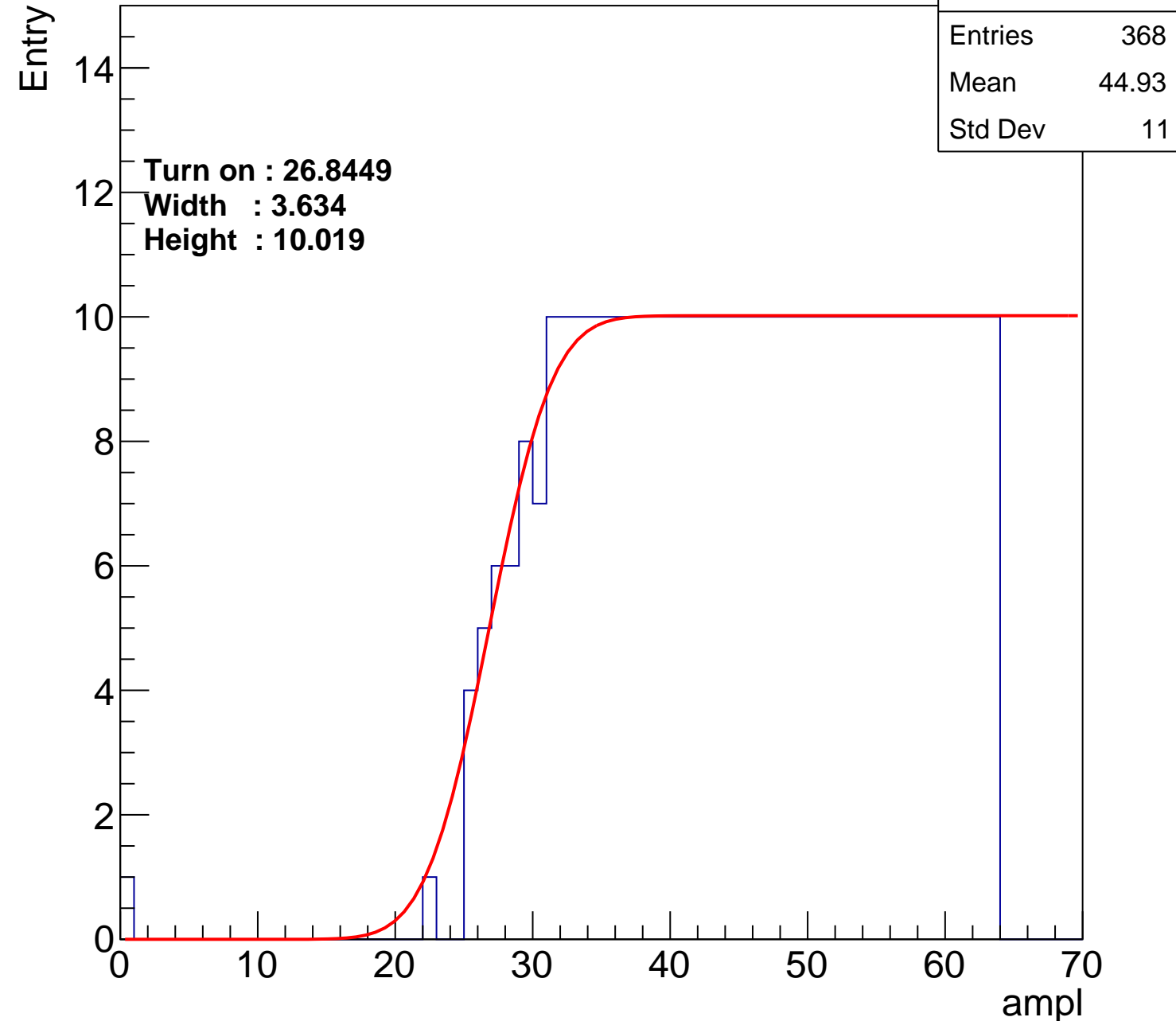
Width : 3.634

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch65

calib_packv5_042523_0143.root, FC#8, port C1

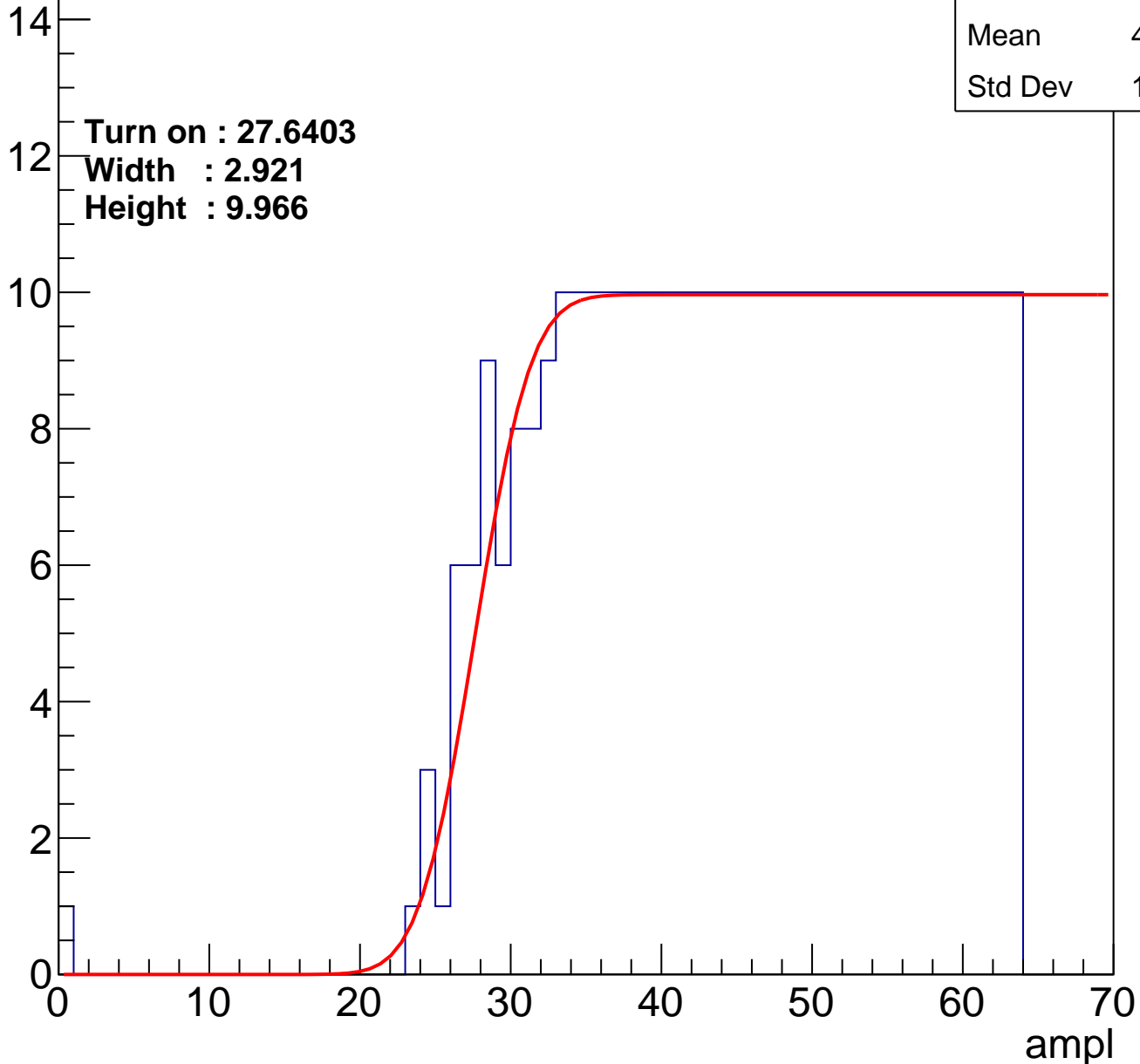
Entries	368
Mean	44.89
Std Dev	11.05

Turn on : 27.6403

Width : 2.921

Height : 9.966

Entry



B0L002S, U11-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 29.0441

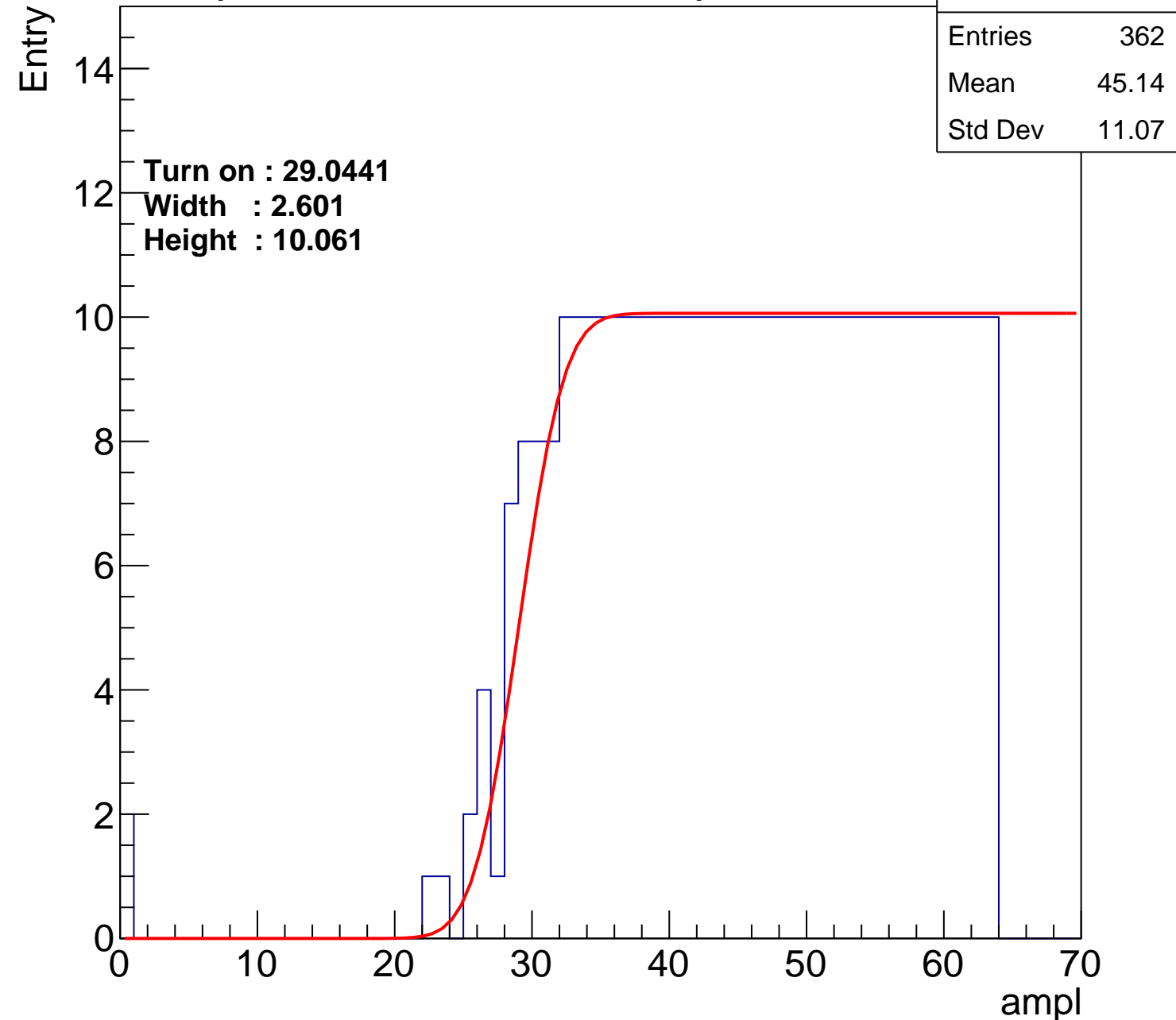
Width : 2.601

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.2
Std Dev	10.83

Turn on : 28.2038

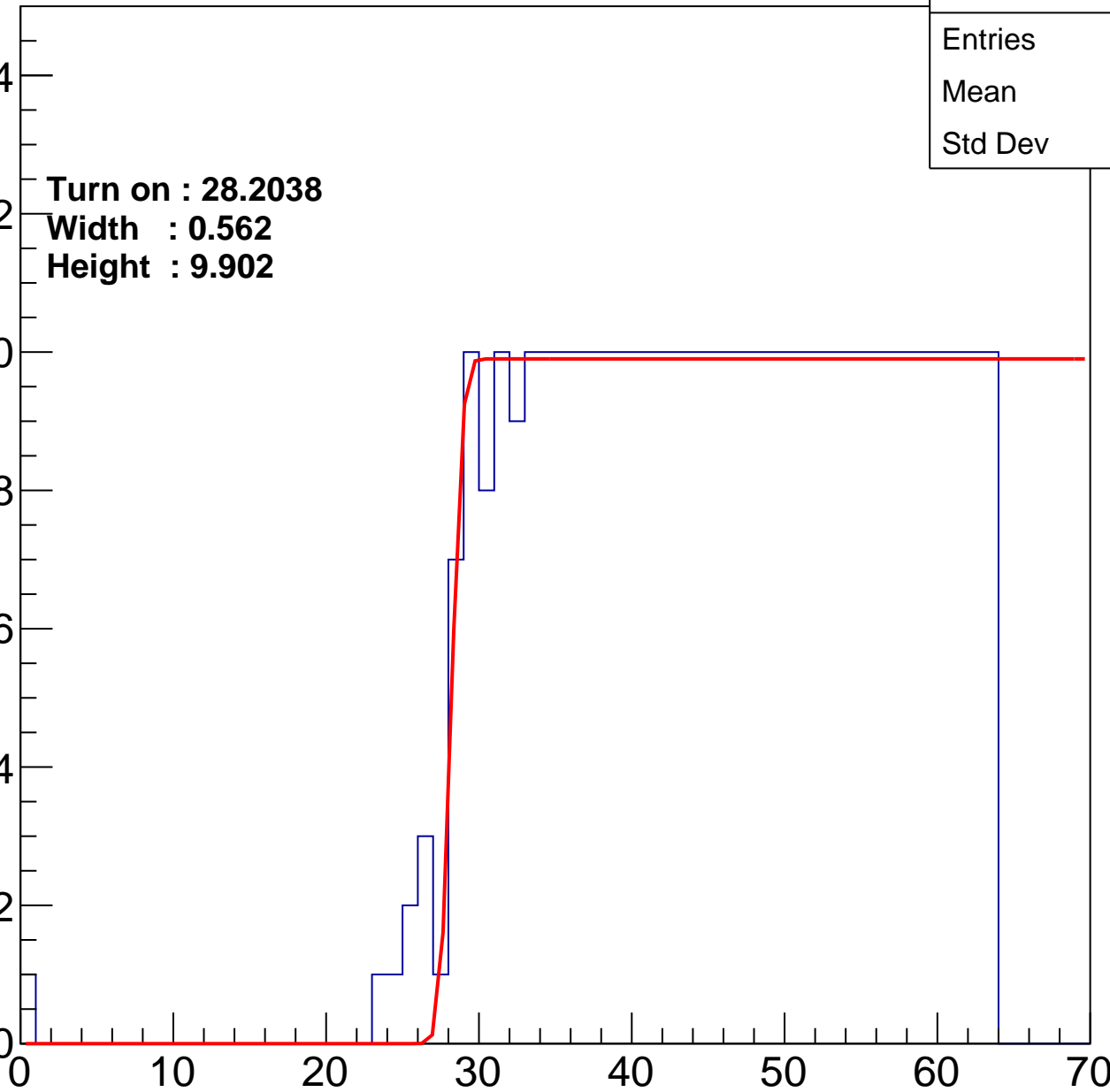
Width : 0.562

Height : 9.902

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.4
Std Dev	11.47

Turn on : 26.9833

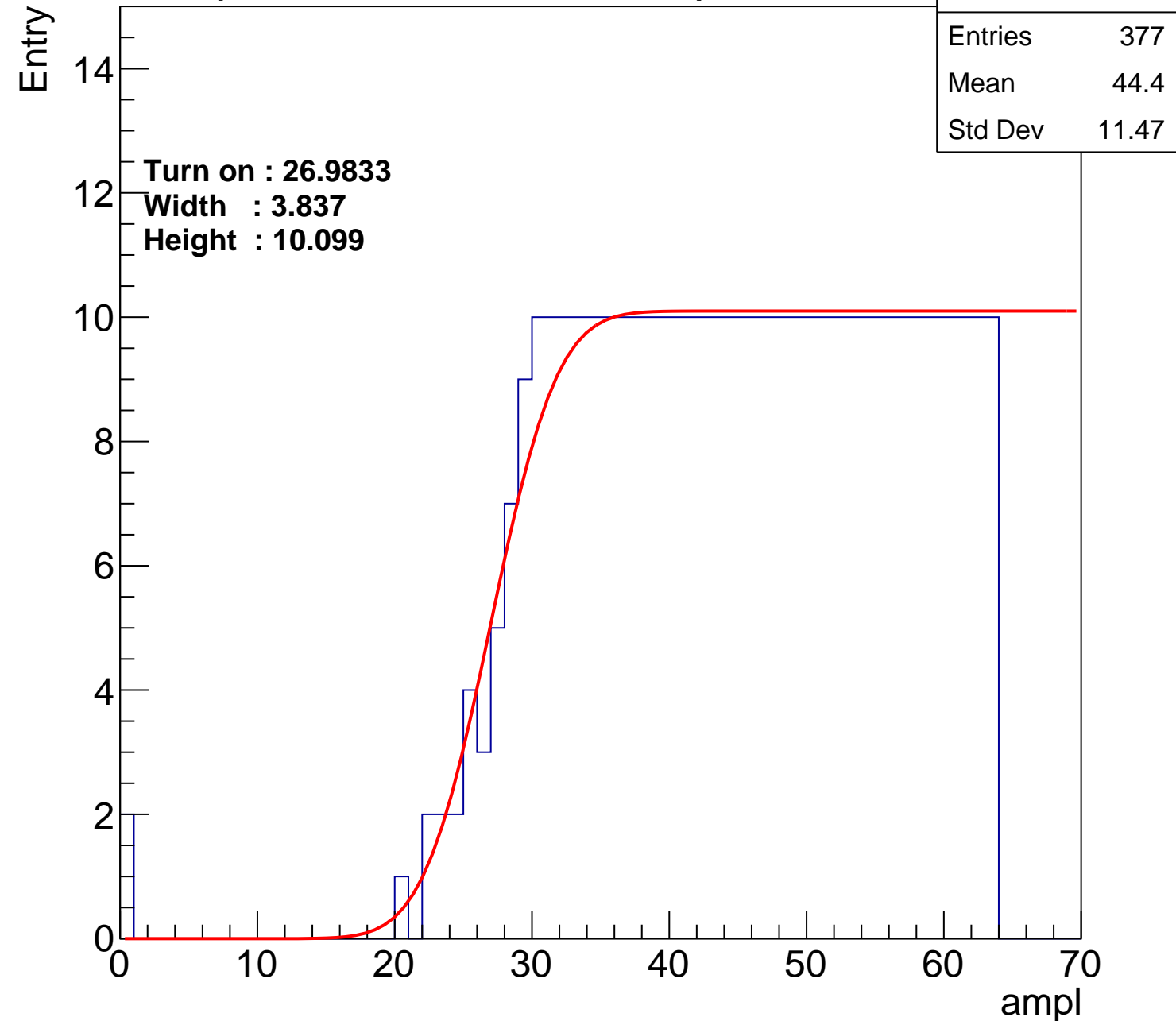
Width : 3.837

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.4723
Width : 2.567
Height : 10.028



B0L002S, U11-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46
Std Dev	10.67

Turn on : 30.2972

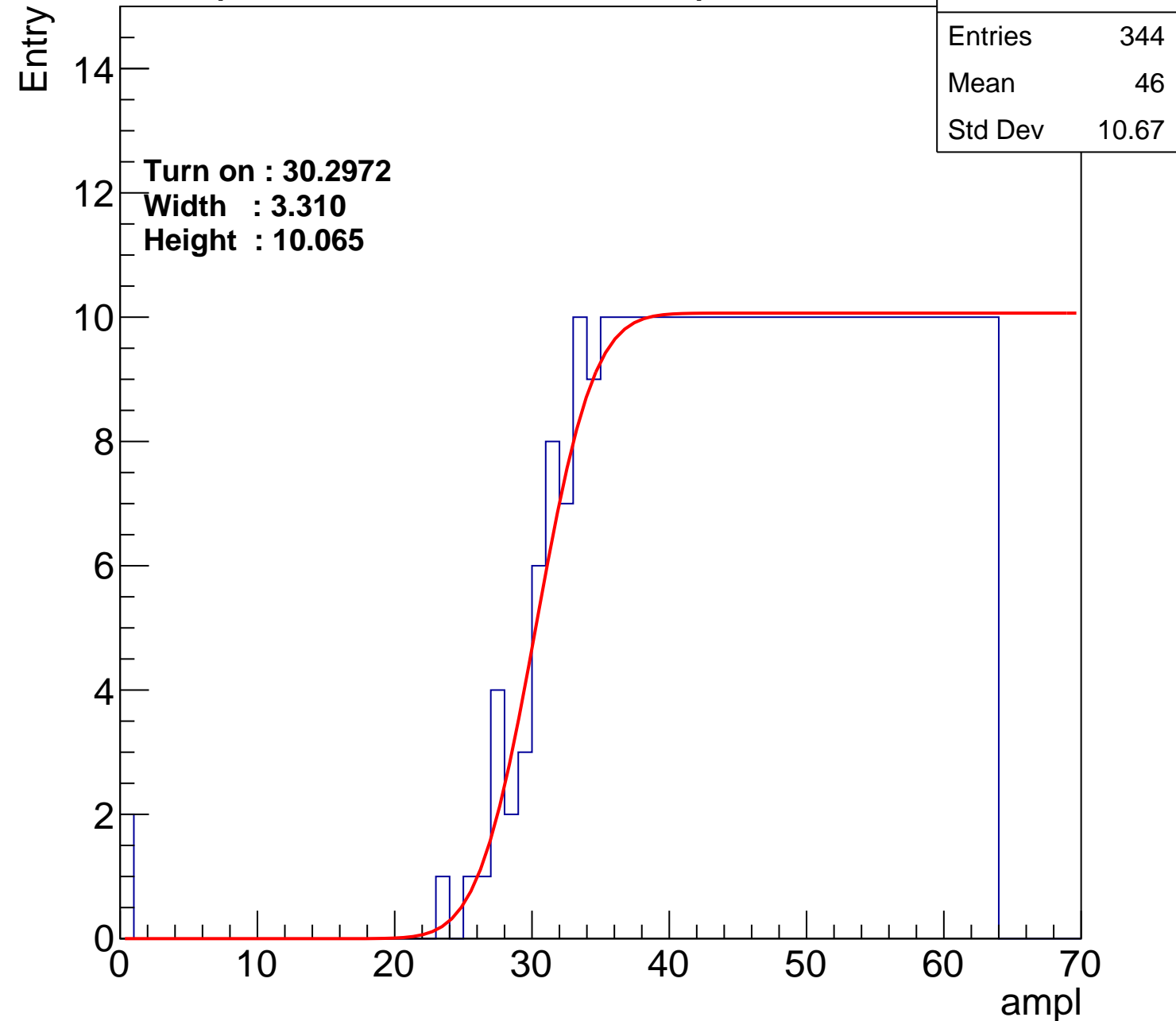
Width : 3.310

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	341
Mean	46.24
Std Dev	10.31

Turn on : 30.0659

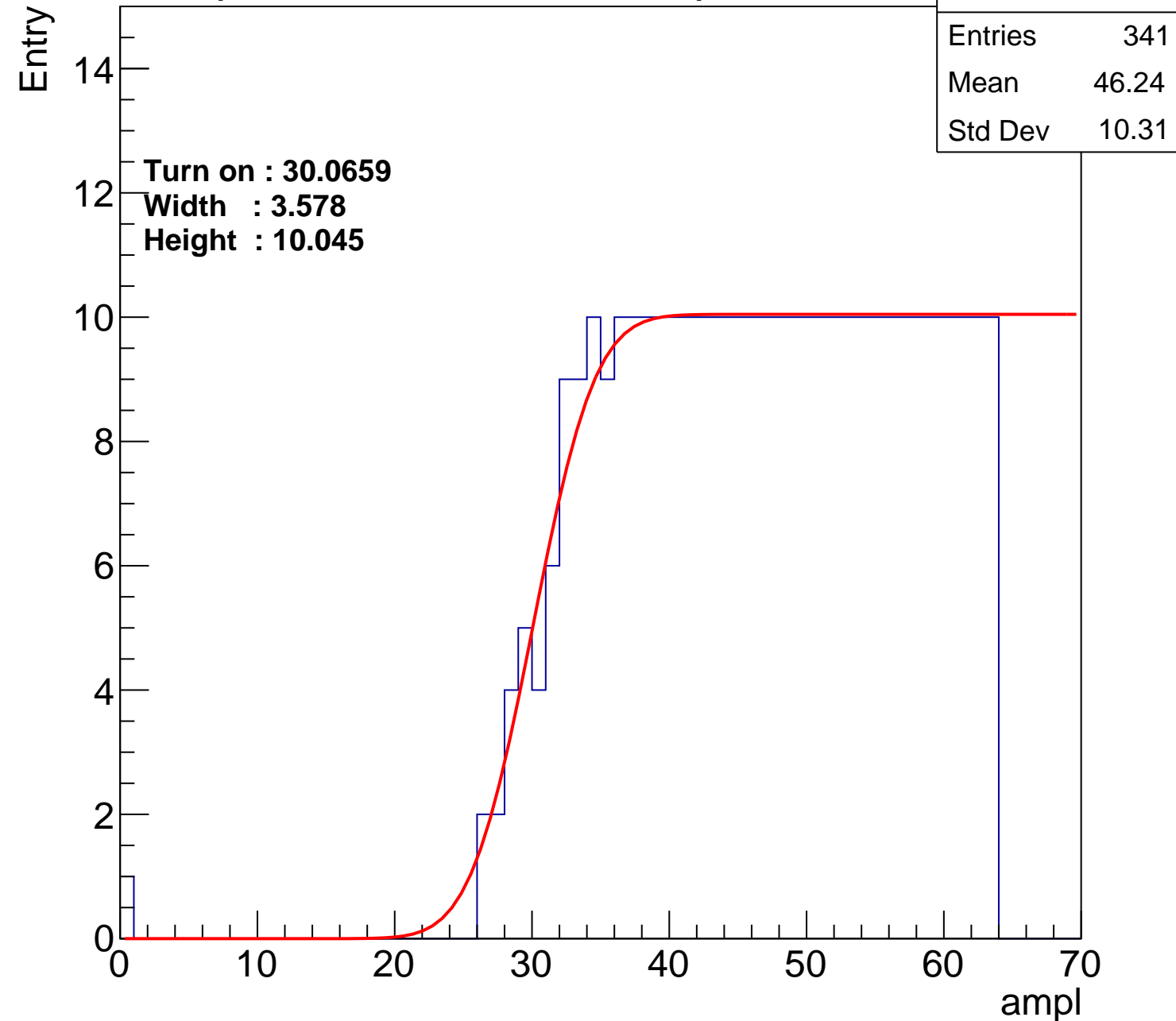
Width : 3.578

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.73
Std Dev	11.57

Turn on : 27.9197

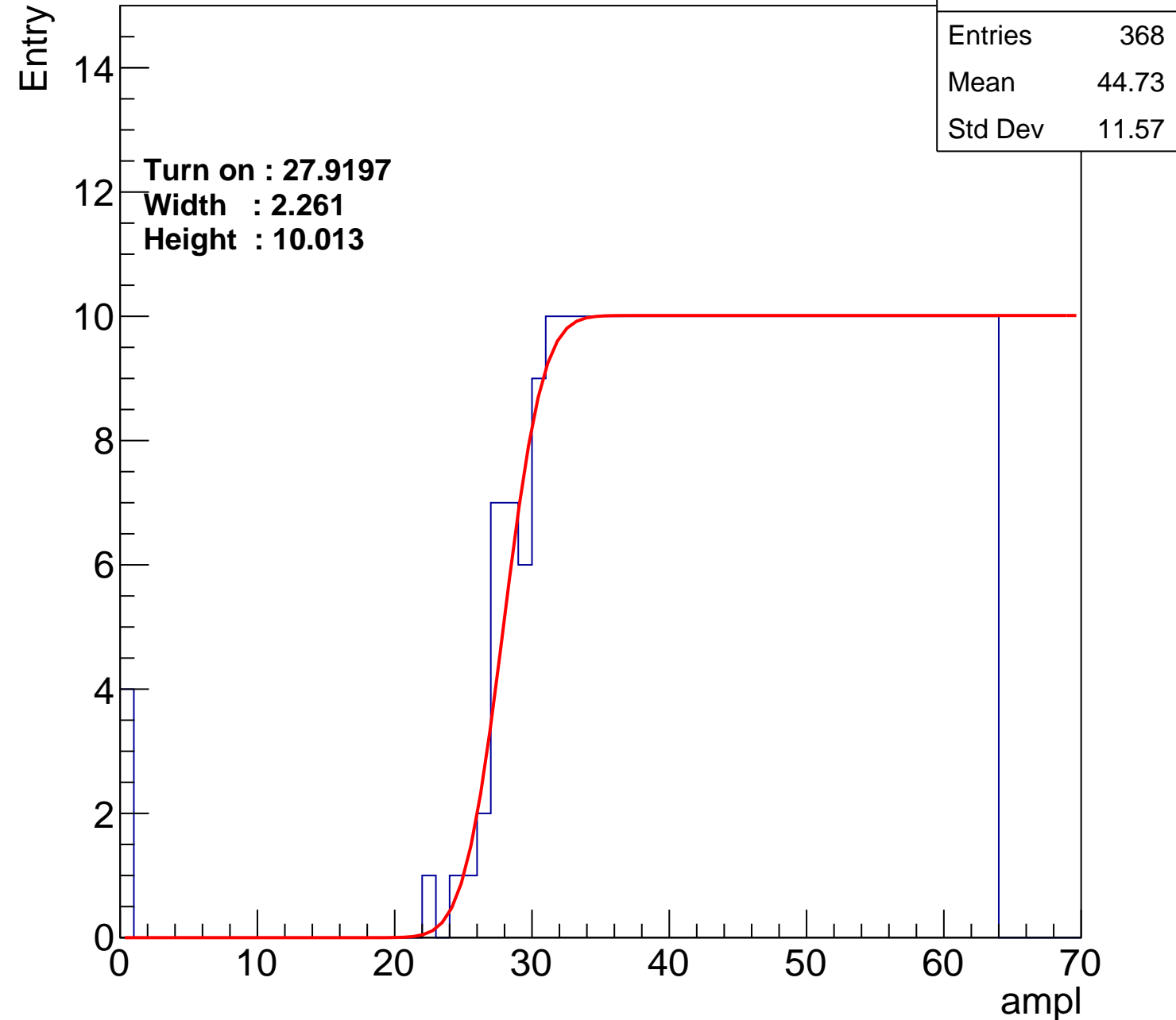
Width : 2.261

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.87
Std Dev	10.48

Turn on : 28.9021

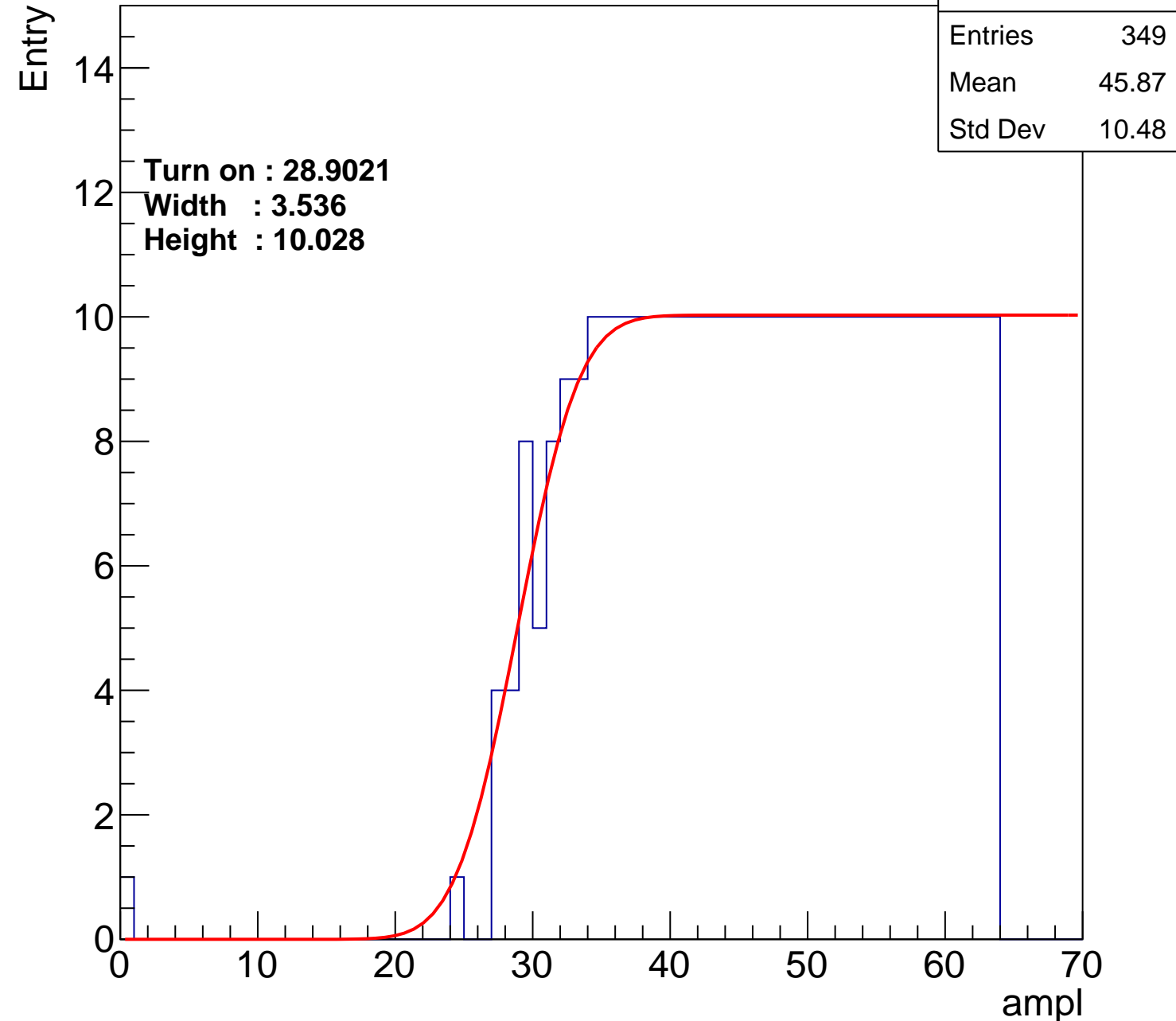
Width : 3.536

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch74

calib_packv5_042523_0143.root, FC#8, port C1

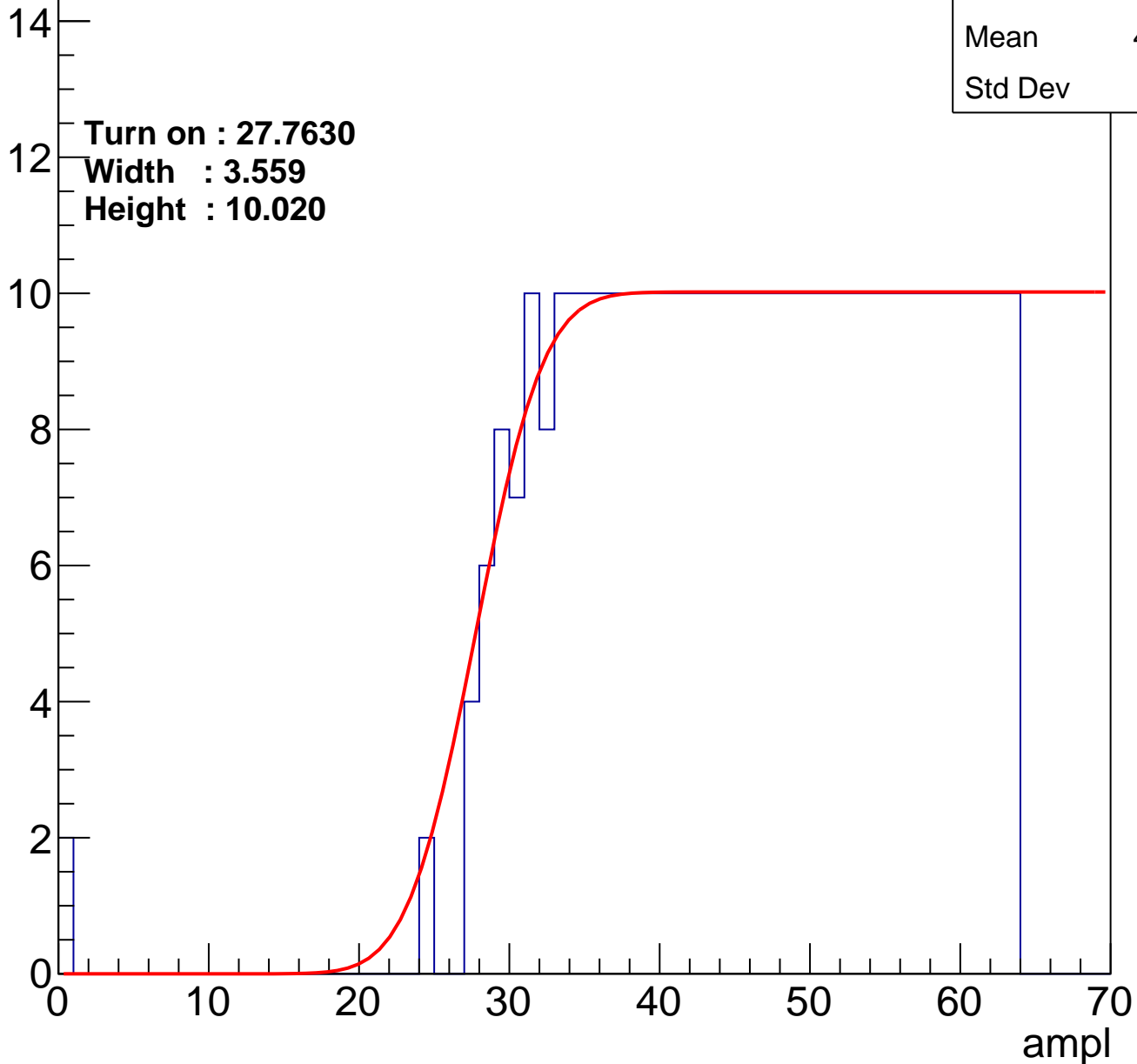
Entries	357
Mean	45.41
Std Dev	10.9

Turn on : 27.7630

Width : 3.559

Height : 10.020

Entry



B0L002S, U11-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.62
Std Dev	10.63

Turn on : 29.0914

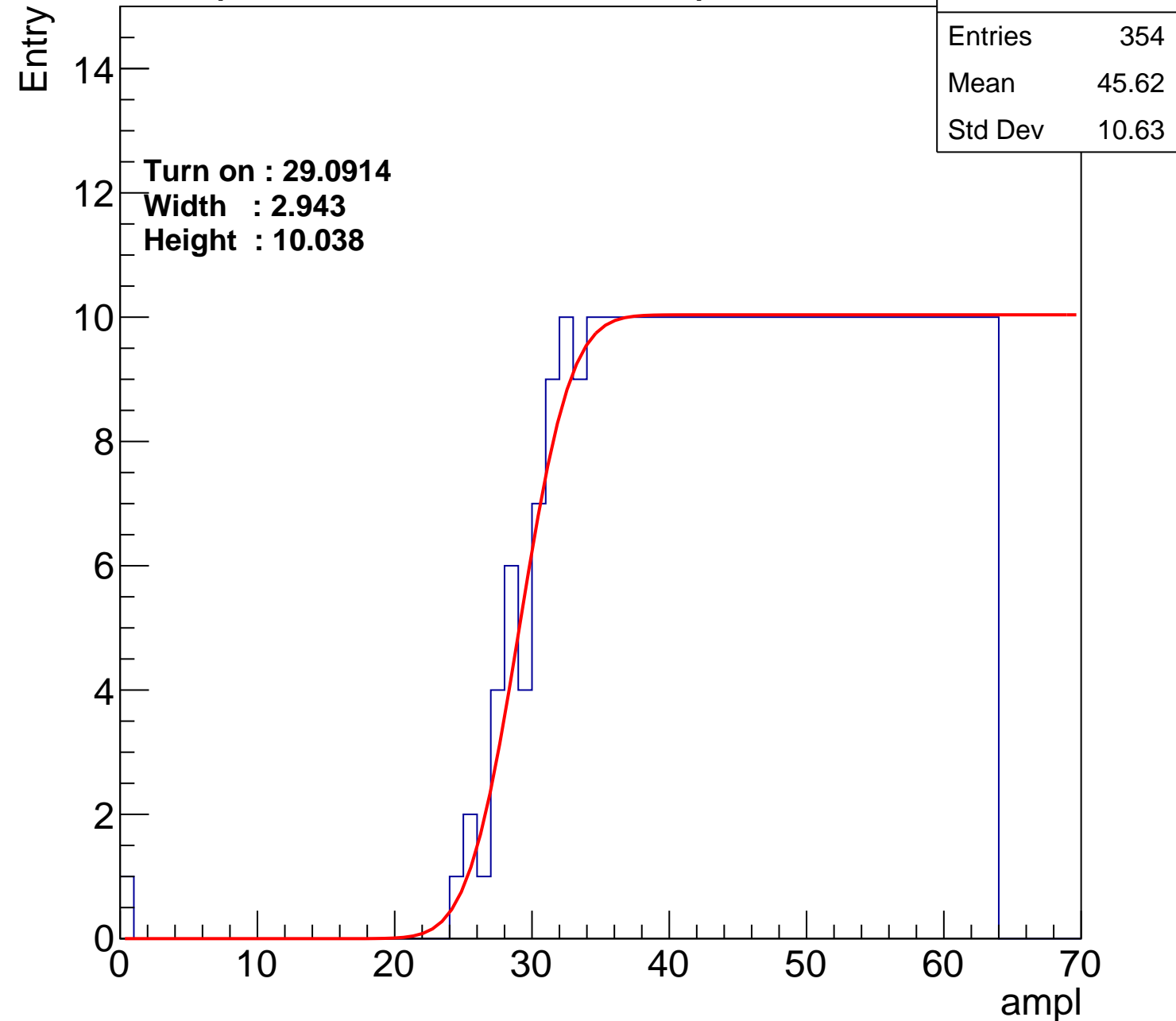
Width : 2.943

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.91
Std Dev	10.47

Turn on : 28.4327

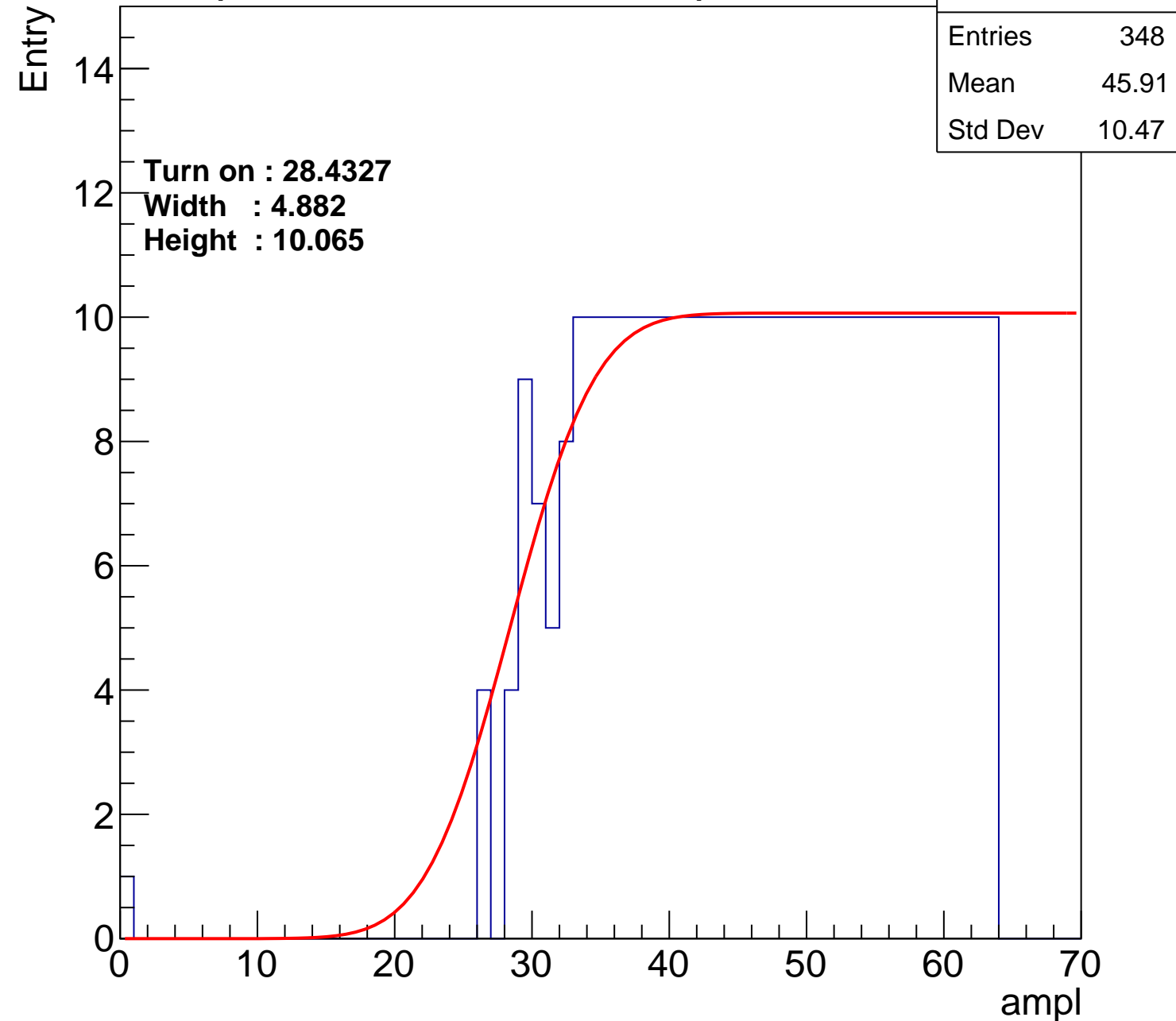
Width : 4.882

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch77

calib_packv5_042523_0143.root, FC#8, port C1

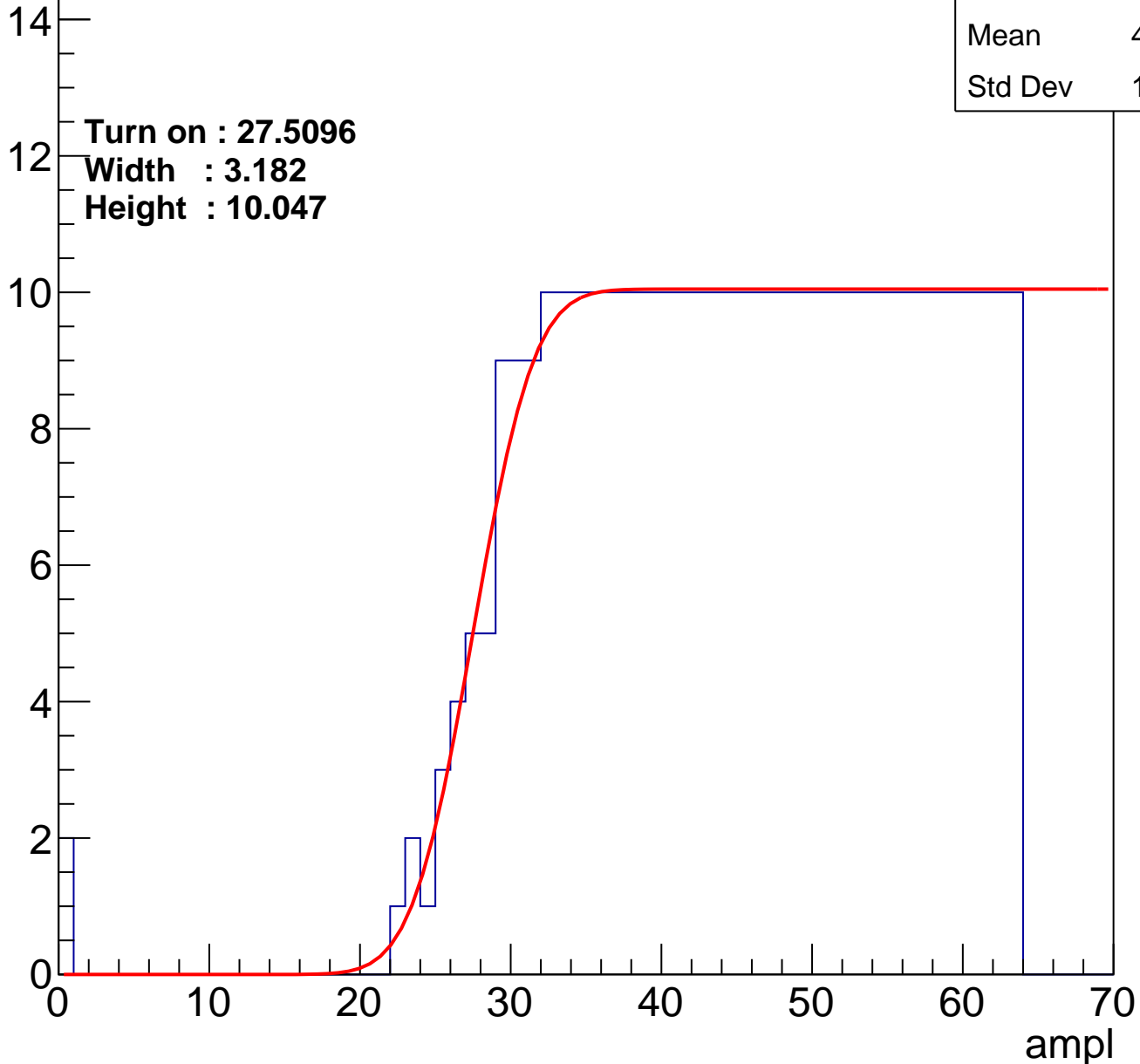
Entries	370
Mean	44.75
Std Dev	11.28

Turn on : 27.5096

Width : 3.182

Height : 10.047

Entry



B0L002S, U11-ch78

calib_packv5_042523_0143.root, FC#8, port C1

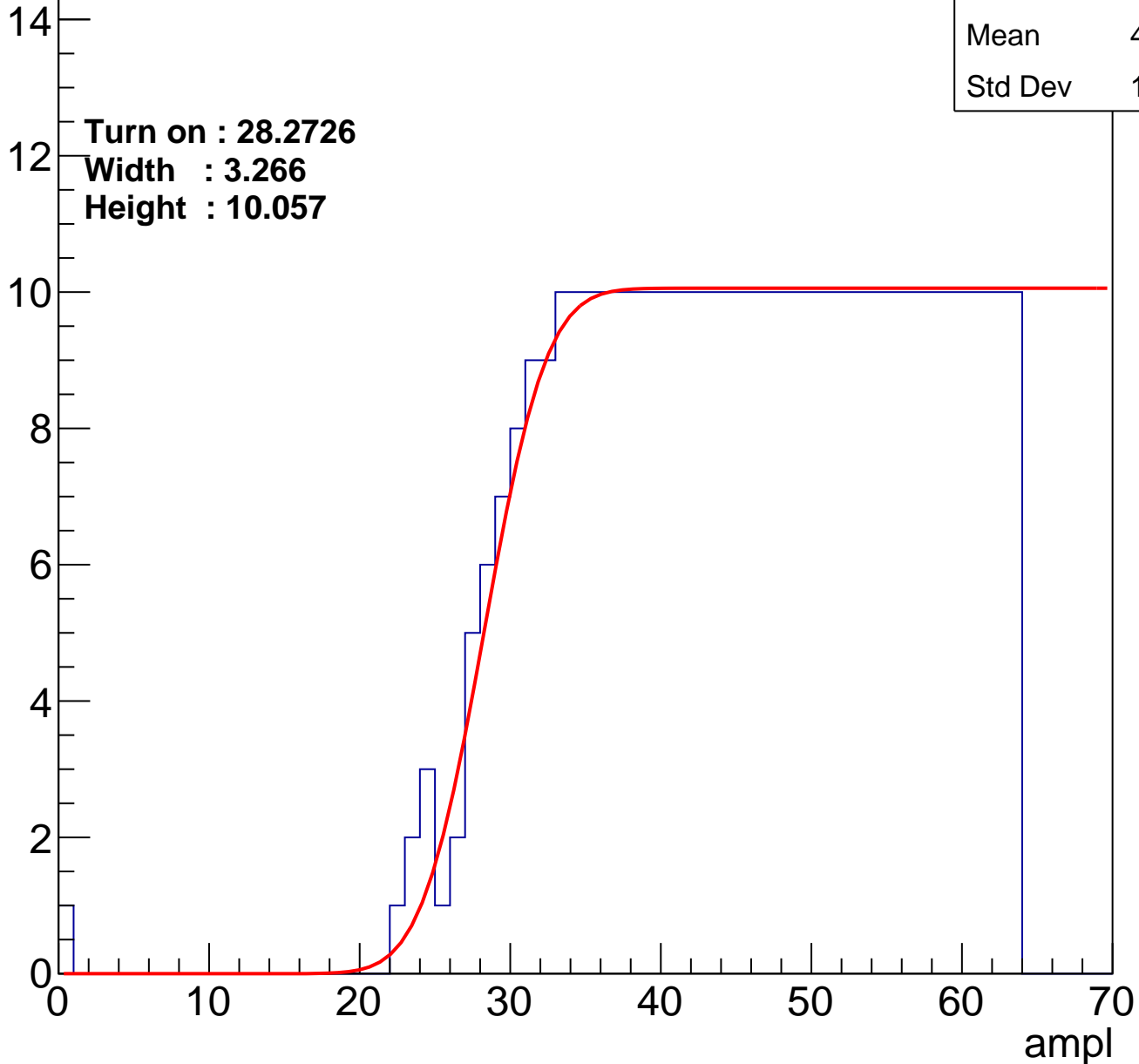
Entries	364
Mean	45.08
Std Dev	10.97

Turn on : 28.2726

Width : 3.266

Height : 10.057

Entry



B0L002S, U11-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.34
Std Dev	11.38

Turn on : 29.2621

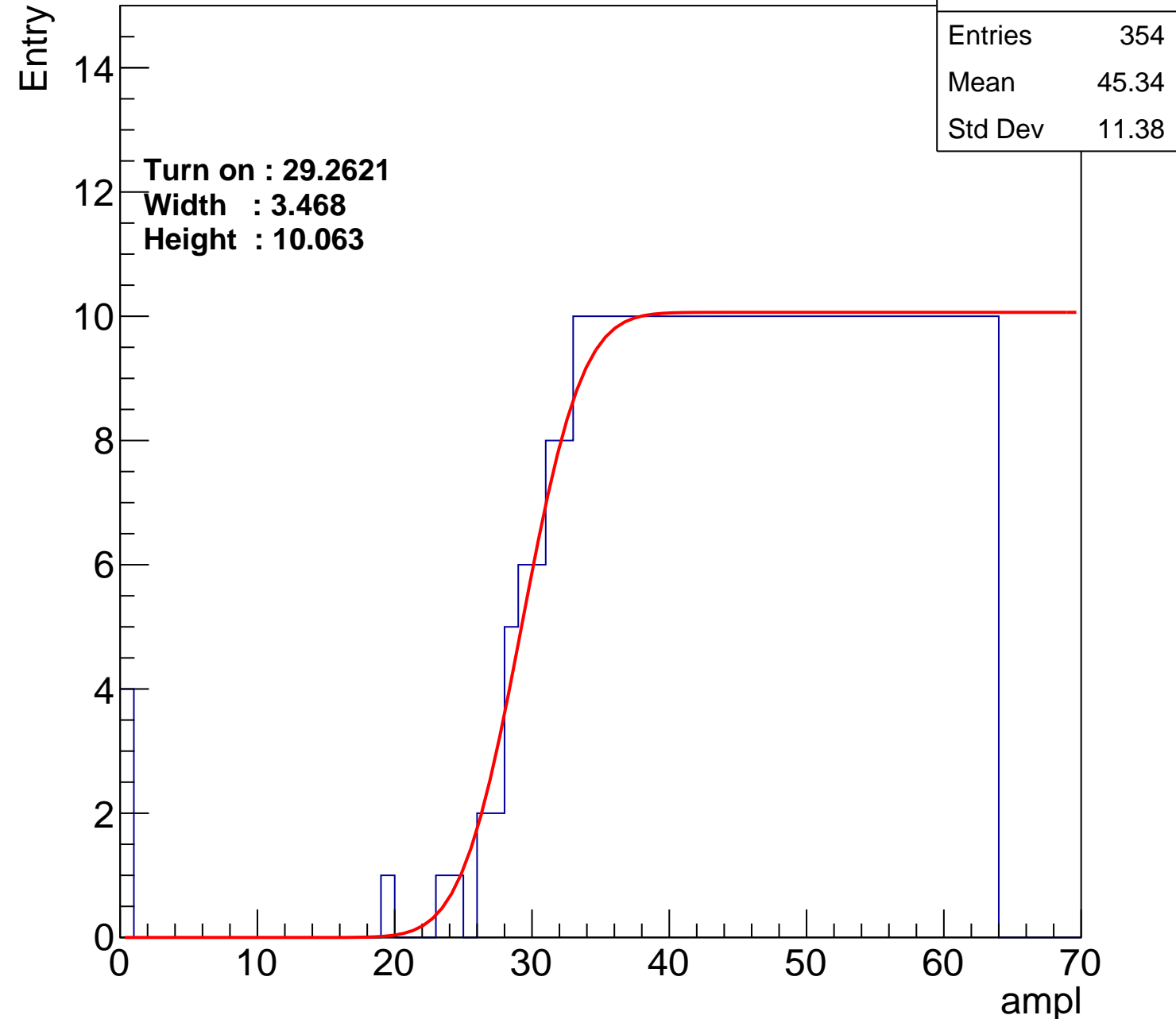
Width : 3.468

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch80

calib_packv5_042523_0143.root, FC#8, port C1

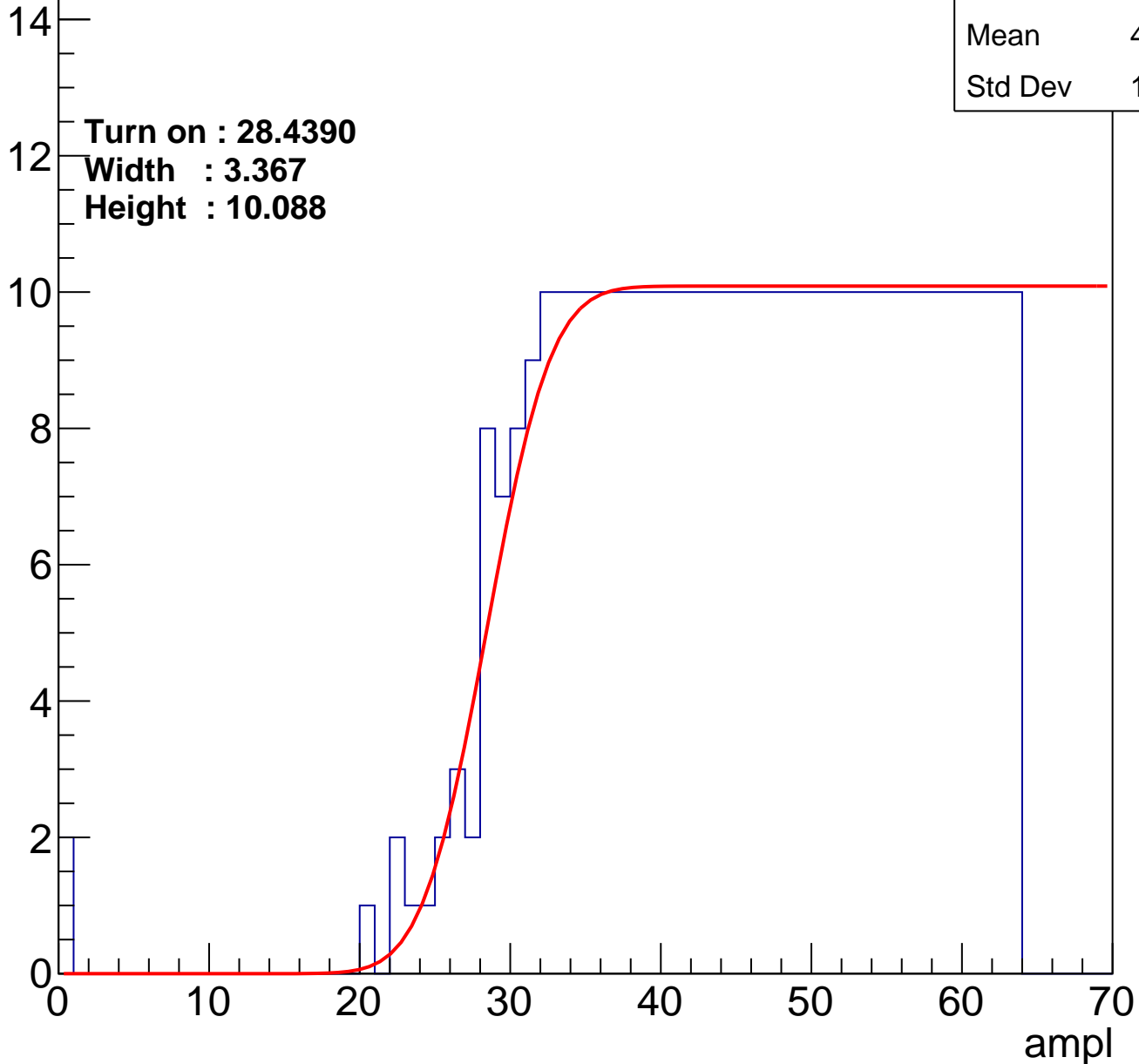
Entries	366
Mean	44.92
Std Dev	11.23

Turn on : 28.4390

Width : 3.367

Height : 10.088

Entry



B0L002S, U11-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.47
Std Dev	11.08

Turn on : 29.0538

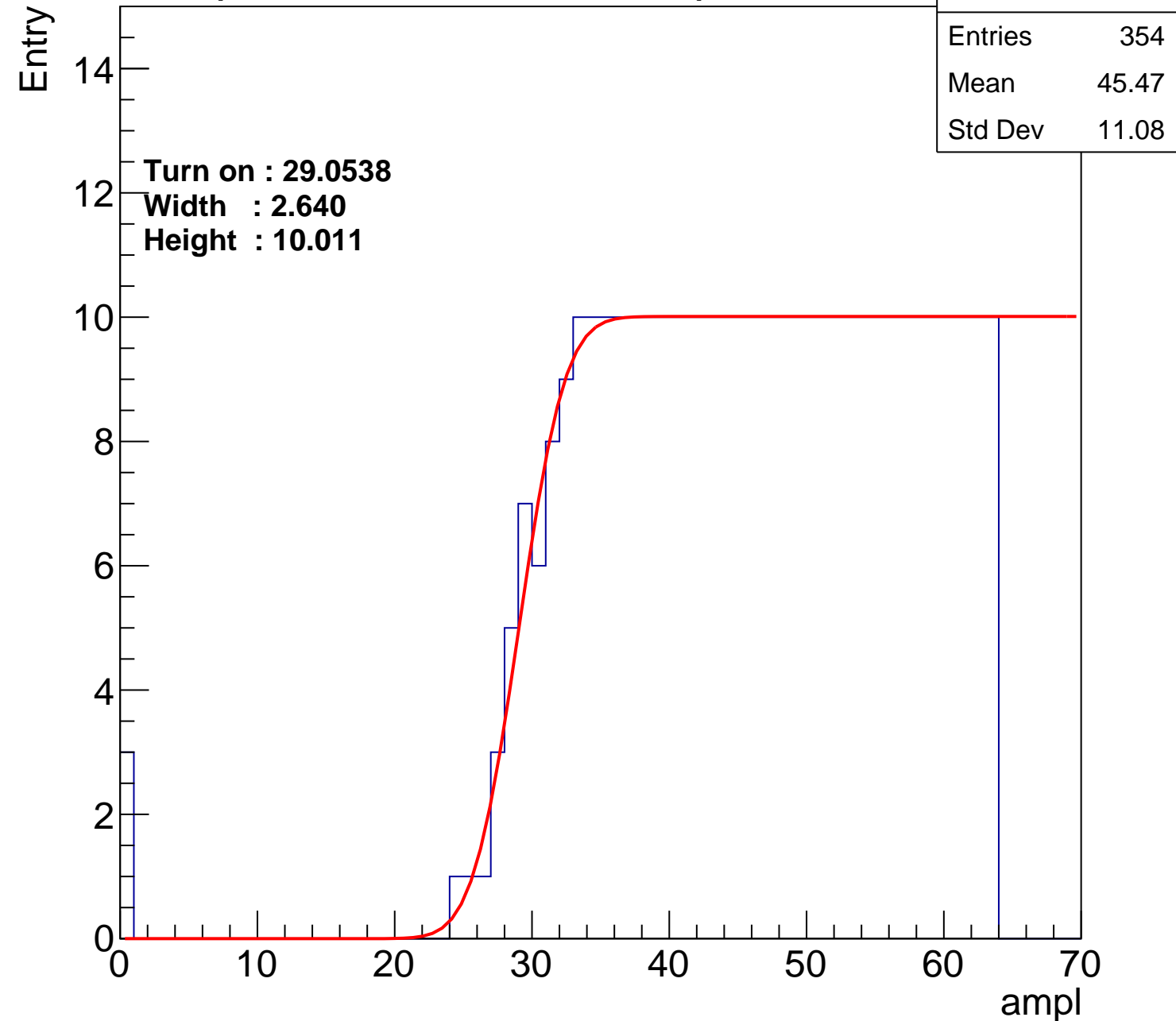
Width : 2.640

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.31
Std Dev	10.75

Turn on : 28.3305

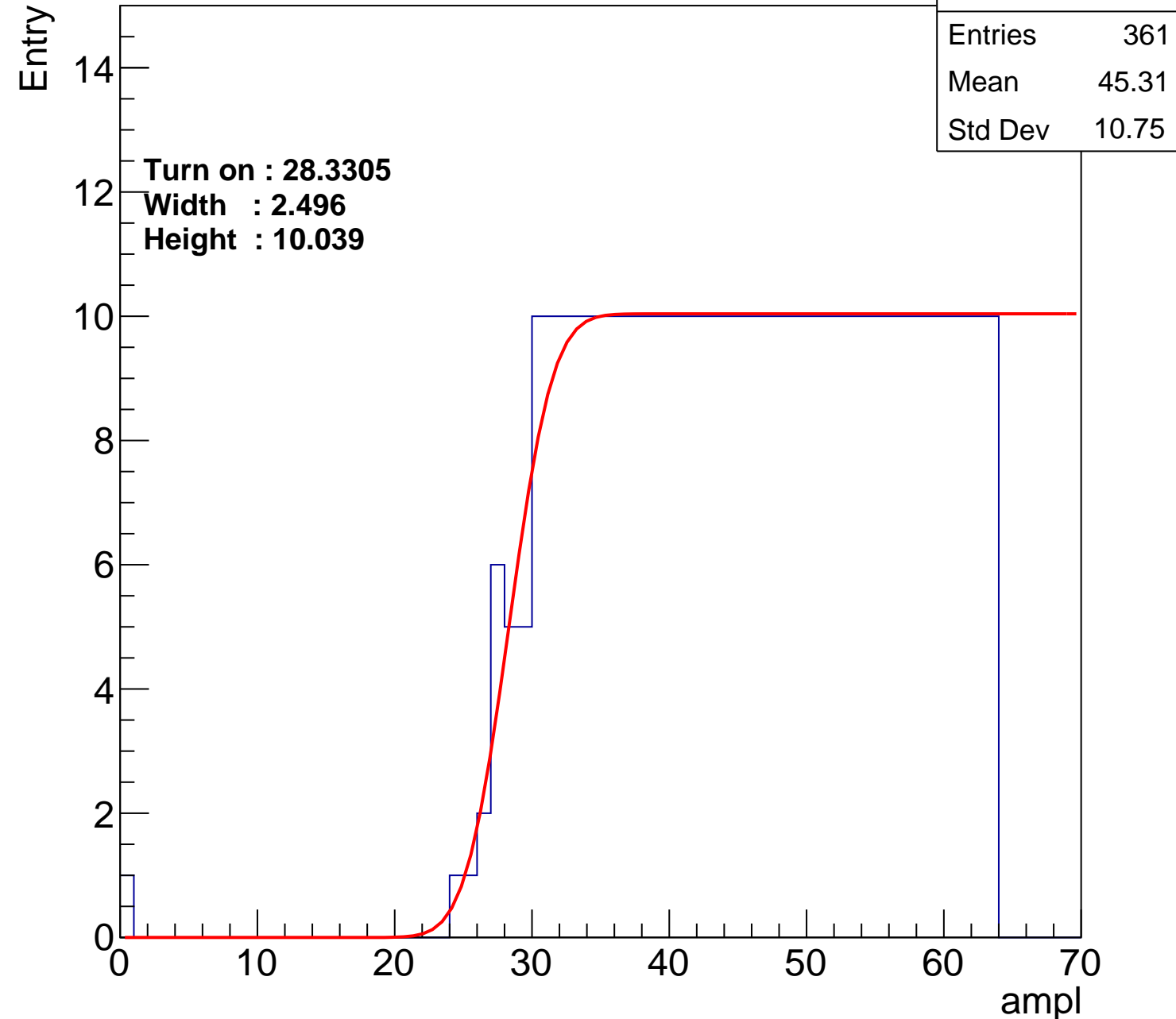
Width : 2.496

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.87

Turn on : 28.1267

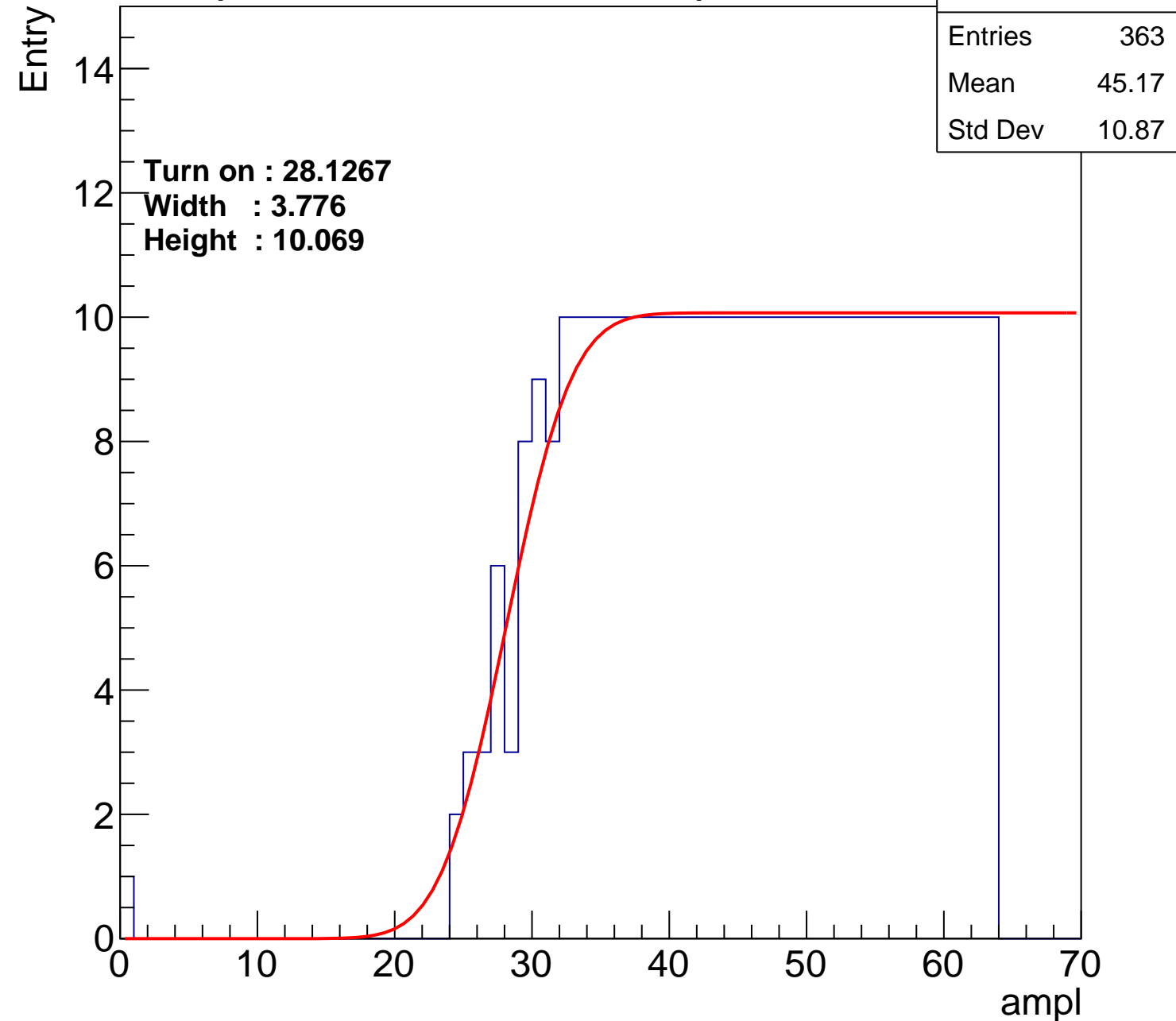
Width : 3.776

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.08
Std Dev	10.54

Turn on : 30.0051

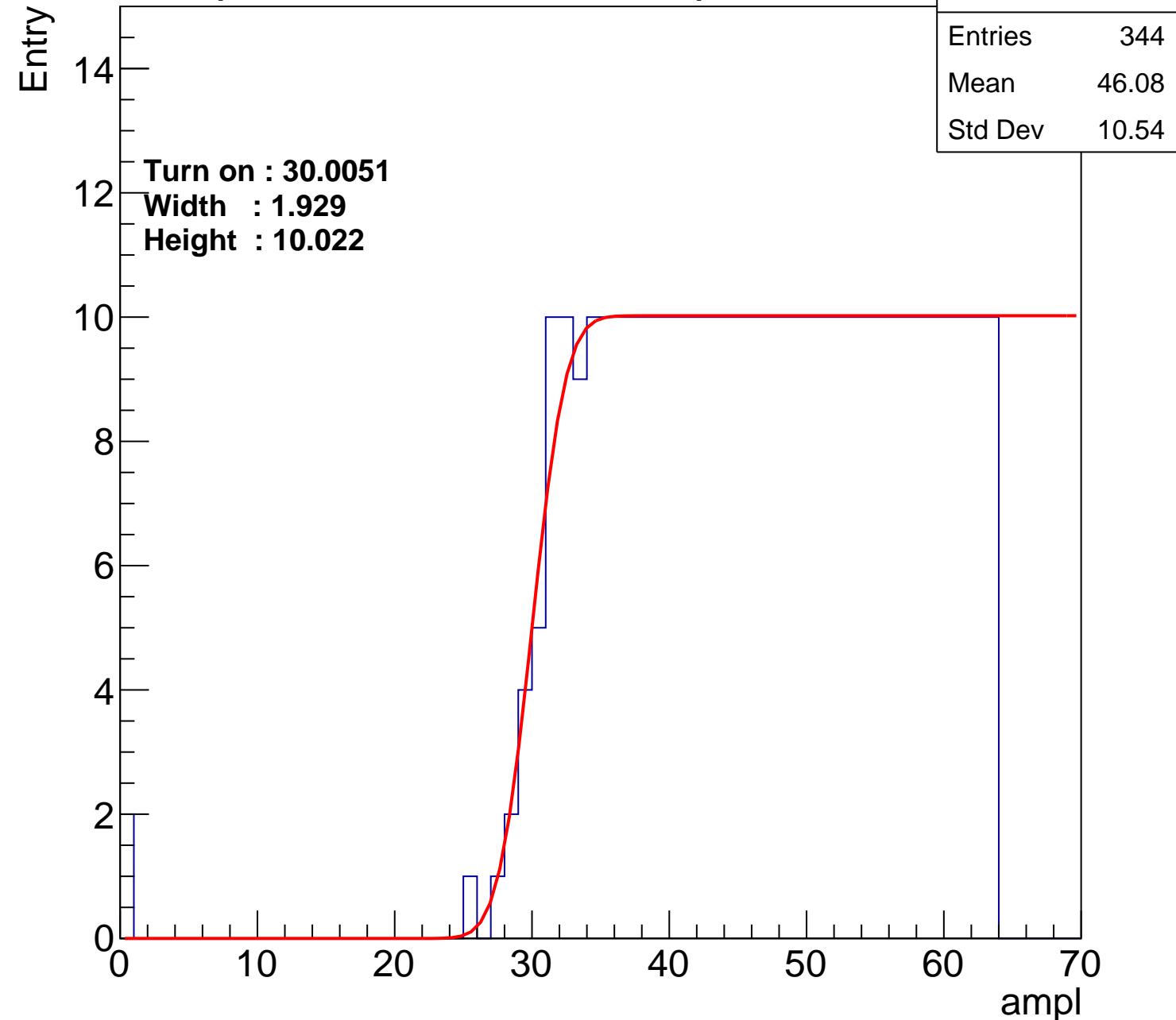
Width : 1.929

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.65
Std Dev	10.79

Turn on : 29.2288

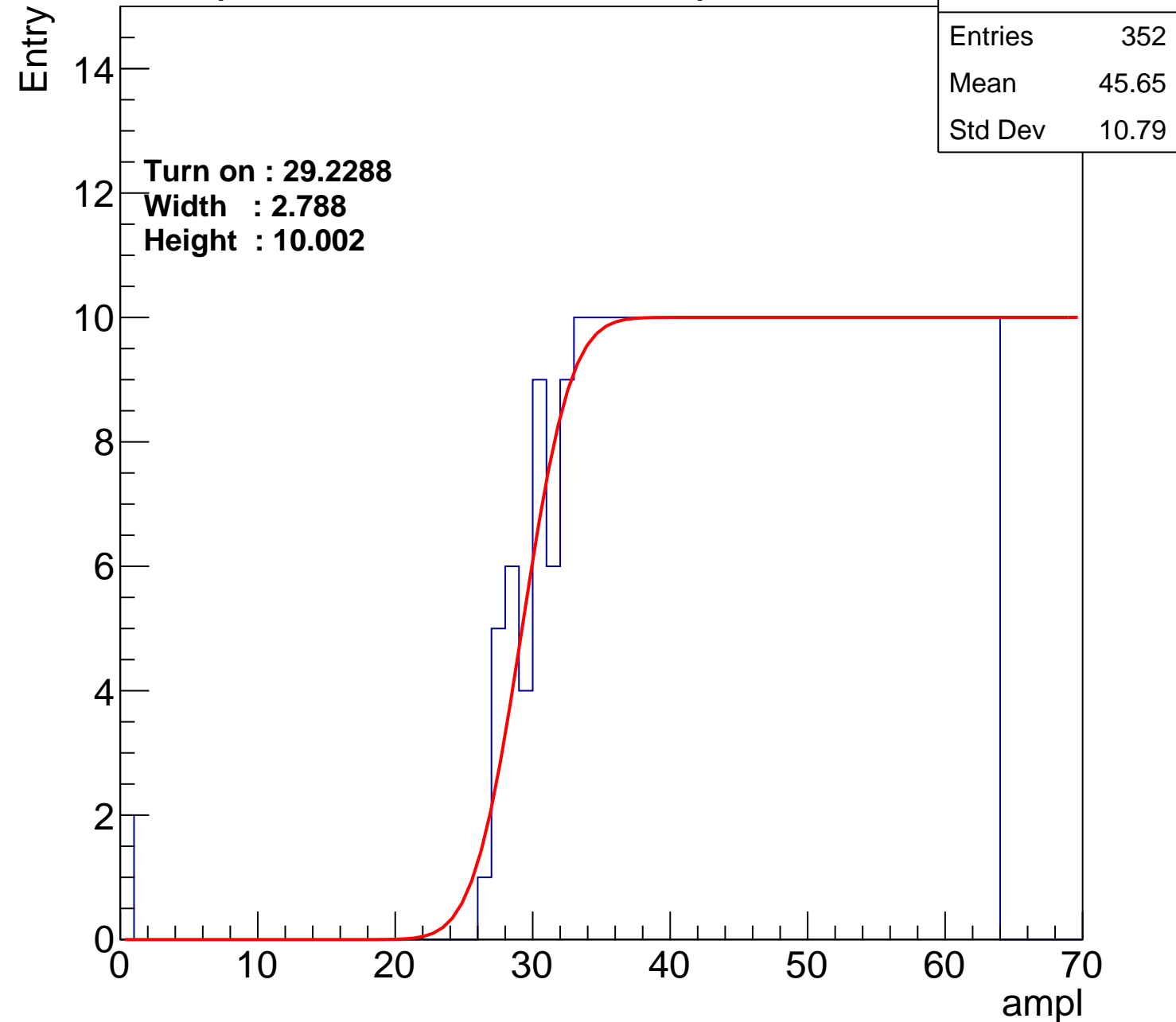
Width : 2.788

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 28.0638

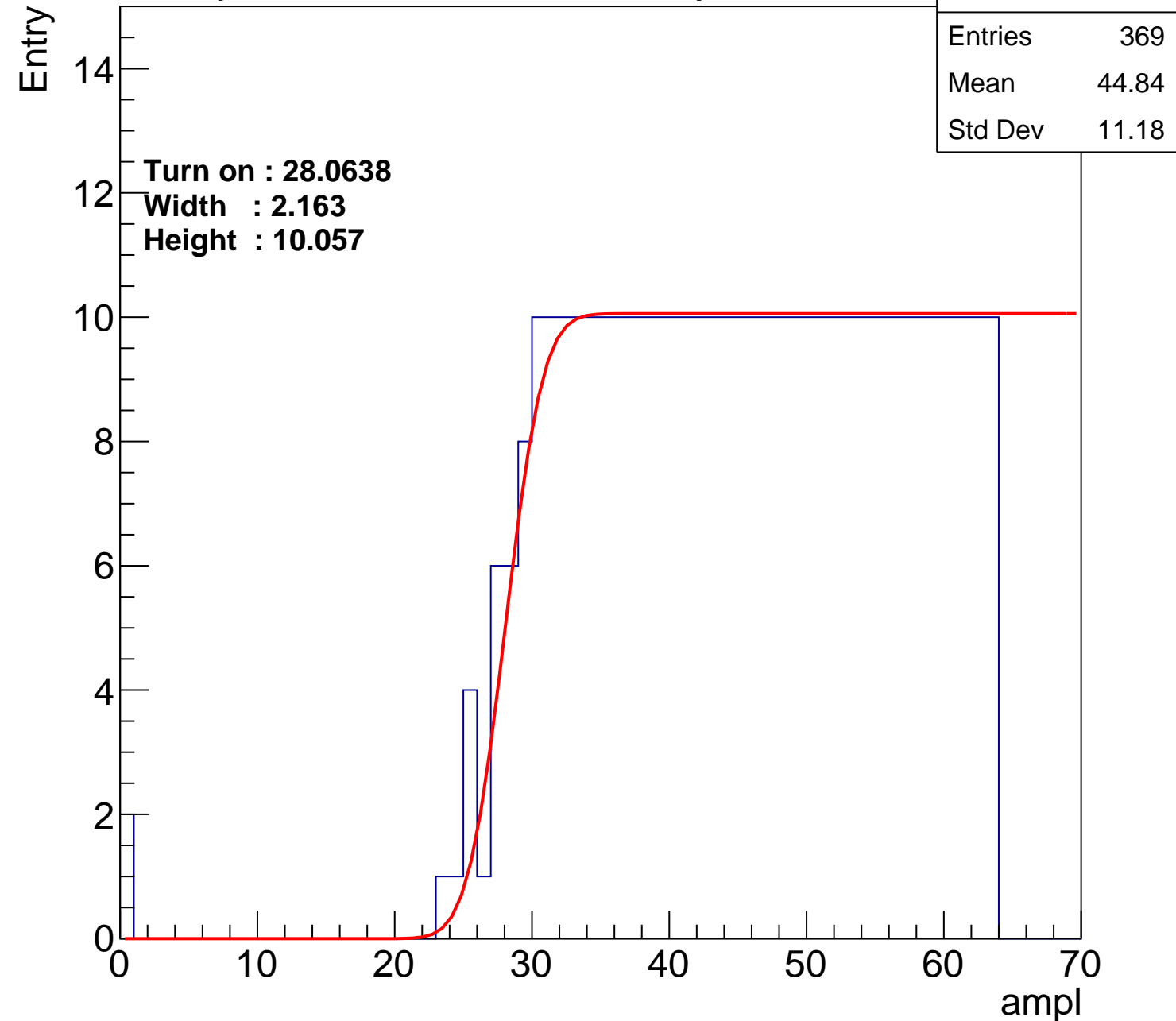
Width : 2.163

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.19
Std Dev	10.84

Turn on : 27.3333

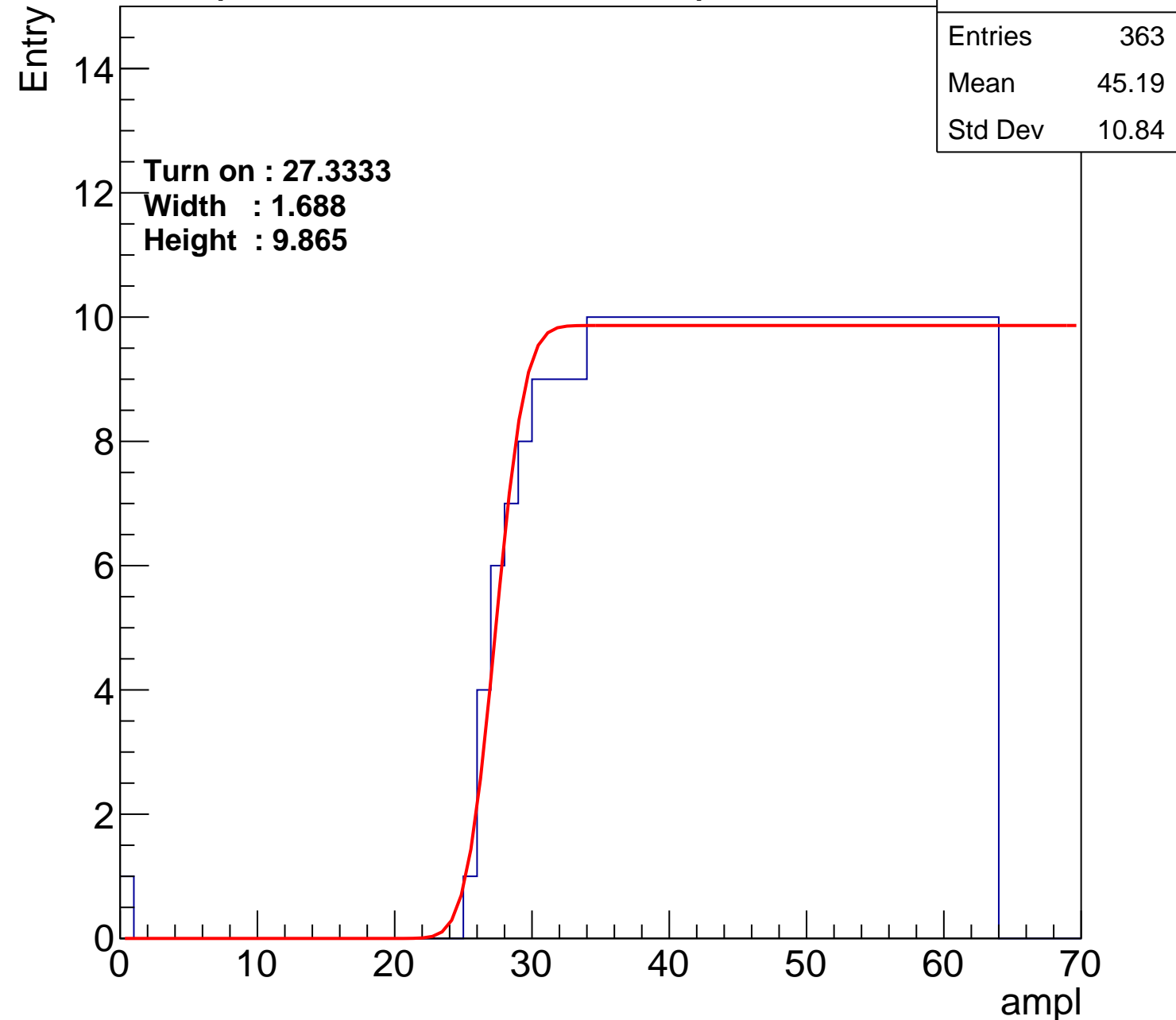
Width : 1.688

Height : 9.865

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.96
Std Dev	10.44

Turn on : 29.4755

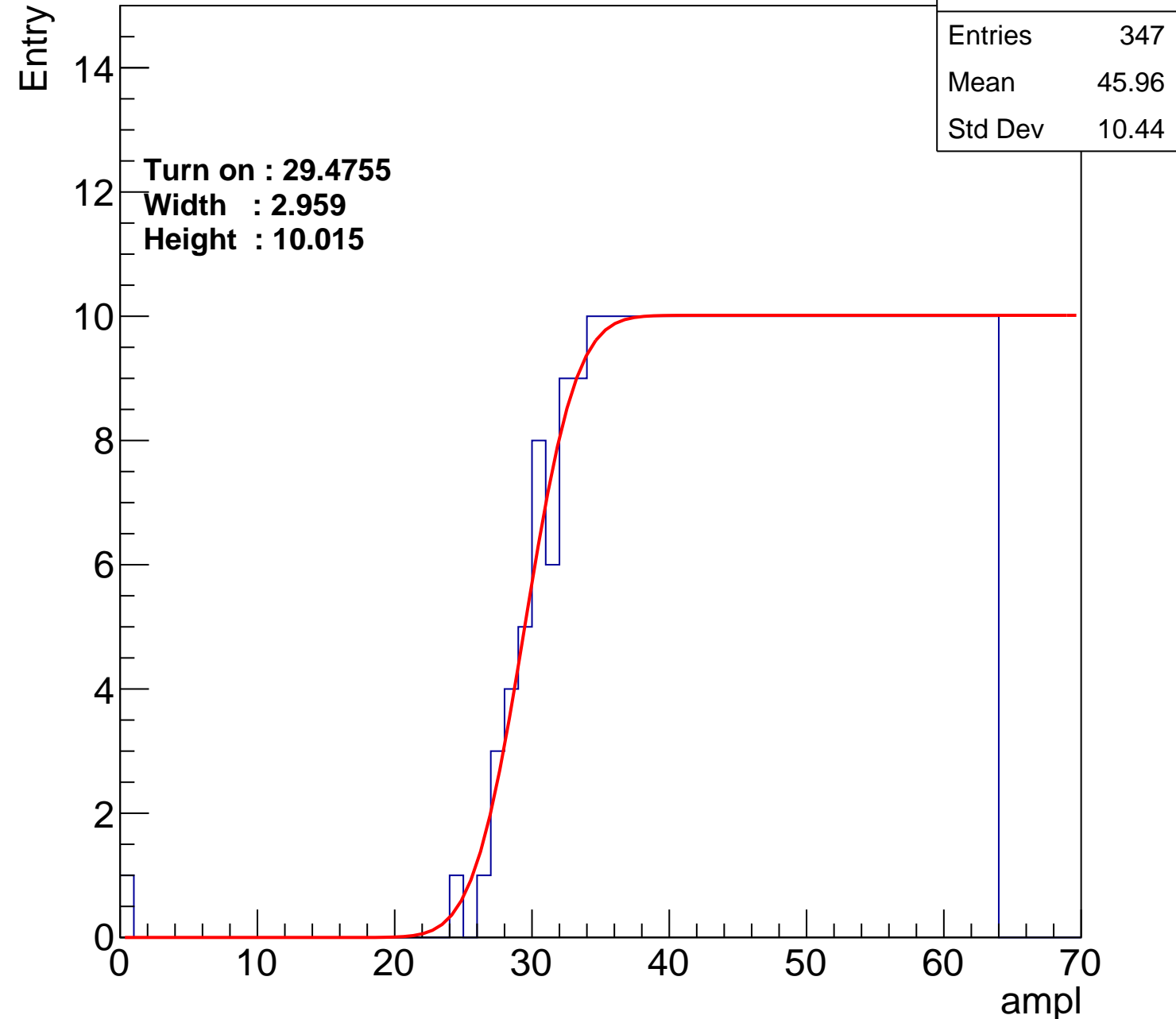
Width : 2.959

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.54
Std Dev	10.81

Turn on : 29.0134

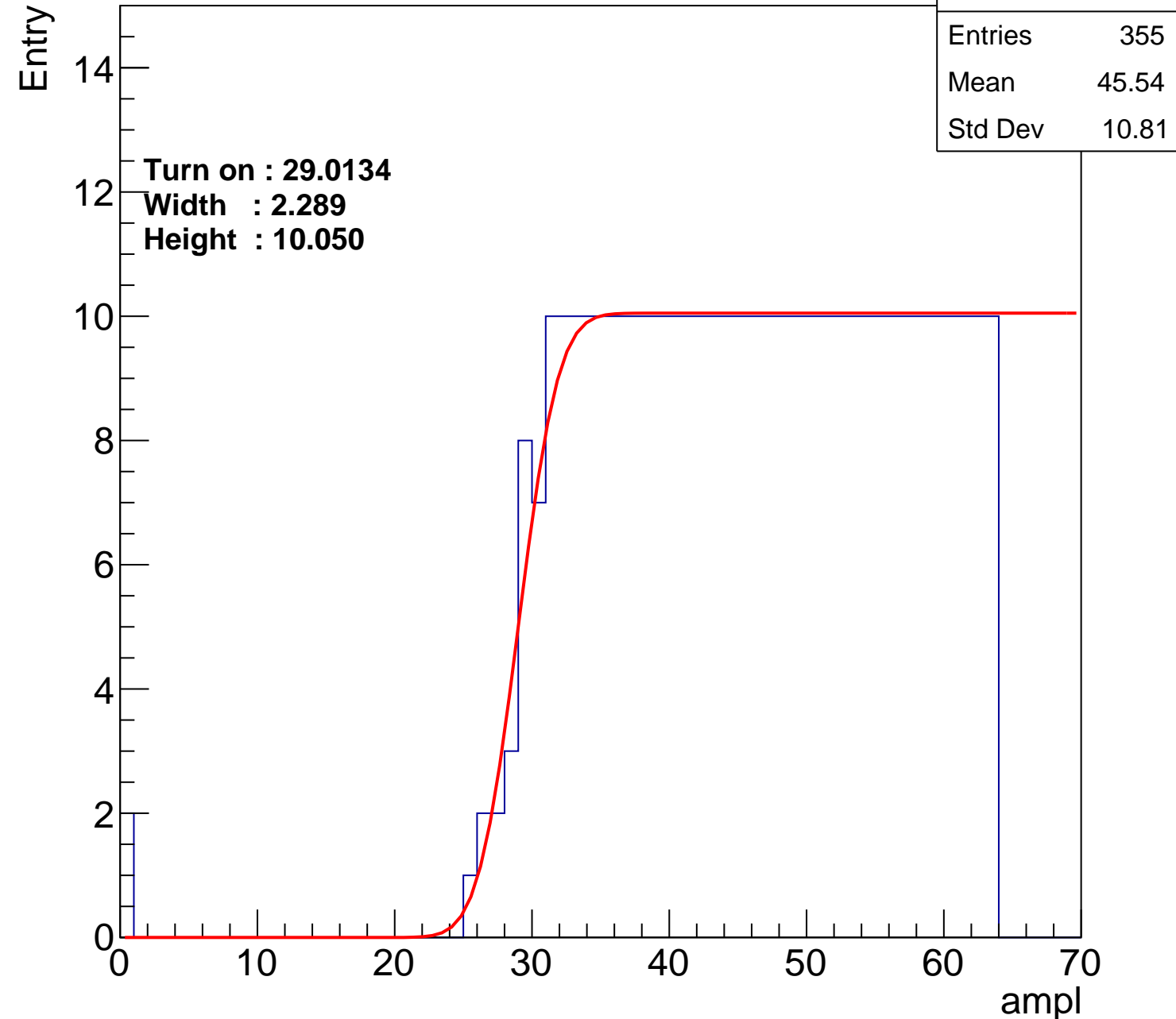
Width : 2.289

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.06
Std Dev	11.1

Turn on : 28.0090

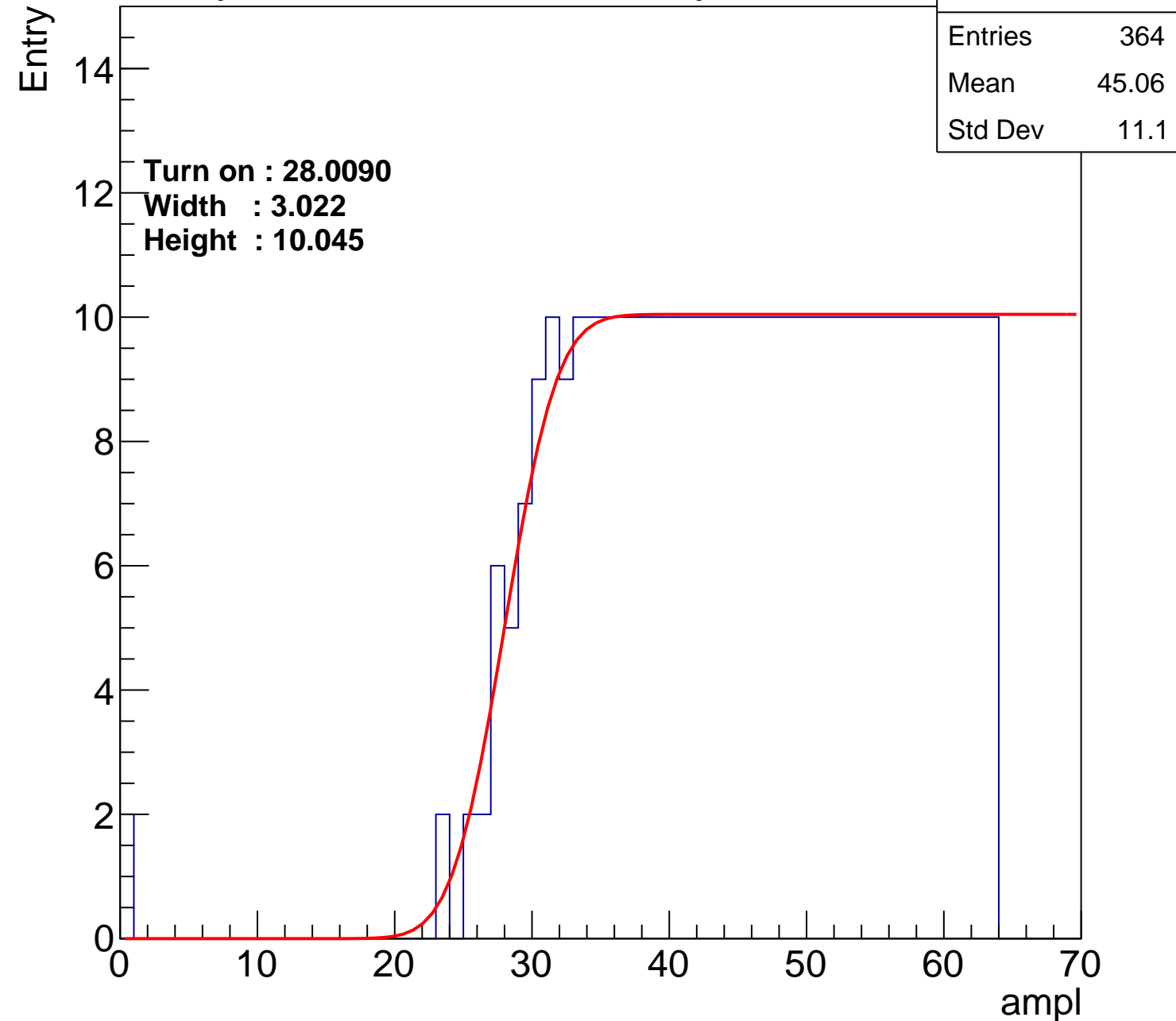
Width : 3.022

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.38
Std Dev	11.11

Turn on : 28.5050

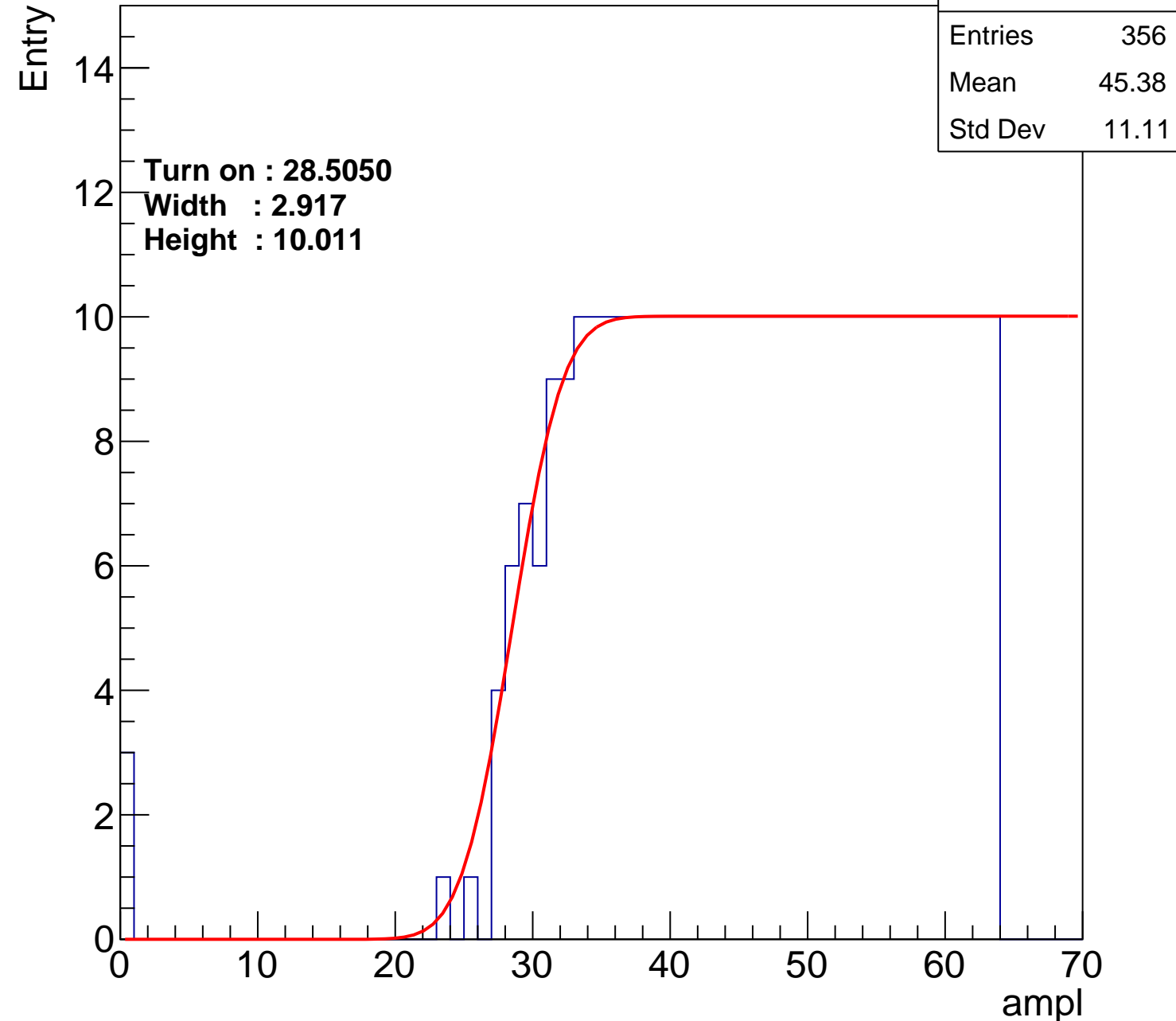
Width : 2.917

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.16
Std Dev	10.98

Turn on : 28.0889

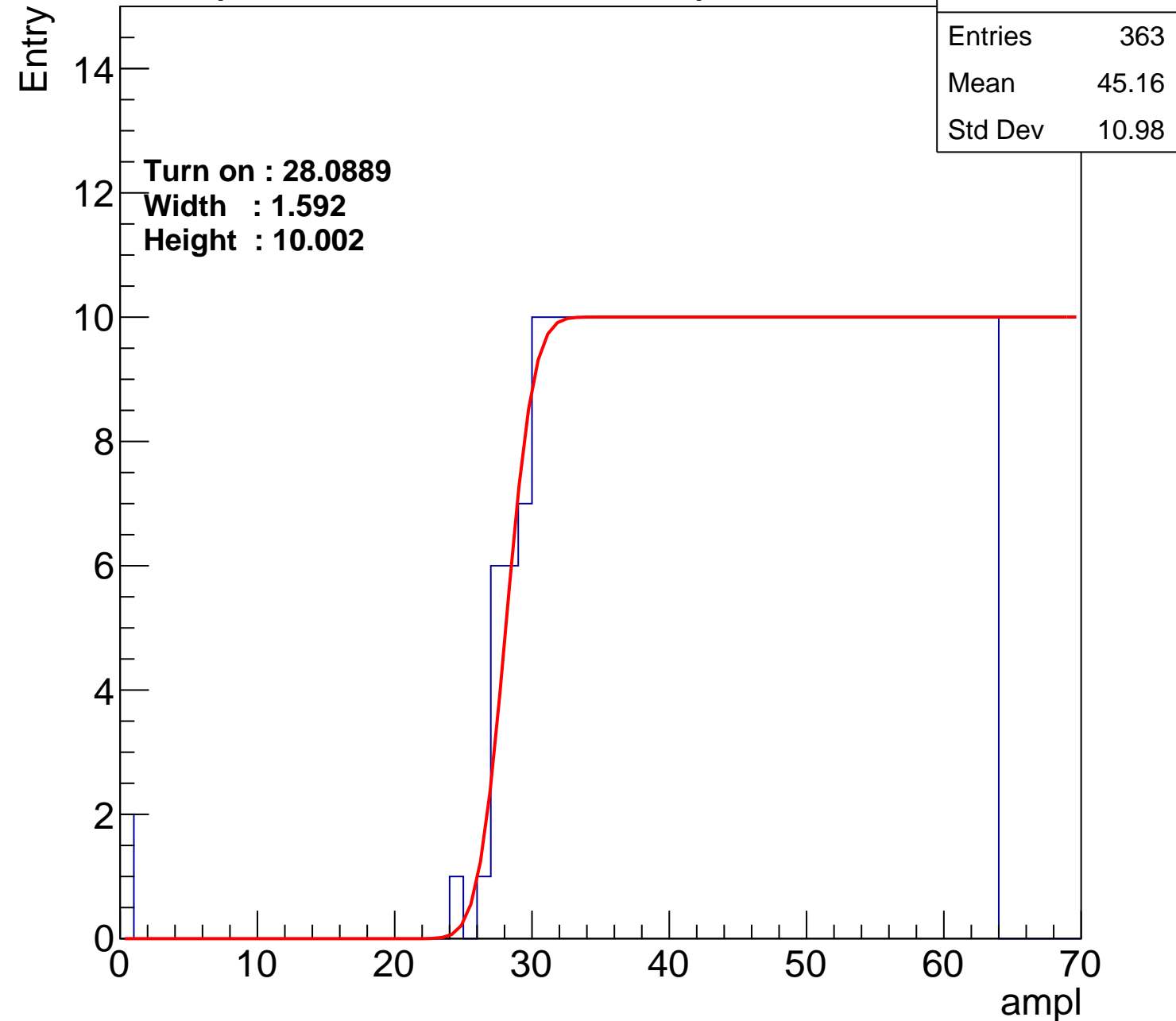
Width : 1.592

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.29
Std Dev	11.78

Turn on : 26.4231

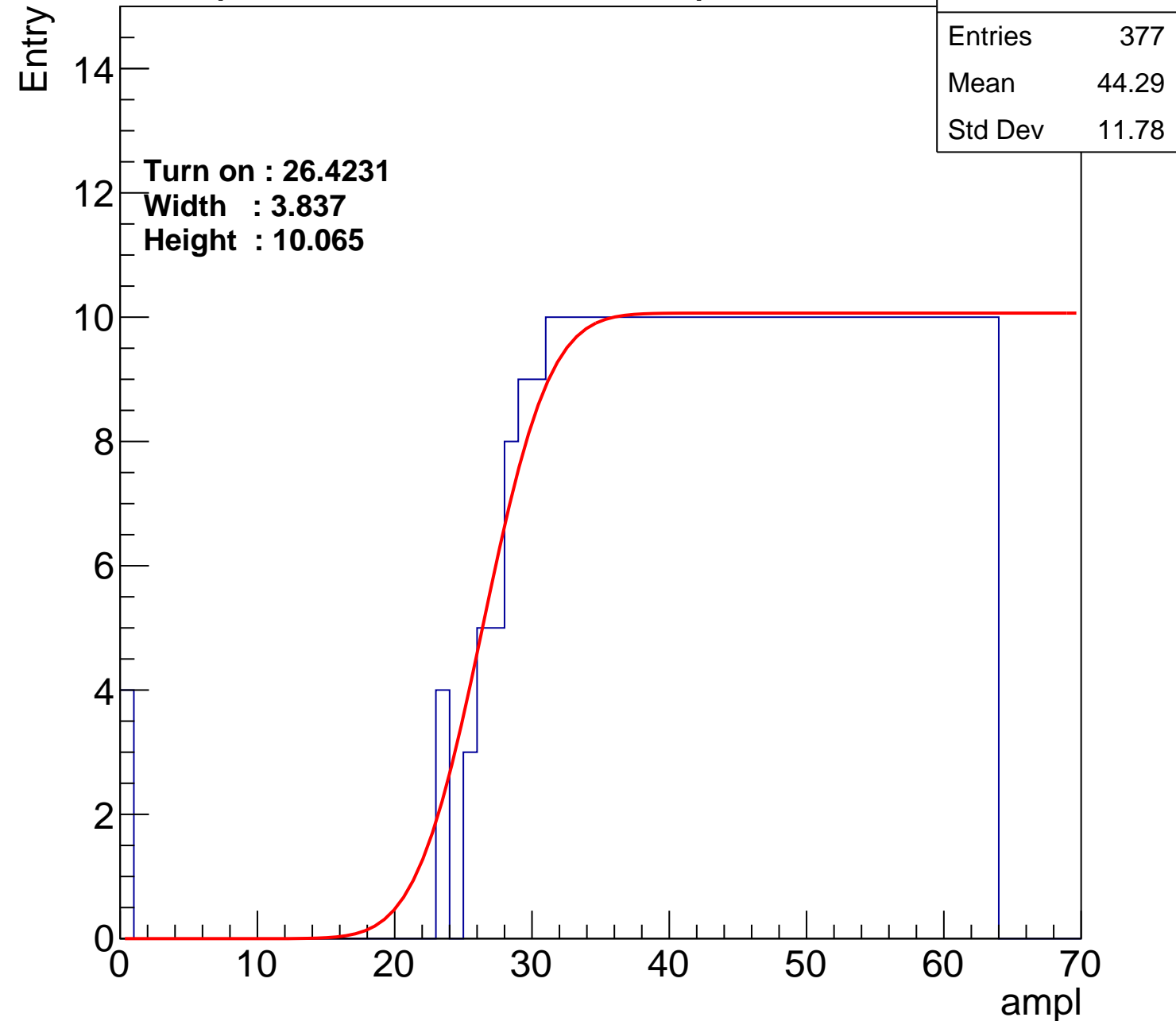
Width : 3.837

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch94

calib_packv5_042523_0143.root, FC#8, port C1

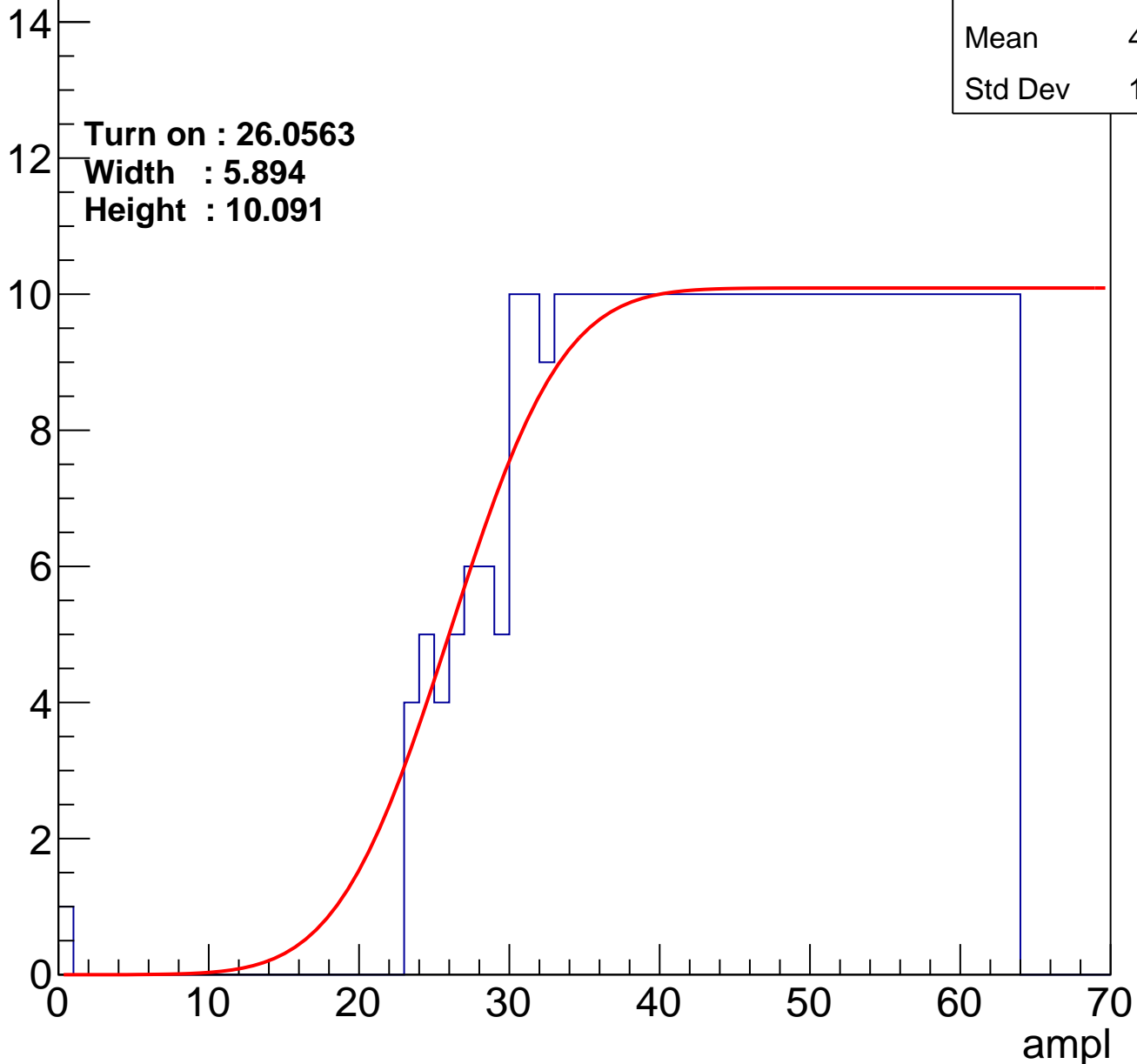
Entries	375
Mean	44.52
Std Dev	11.29

Turn on : 26.0563

Width : 5.894

Height : 10.091

Entry



B0L002S, U11-ch95

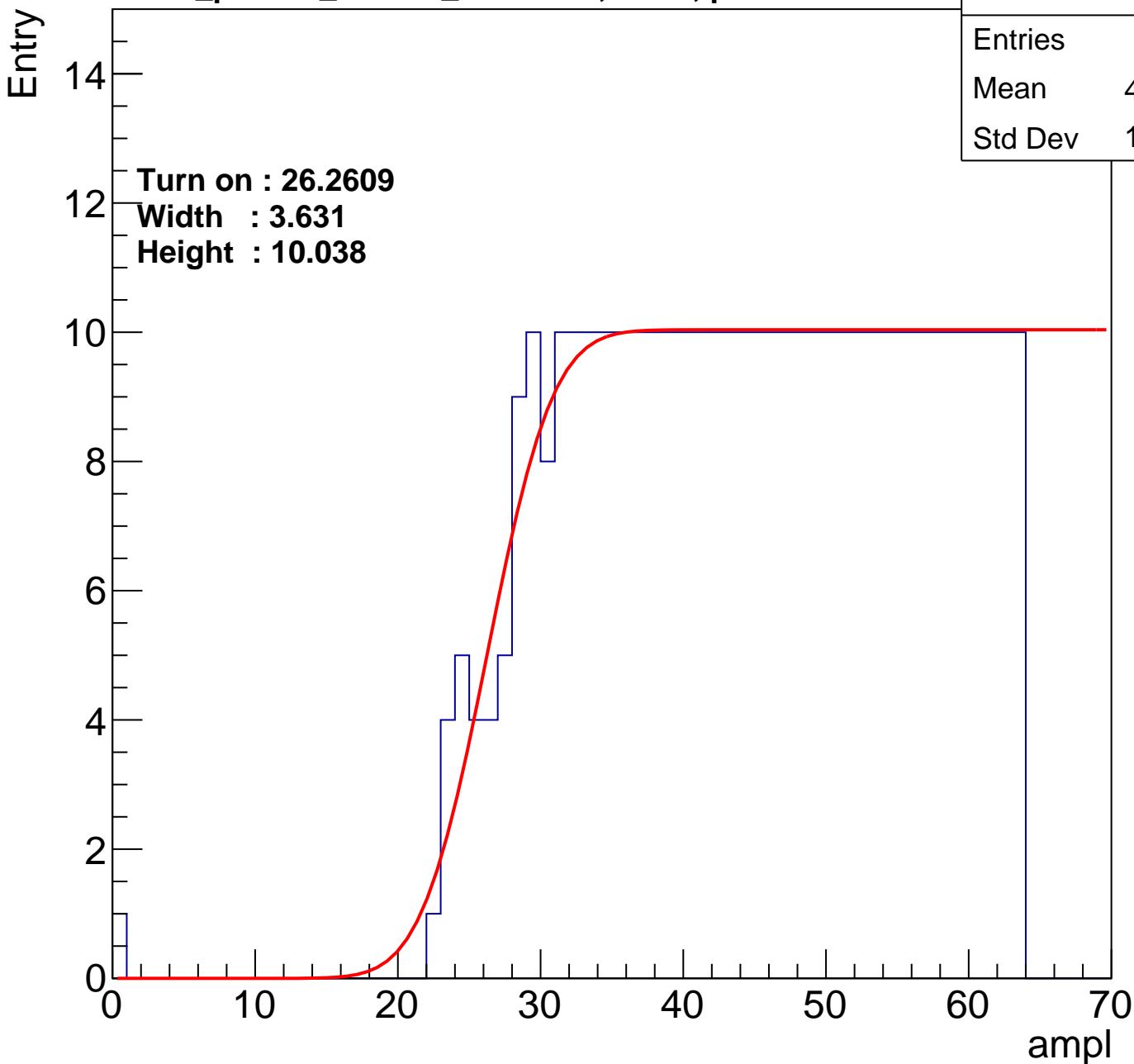
calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.27
Std Dev	11.38

Turn on : 26.2609

Width : 3.631

Height : 10.038



B0L002S, U11-ch96

calib_packv5_042523_0143.root, FC#8, port C1

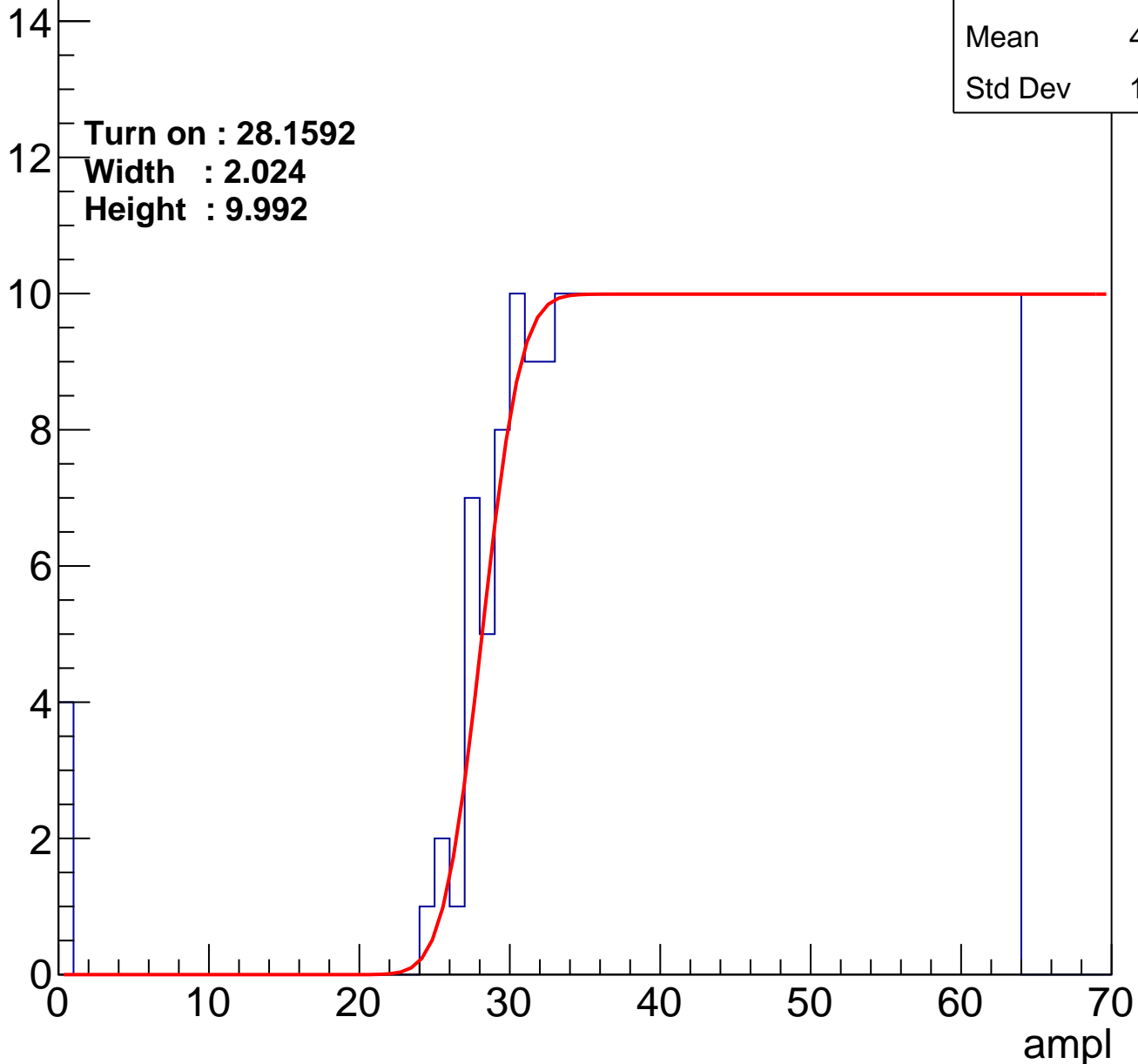
Entries	366
Mean	44.83
Std Dev	11.52

Turn on : 28.1592

Width : 2.024

Height : 9.992

Entry



B0L002S, U11-ch97

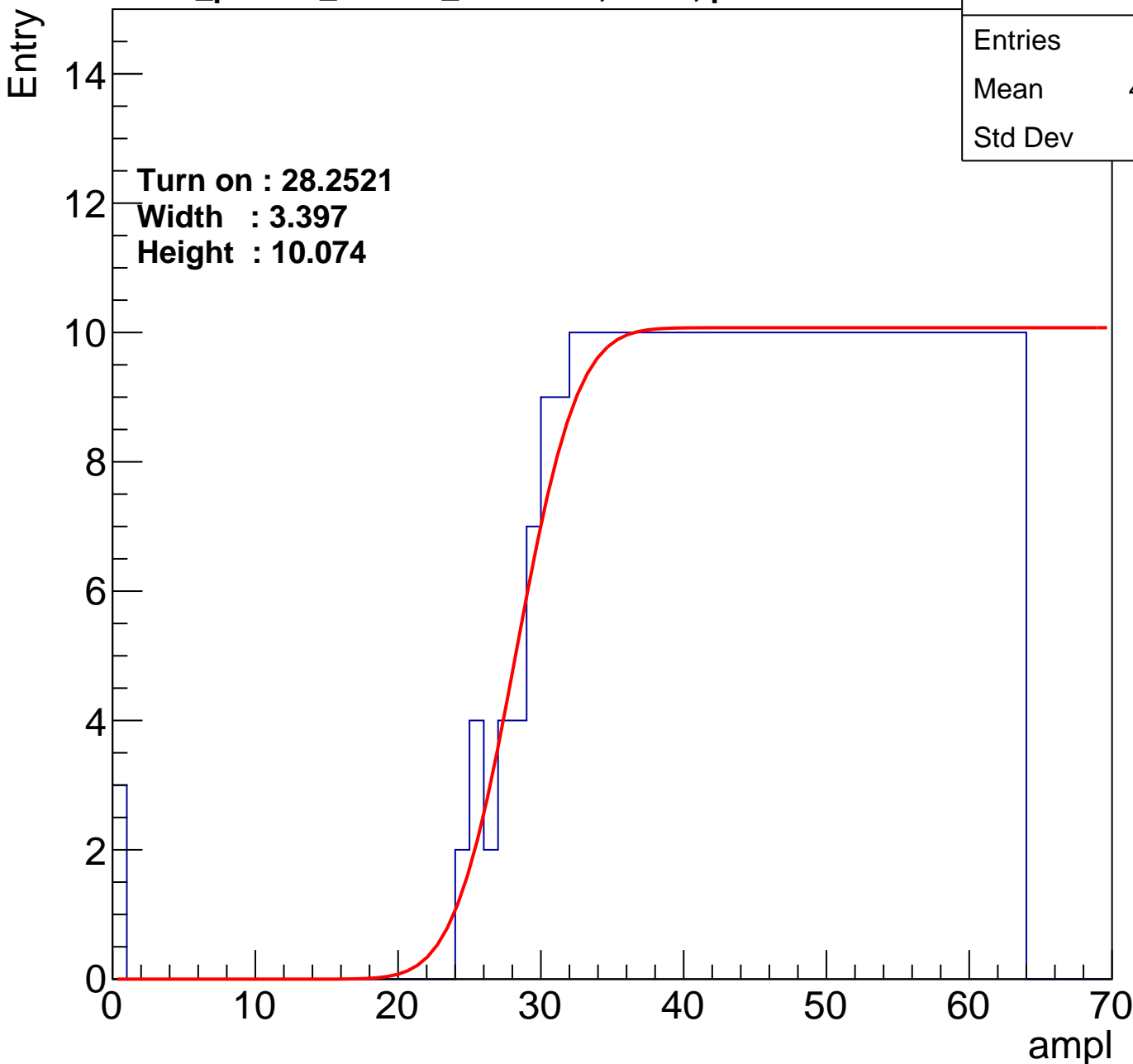
calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.98
Std Dev	11.31

Turn on : 28.2521

Width : 3.397

Height : 10.074



B0L002S, U11-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	43.88
Std Dev	11.88

Turn on : 26.0833

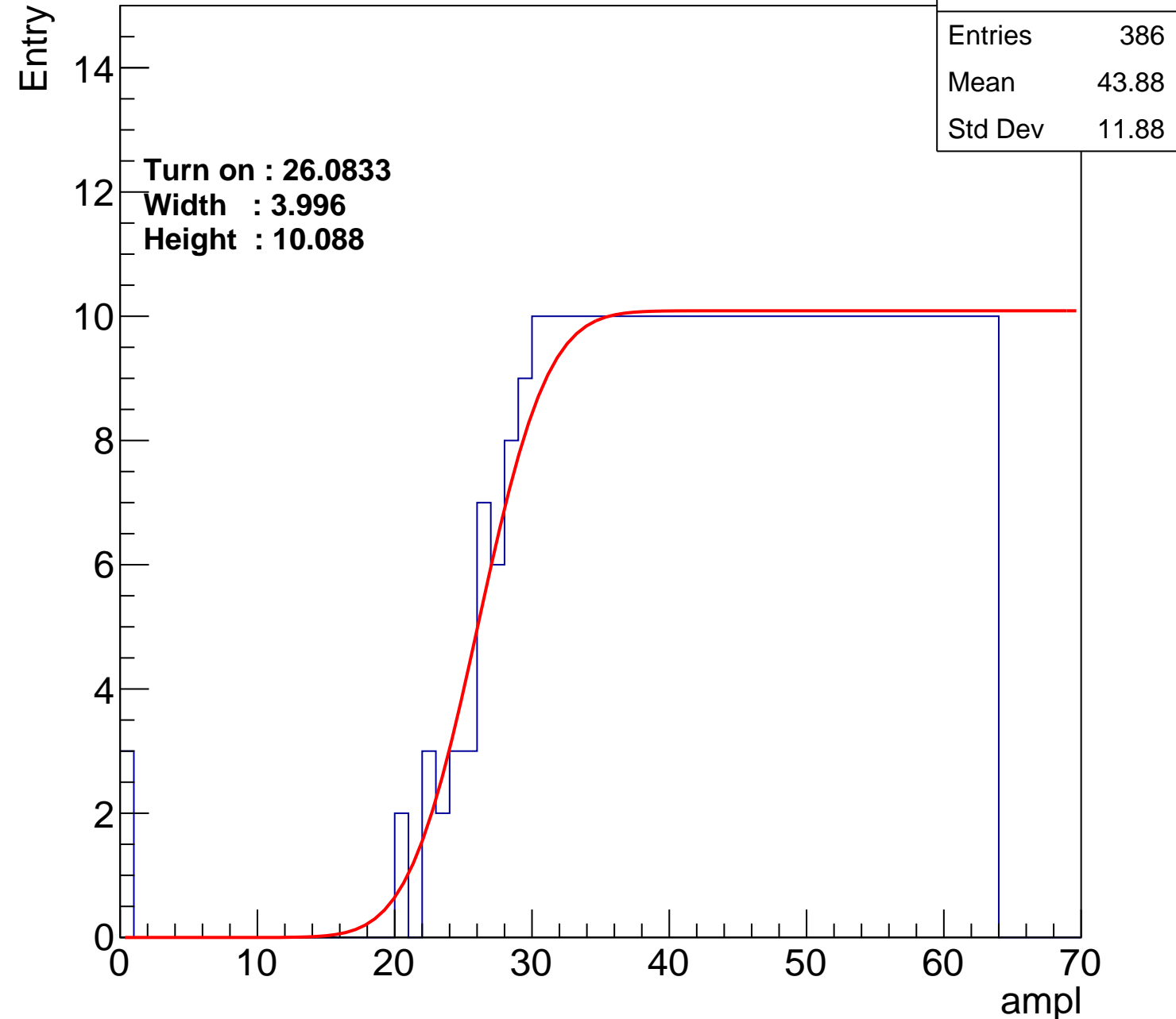
Width : 3.996

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.85
Std Dev	11.4

Turn on : 28.1747

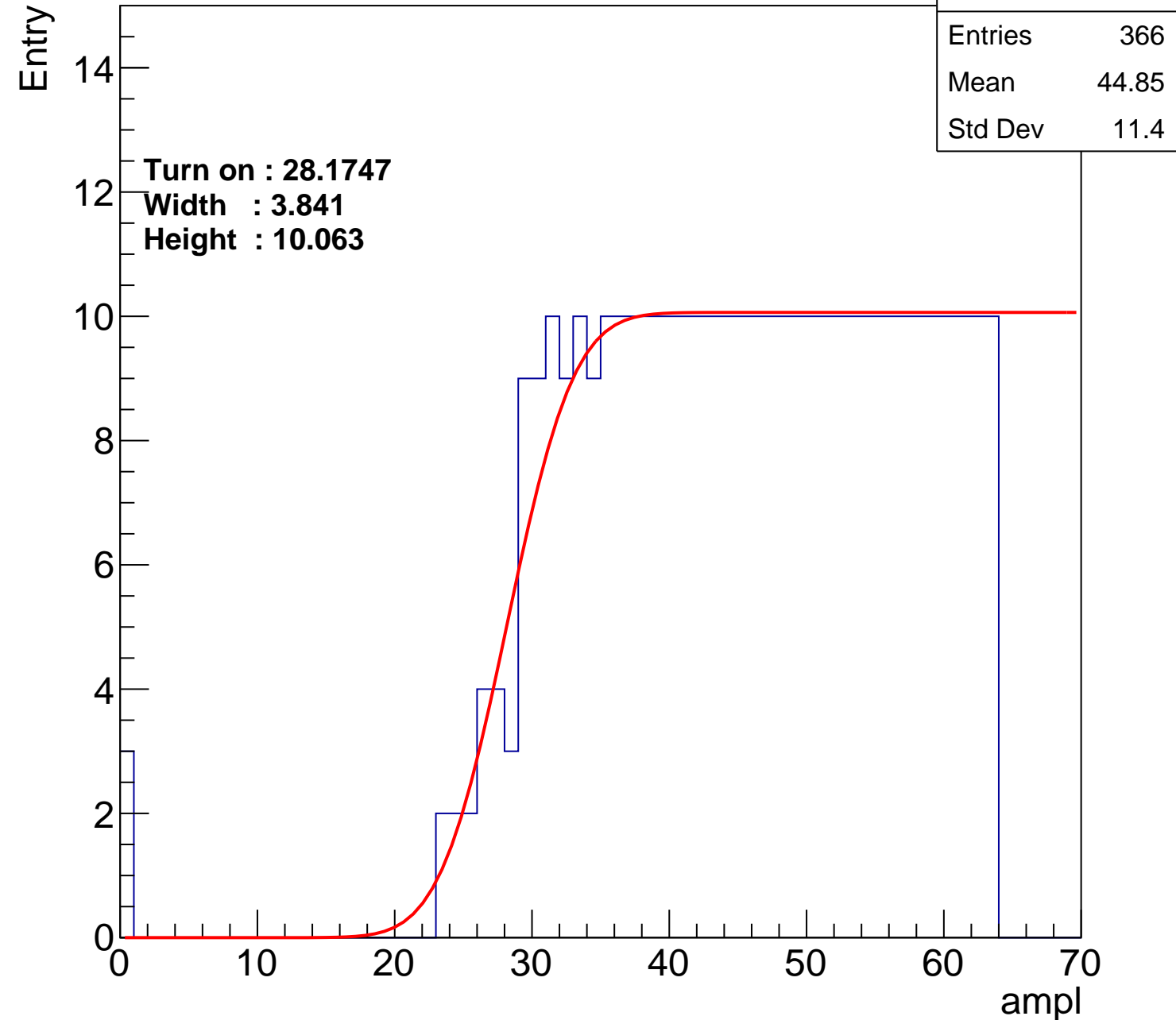
Width : 3.841

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	46
Std Dev	10.38

Turn on : 29.5491

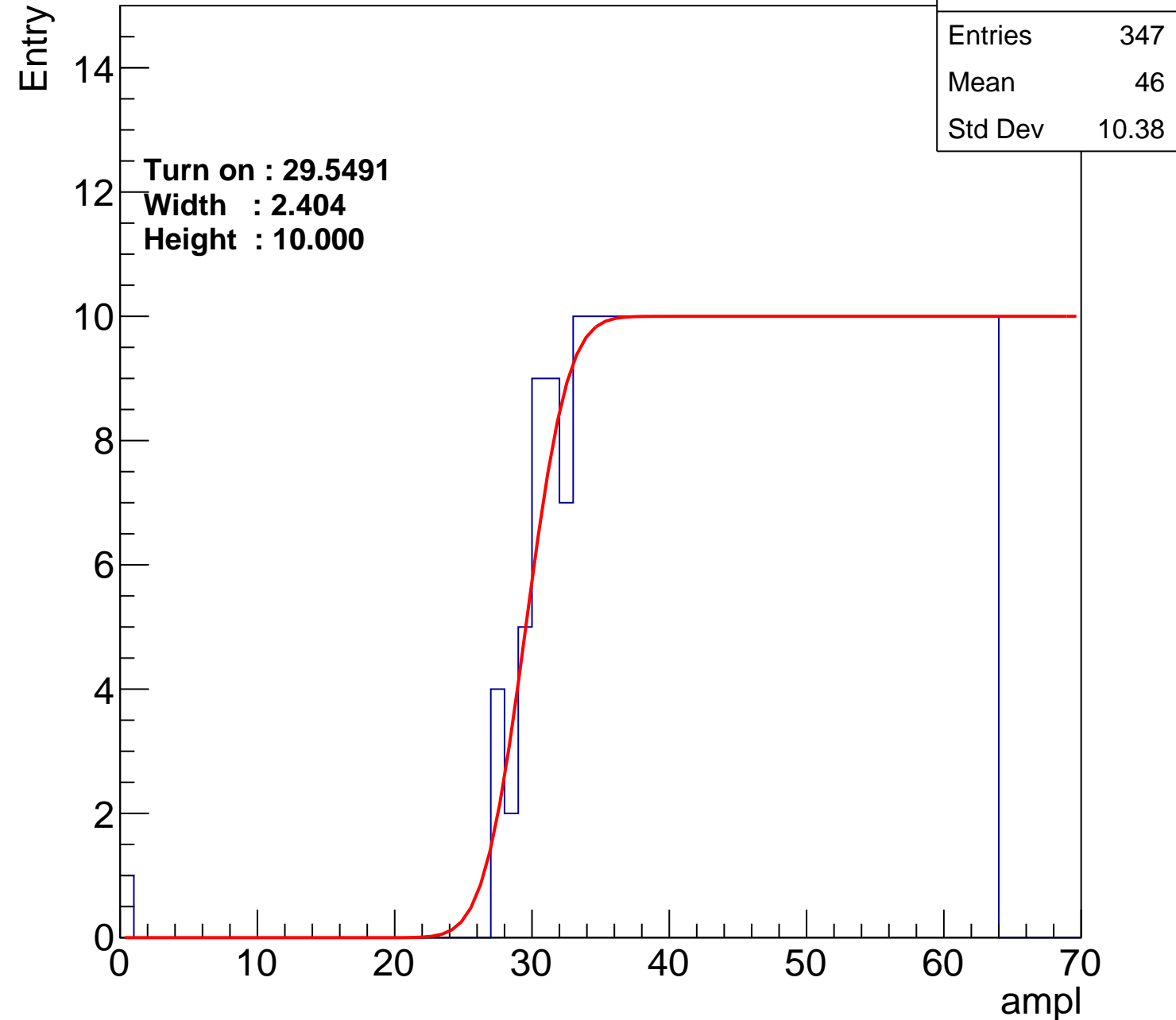
Width : 2.404

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	335
Mean	46.55
Std Dev	10.13

Turn on : 30.8116

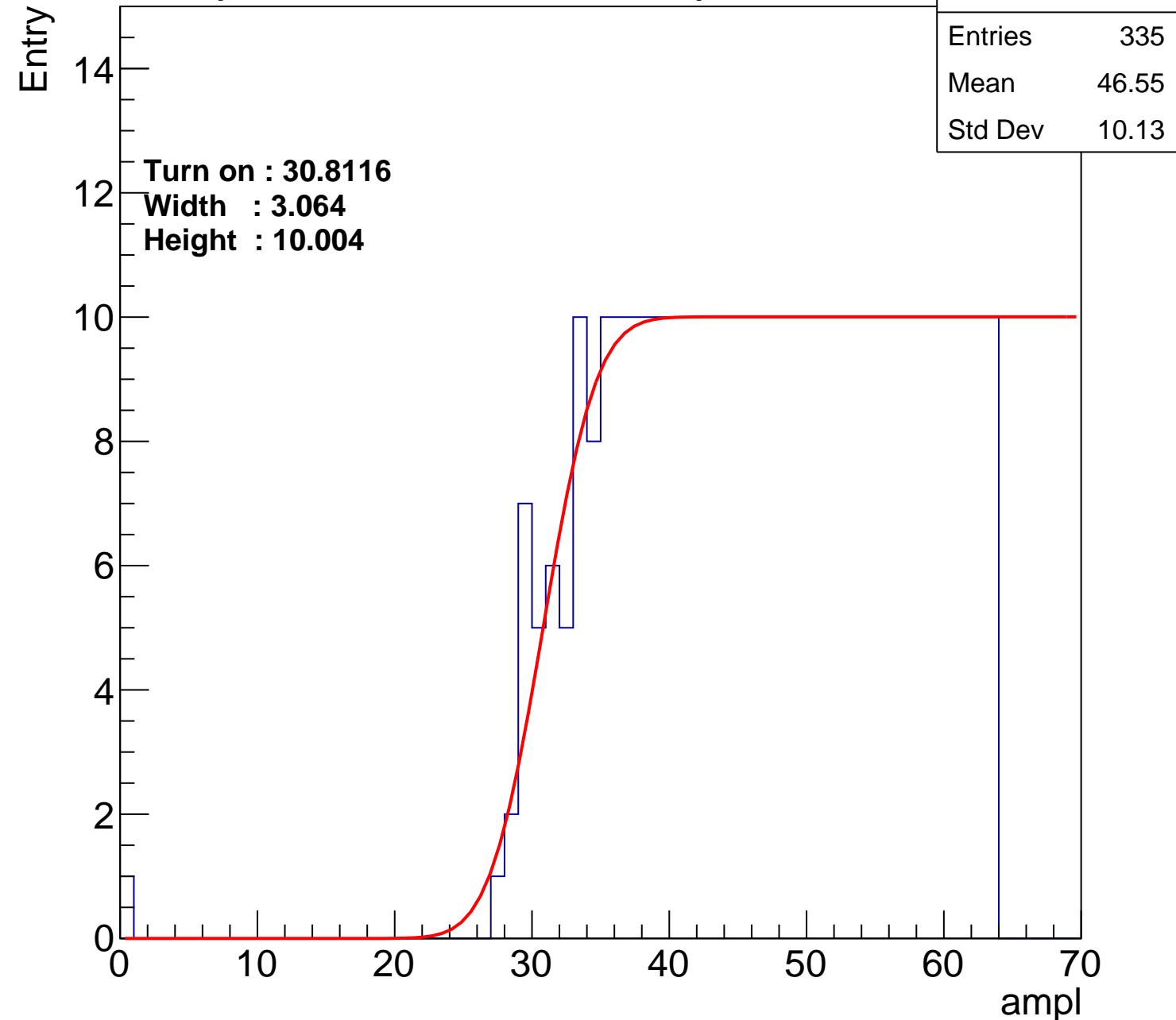
Width : 3.064

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 28.2374

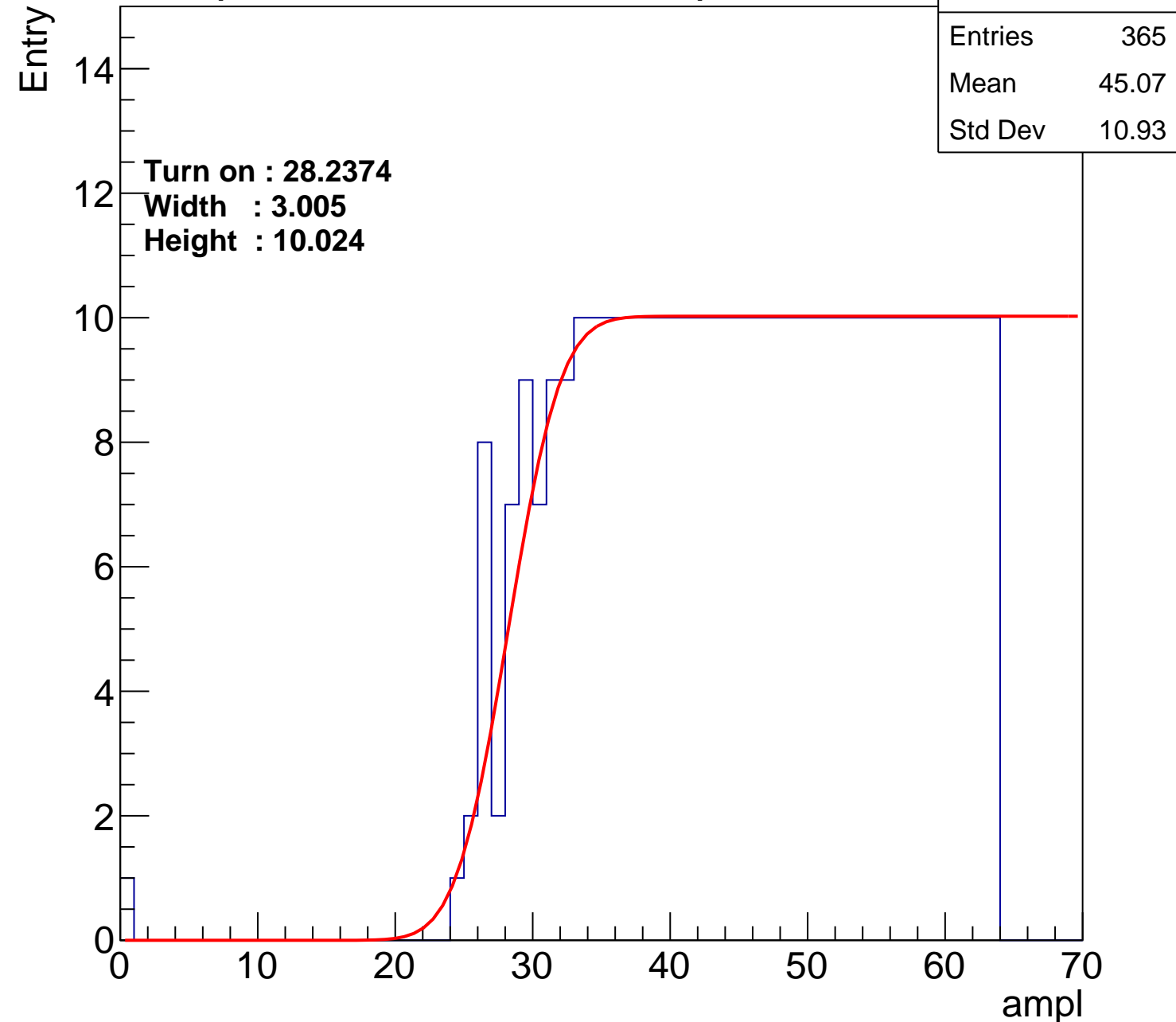
Width : 3.005

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch103

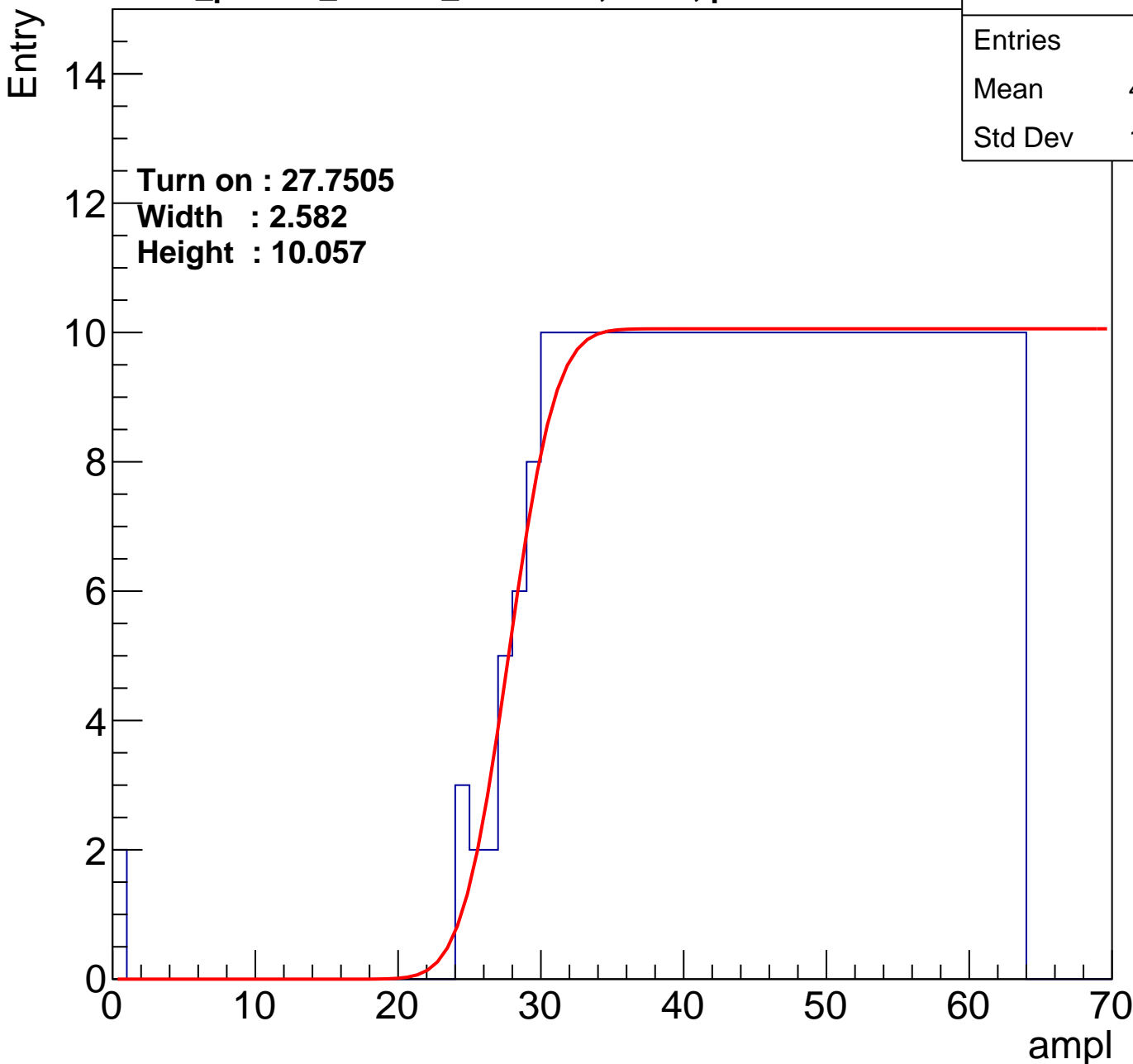
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.7505

Width : 2.582

Height : 10.057



B0L002S, U11-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
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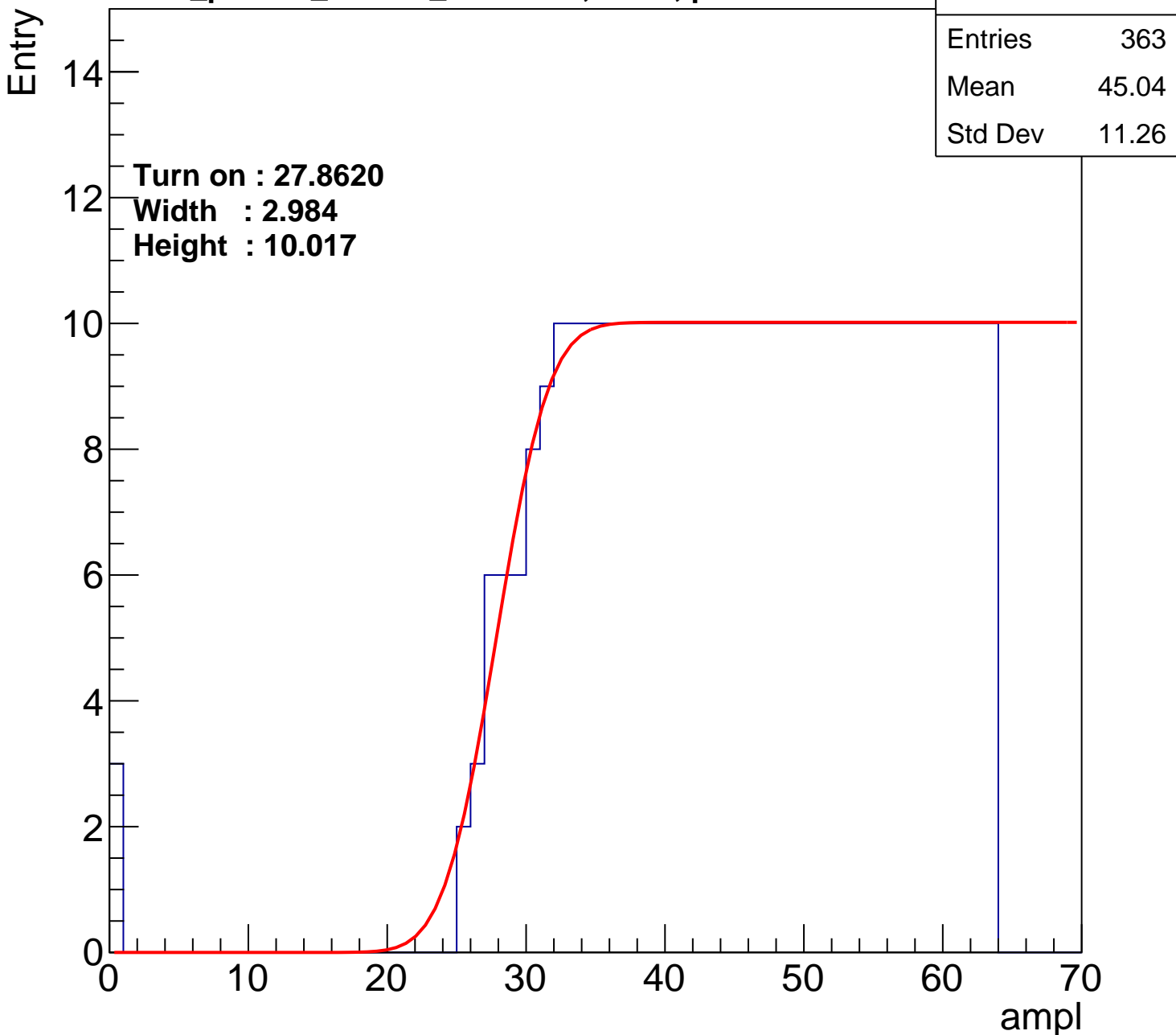
Mean	45.04
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Std Dev	11.26
---------	-------

Turn on : 27.8620

Width : 2.984

Height : 10.017



B0L002S, U11-ch105

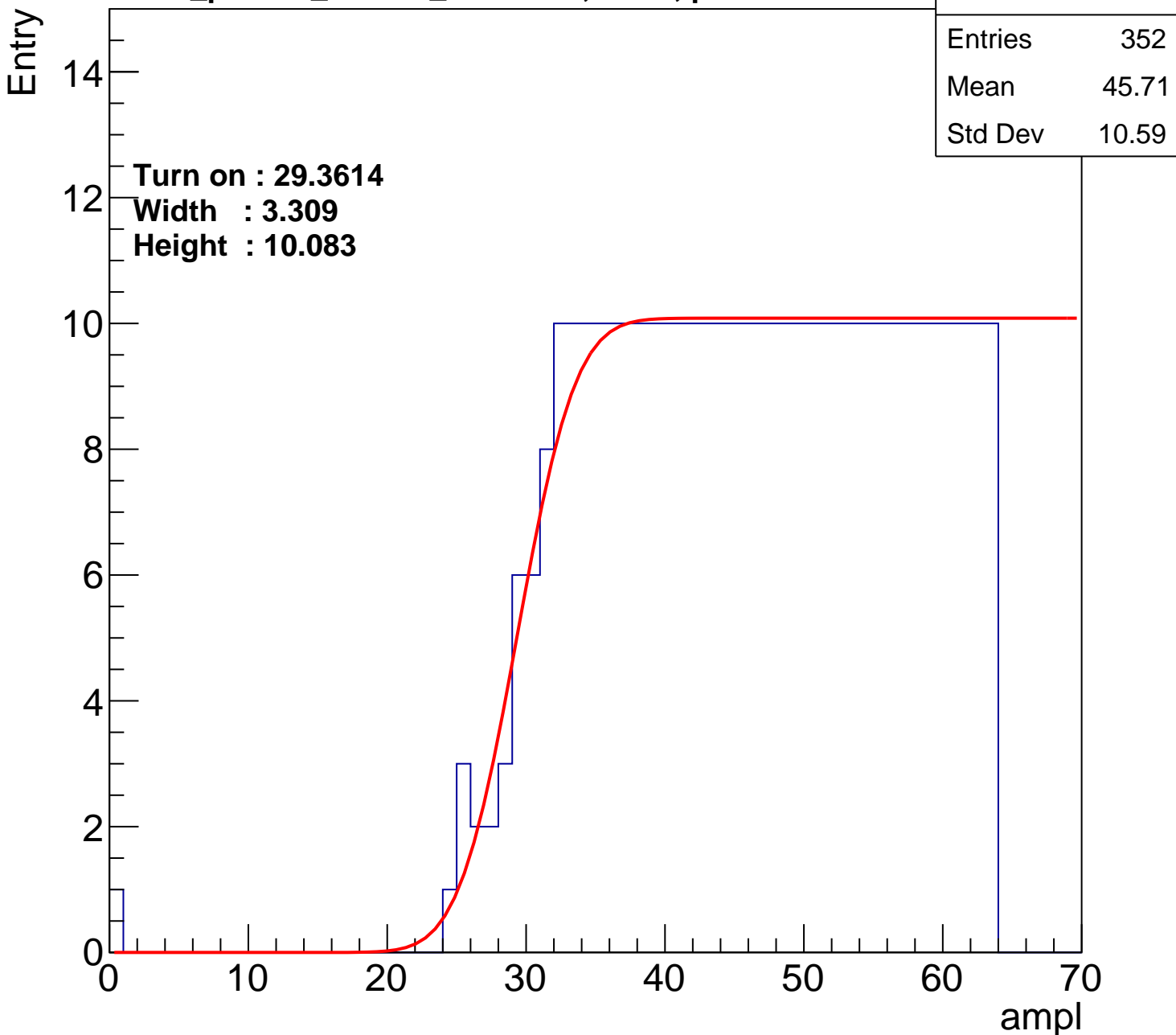
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 29.3614

Width : 3.309

Height : 10.083

Entries	352
Mean	45.71
Std Dev	10.59



B0L002S, U11-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	45.01
Std Dev	10.93

Turn on : 27.3804

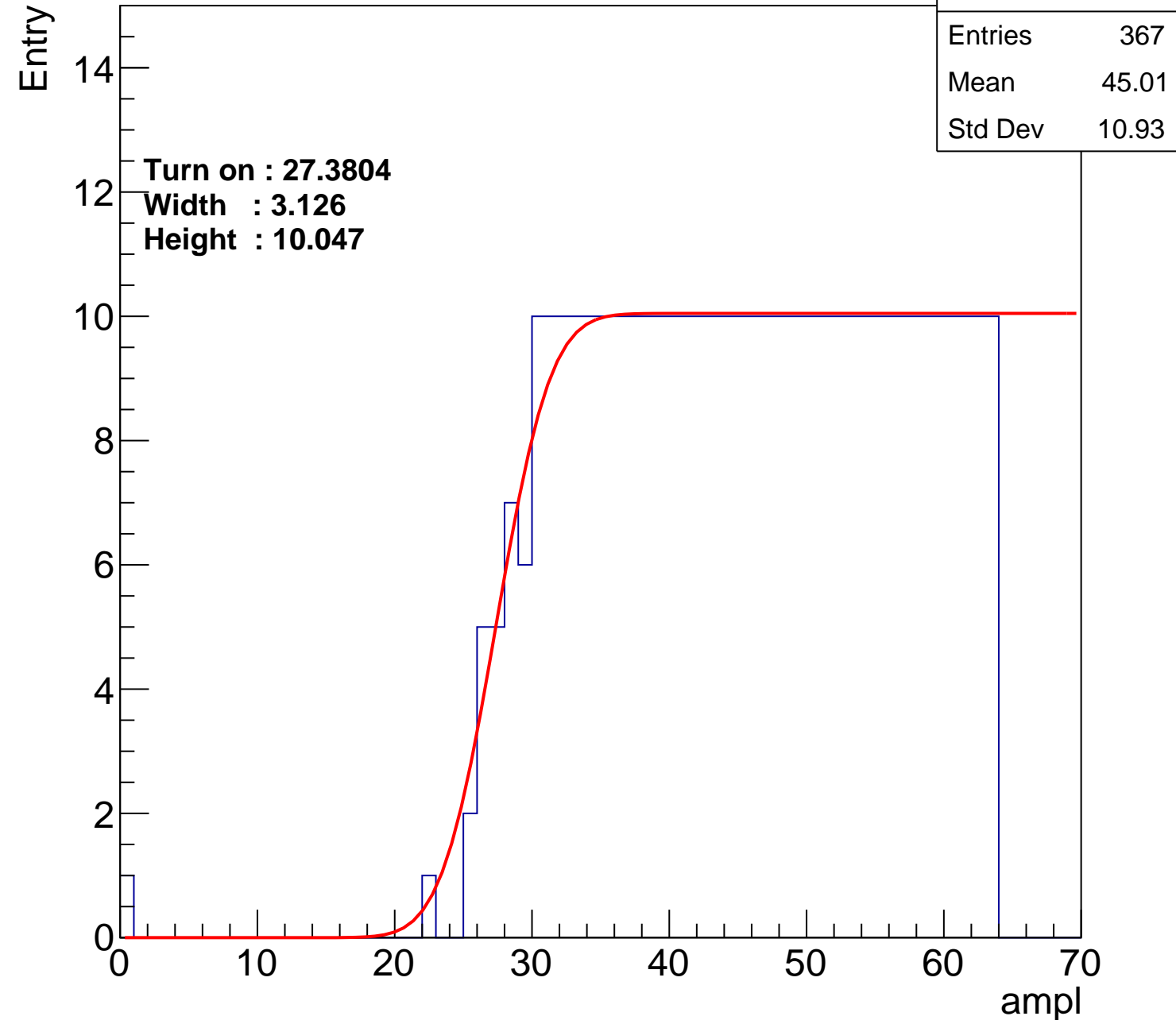
Width : 3.126

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch107

calib_packv5_042523_0143.root, FC#8, port C1

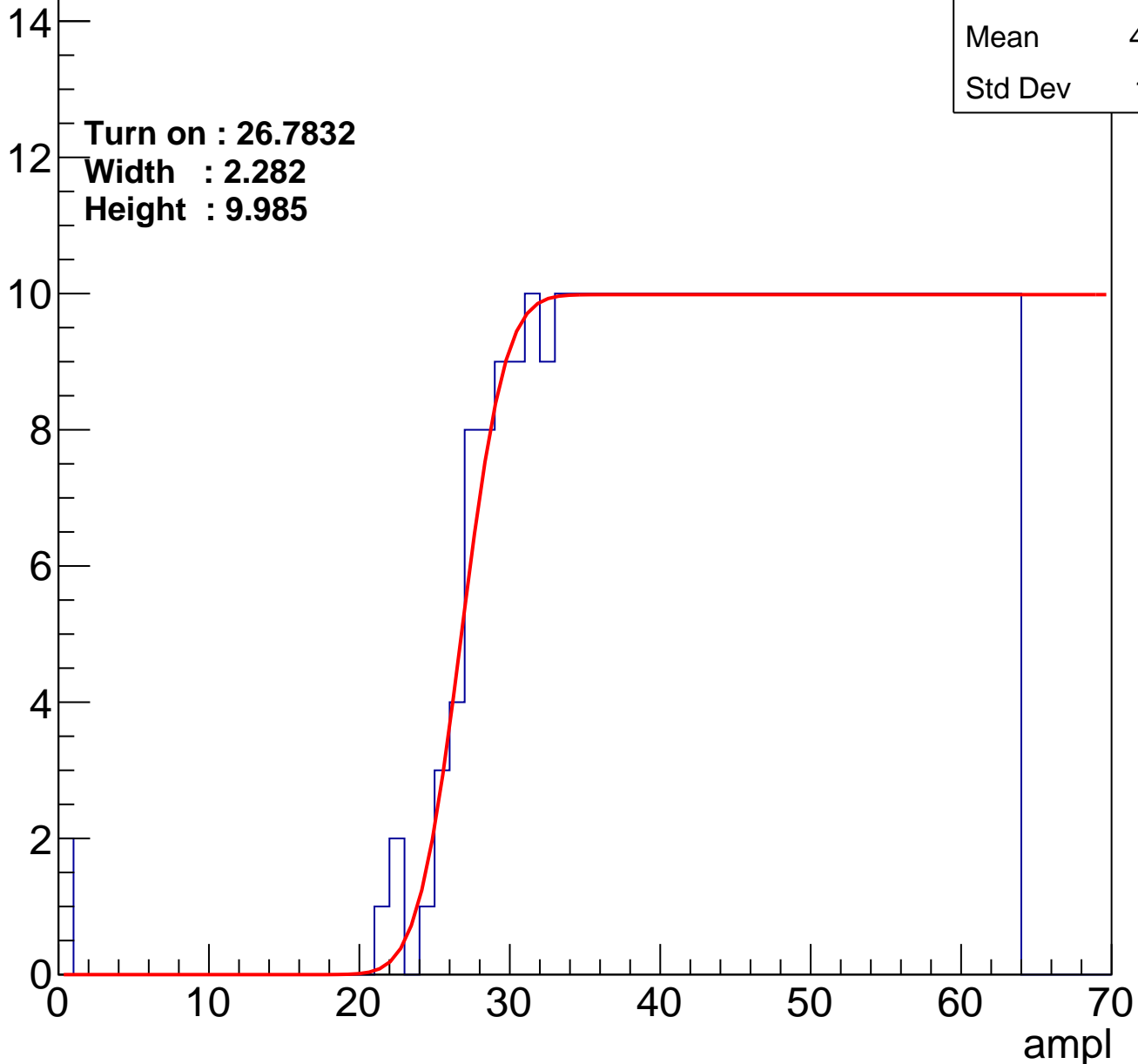
Entries	376
Mean	44.46
Std Dev	11.41

Turn on : 26.7832

Width : 2.282

Height : 9.985

Entry



B0L002S, U11-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.83
Std Dev	10.56

Turn on : 29.8472

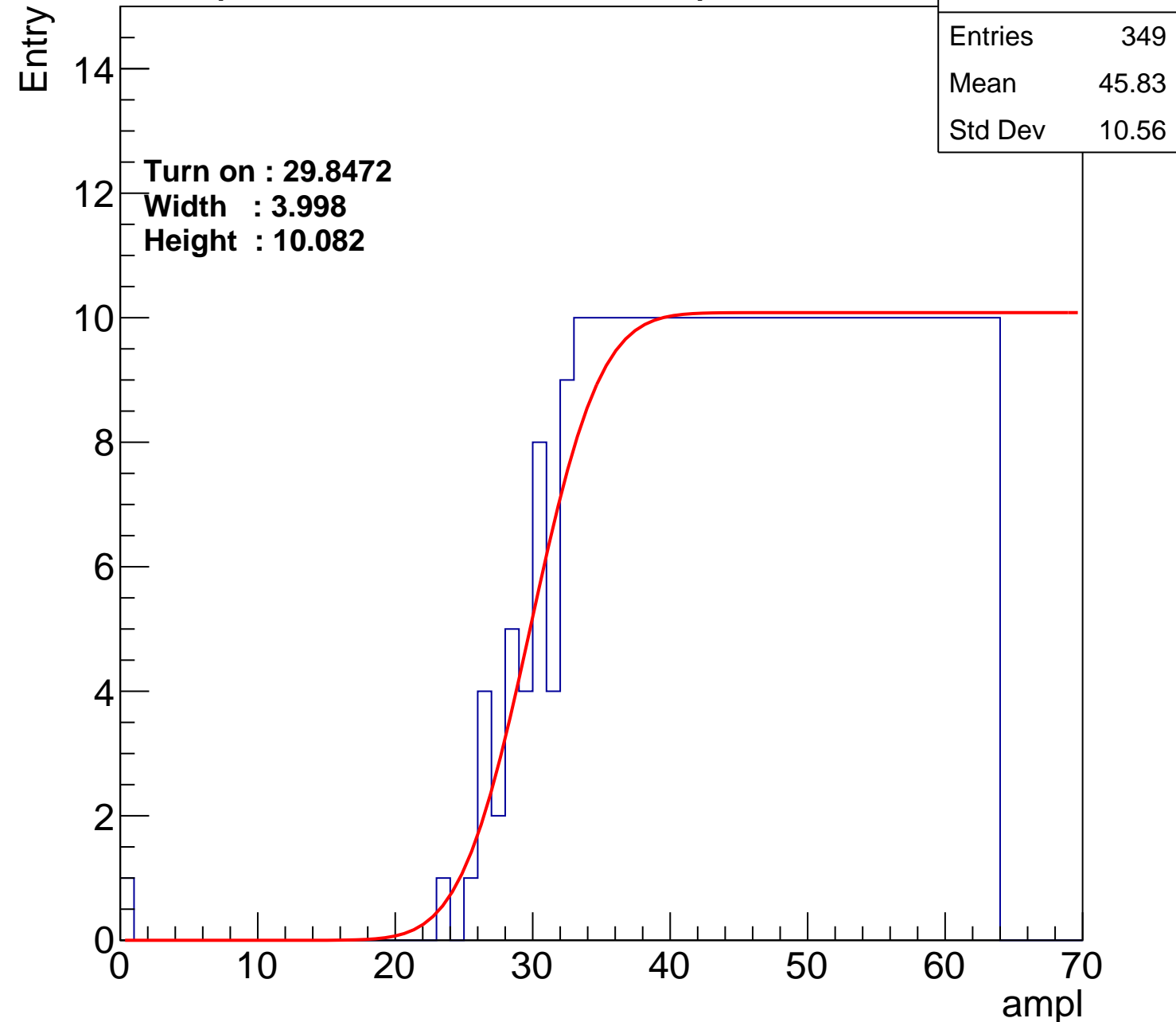
Width : 3.998

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 27.1465

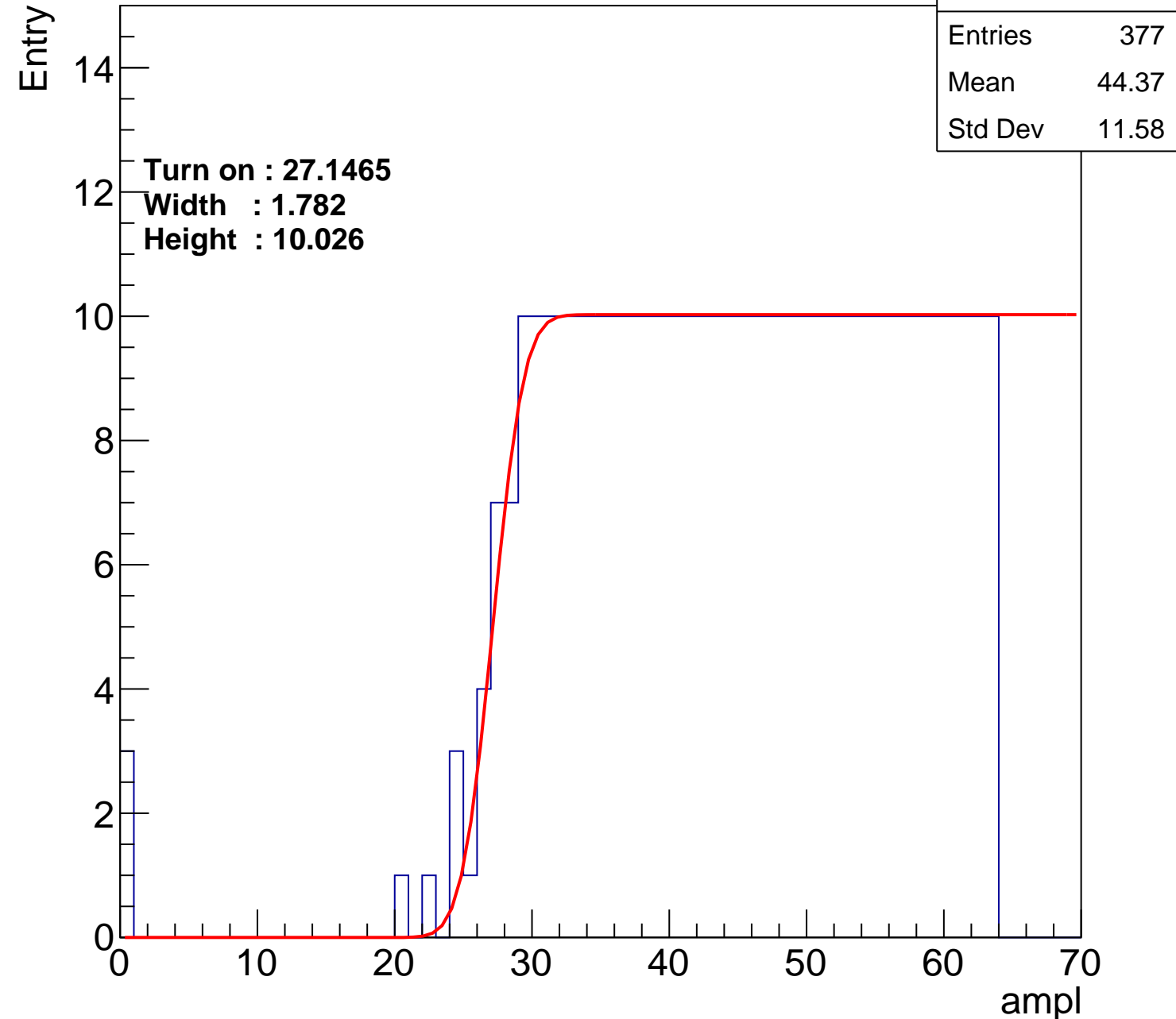
Width : 1.782

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch110

calib_packv5_042523_0143.root, FC#8, port C1

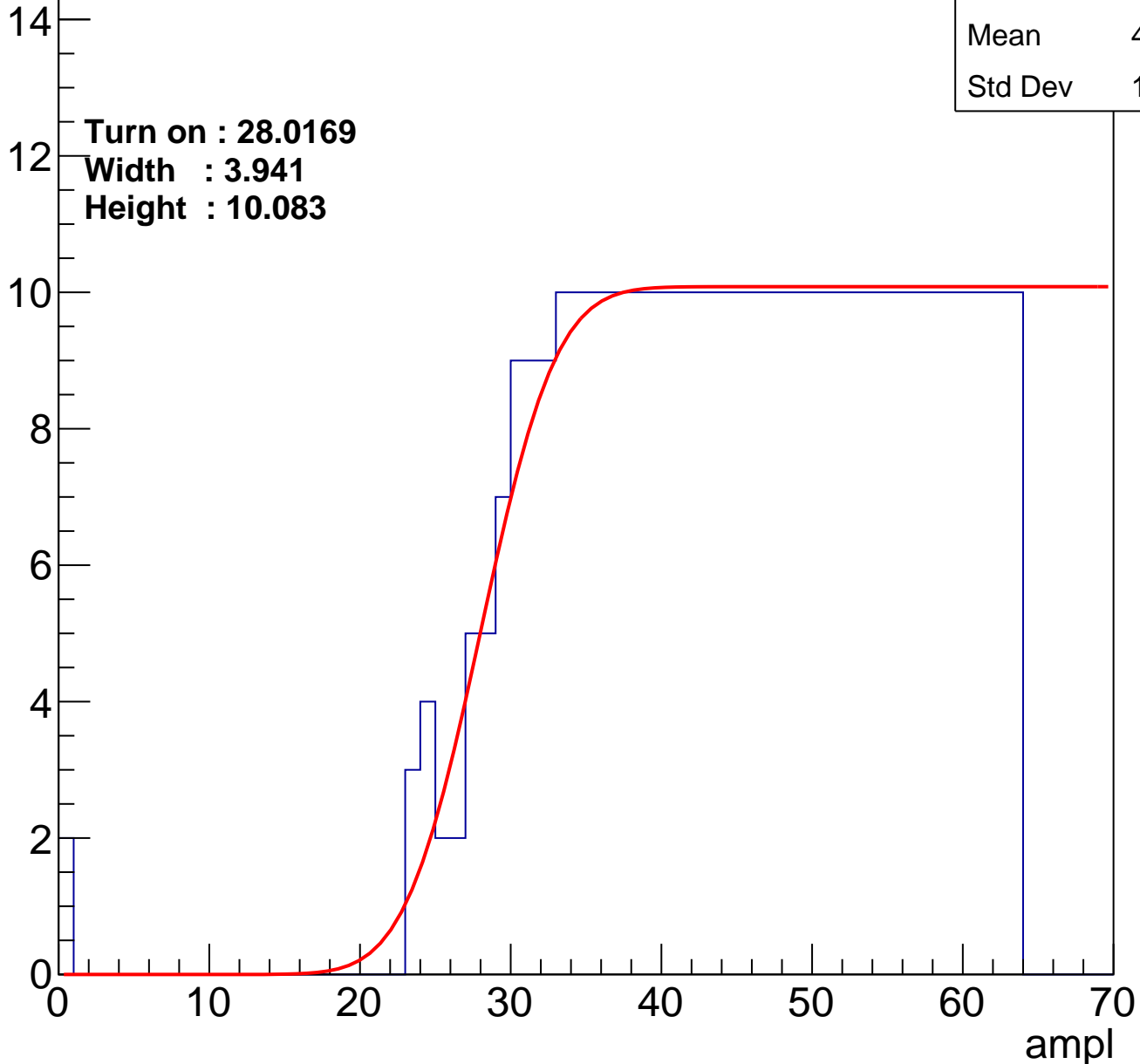
Entries	367
Mean	44.86
Std Dev	11.26

Turn on : 28.0169

Width : 3.941

Height : 10.083

Entry



B0L002S, U11-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.73
Std Dev	10.77

Turn on : 28.9880

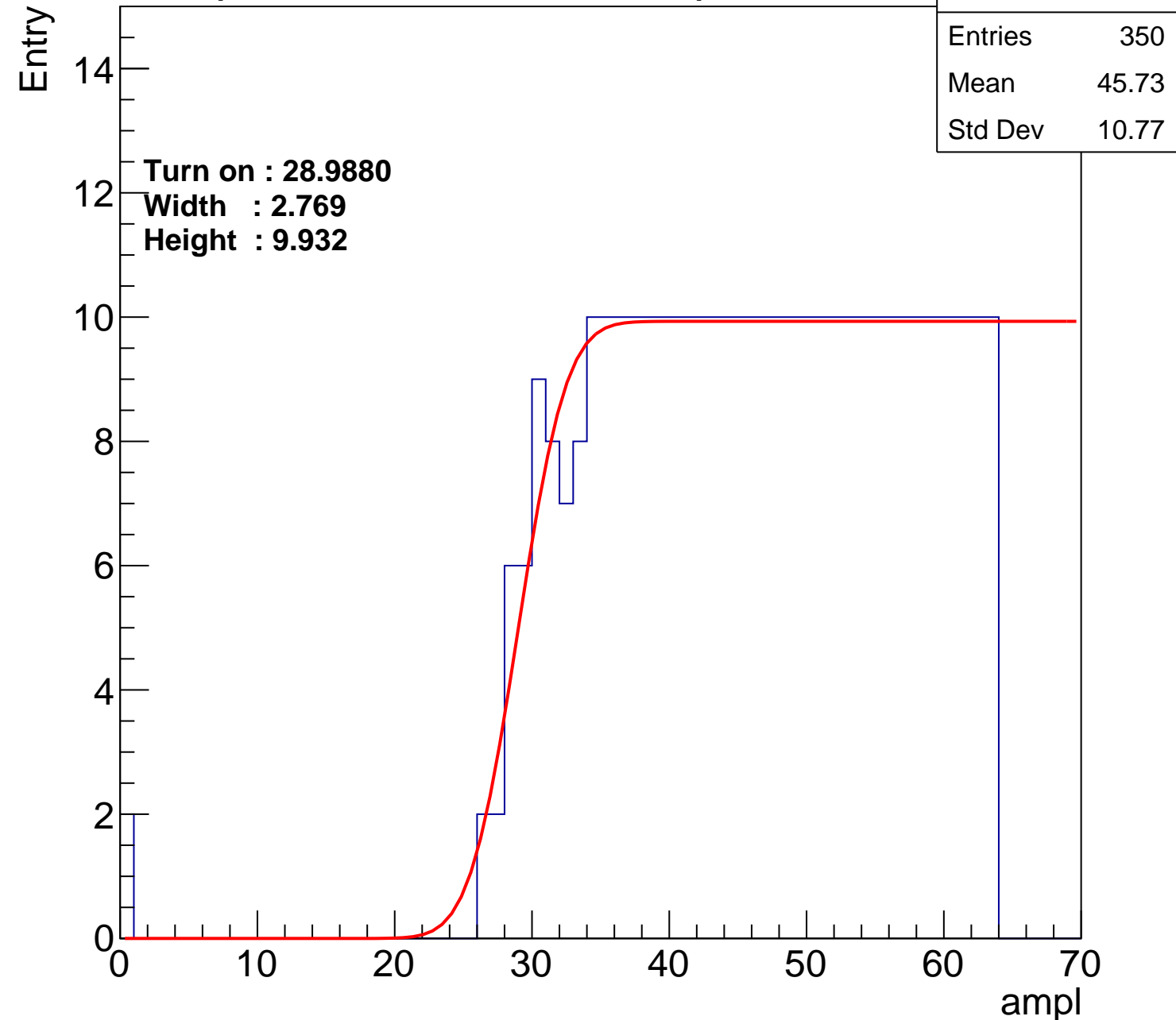
Width : 2.769

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.95
Std Dev	10.64

Turn on : 29.9378

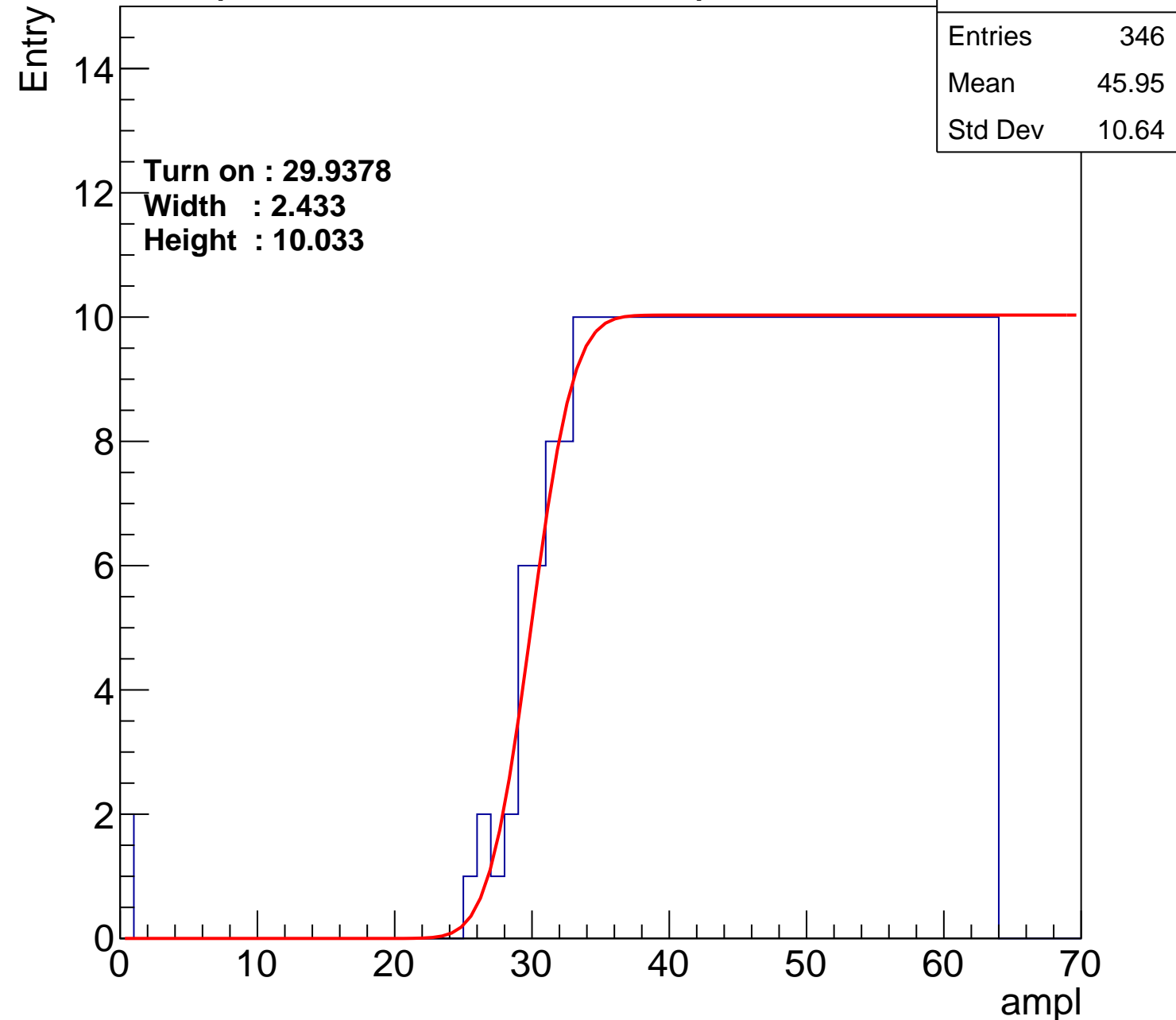
Width : 2.433

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.67
Std Dev	11.48

Turn on : 27.5060

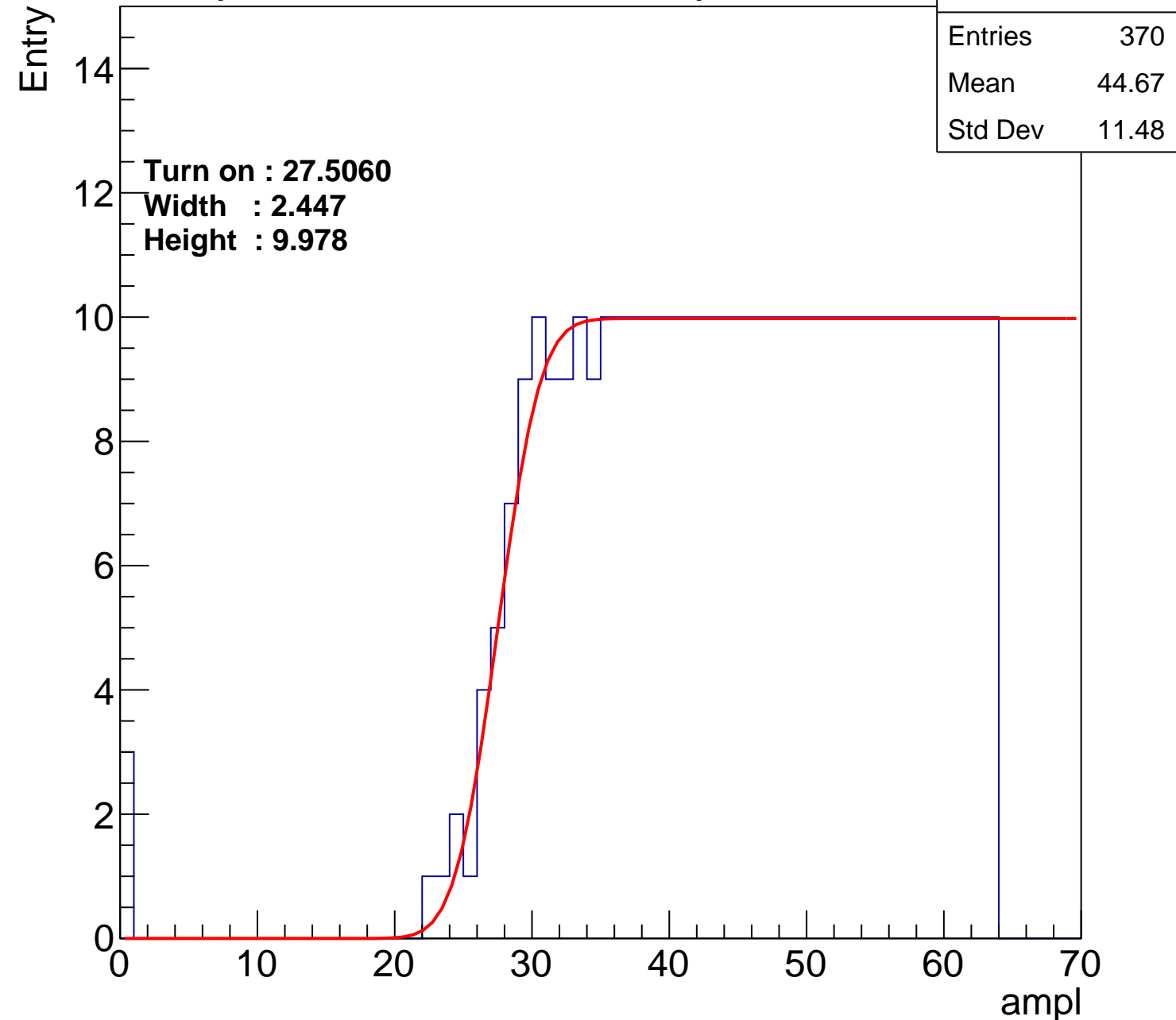
Width : 2.447

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch114

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.46
Std Dev	11.03

Turn on : 28.9023

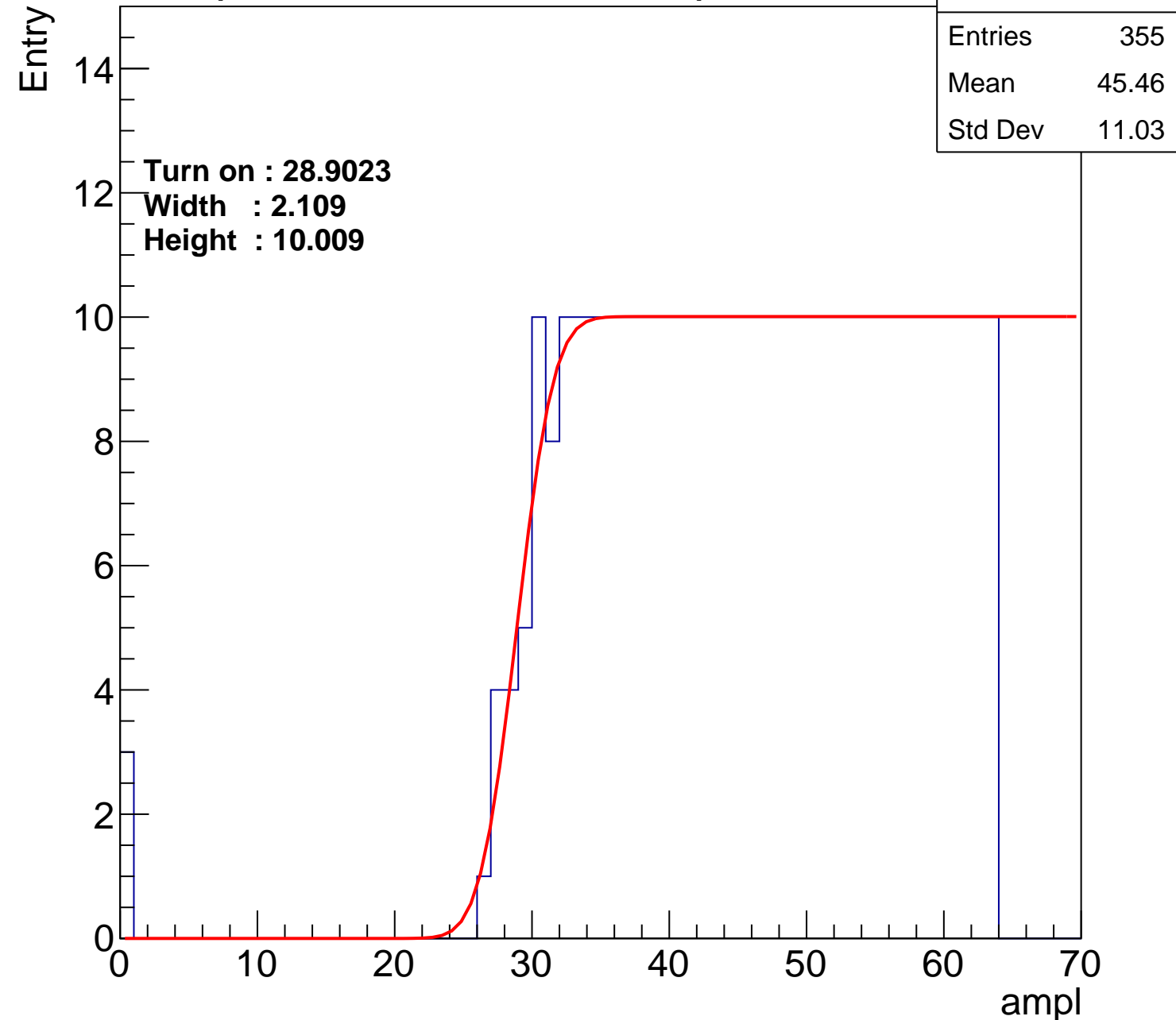
Width : 2.109

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	45.01
Std Dev	10.91

Turn on : 27.1803

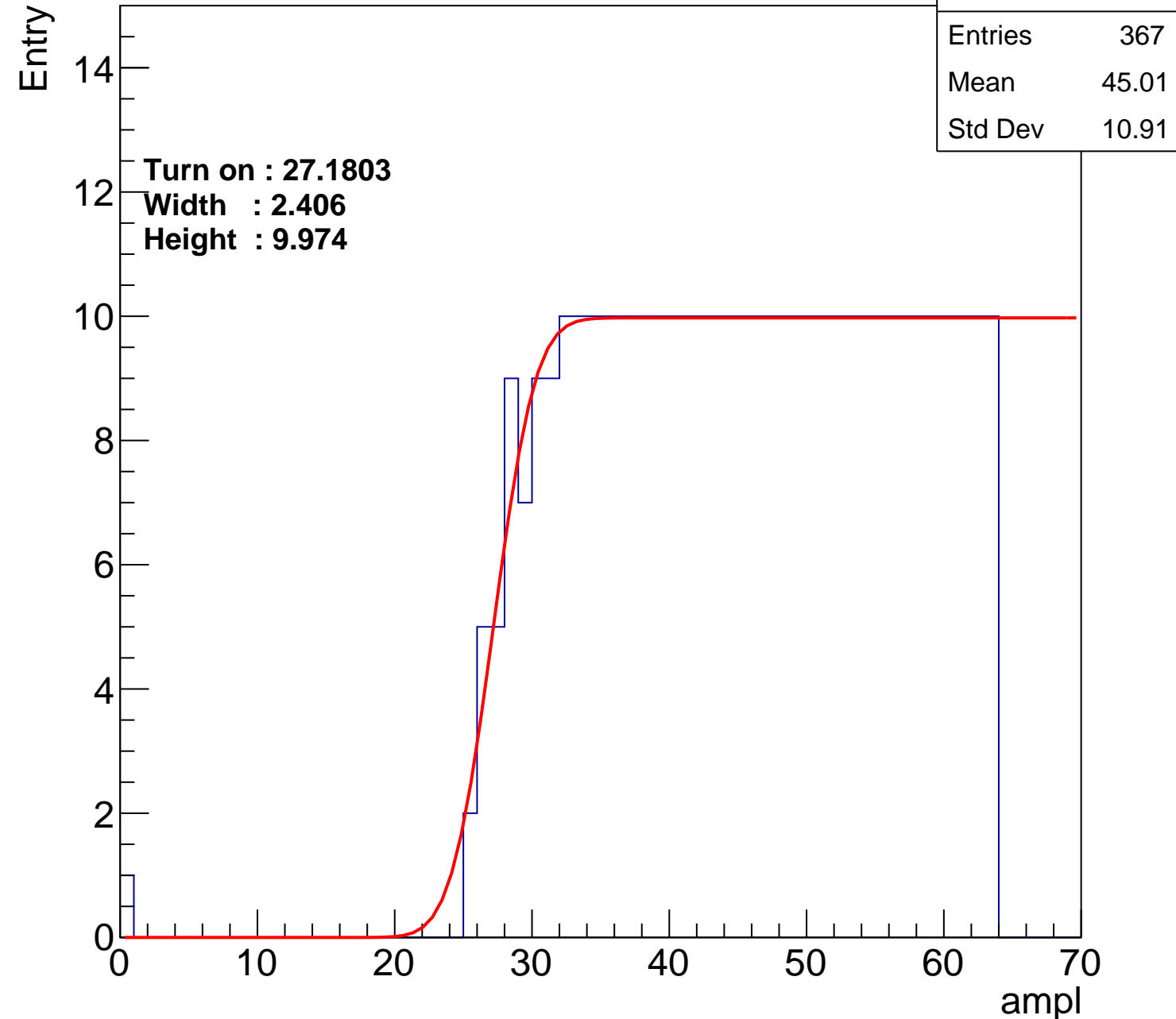
Width : 2.406

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.18
Std Dev	10.87

Turn on : 27.7995

Width : 2.264

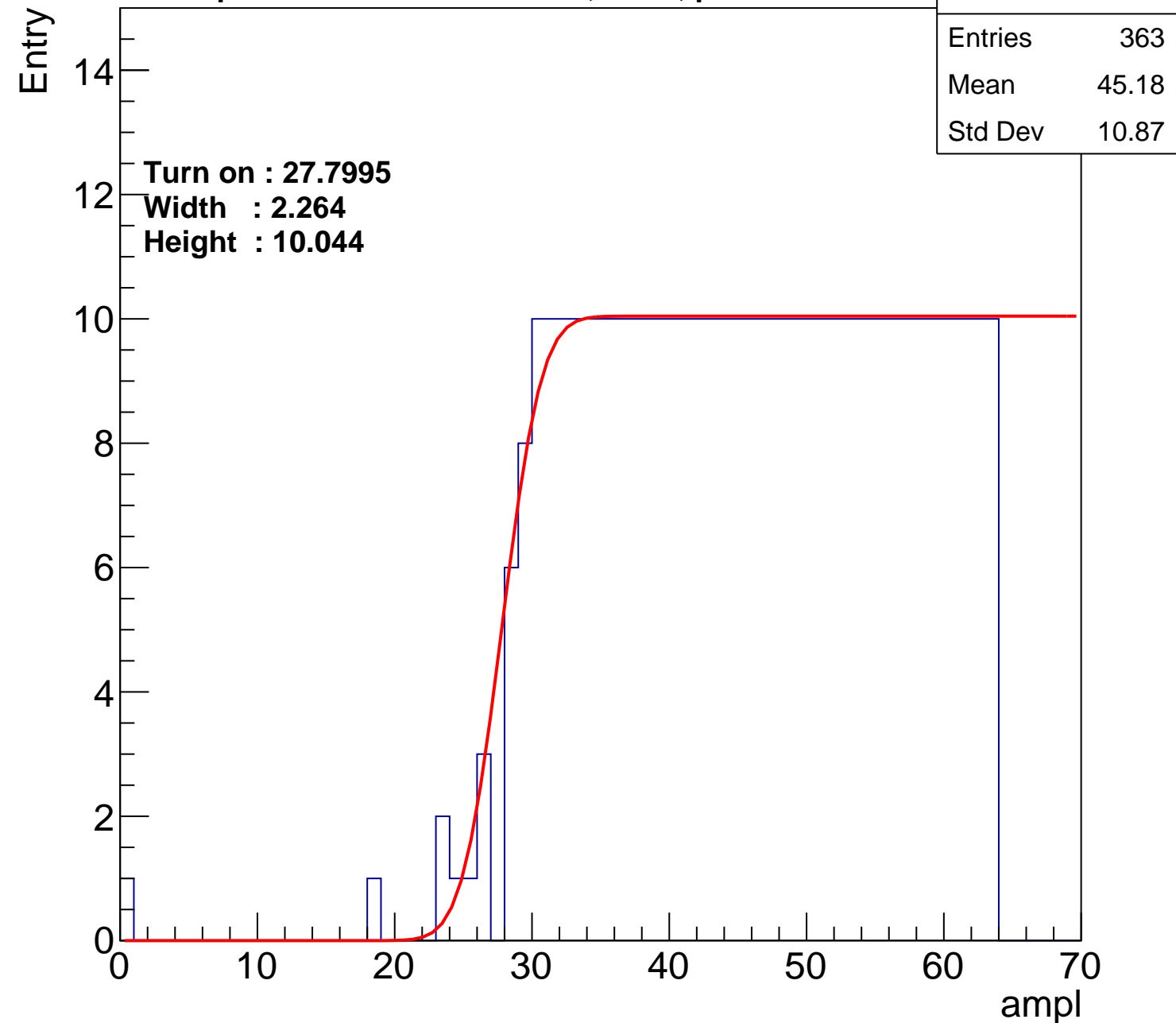
Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U11-ch117

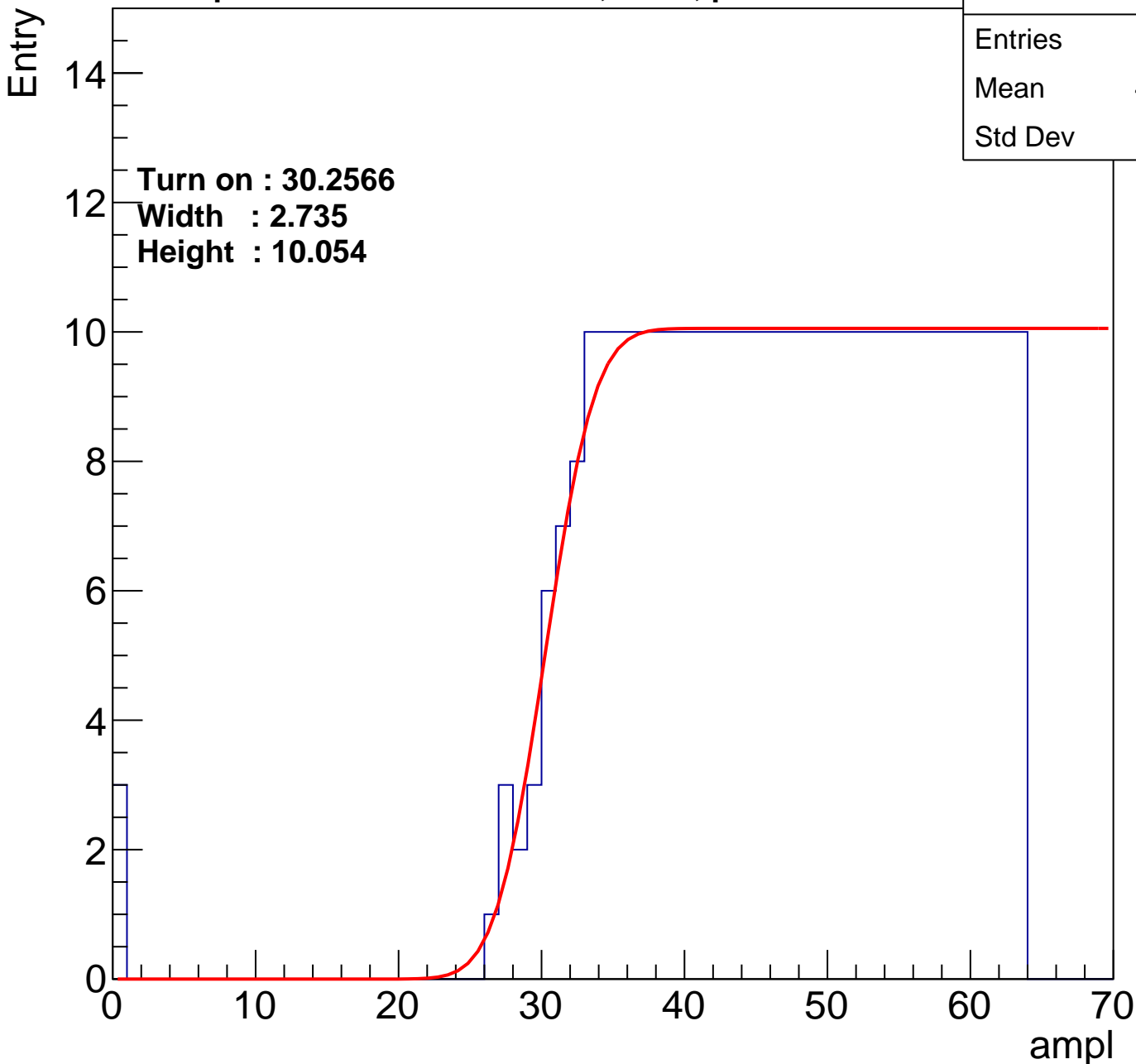
calib_packv5_042523_0143.root, FC#8, port C1

Entries	343
Mean	46.01
Std Dev	10.81

Turn on : 30.2566

Width : 2.735

Height : 10.054



B0L002S, U11-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.29
Std Dev	10.99

Turn on : 29.0534

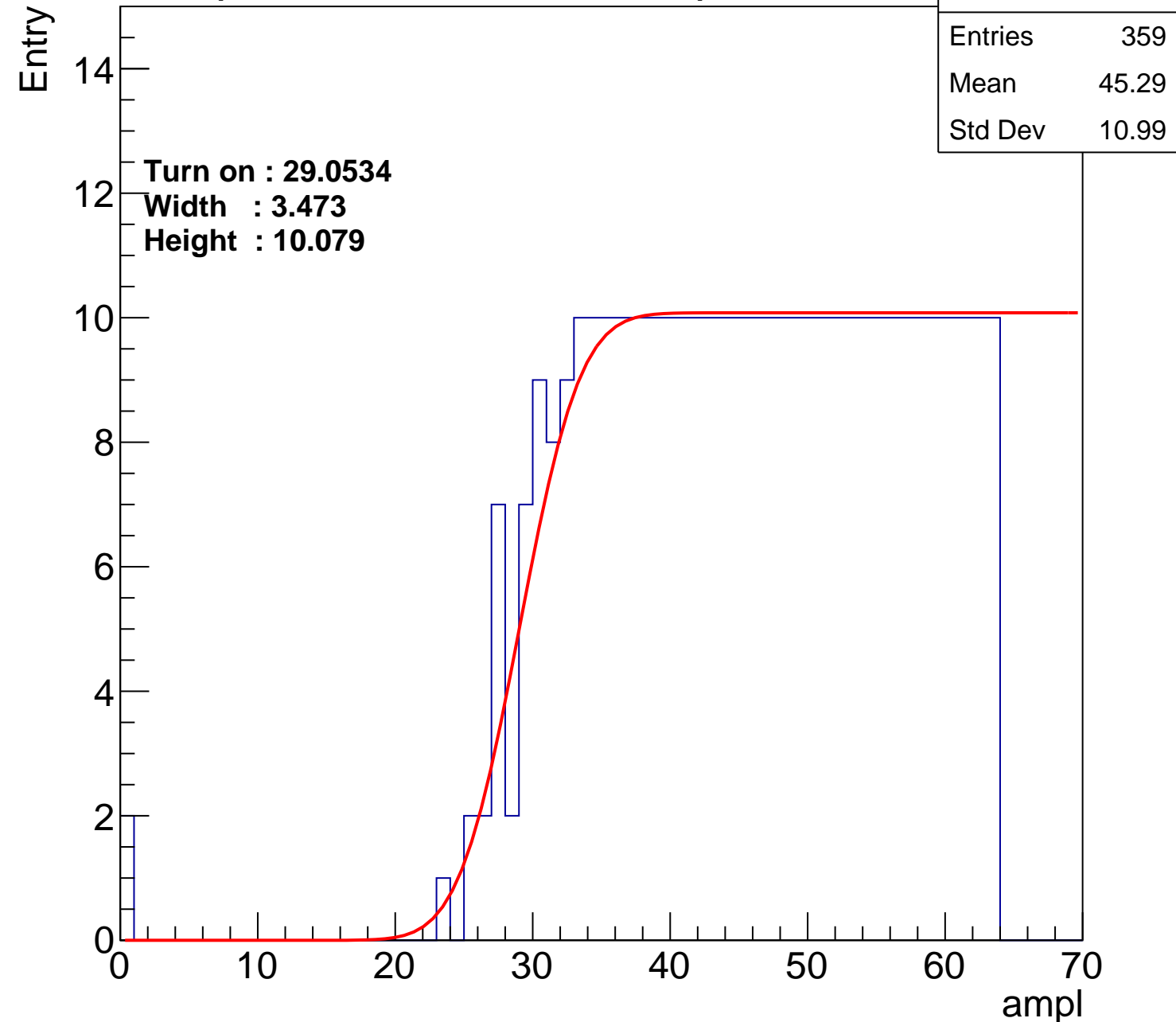
Width : 3.473

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.41
Std Dev	11.54

Turn on : 26.5949

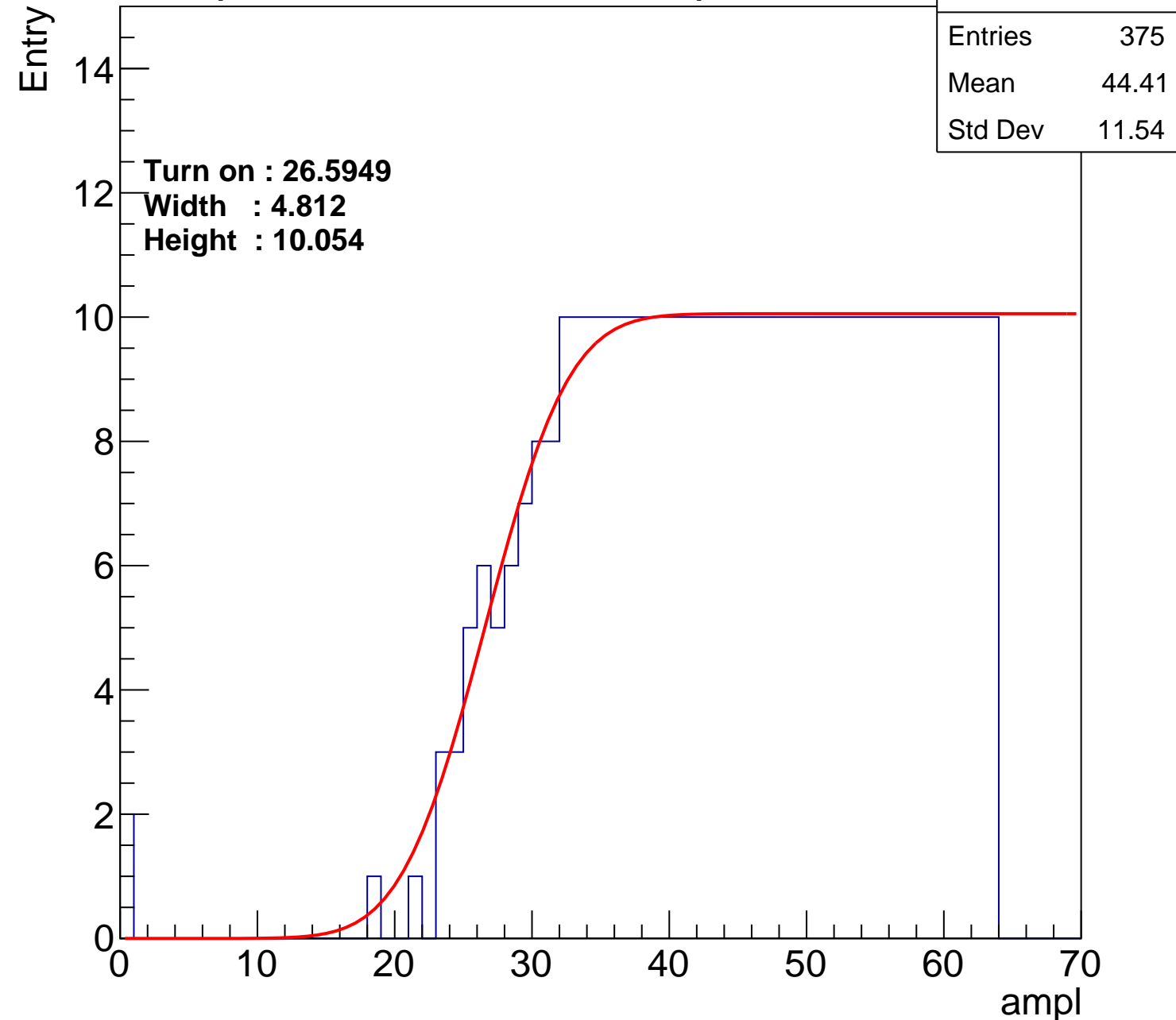
Width : 4.812

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.42
Std Dev	10.89

Turn on : 28.5748

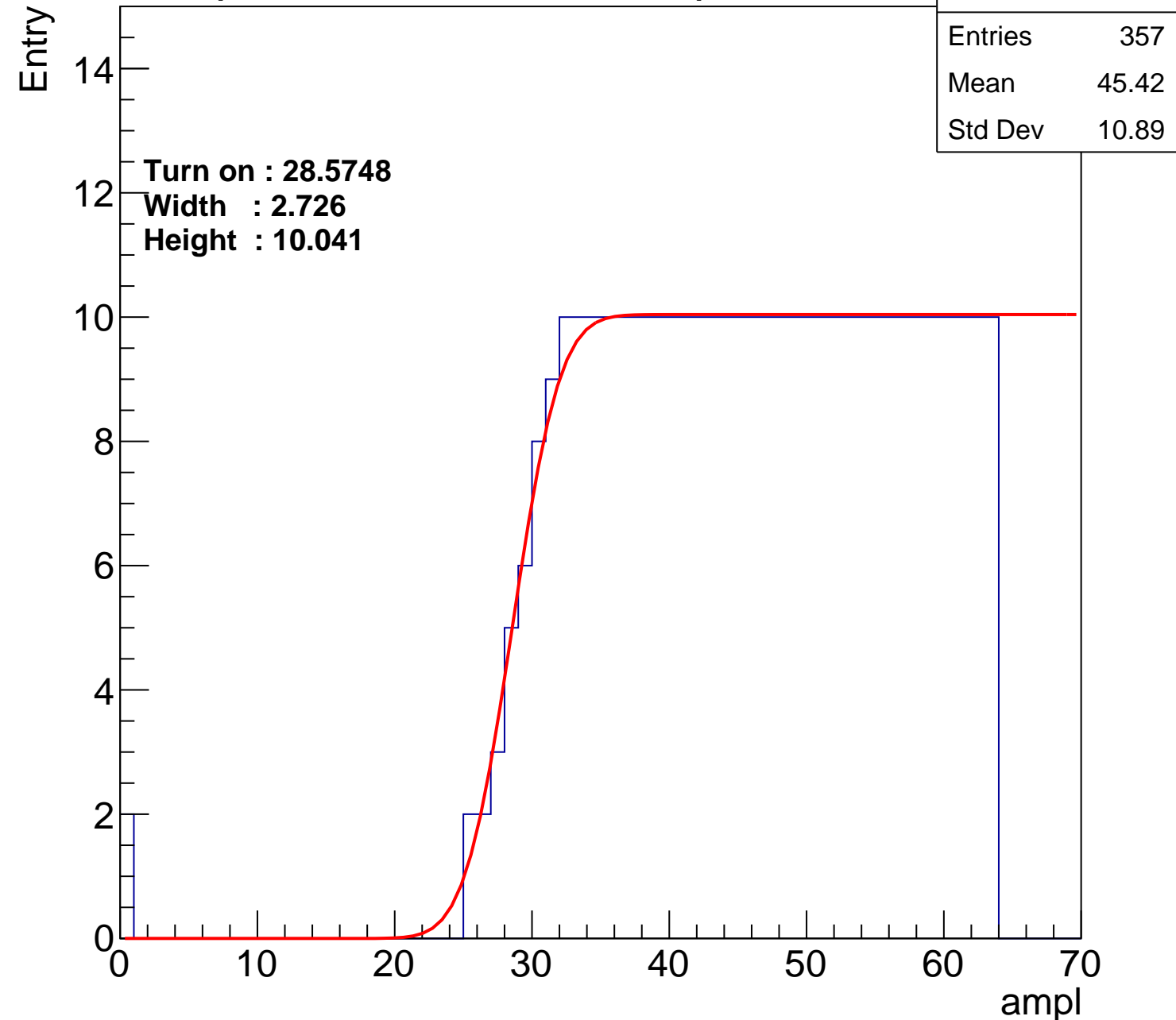
Width : 2.726

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch121

calib_packv5_042523_0143.root, FC#8, port C1

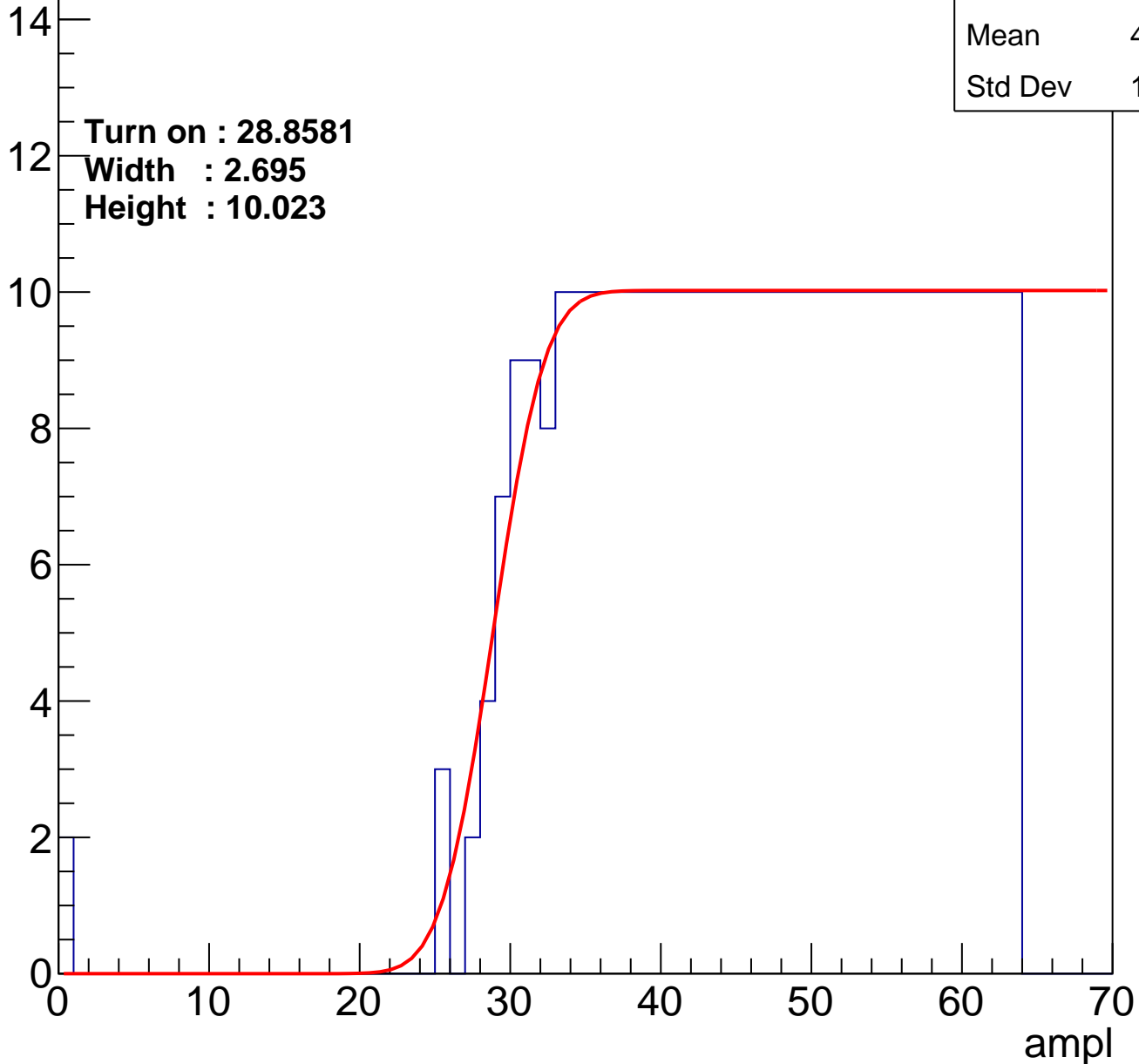
Entries	354
Mean	45.56
Std Dev	10.82

Turn on : 28.8581

Width : 2.695

Height : 10.023

Entry



B0L002S, U11-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.52
Std Dev	11.04

Turn on : 28.3769

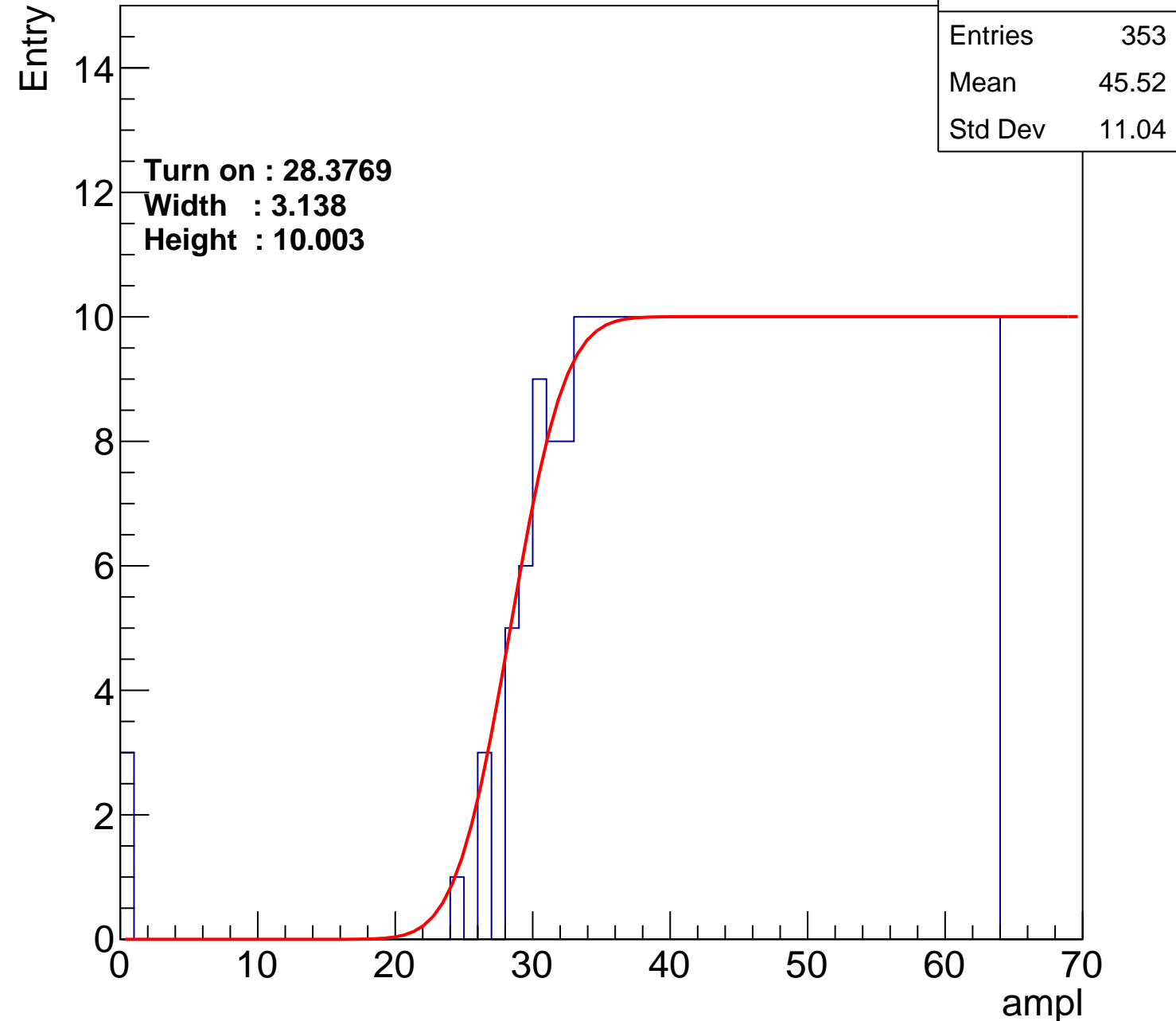
Width : 3.138

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.84
Std Dev	11.18

Turn on : 27.6494

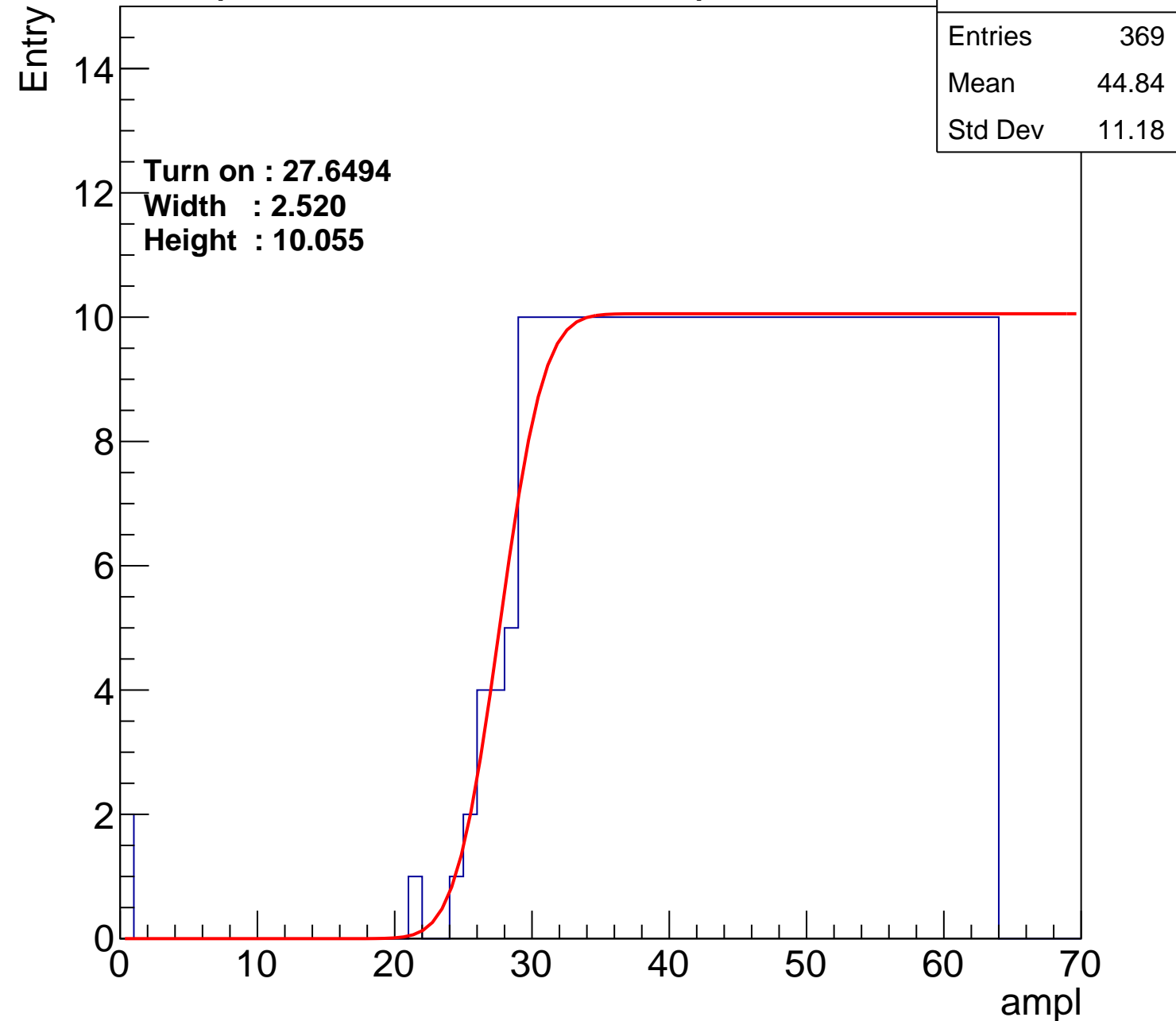
Width : 2.520

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.61
Std Dev	11.32

Turn on : 27.0631

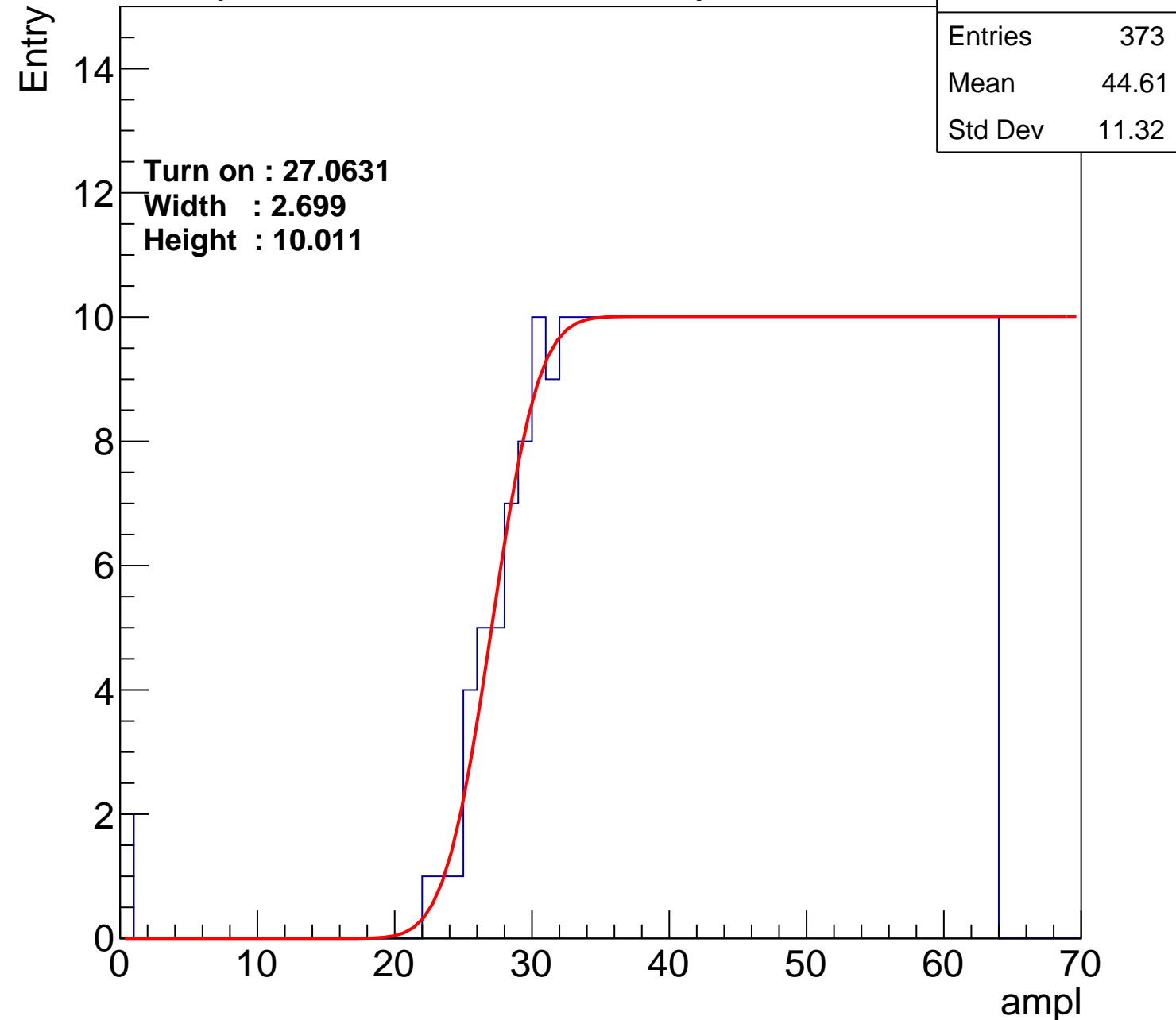
Width : 2.699

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.33
Std Dev	10.97

Turn on : 28.3275

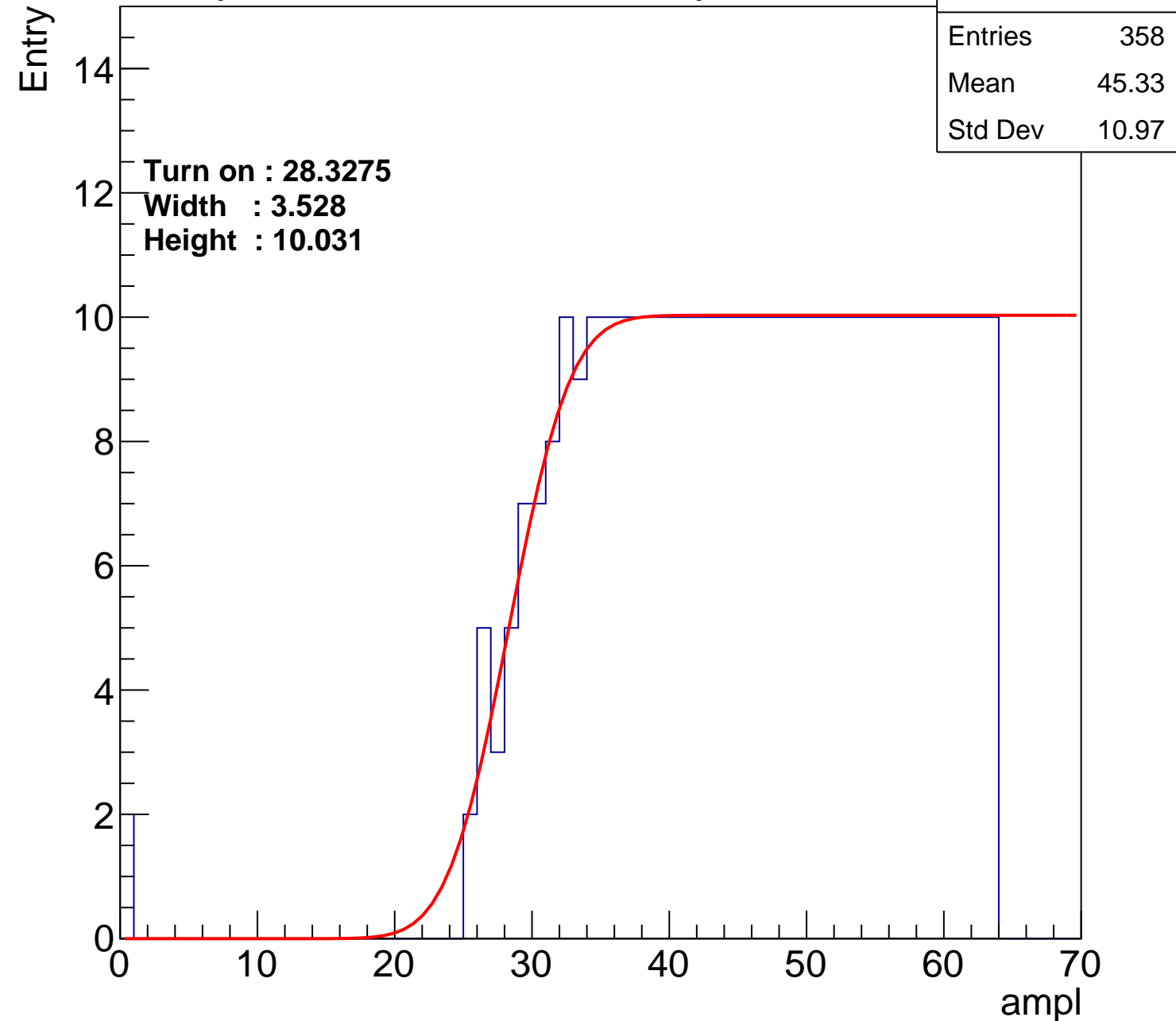
Width : 3.528

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.99

Turn on : 27.6305

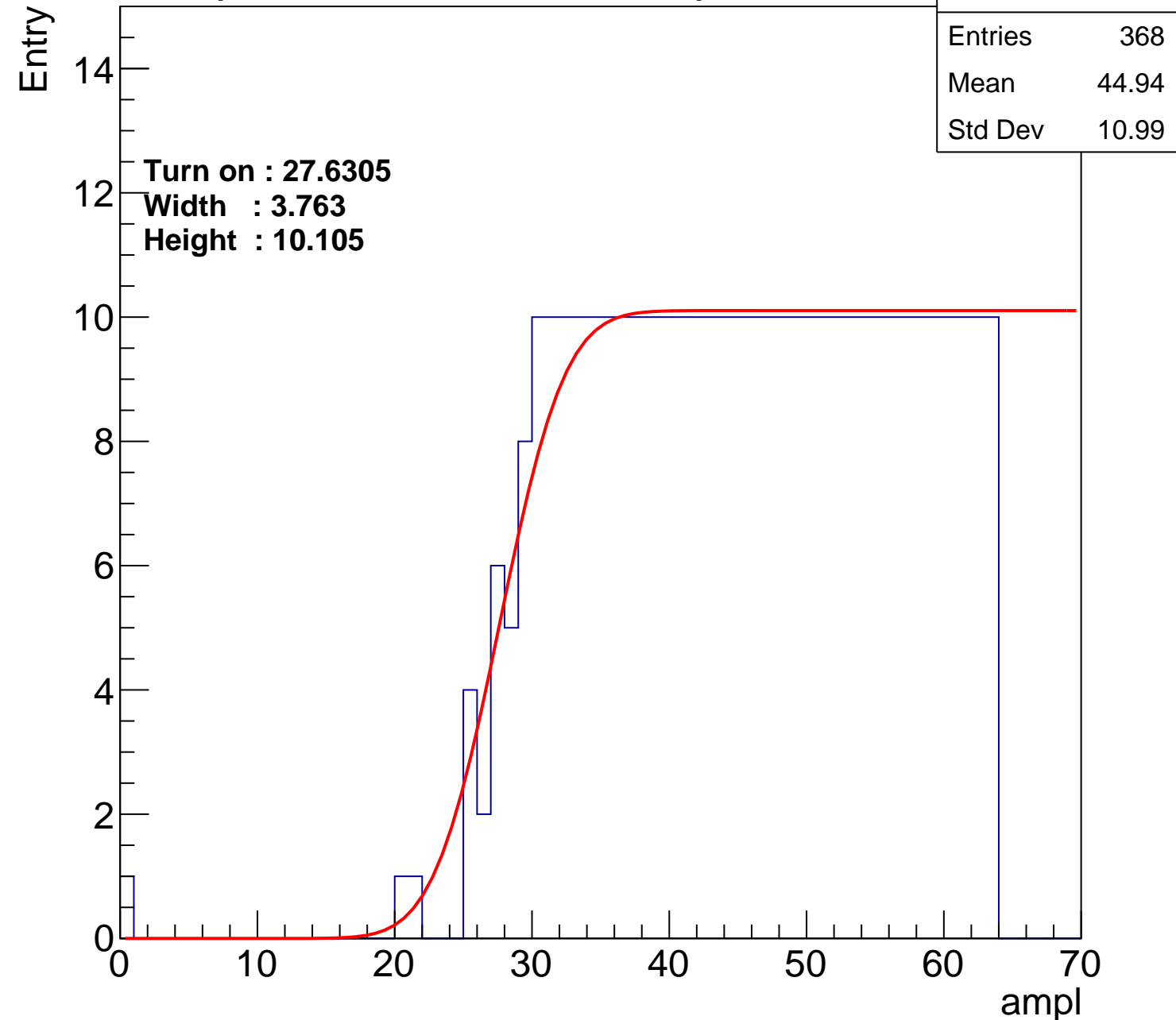
Width : 3.763

Height : 10.105

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	334
Mean	46.54
Std Dev	10.34

Turn on : 31.2517

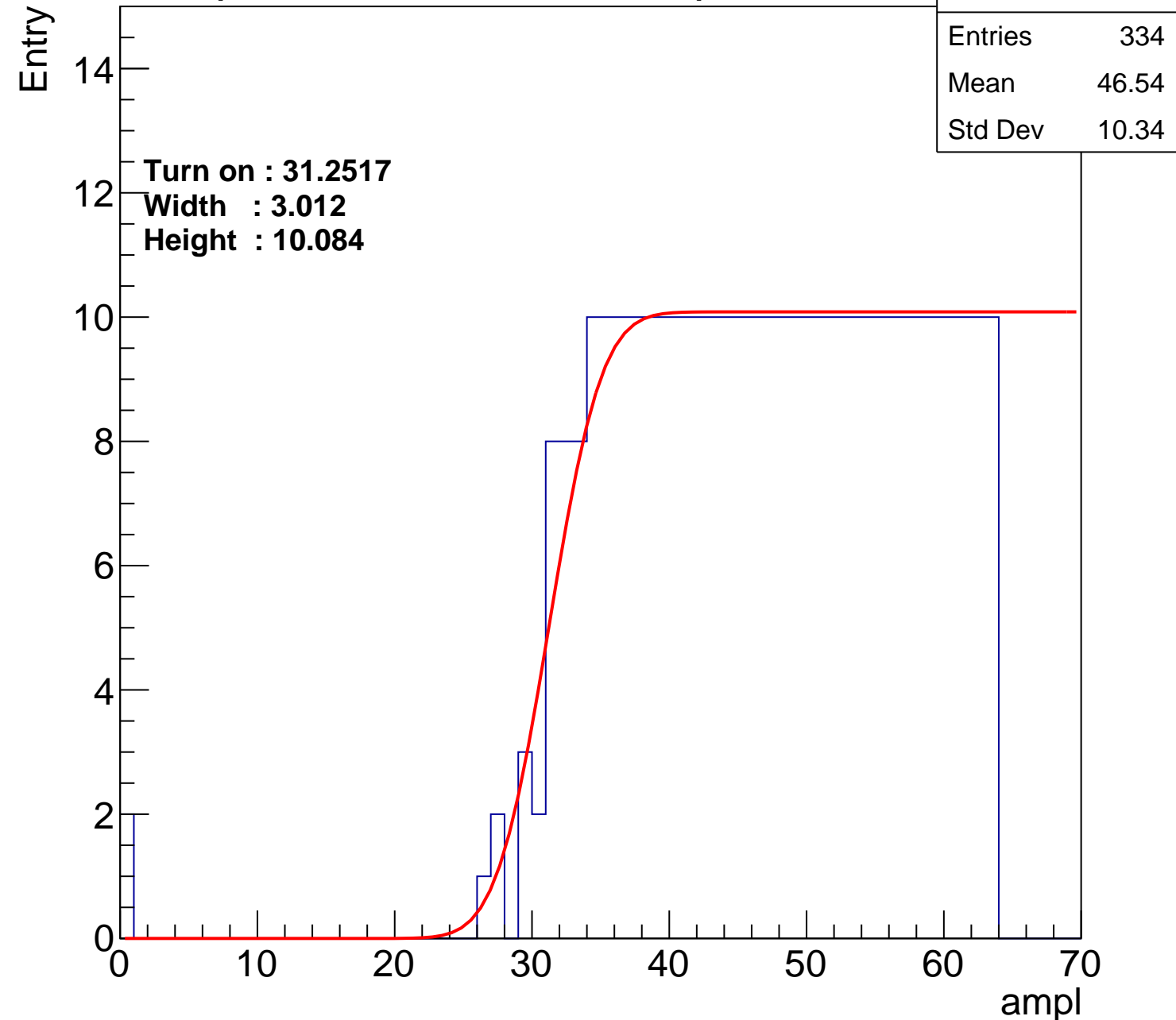
Width : 3.012

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U11-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	334
Mean	46.54
Std Dev	10.34

Turn on : 31.2517

Width : 3.012

Height : 10.084

Entry

