

B0L002S, U6-ch0

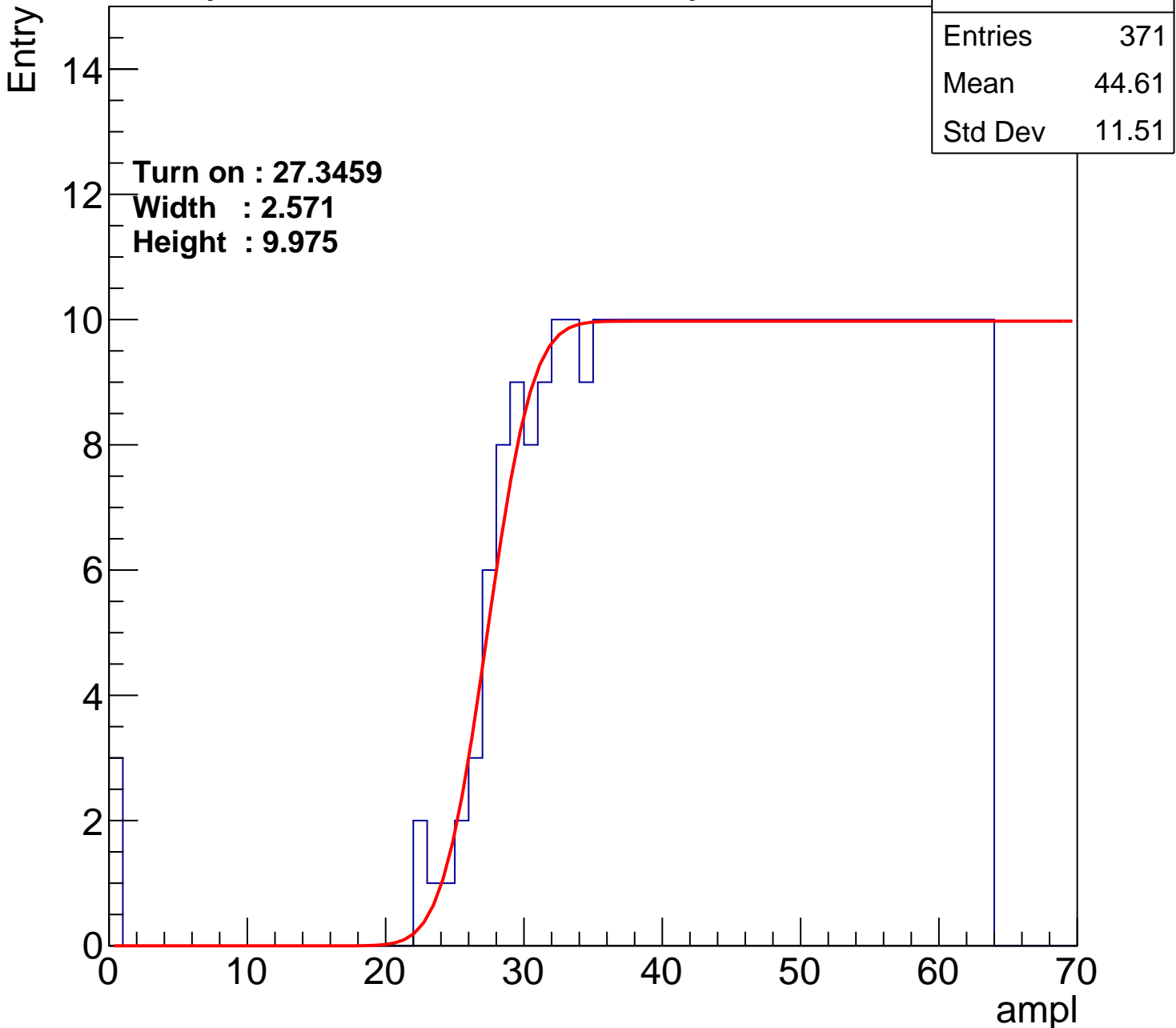
calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.61
Std Dev	11.51

Turn on : 27.3459

Width : 2.571

Height : 9.975



B0L002S, U6-ch1

calib_packv5_042523_0143.root, FC#8, port C1

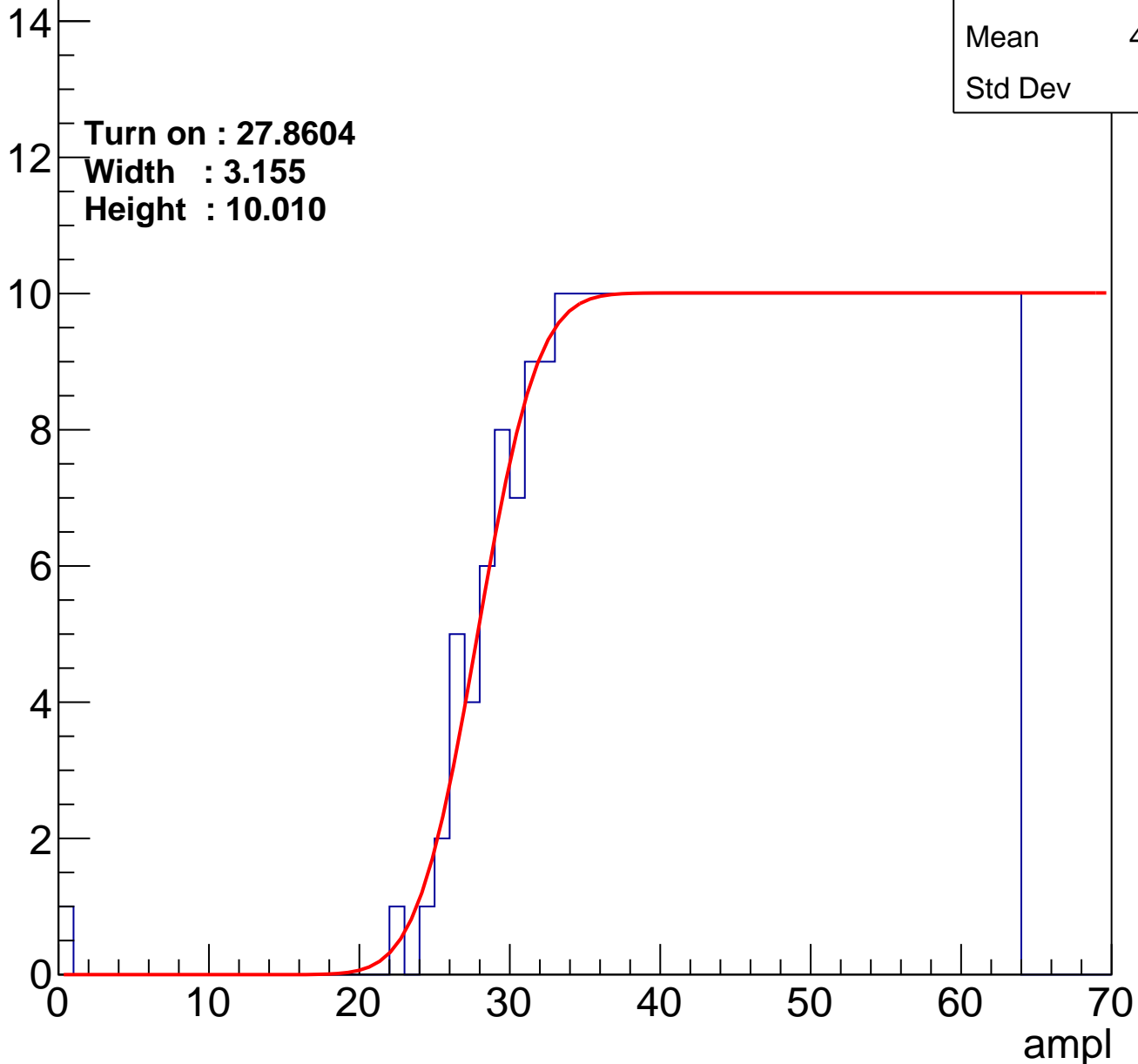
Entries	363
Mean	45.15
Std Dev	10.9

Turn on : 27.8604

Width : 3.155

Height : 10.010

Entry



B0L002S, U6-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.23
Std Dev	11.45

Turn on : 26.3857

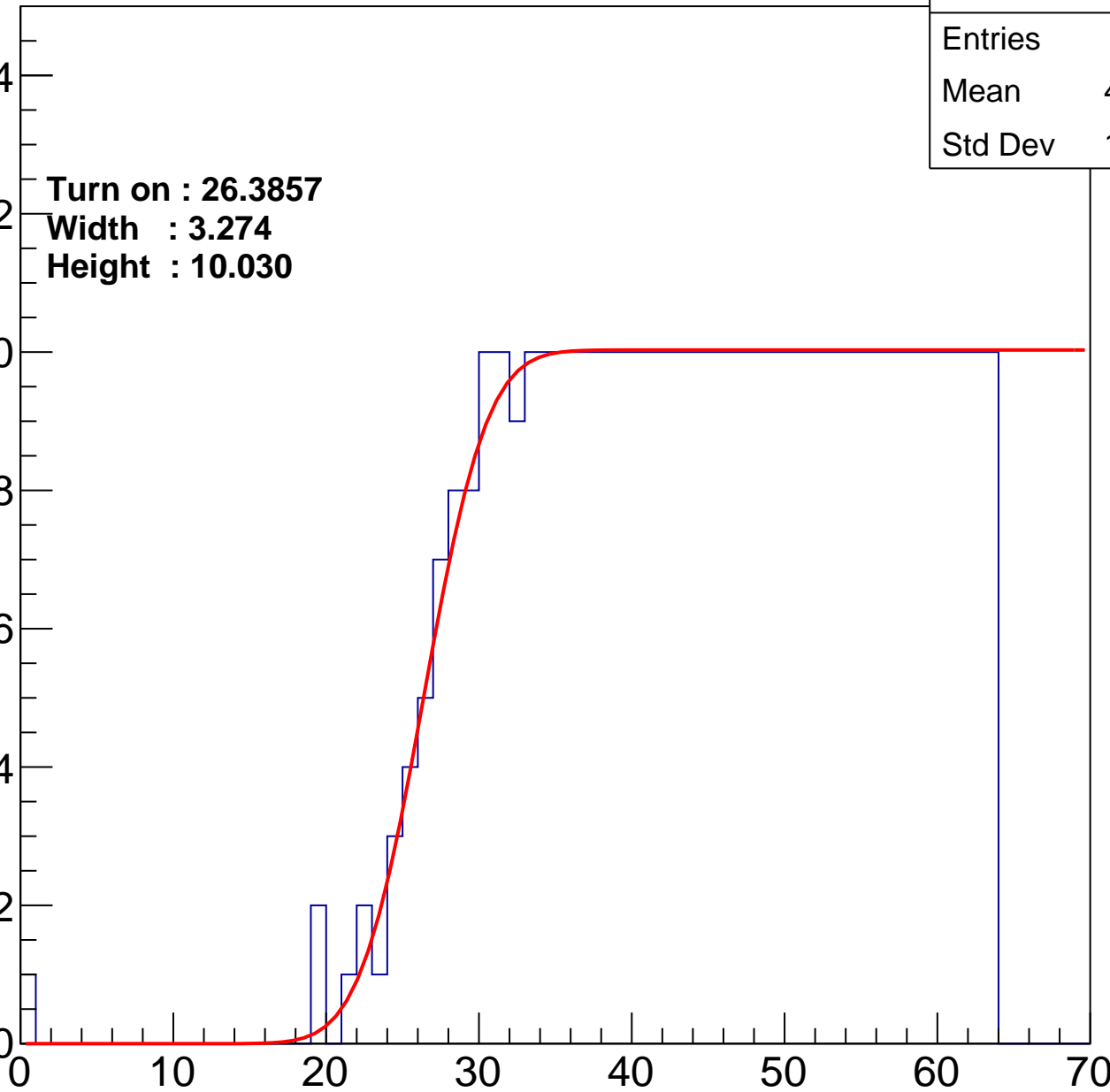
Width : 3.274

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch3

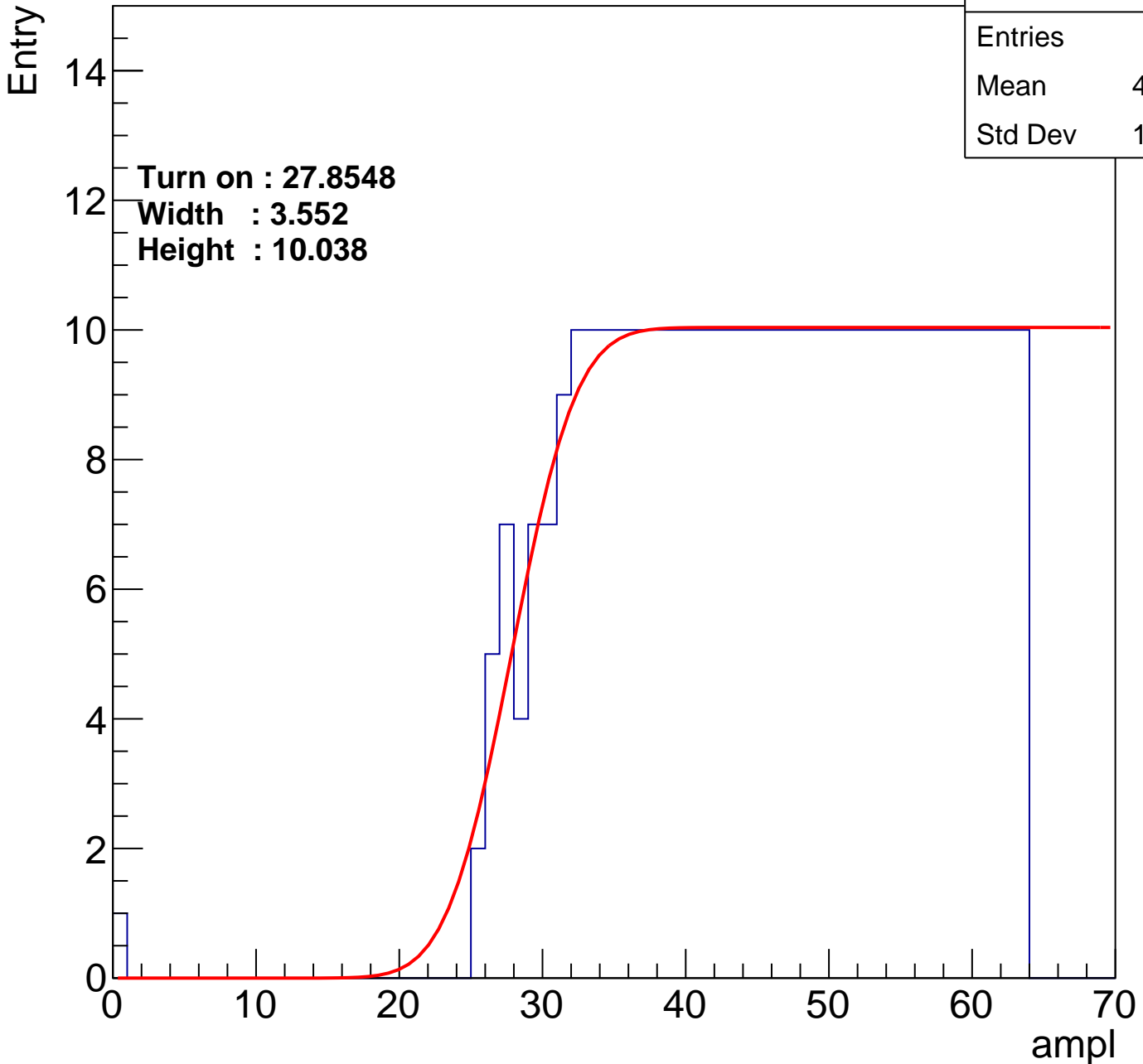
calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 27.8548

Width : 3.552

Height : 10.038



B0L002S, U6-ch4

calib_packv5_042523_0143.root, FC#8, port C1

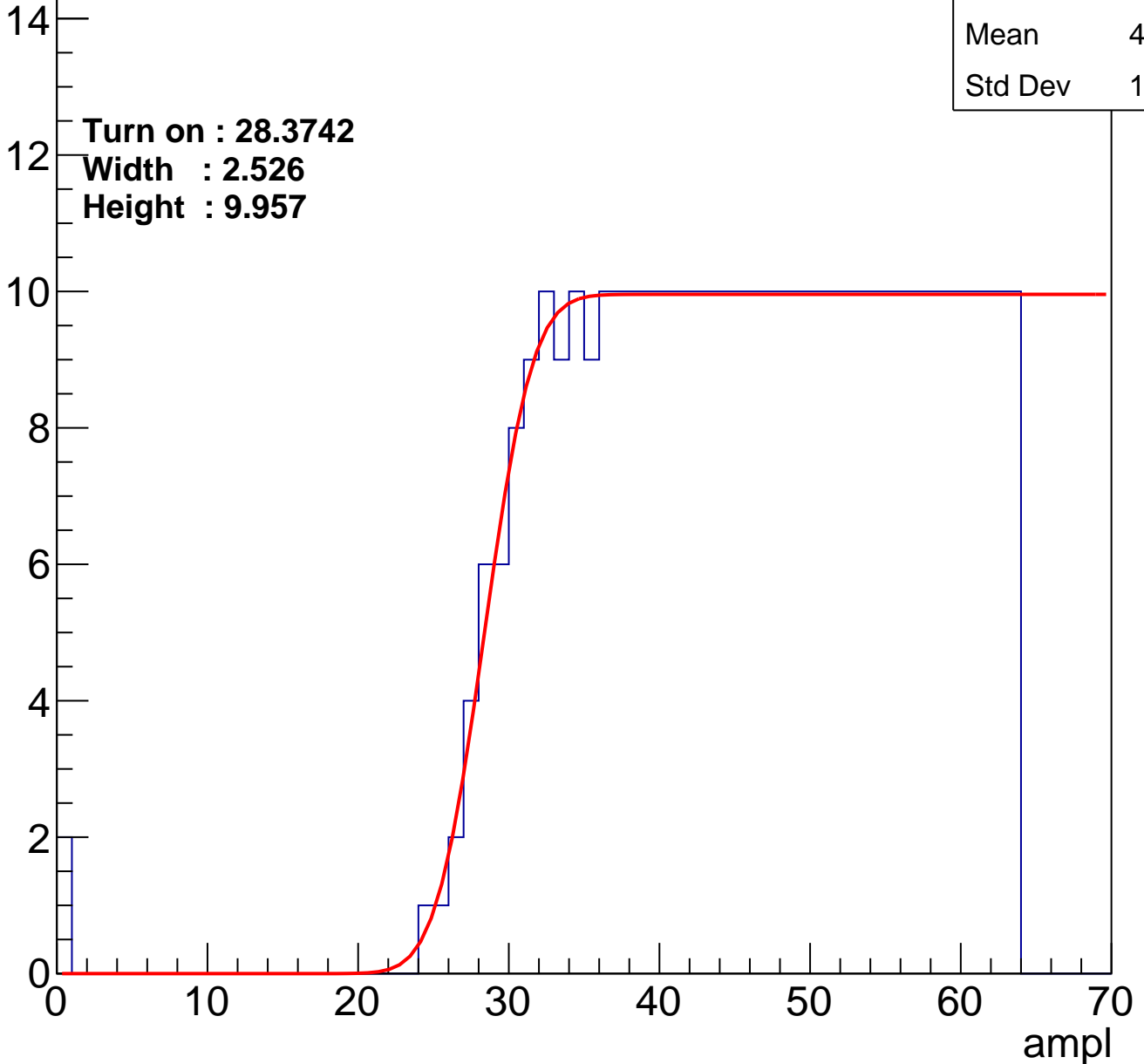
Entry

Entries	357
Mean	45.38
Std Dev	10.94

Turn on : 28.3742

Width : 2.526

Height : 9.957



B0L002S, U6-ch5

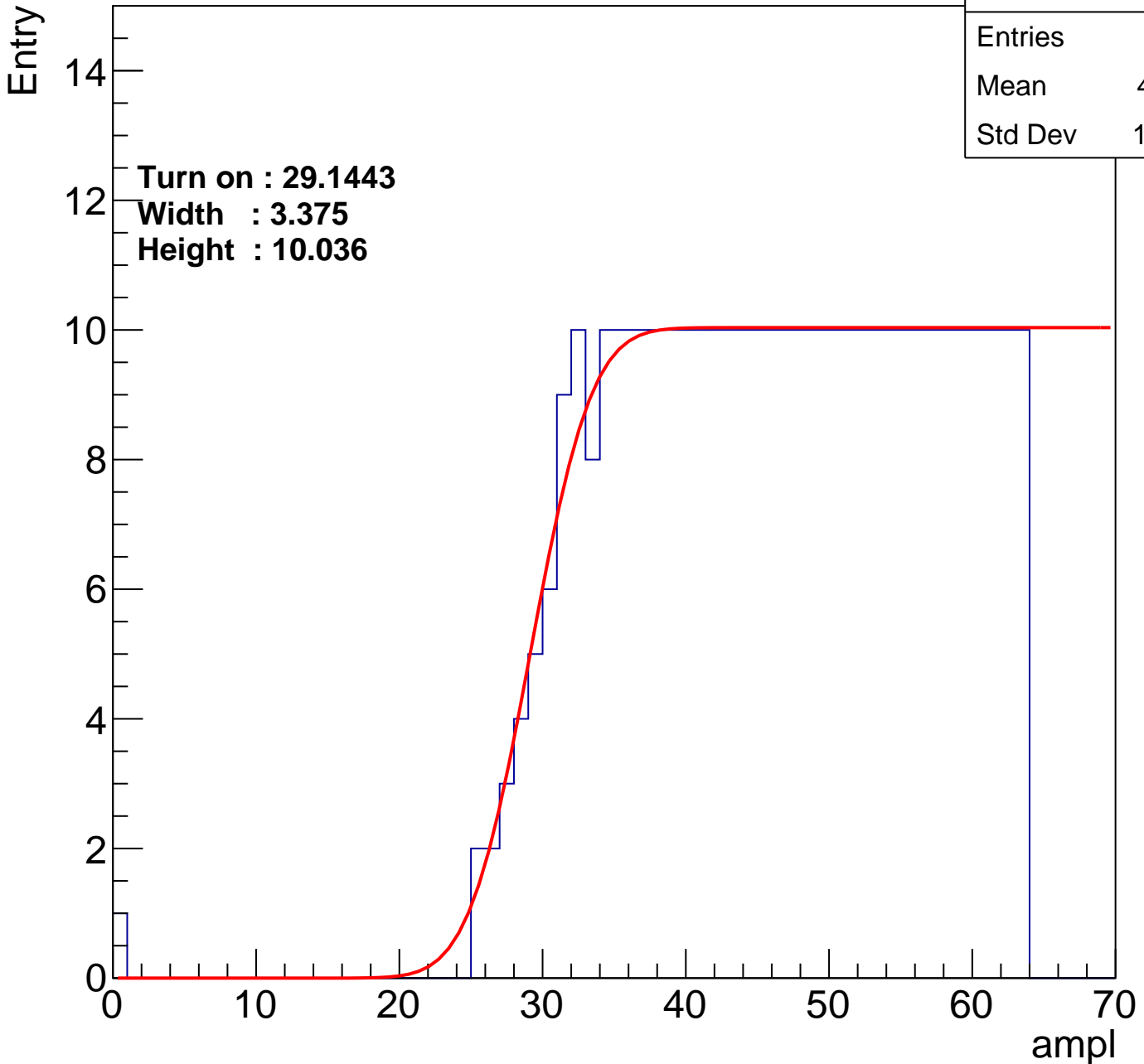
calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.81
Std Dev	10.53

Turn on : 29.1443

Width : 3.375

Height : 10.036



B0L002S, U6-ch6

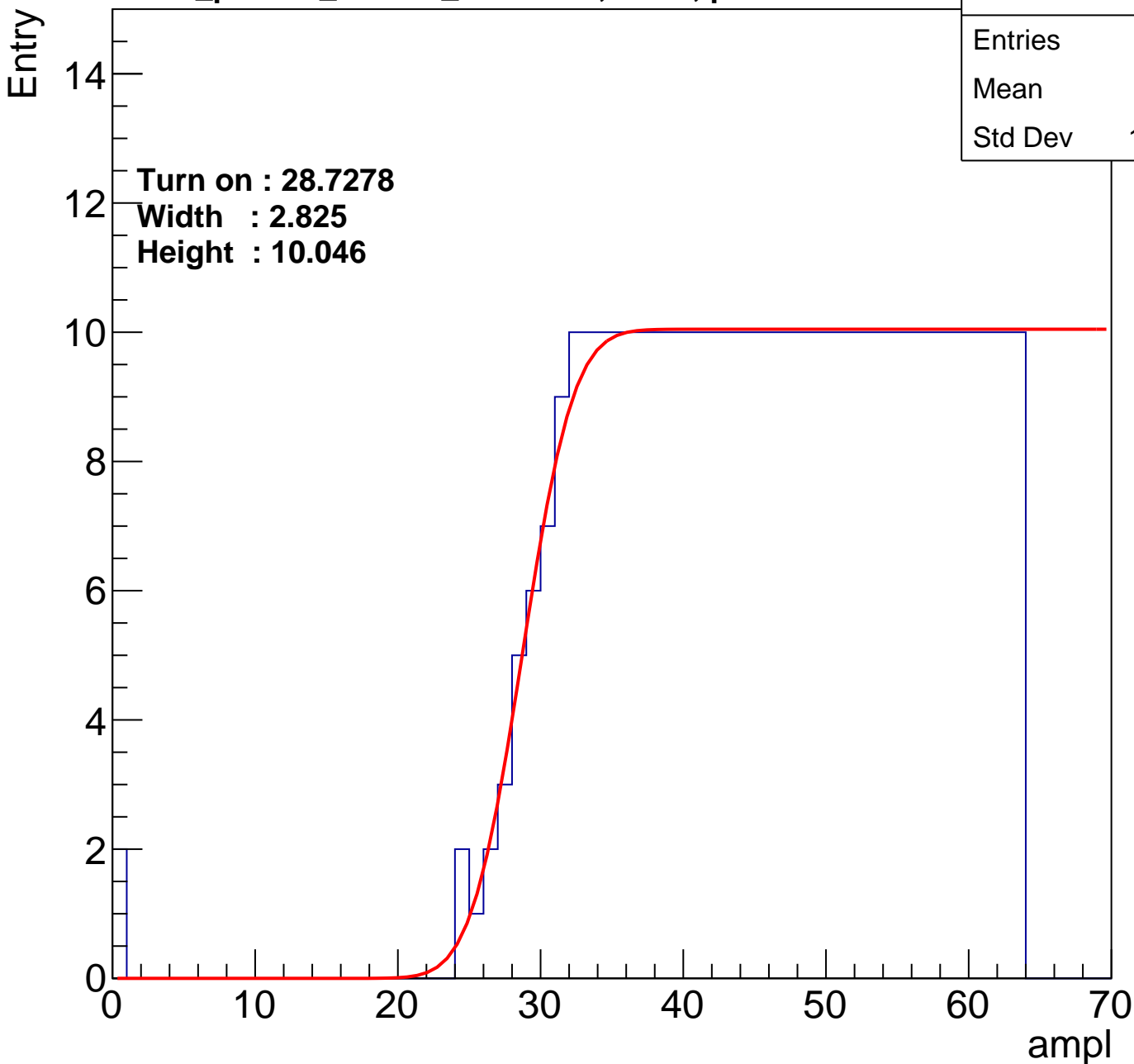
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.7278

Width : 2.825

Height : 10.046

Entries	357
Mean	45.4
Std Dev	10.92



B0L002S, U6-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.81
Std Dev	10.91

Turn on : 29.6674

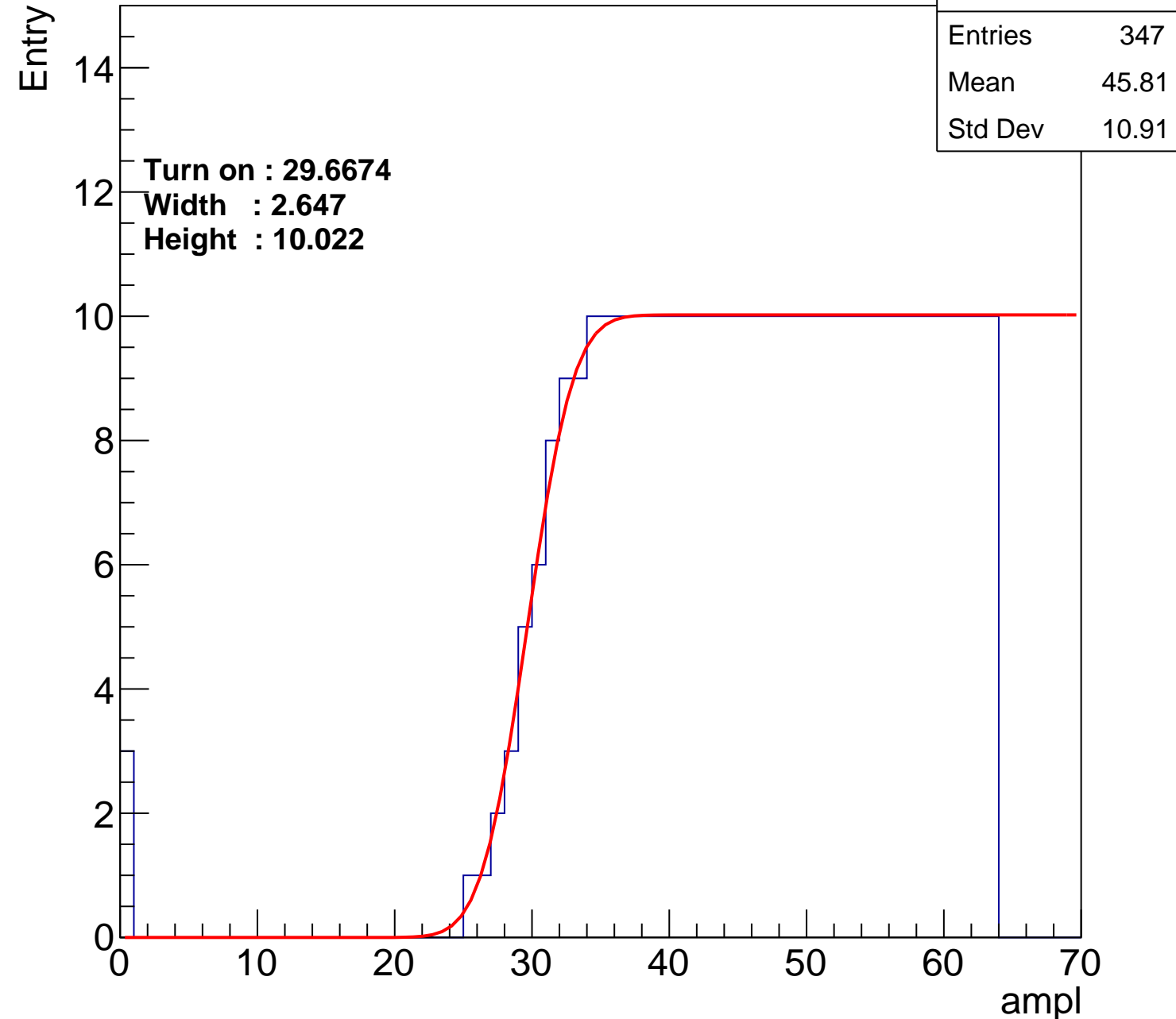
Width : 2.647

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch8

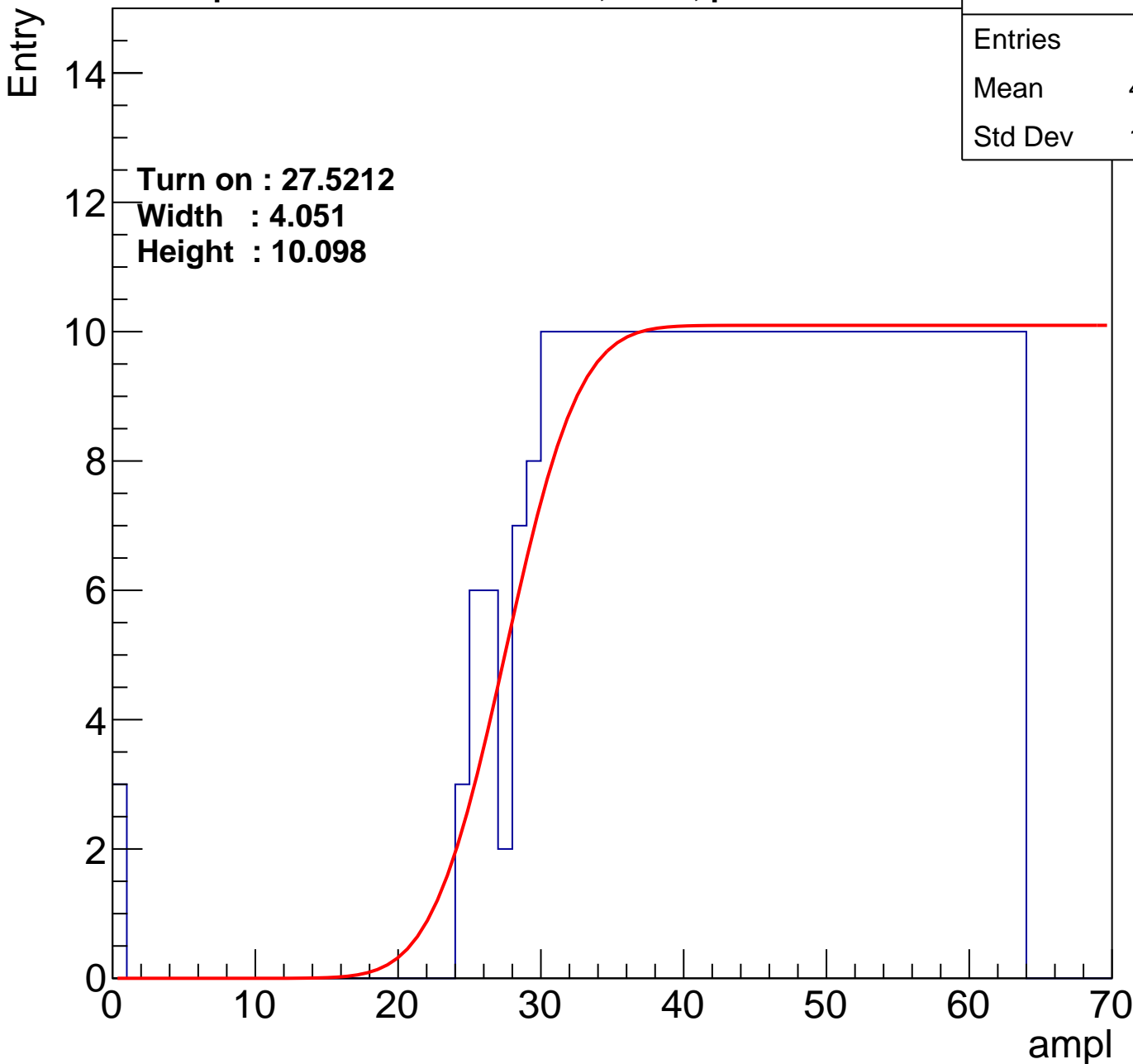
calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.45
Std Dev	11.55

Turn on : 27.5212

Width : 4.051

Height : 10.098



B0L002S, U6-ch9

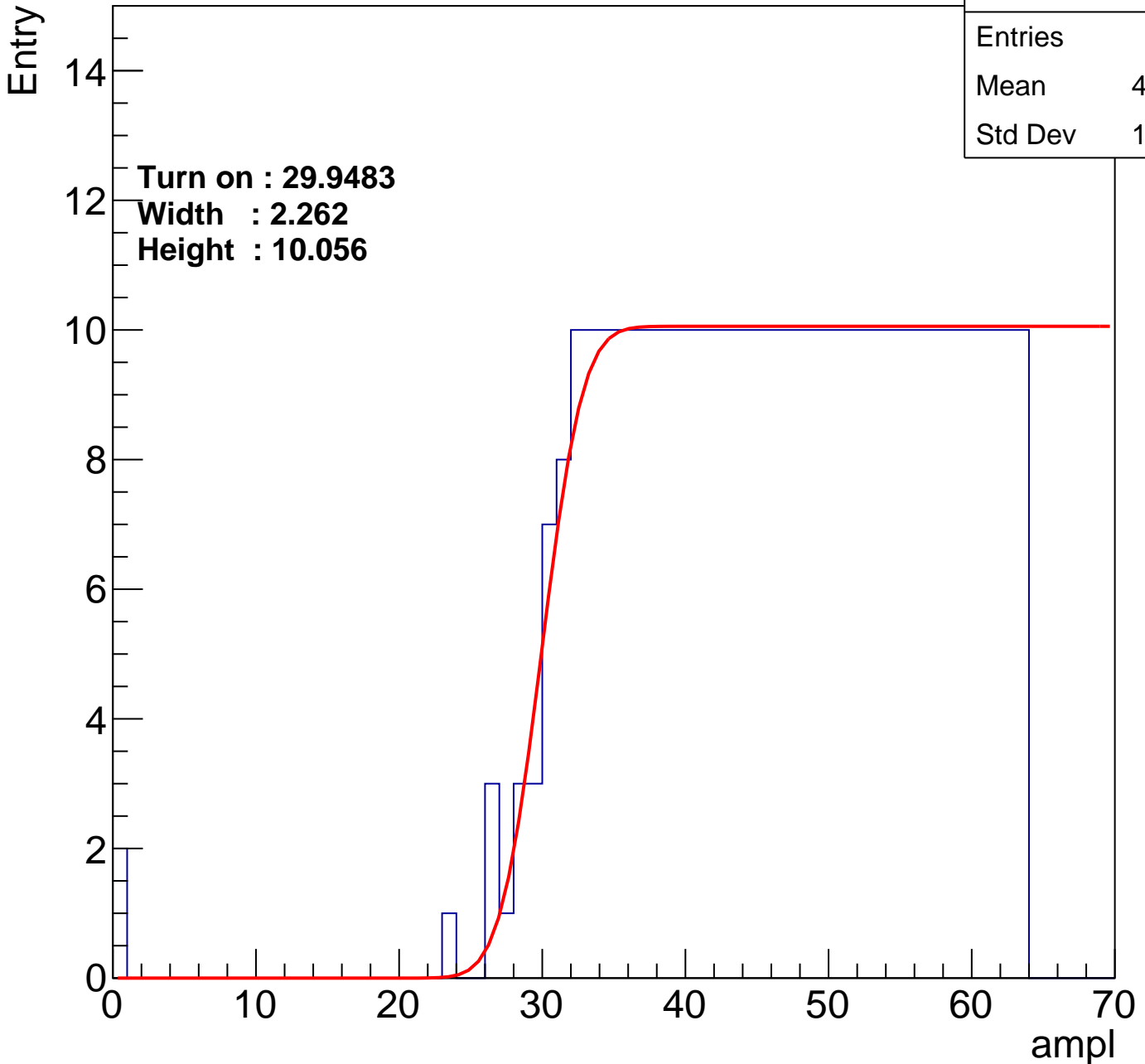
calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.85
Std Dev	10.69

Turn on : 29.9483

Width : 2.262

Height : 10.056



B0L002S, U6-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.76
Std Dev	11.24

Turn on : 27.4363

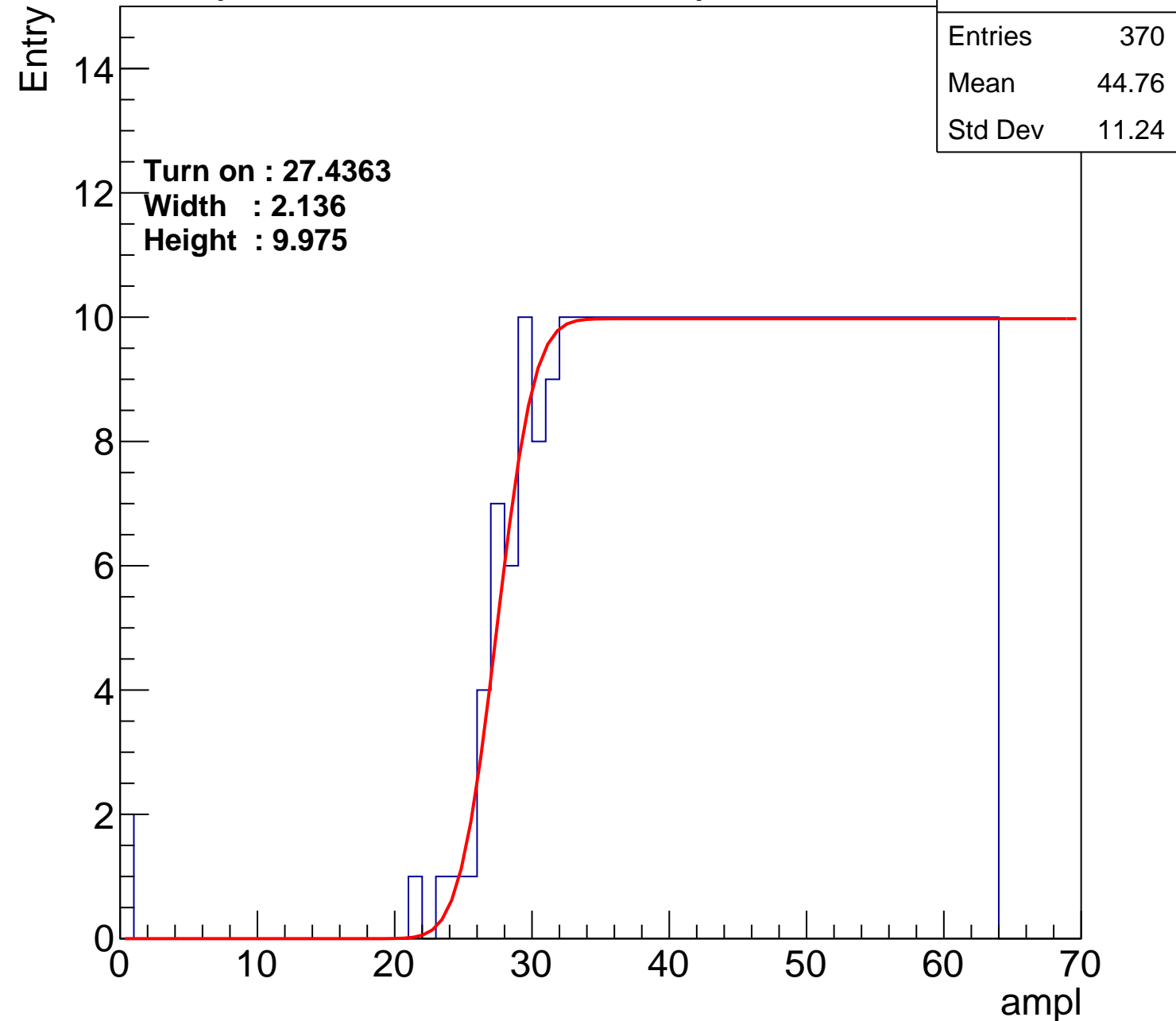
Width : 2.136

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45.1
Std Dev	10.88

Turn on : 27.8075

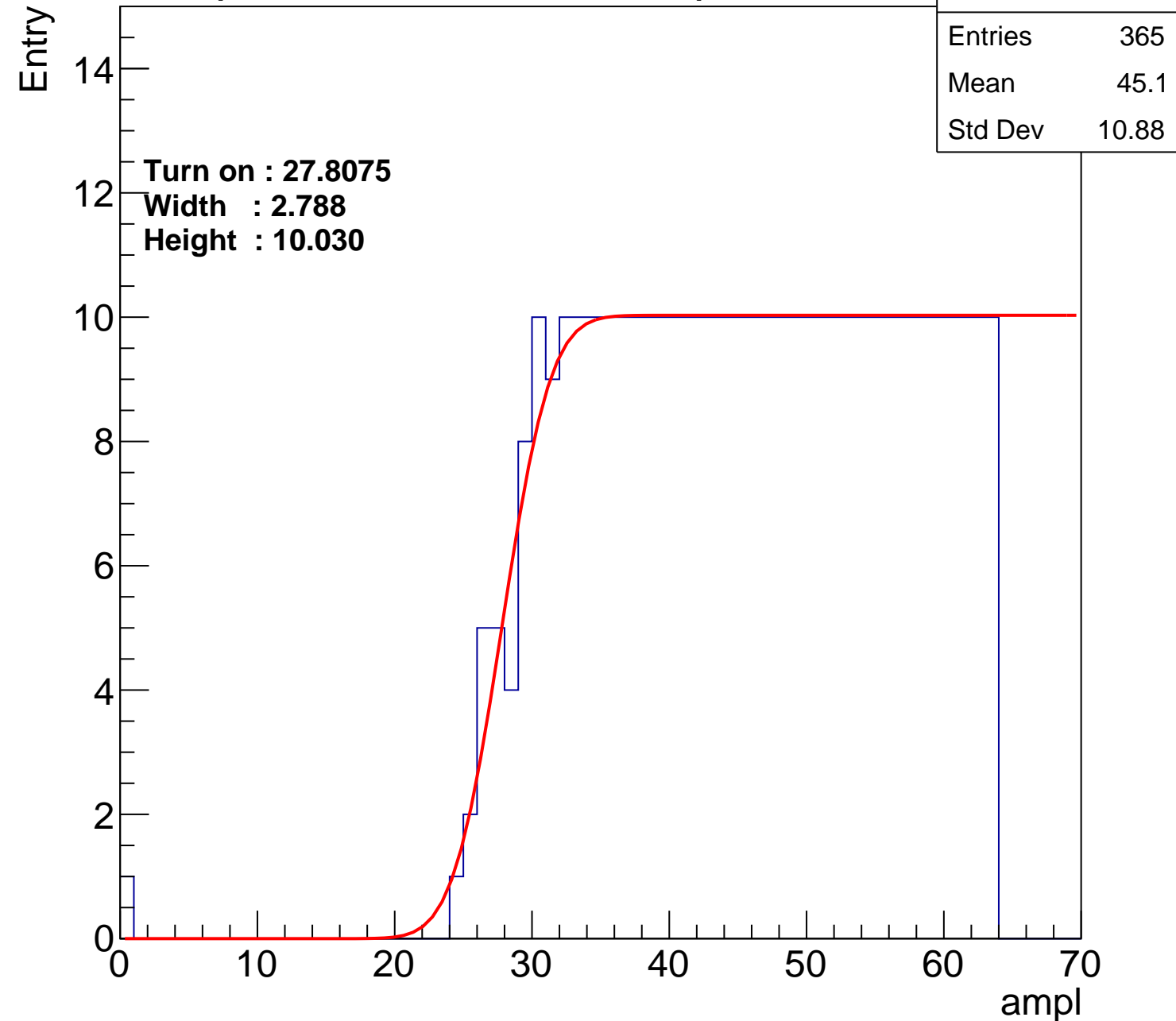
Width : 2.788

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.2790

Width : 1.966

Height : 10.006

Entries	362
Mean	45.18
Std Dev	11.01



B0L002S, U6-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.87
Std Dev	10.98

Turn on : 27.4271

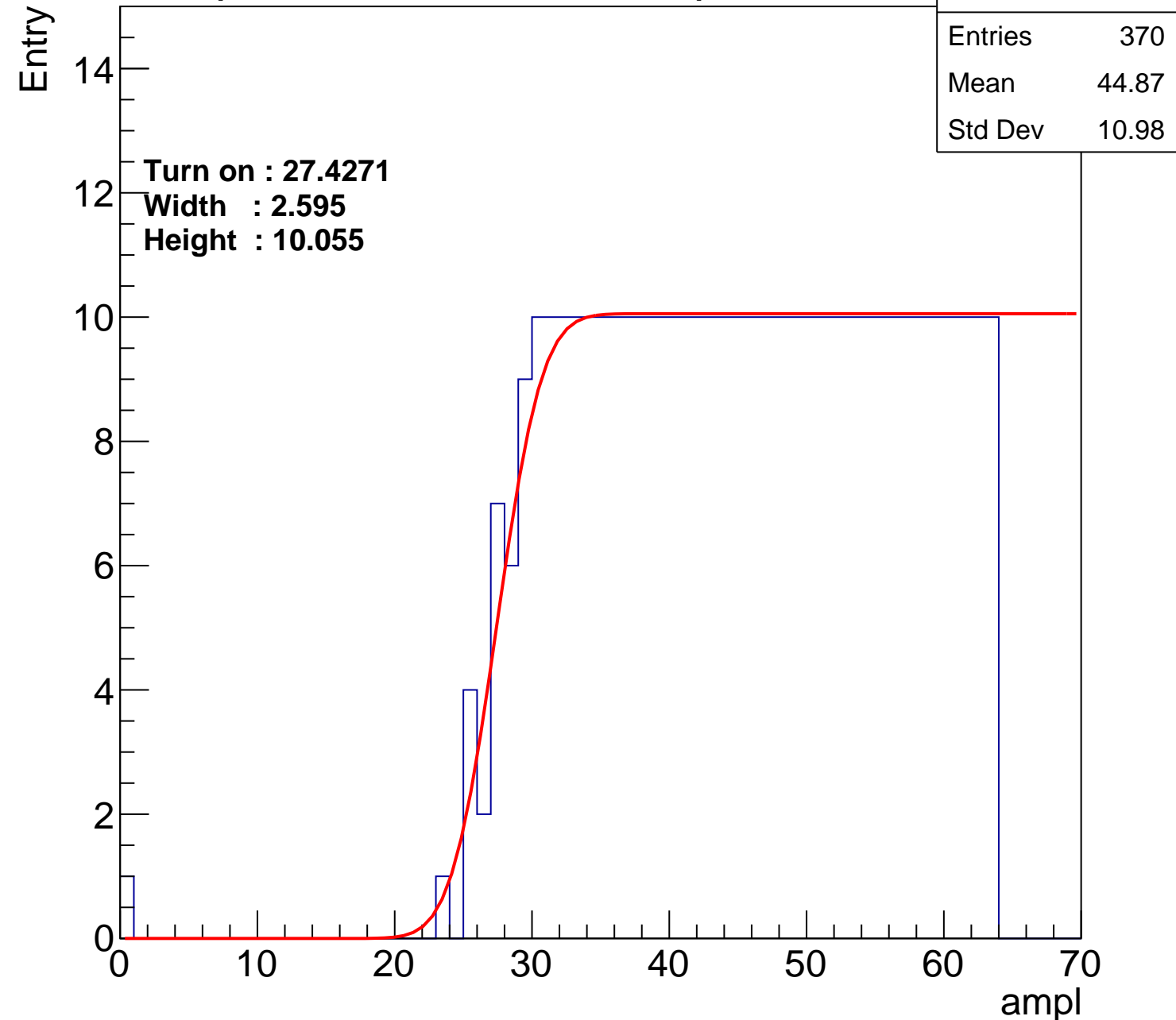
Width : 2.595

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.18
Std Dev	11.53

Turn on : 25.8305

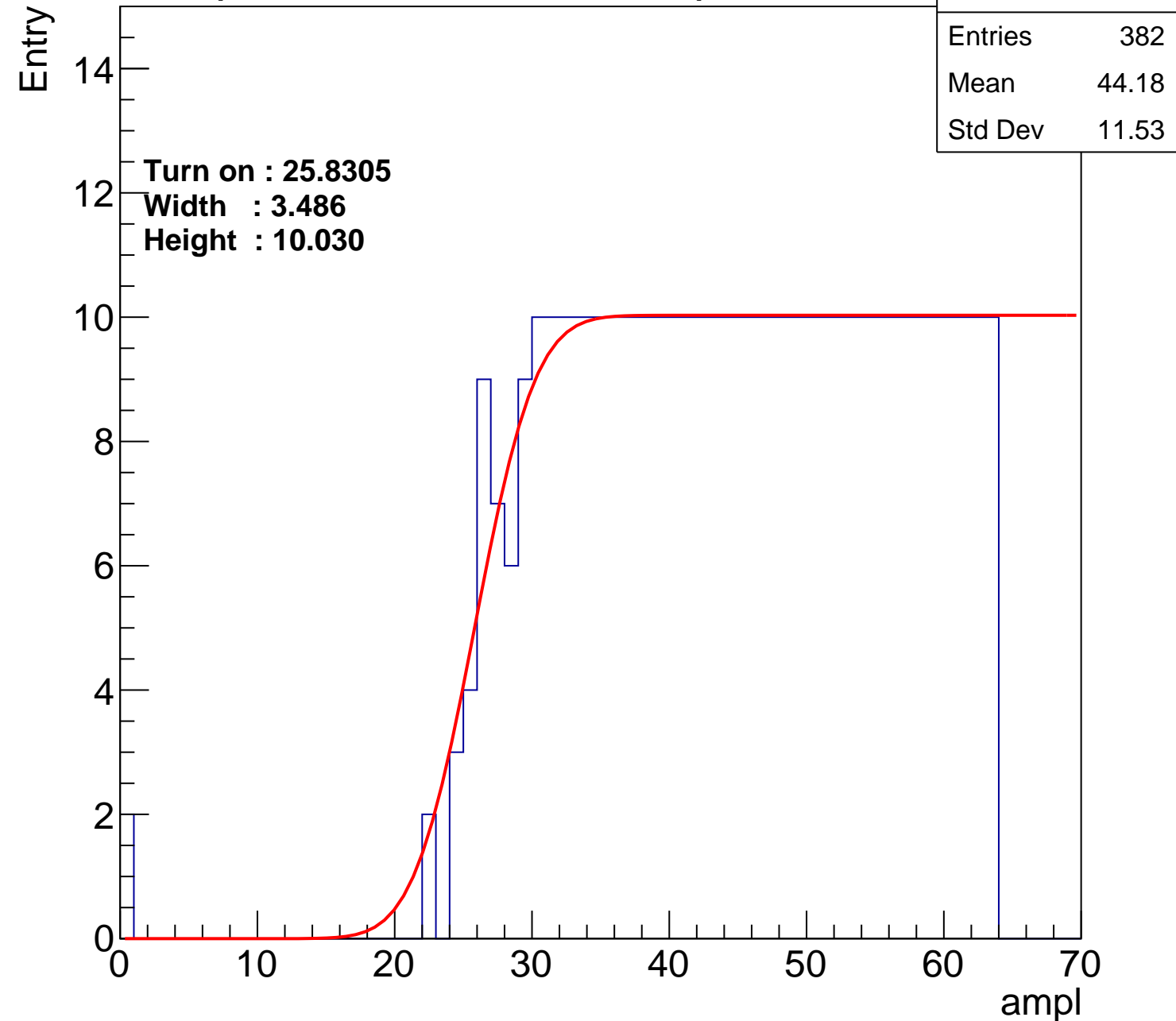
Width : 3.486

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.59
Std Dev	10.62

Turn on : 29.2765

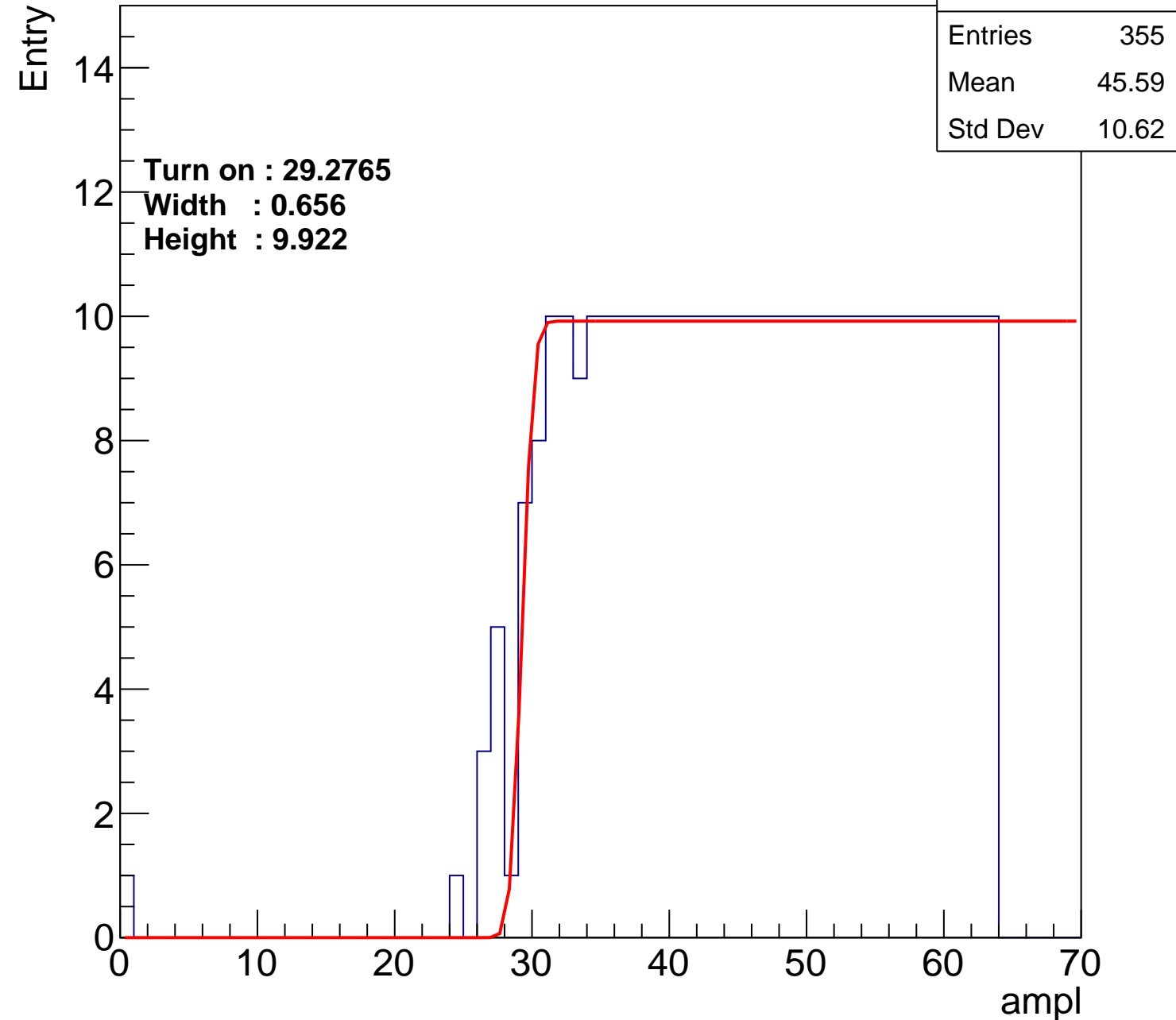
Width : 0.656

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.77
Std Dev	11.15

Turn on : 27.2789

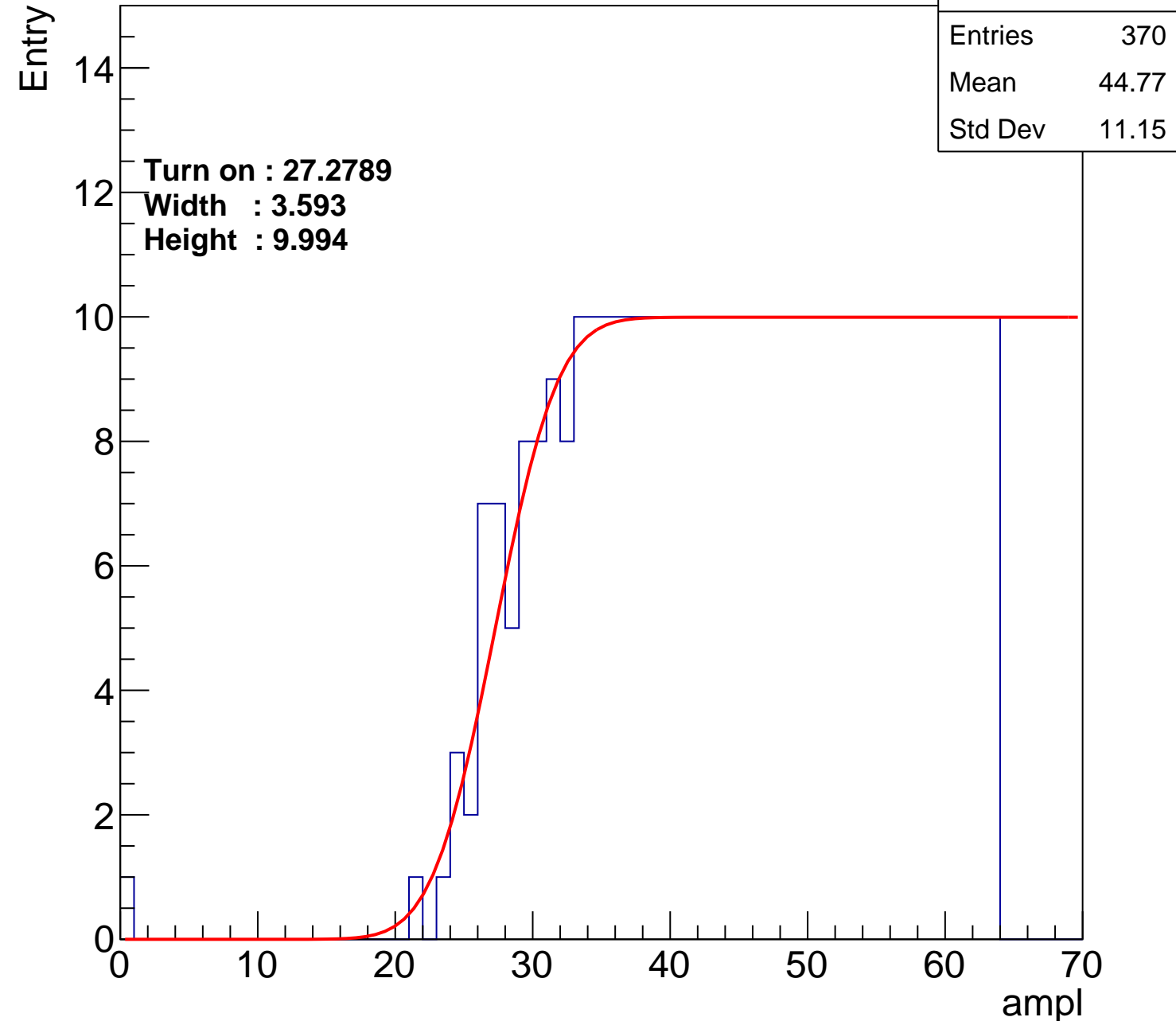
Width : 3.593

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch17

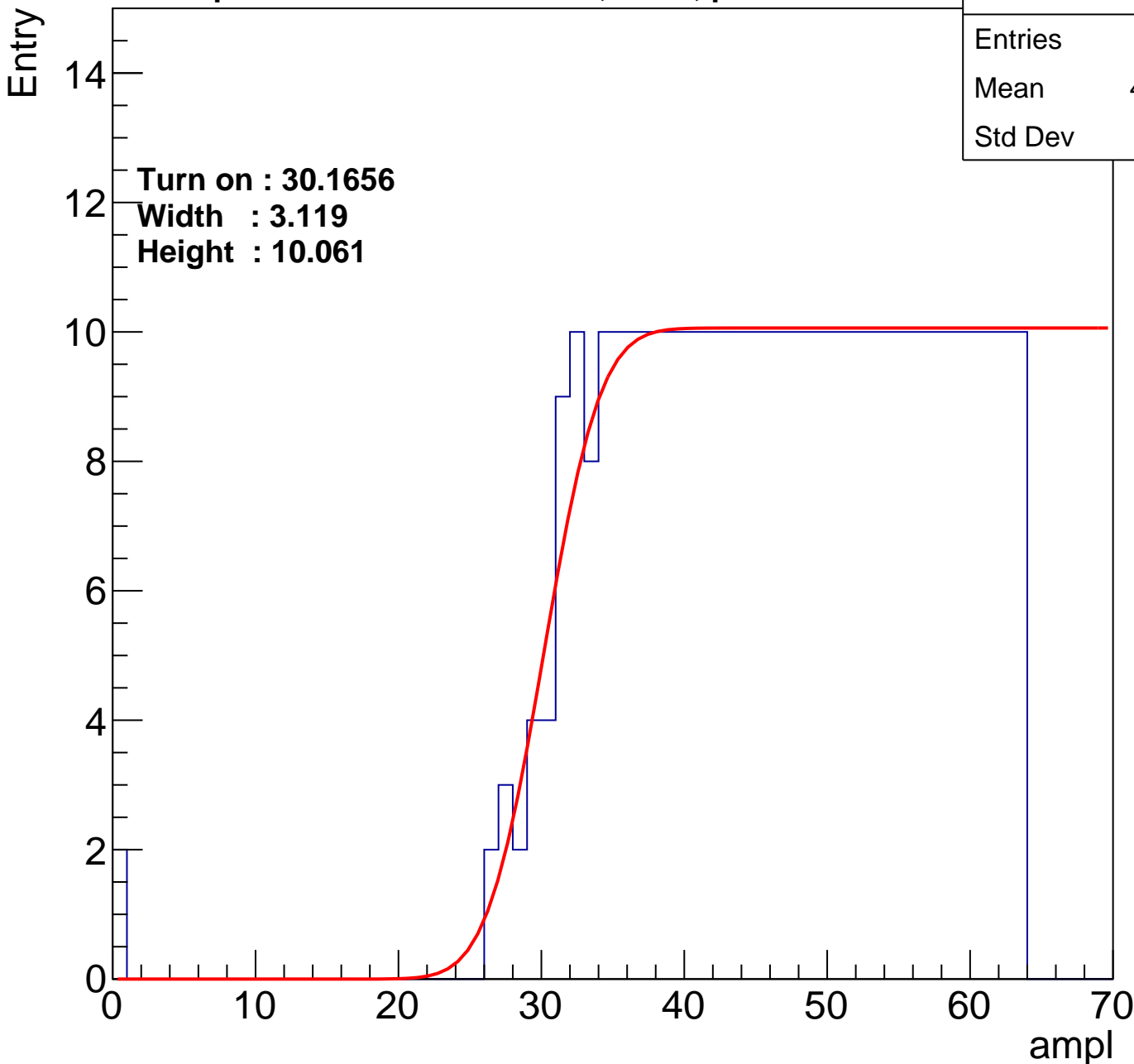
calib_packv5_042523_0143.root, FC#8, port C1

Entries	344
Mean	46.04
Std Dev	10.6

Turn on : 30.1656

Width : 3.119

Height : 10.061



B0L002S, U6-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 27.3771

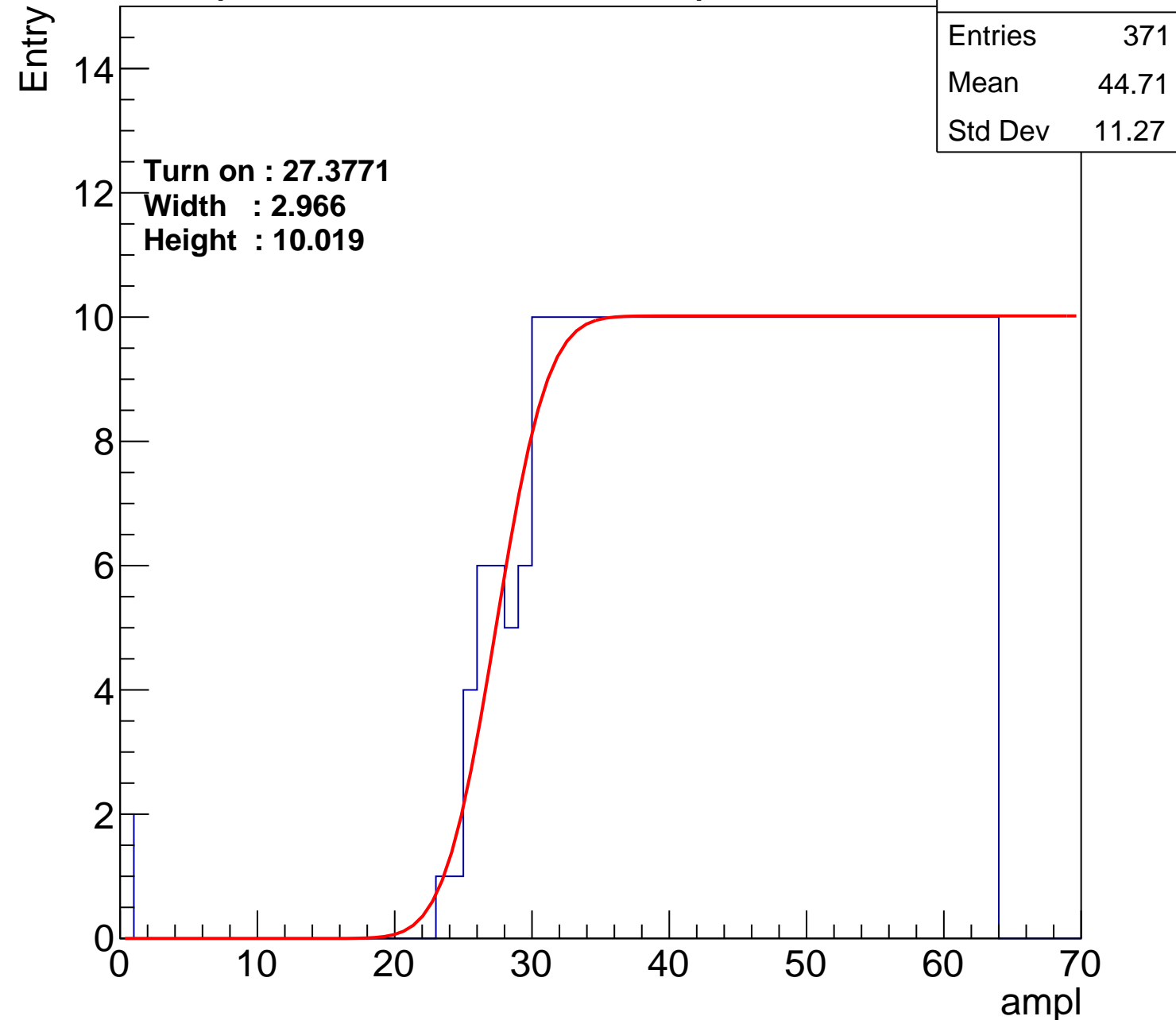
Width : 2.966

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.86
Std Dev	11.58

Turn on : 28.5029

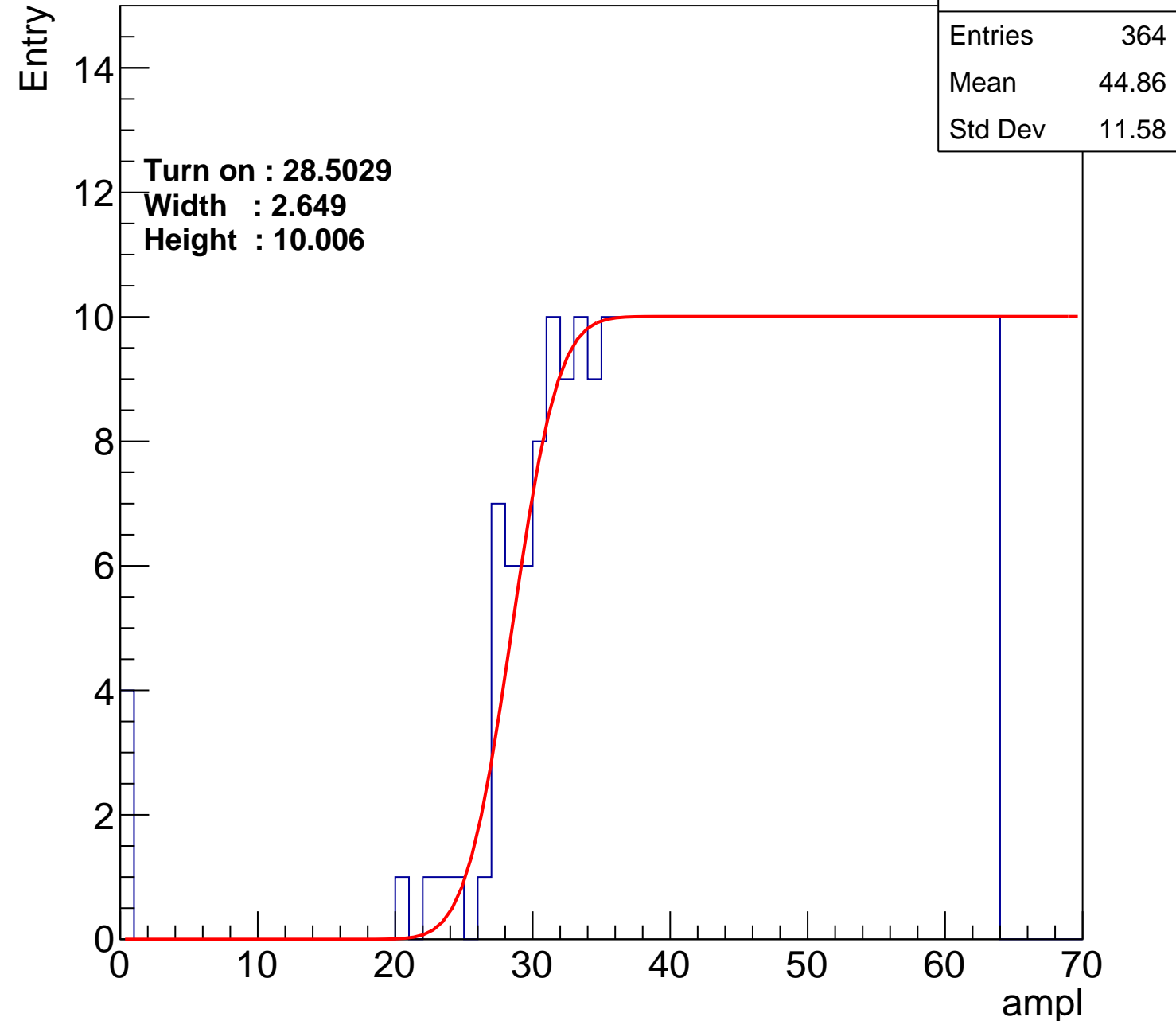
Width : 2.649

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.82
Std Dev	11.21

Turn on : 27.4928

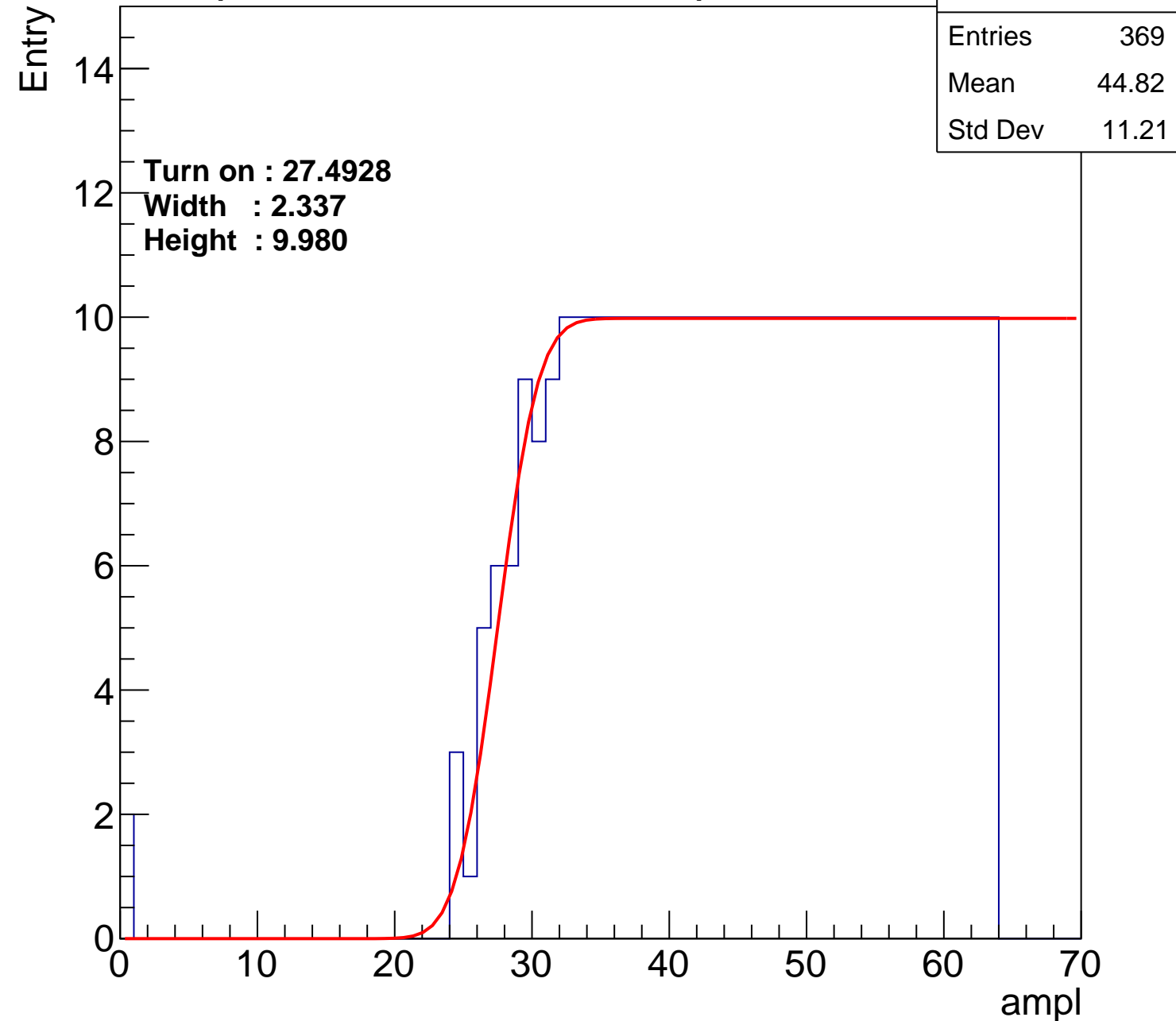
Width : 2.337

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.69
Std Dev	10.57

Turn on : 29.2057

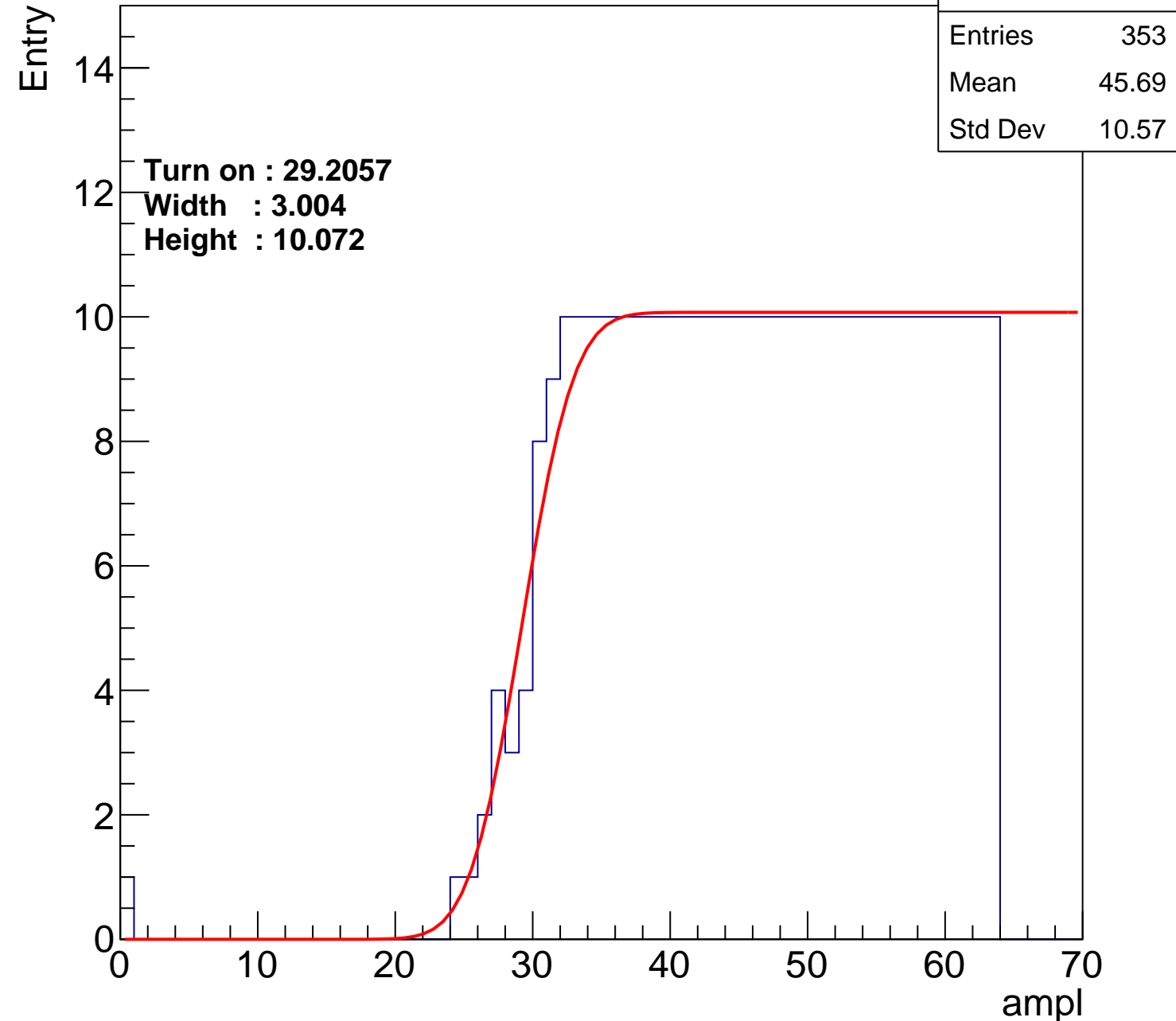
Width : 3.004

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch22

calib_packv5_042523_0143.root, FC#8, port C1

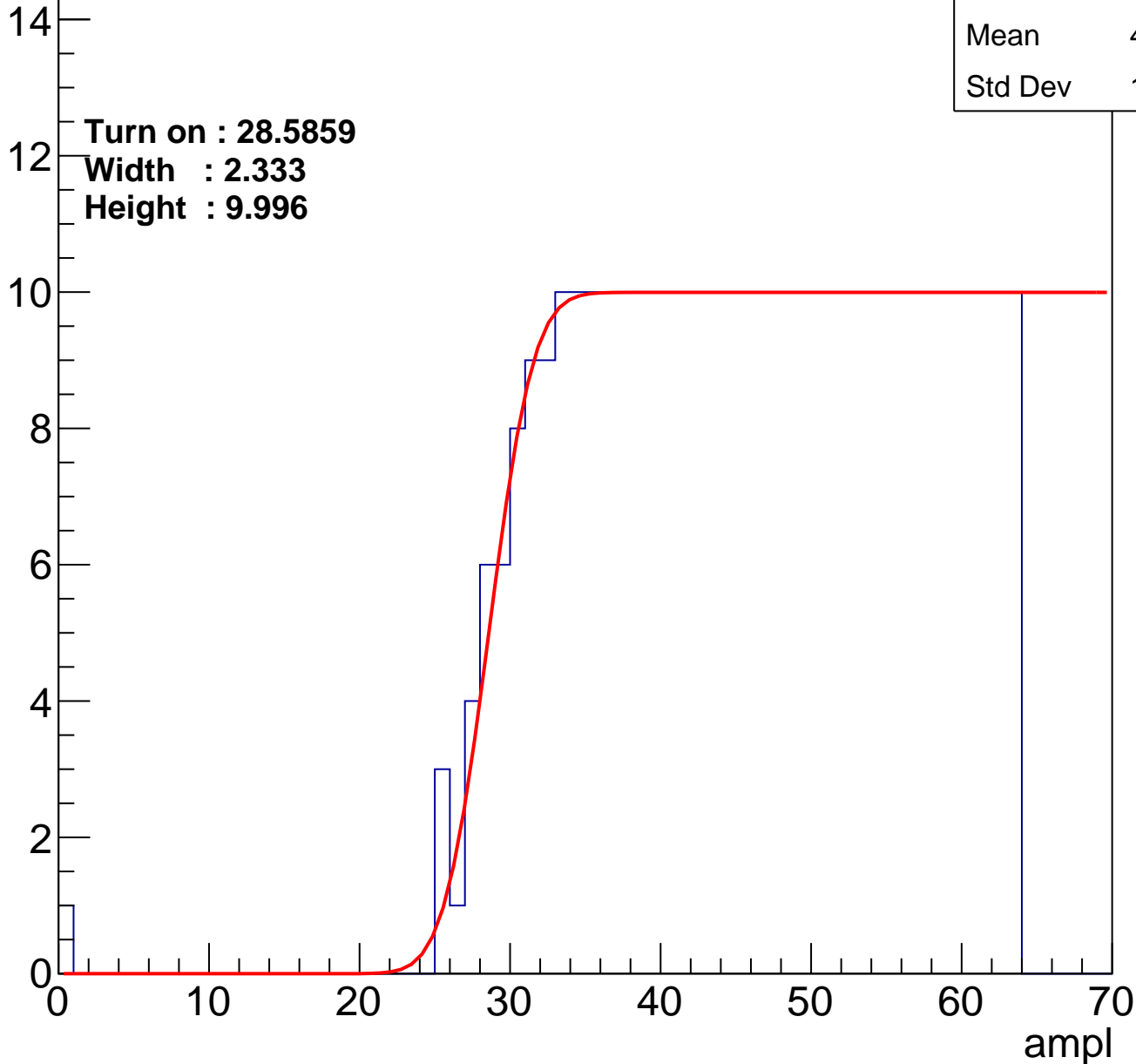
Entry

Entries	357
Mean	45.48
Std Dev	10.68

Turn on : 28.5859

Width : 2.333

Height : 9.996



B0L002S, U6-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.2674

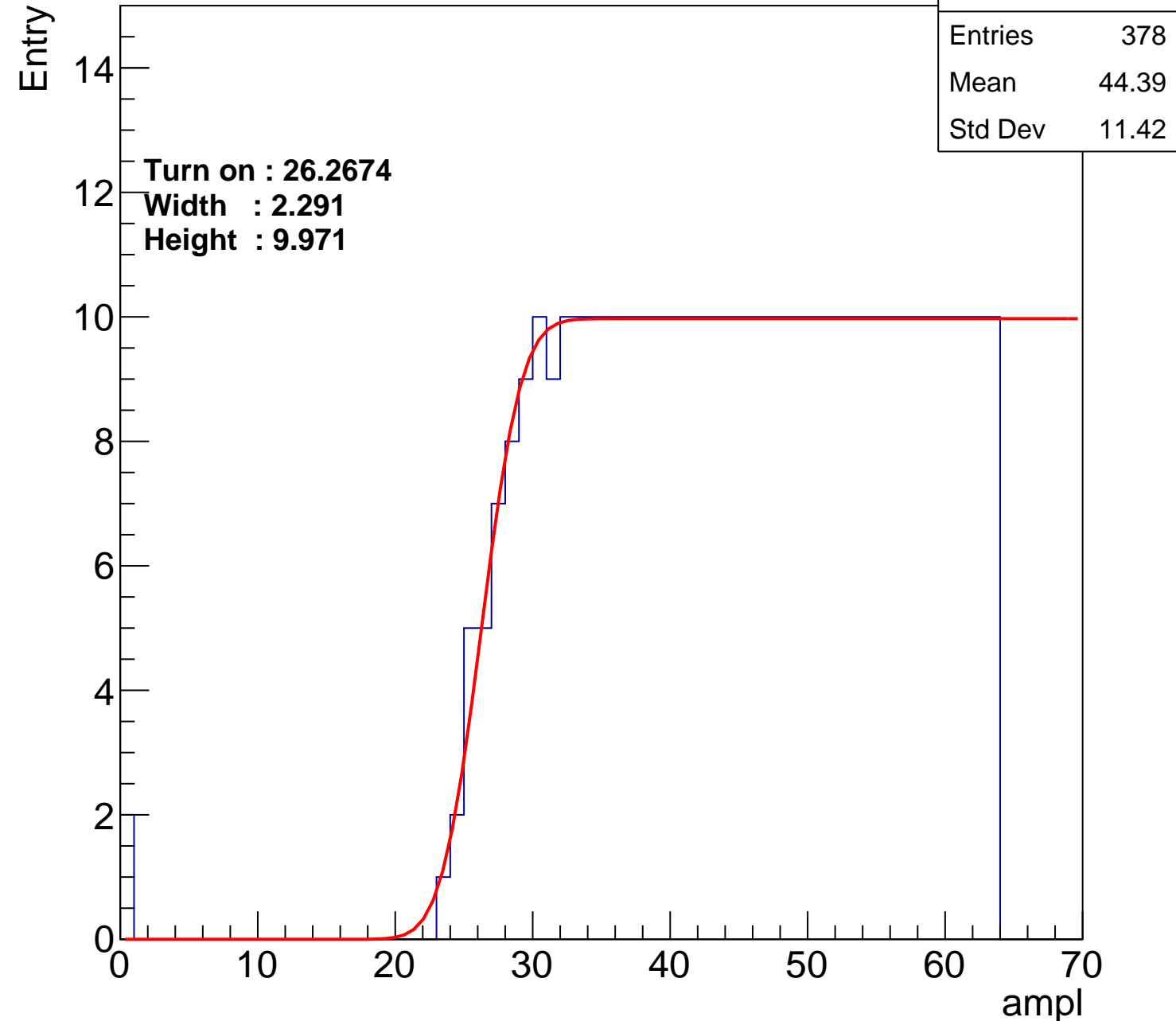
Width : 2.291

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.53
Std Dev	11.5

Turn on : 27.5627

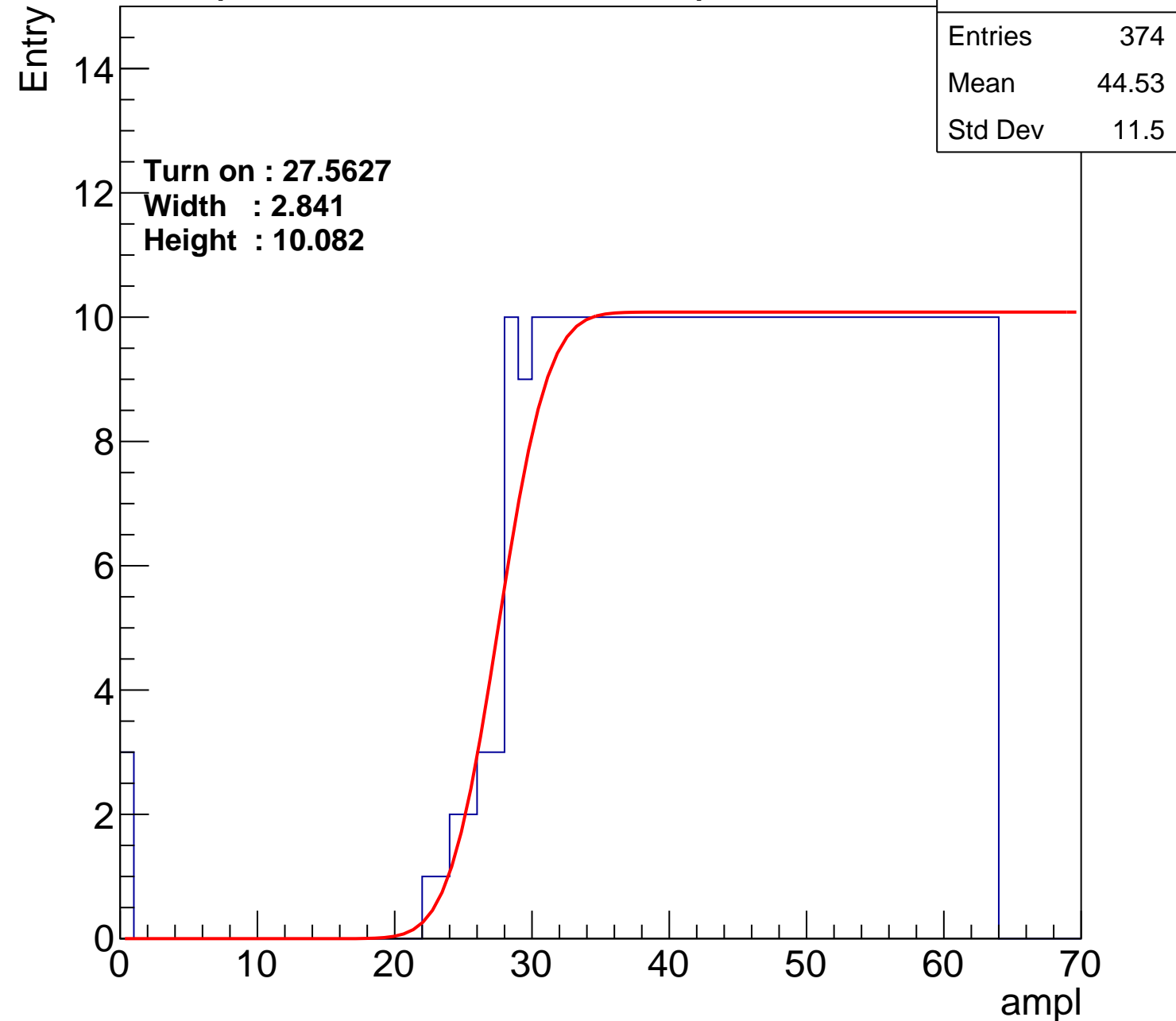
Width : 2.841

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.19
Std Dev	11.34

Turn on : 25.8756

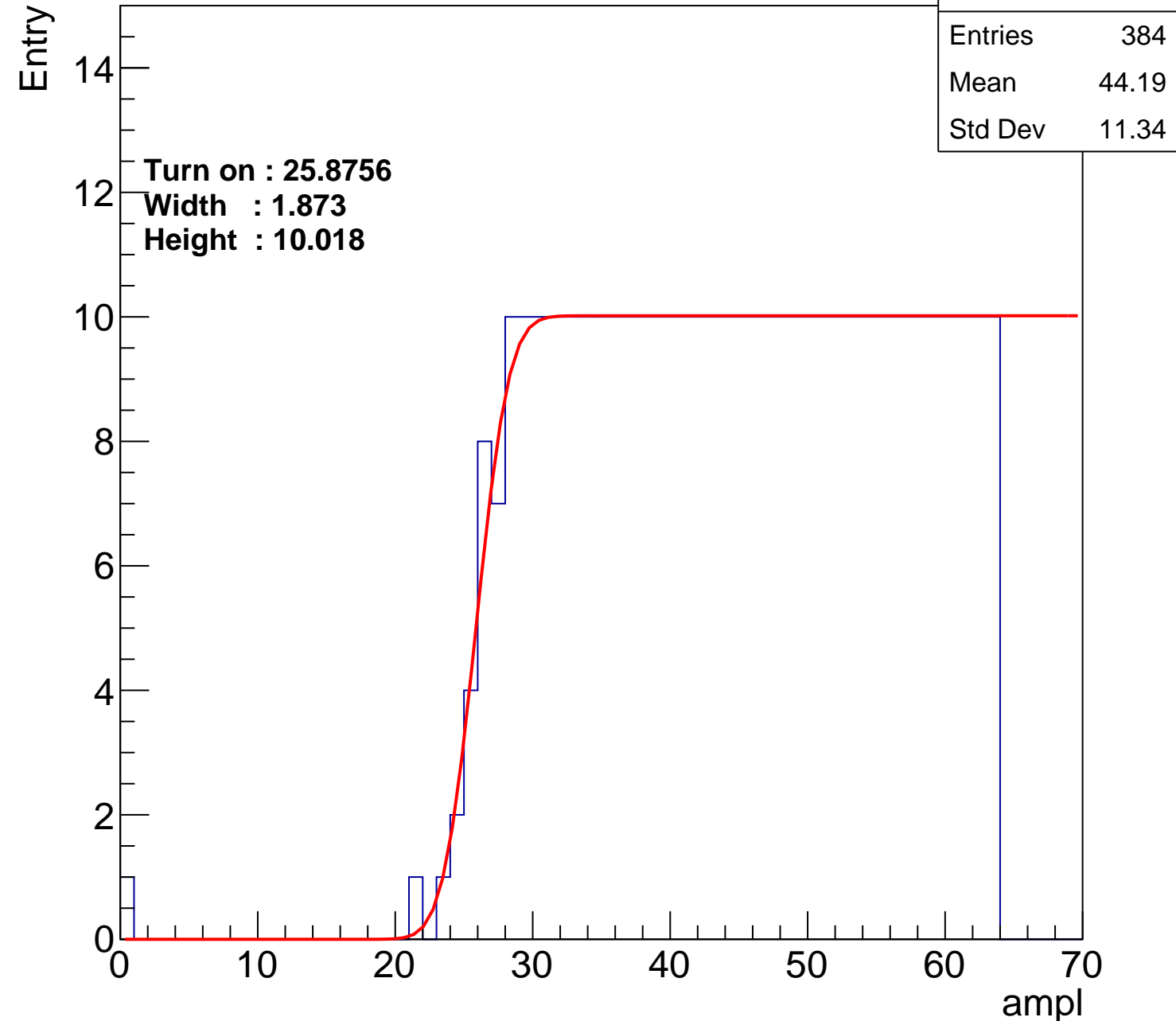
Width : 1.873

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch26

calib_packv5_042523_0143.root, FC#8, port C1

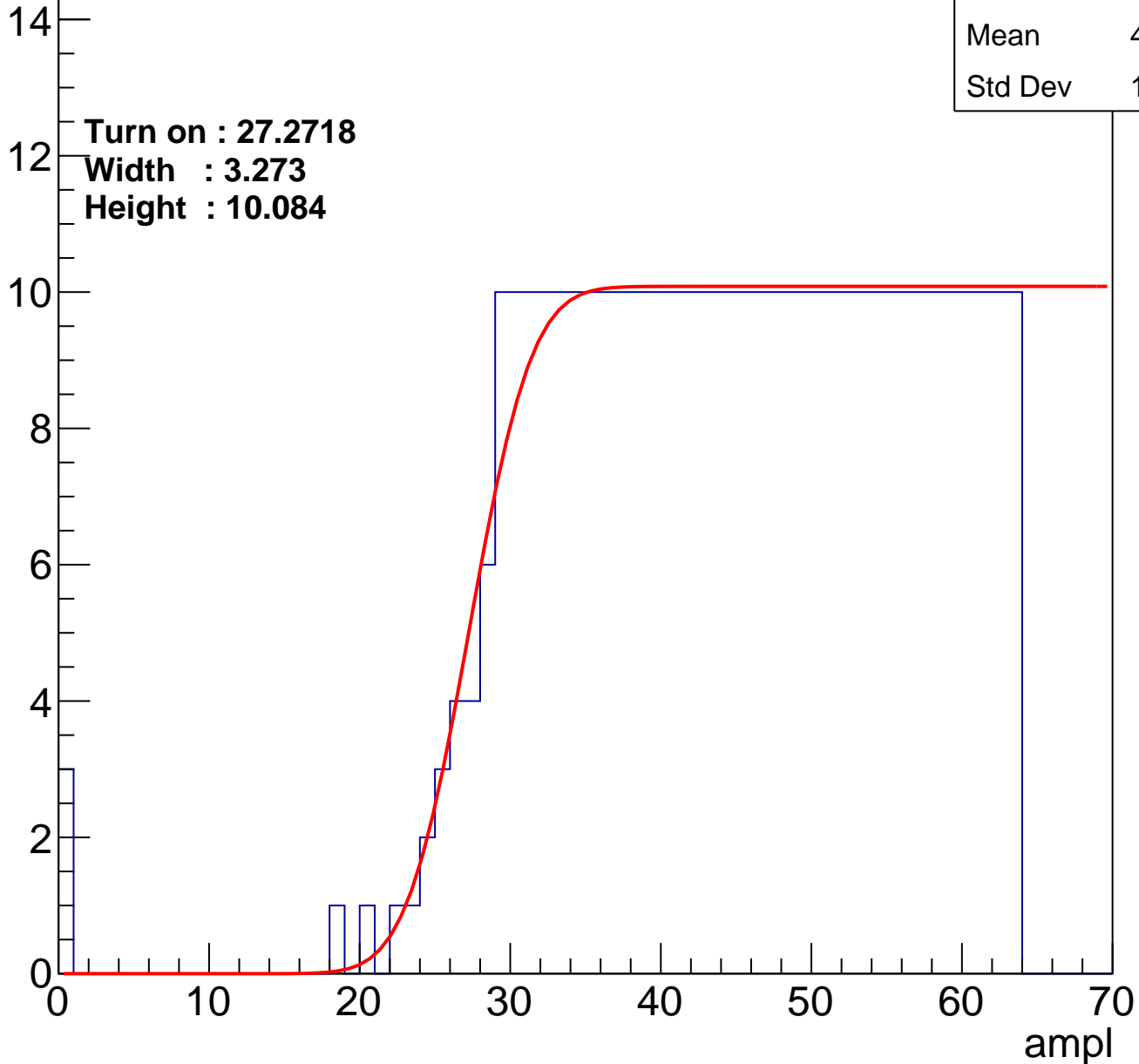
Entries	376
Mean	44.38
Std Dev	11.64

Turn on : 27.2718

Width : 3.273

Height : 10.084

Entry



B0L002S, U6-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	46.1
Std Dev	10.33

Turn on : 29.8373

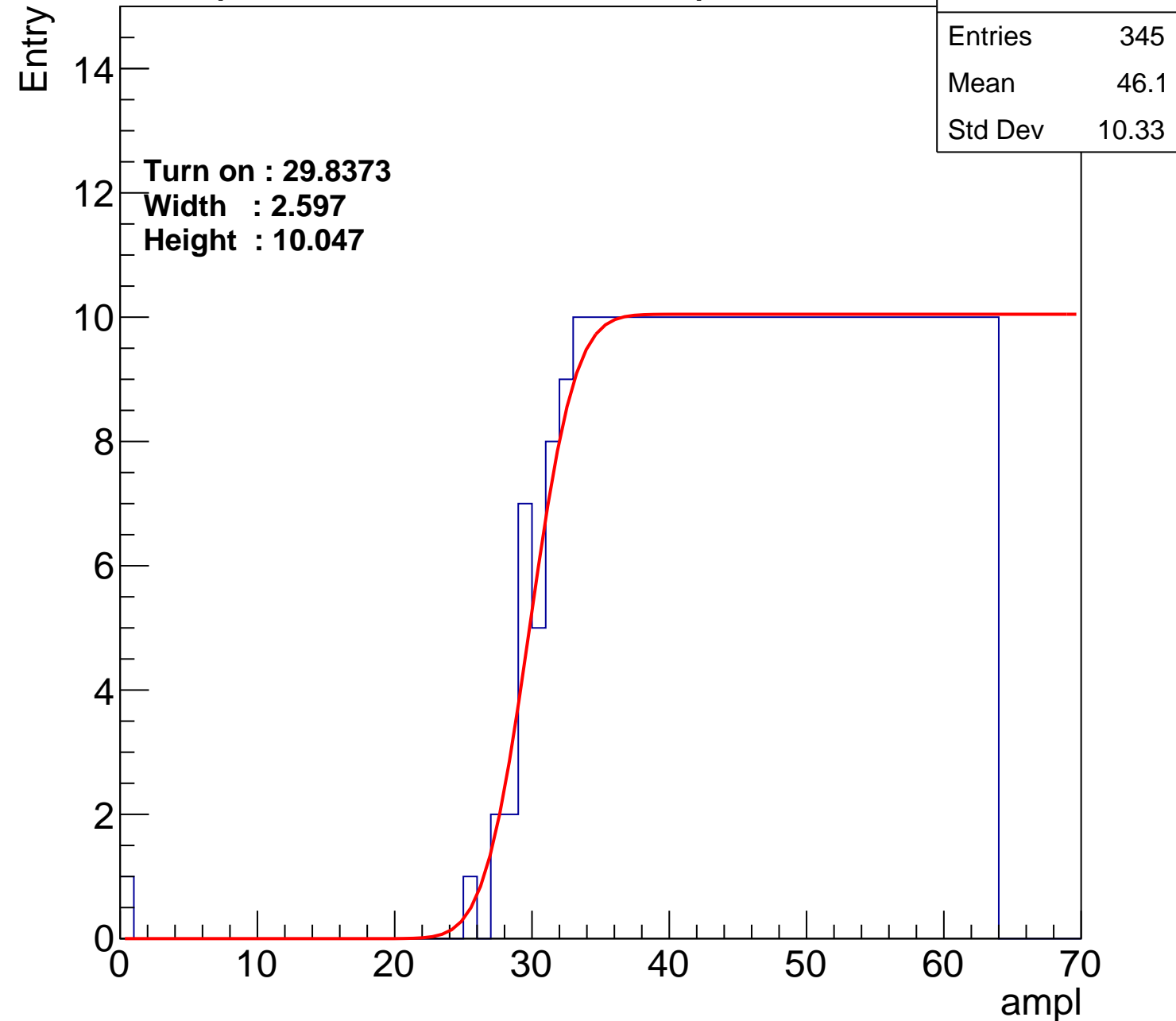
Width : 2.597

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.25
Std Dev	11.41

Turn on : 26.2098

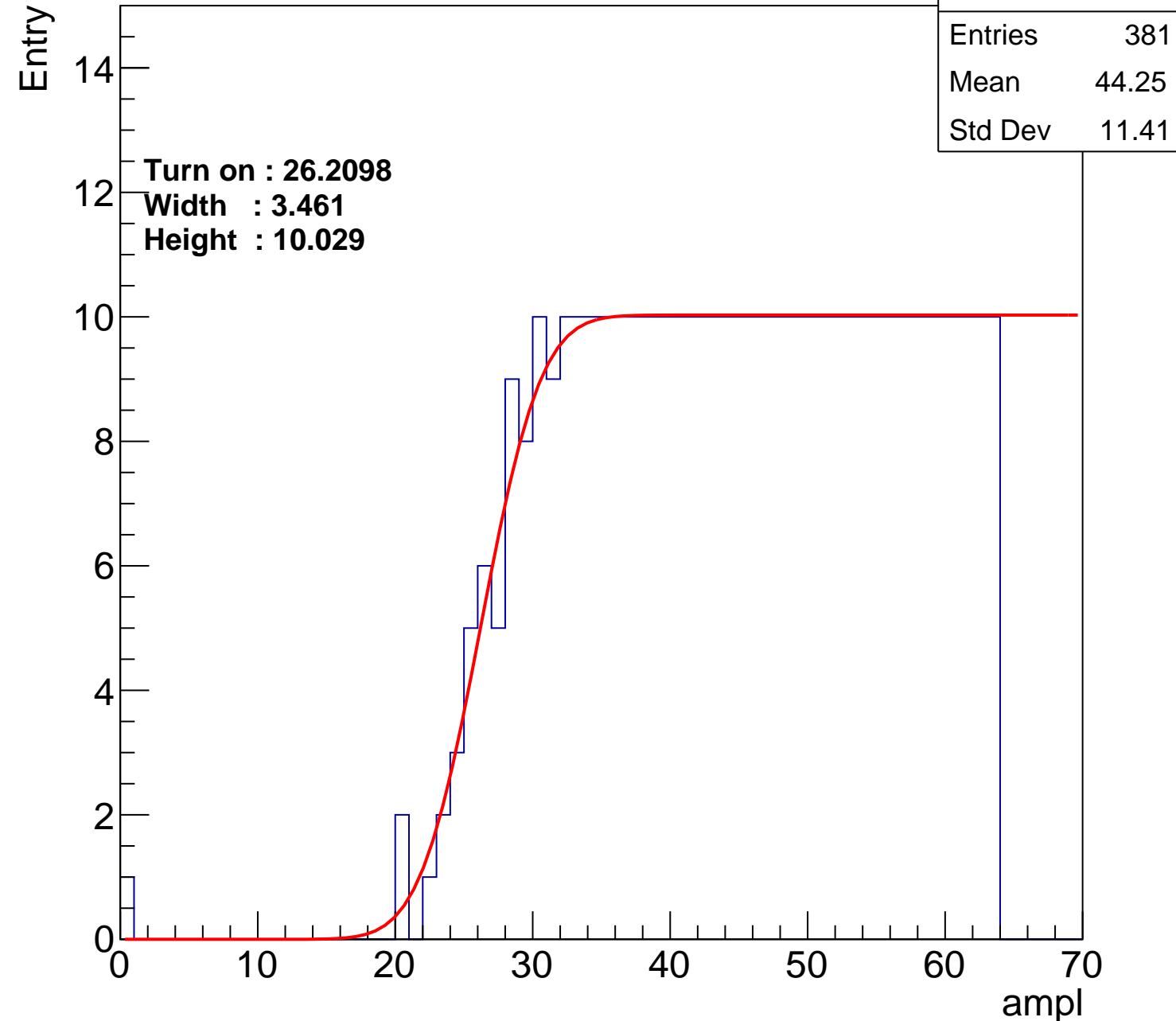
Width : 3.461

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch29

calib_packv5_042523_0143.root, FC#8, port C1

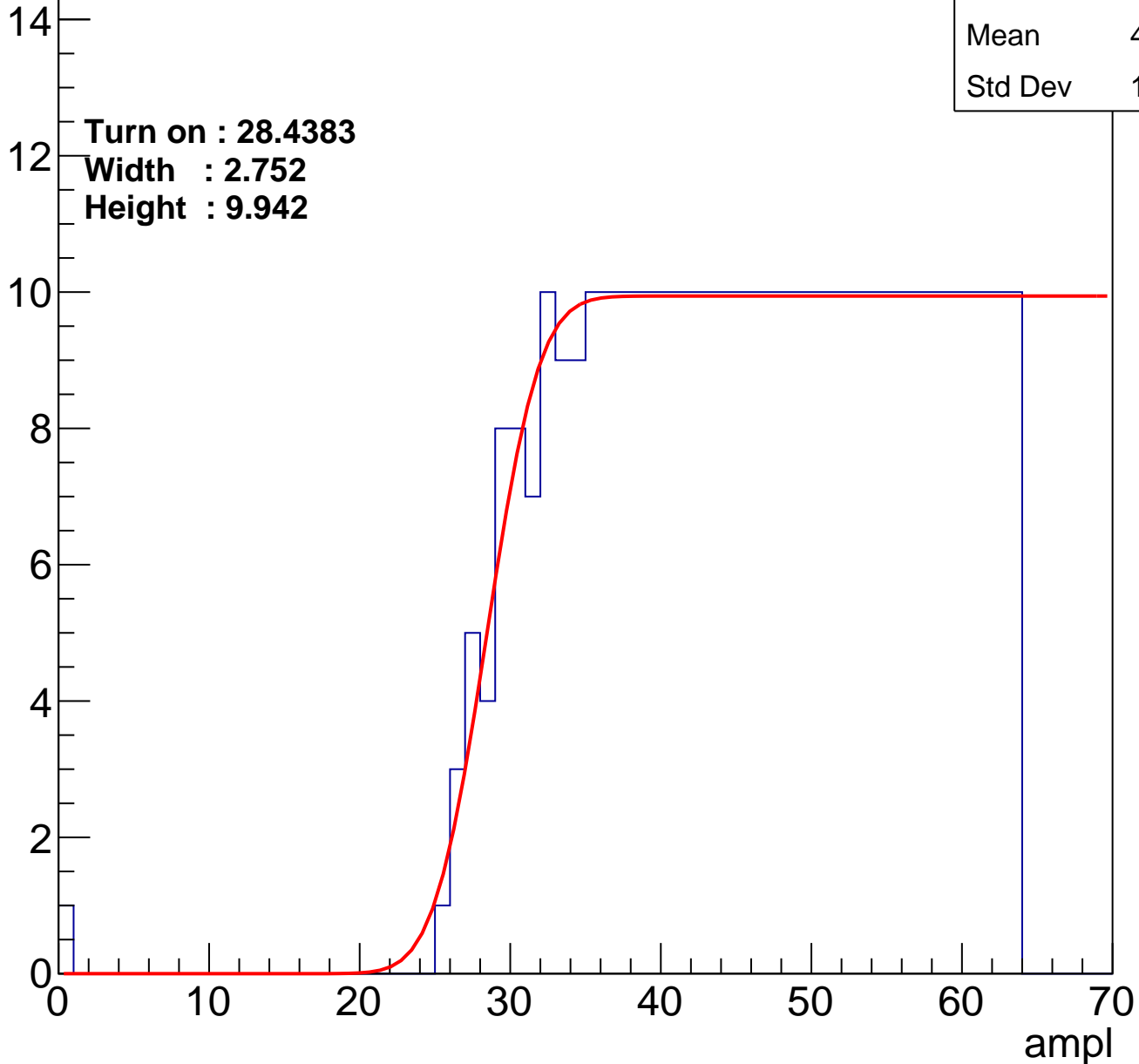
Entry

Entries	355
Mean	45.55
Std Dev	10.67

Turn on : 28.4383

Width : 2.752

Height : 9.942



B0L002S, U6-ch30

calib_packv5_042523_0143.root, FC#8, port C1

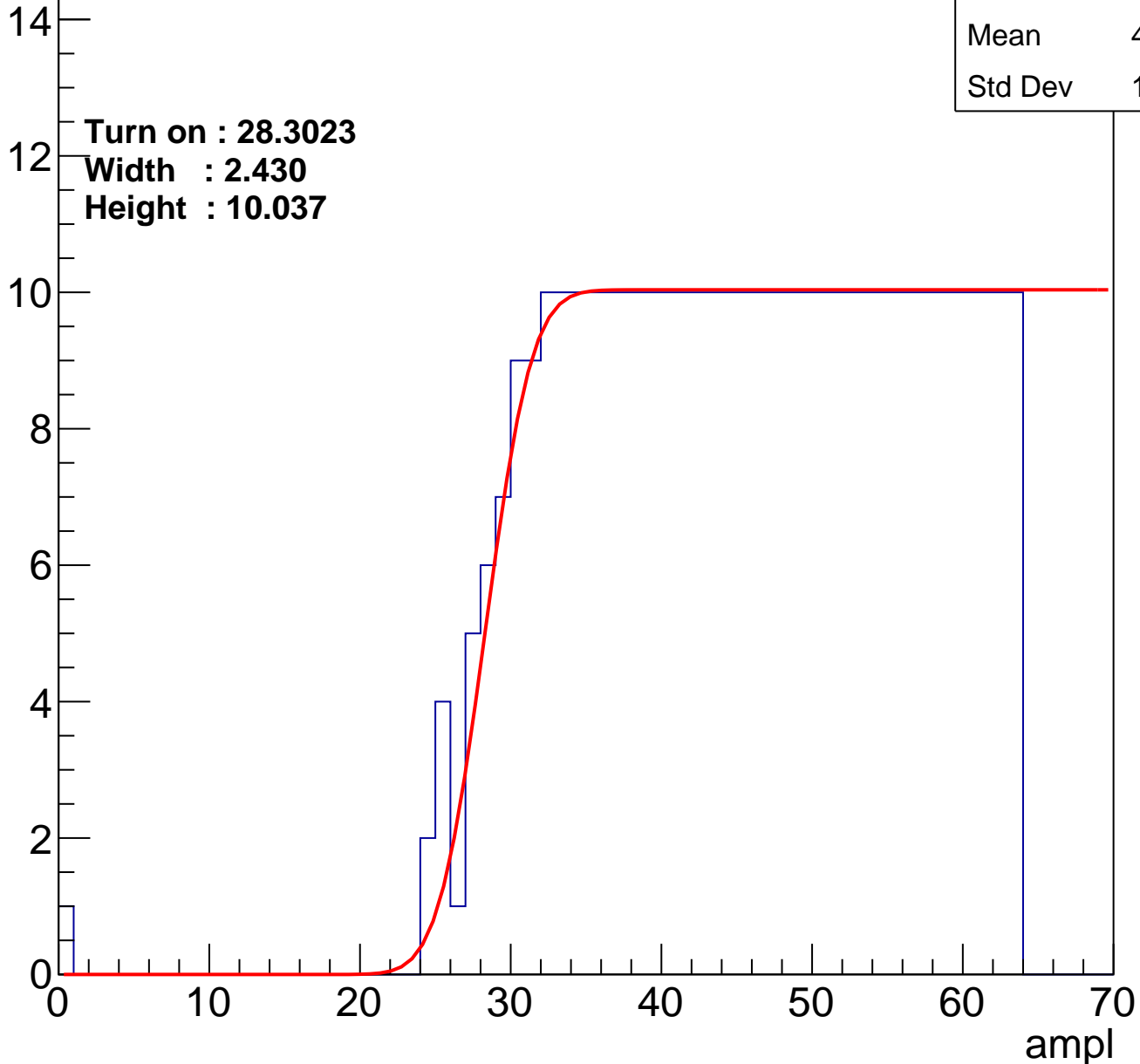
Entries	364
Mean	45.13
Std Dev	10.88

Turn on : 28.3023

Width : 2.430

Height : 10.037

Entry



B0L002S, U6-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
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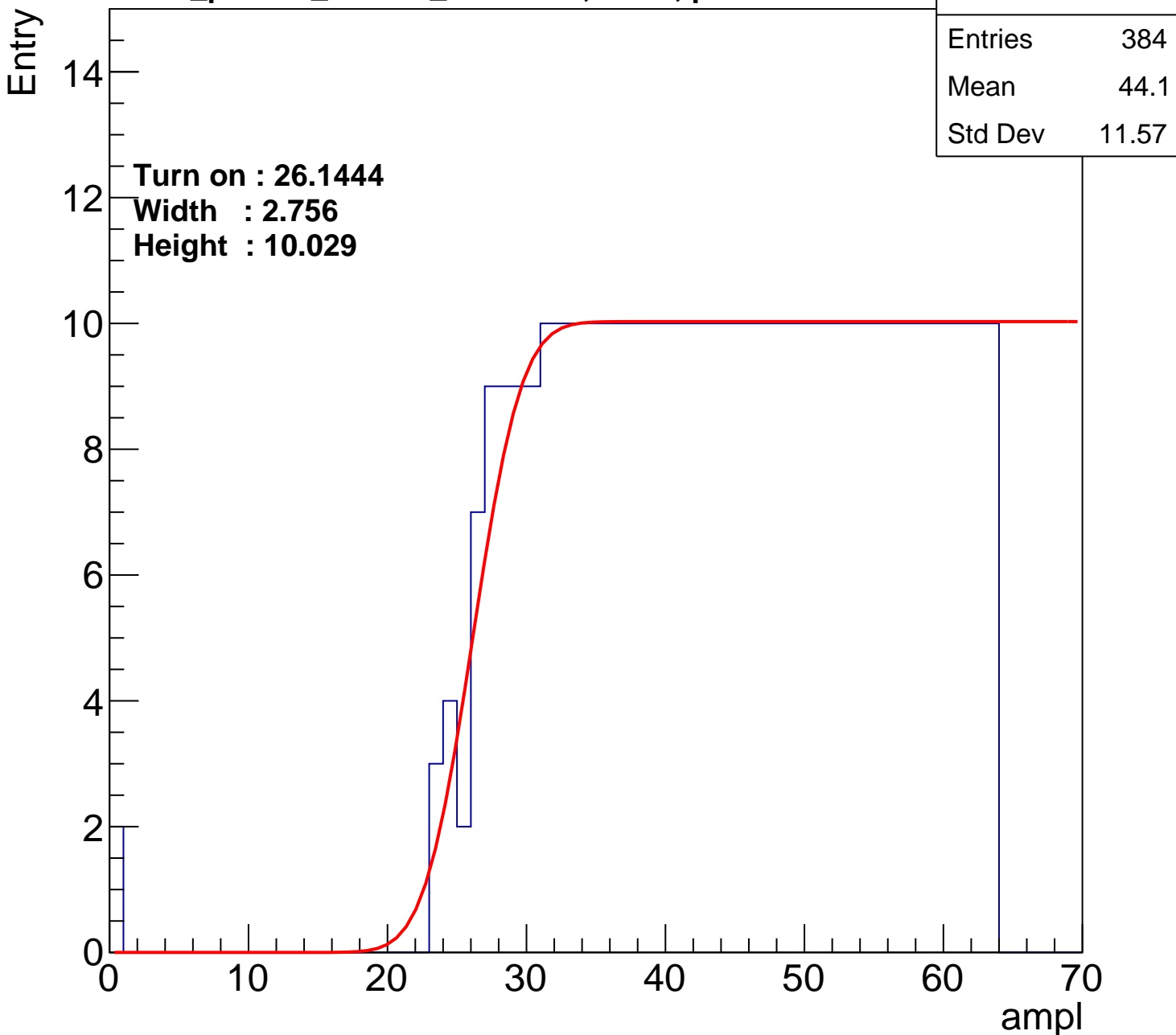
Mean	44.1
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Std Dev	11.57
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Turn on : 26.1444

Width : 2.756

Height : 10.029



B0L002S, U6-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.51
Std Dev	10.84

Turn on : 28.6430

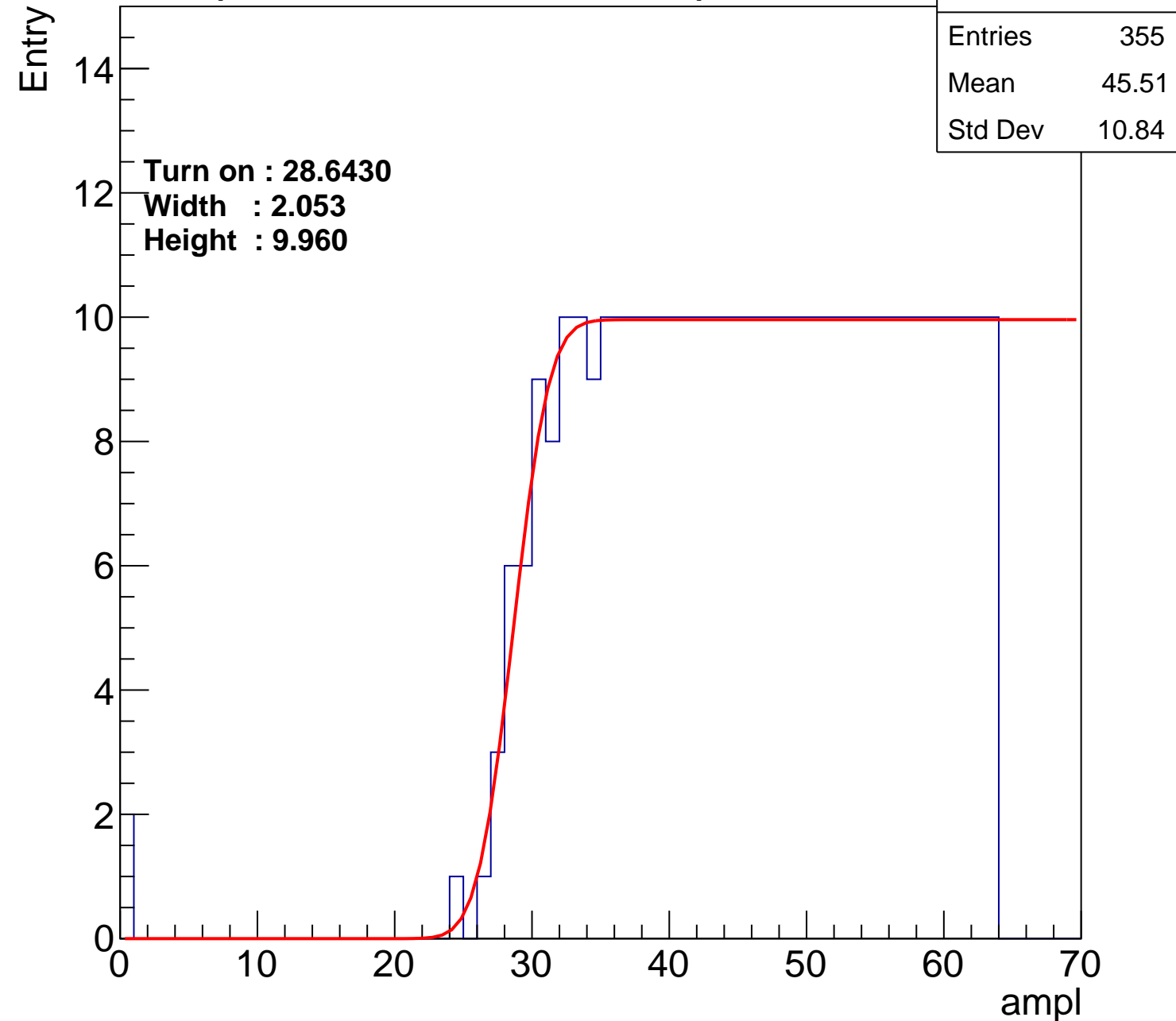
Width : 2.053

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	335
Mean	46.57
Std Dev	10.1

Turn on : 31.3714

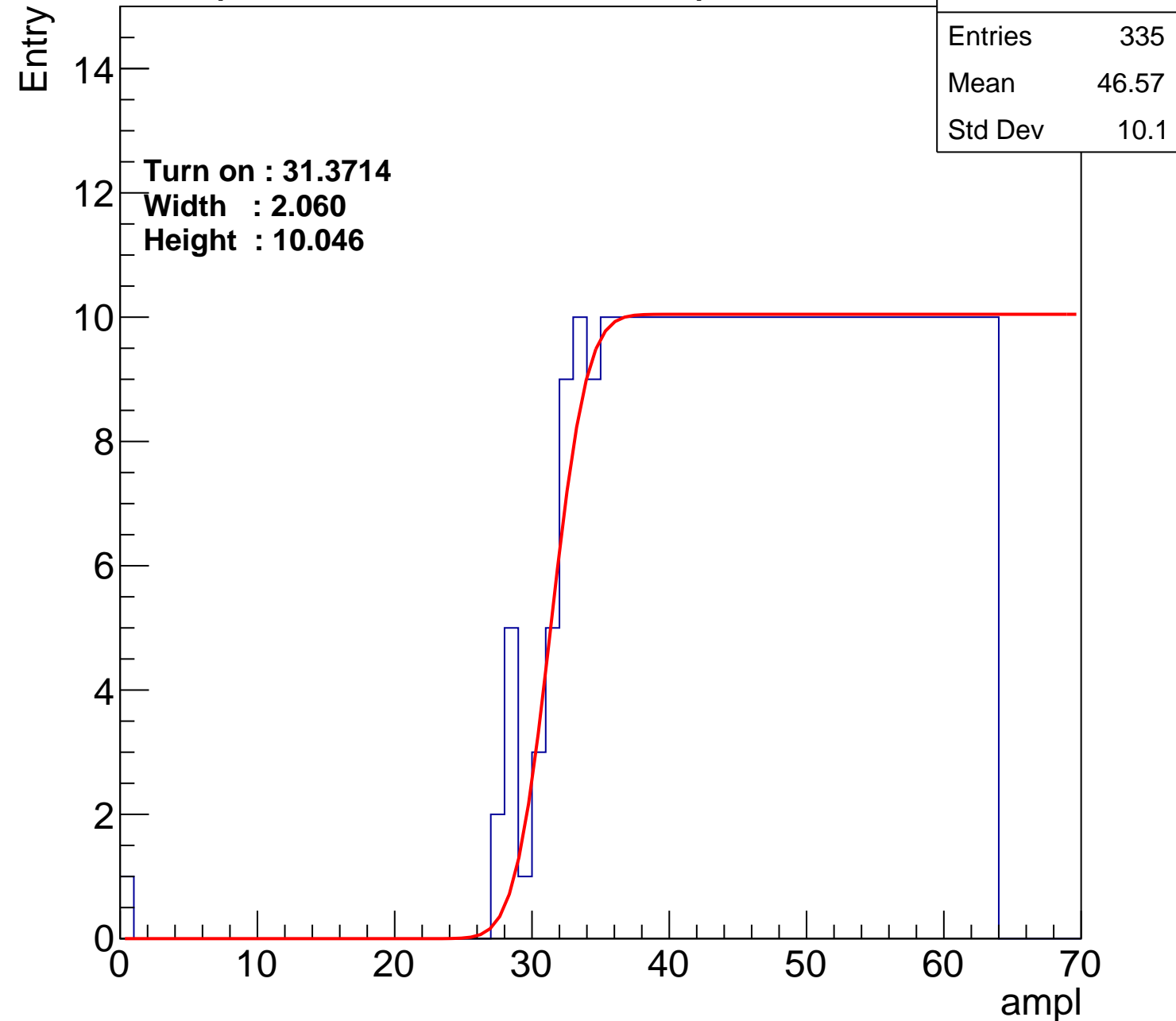
Width : 2.060

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch34

calib_packv5_042523_0143.root, FC#8, port C1

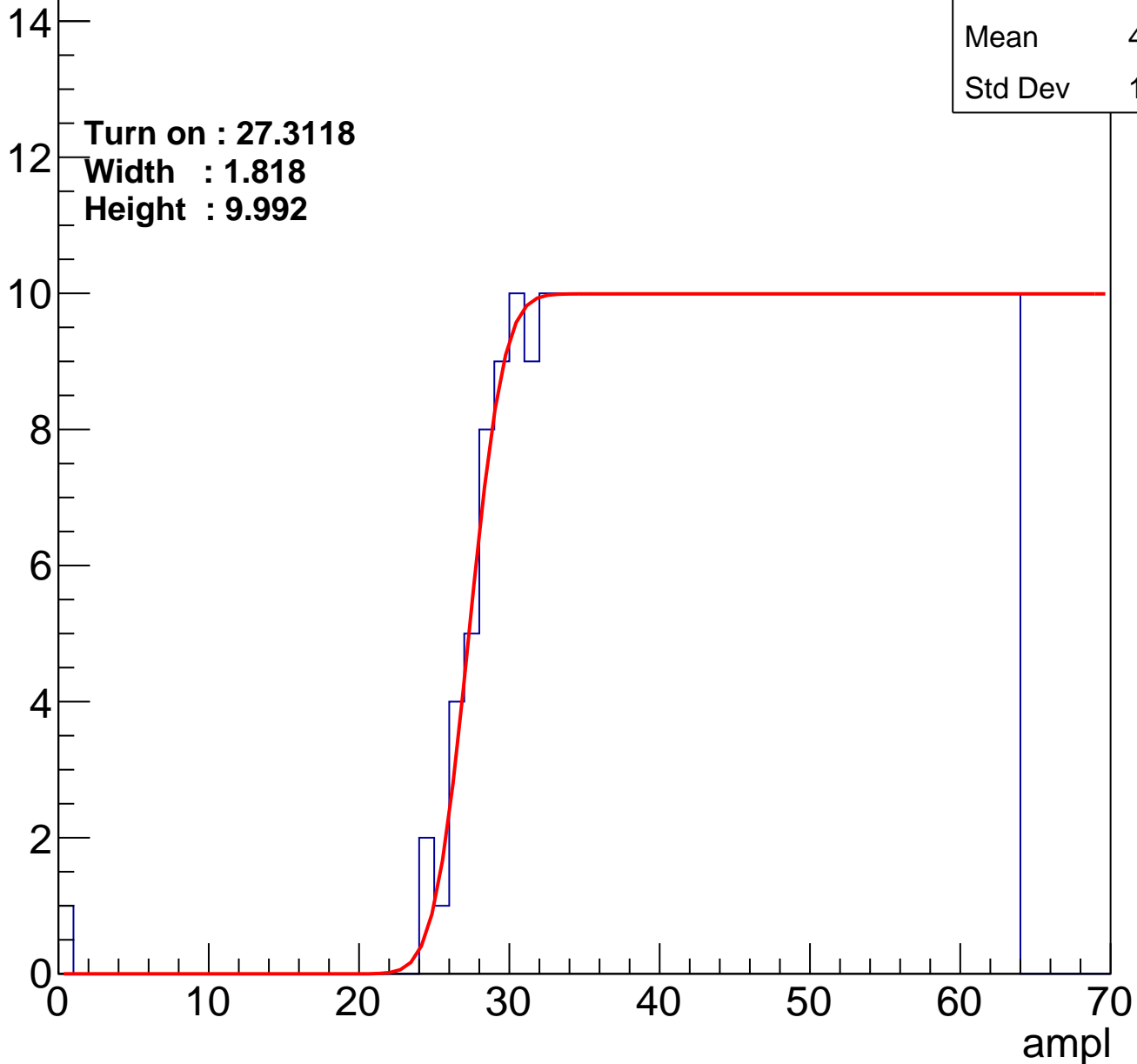
Entry

Entries	369
Mean	44.92
Std Dev	10.95

Turn on : 27.3118

Width : 1.818

Height : 9.992



B0L002S, U6-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.58
Std Dev	10.84

Turn on : 29.1190

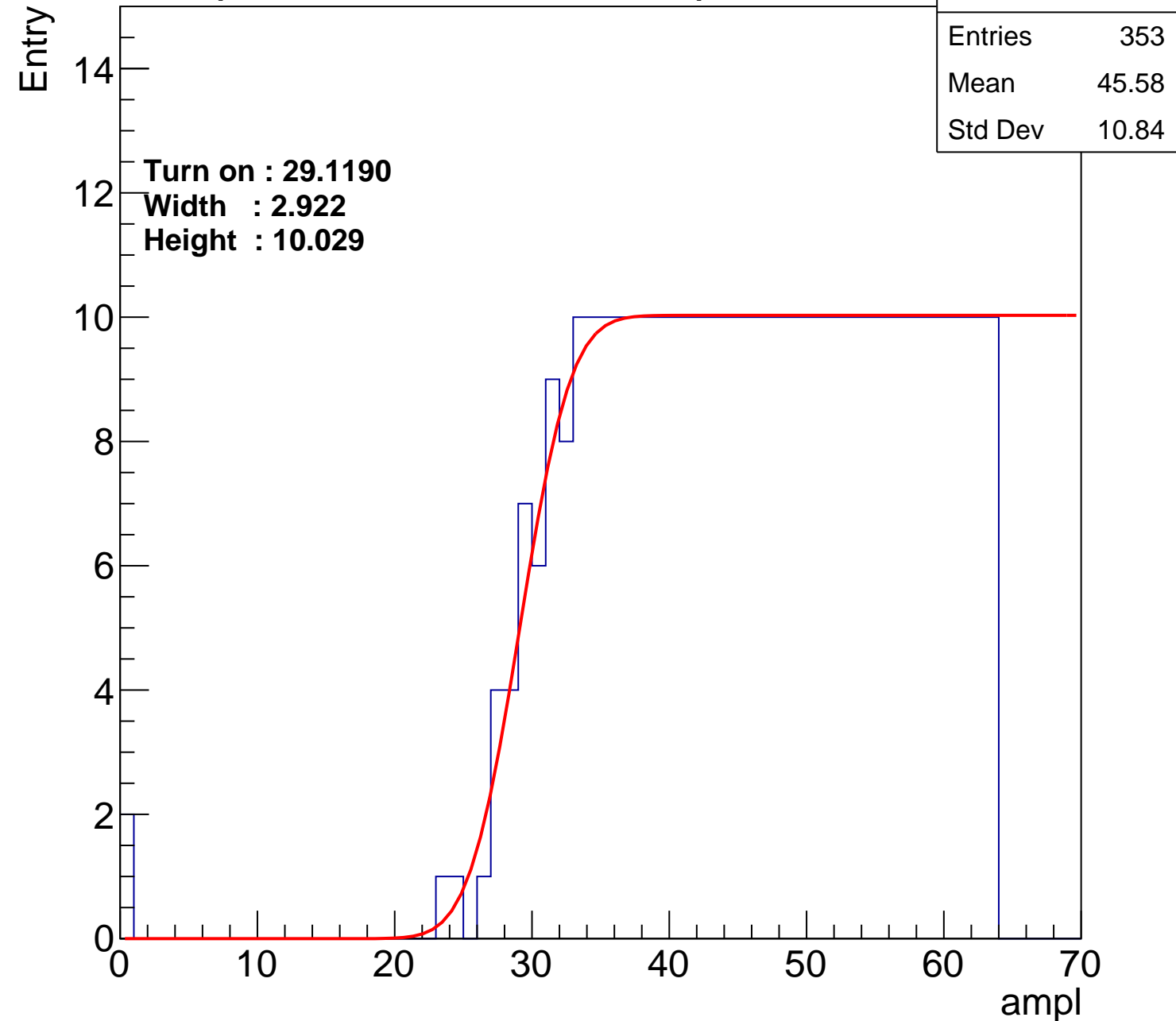
Width : 2.922

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.5231

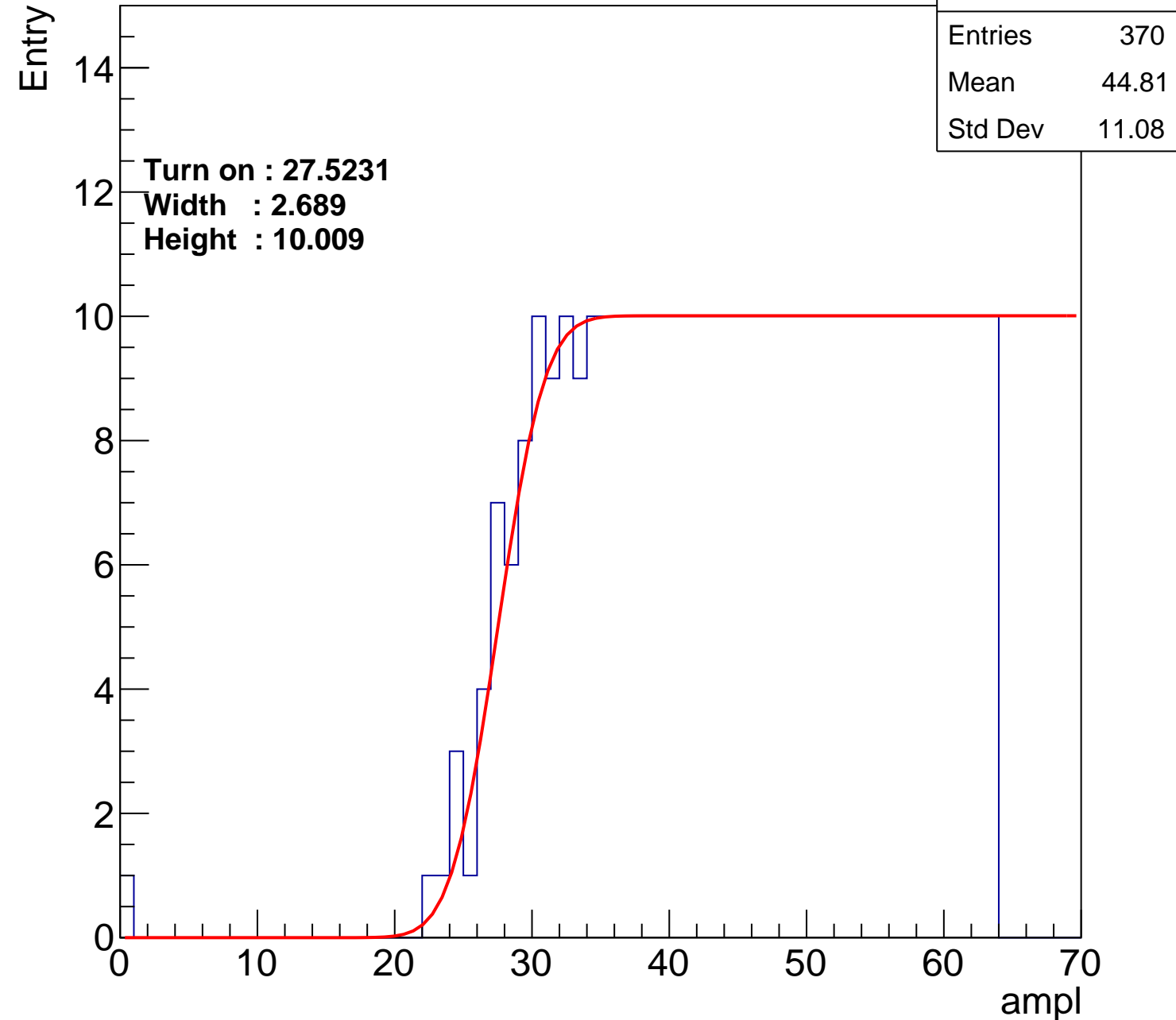
Width : 2.689

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.47
Std Dev	10.86

Turn on : 28.9237

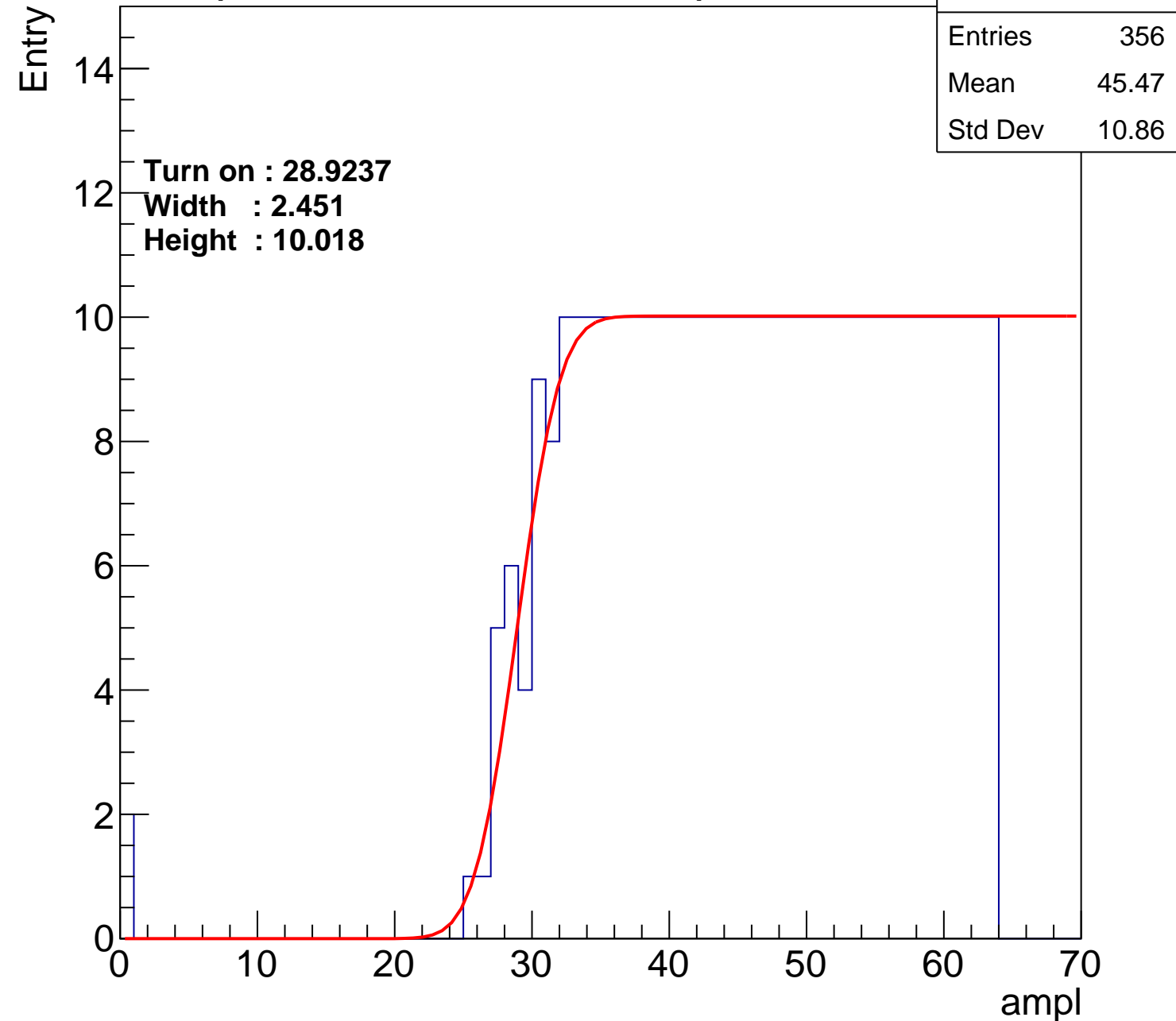
Width : 2.451

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch38

calib_packv5_042523_0143.root, FC#8, port C1

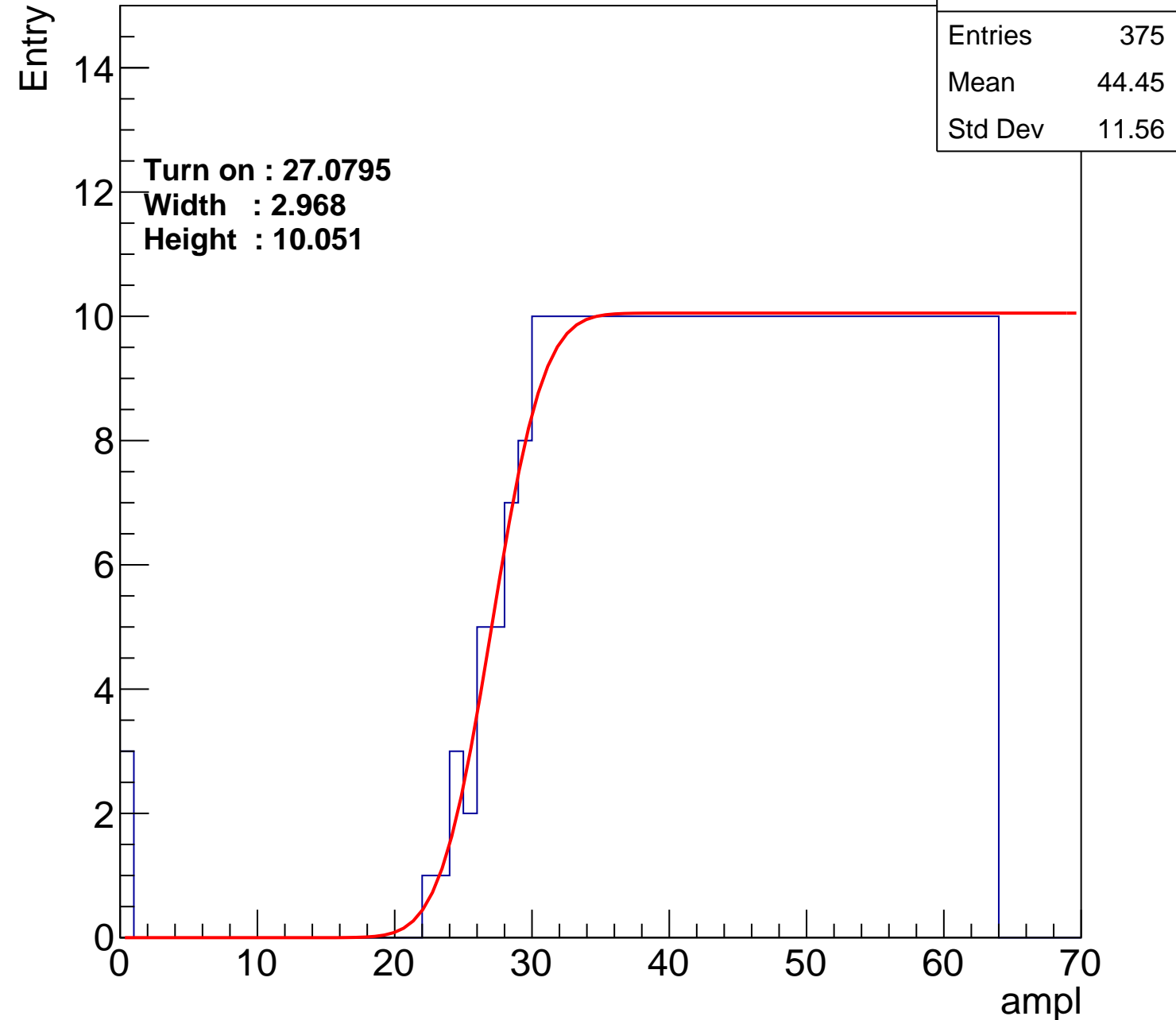
Entry

14
12
10
8
6
4
2
0

Turn on : 27.0795
Width : 2.968
Height : 10.051

Entries	375
Mean	44.45
Std Dev	11.56

ampl



B0L002S, U6-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.18
Std Dev	11.05

Turn on : 28.4734

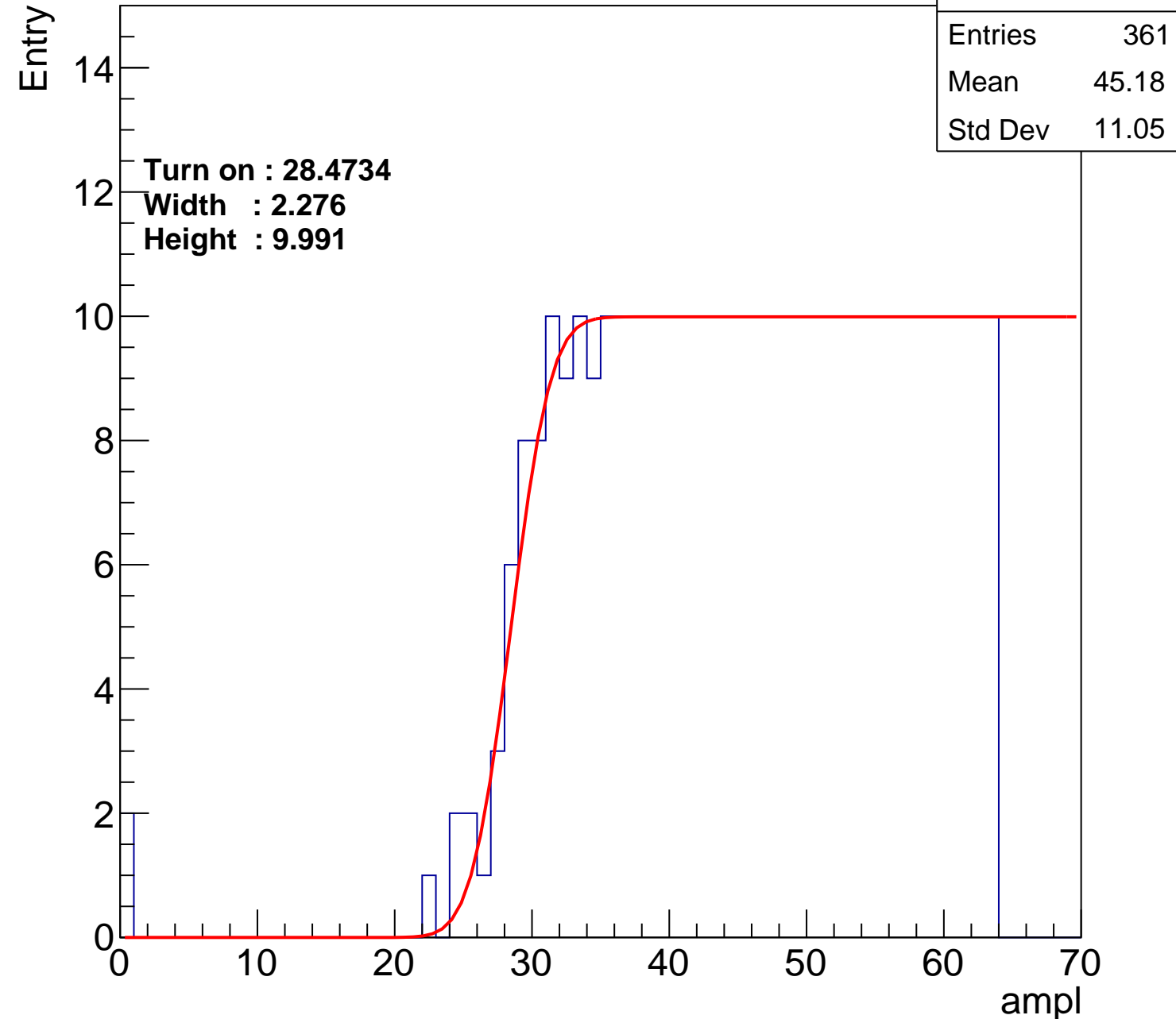
Width : 2.276

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.63
Std Dev	10.66

Turn on : 29.0573

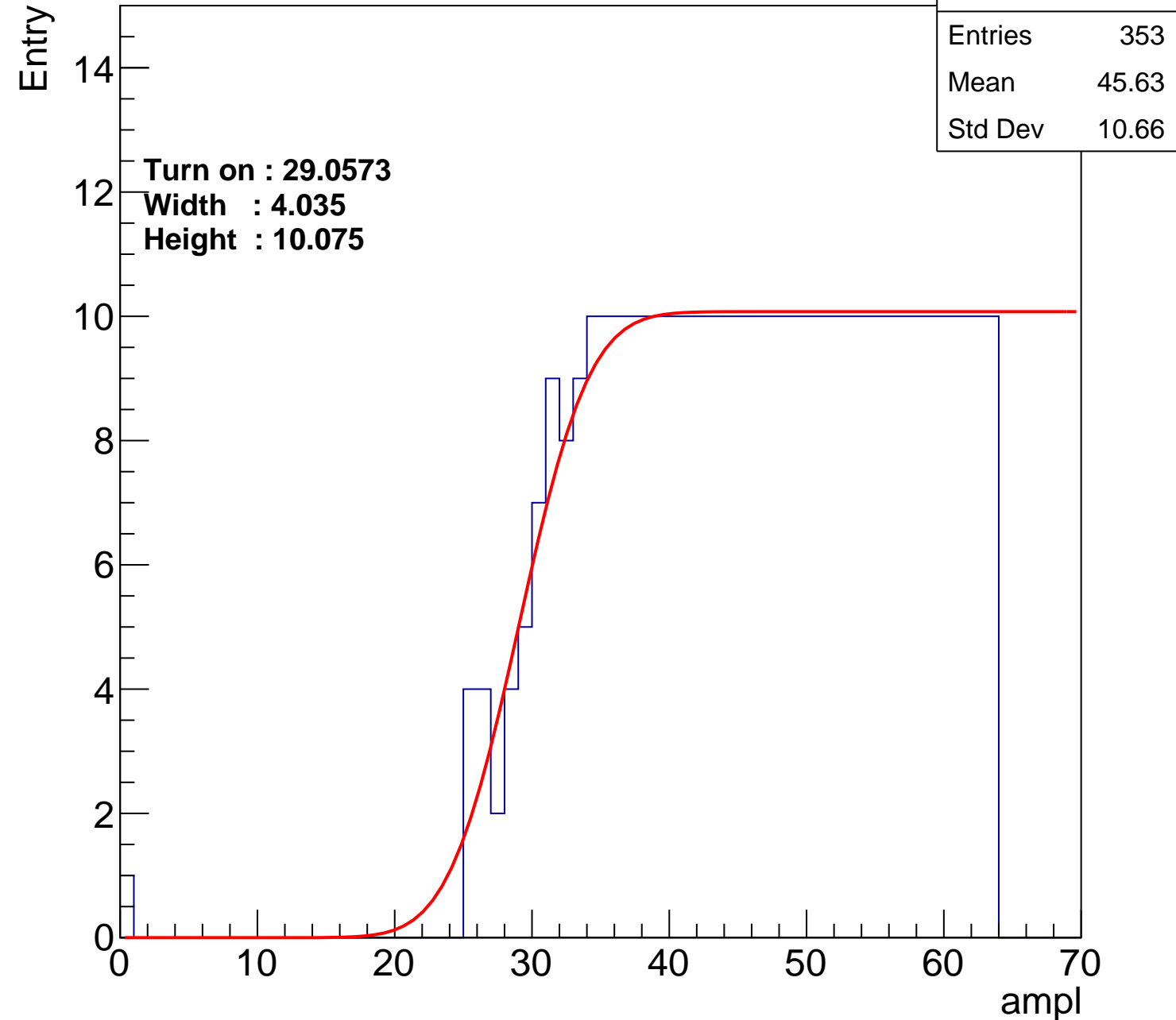
Width : 4.035

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch41

calib_packv5_042523_0143.root, FC#8, port C1

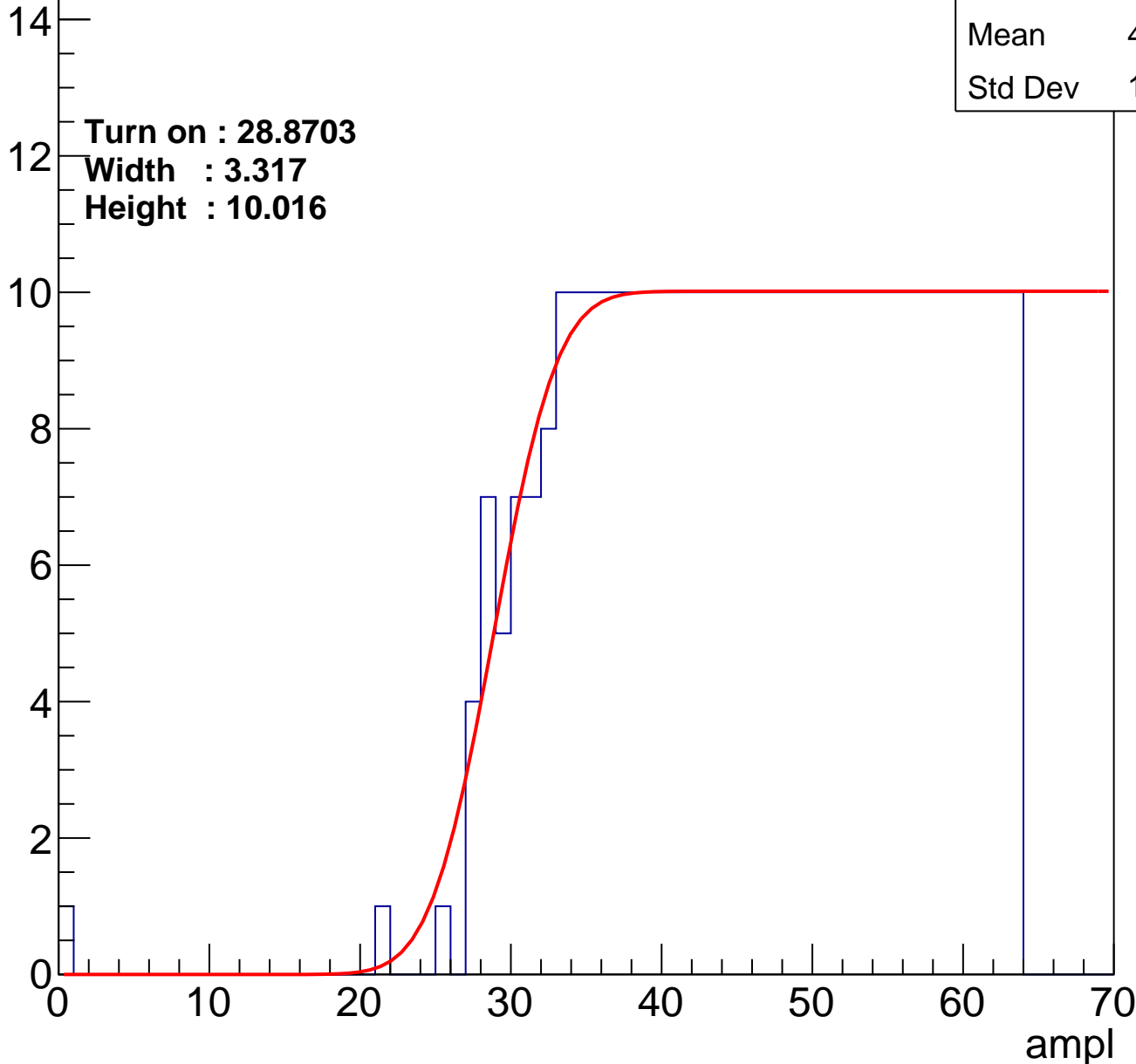
Entries	351
Mean	45.75
Std Dev	10.58

Turn on : 28.8703

Width : 3.317

Height : 10.016

Entry



B0L002S, U6-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.6211

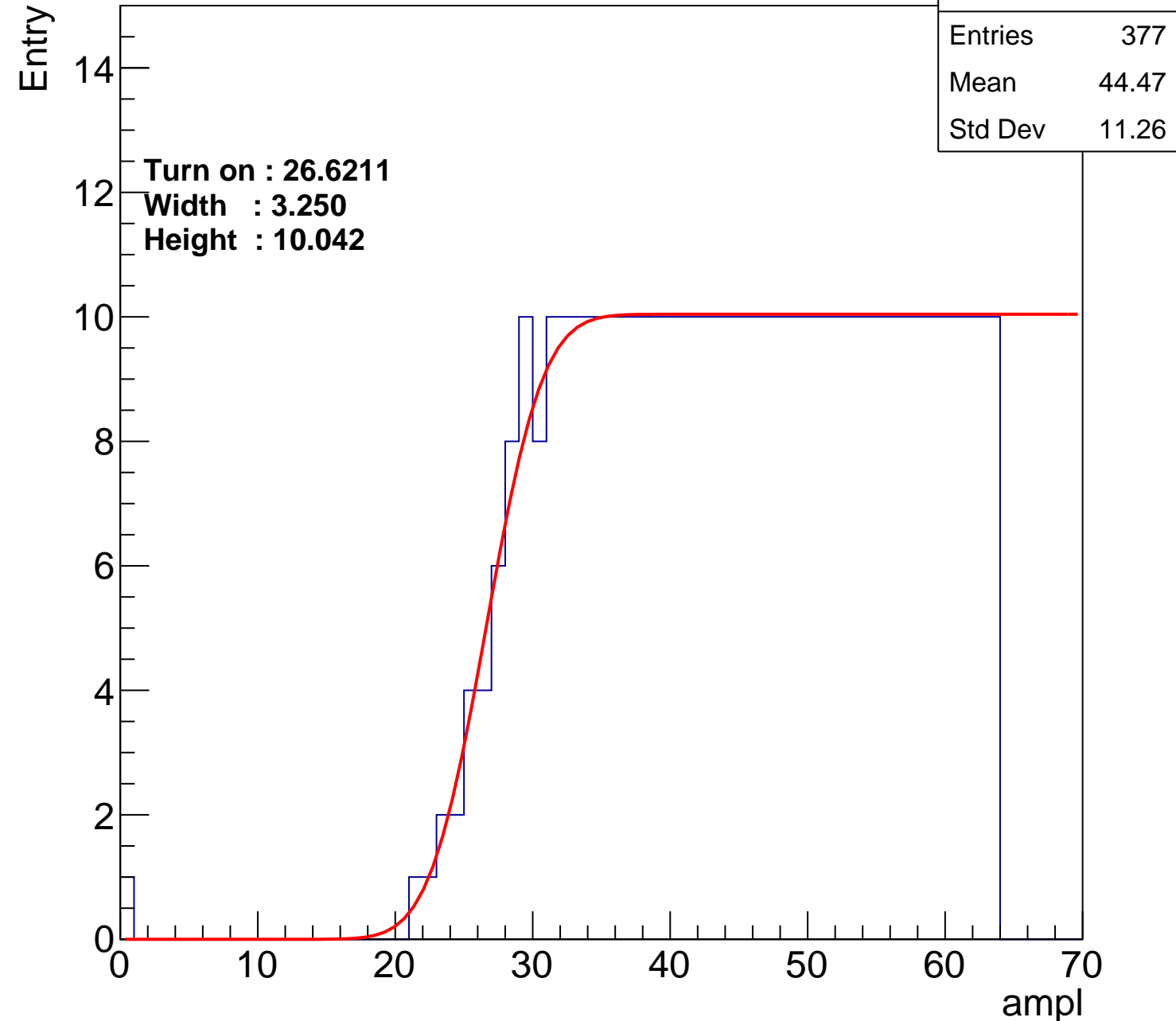
Width : 3.250

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch43

calib_packv5_042523_0143.root, FC#8, port C1

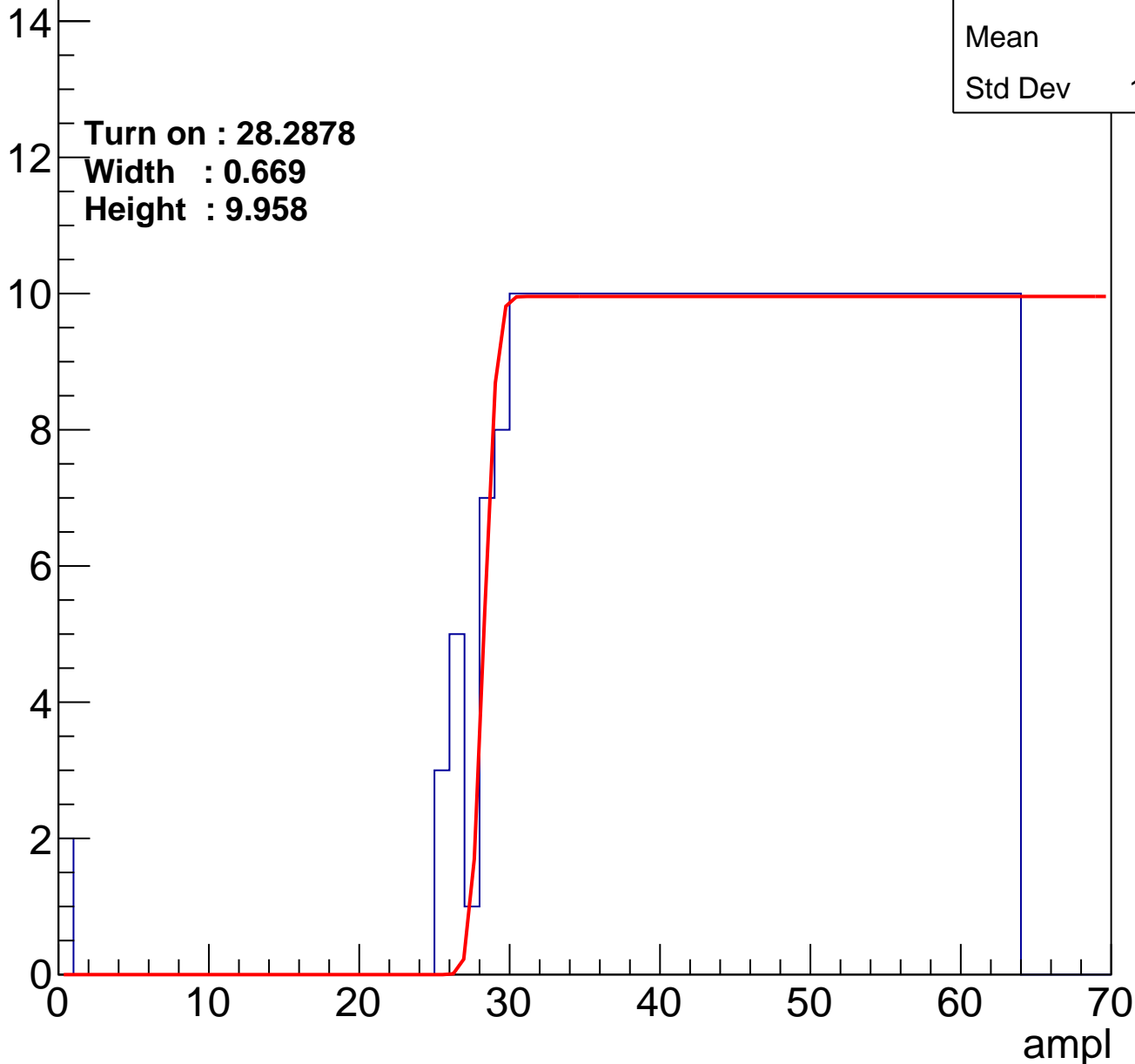
Entry

Entries	366
Mean	45
Std Dev	11.08

Turn on : 28.2878

Width : 0.669

Height : 9.958



B0L002S, U6-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.5
Std Dev	11.45

Turn on : 27.3144

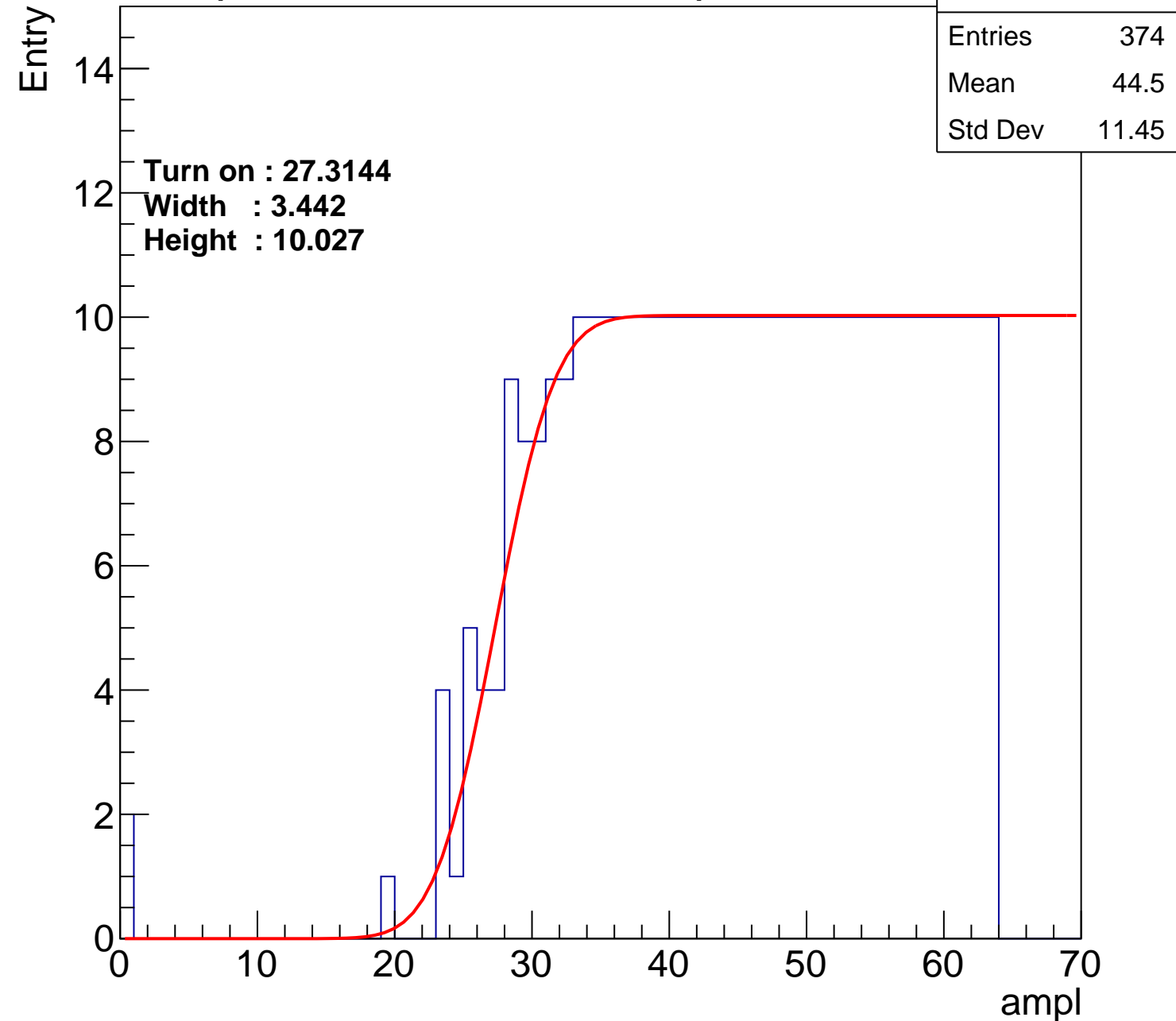
Width : 3.442

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.97
Std Dev	11.18

Turn on : 27.9172

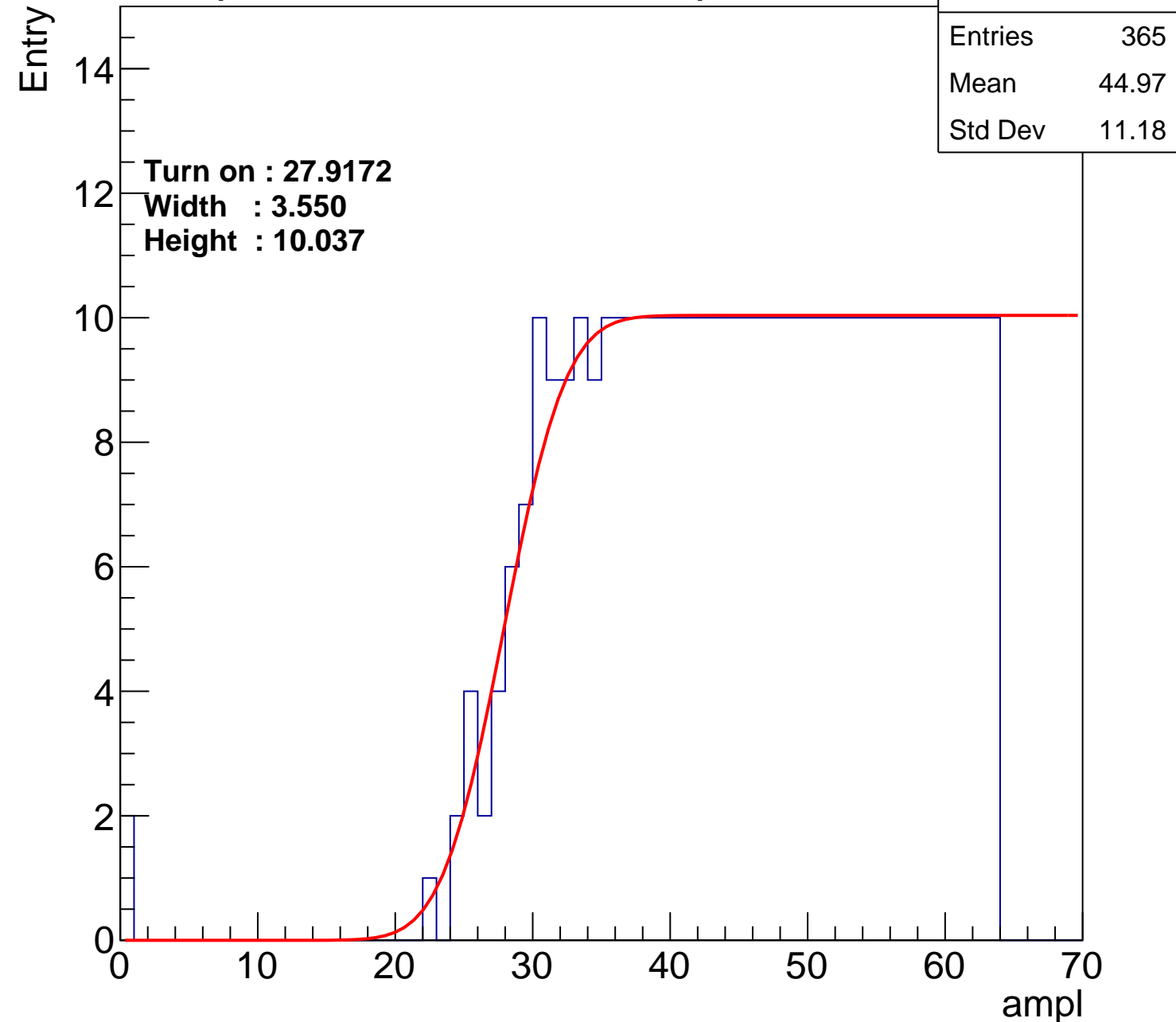
Width : 3.550

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch46

calib_packv5_042523_0143.root, FC#8, port C1

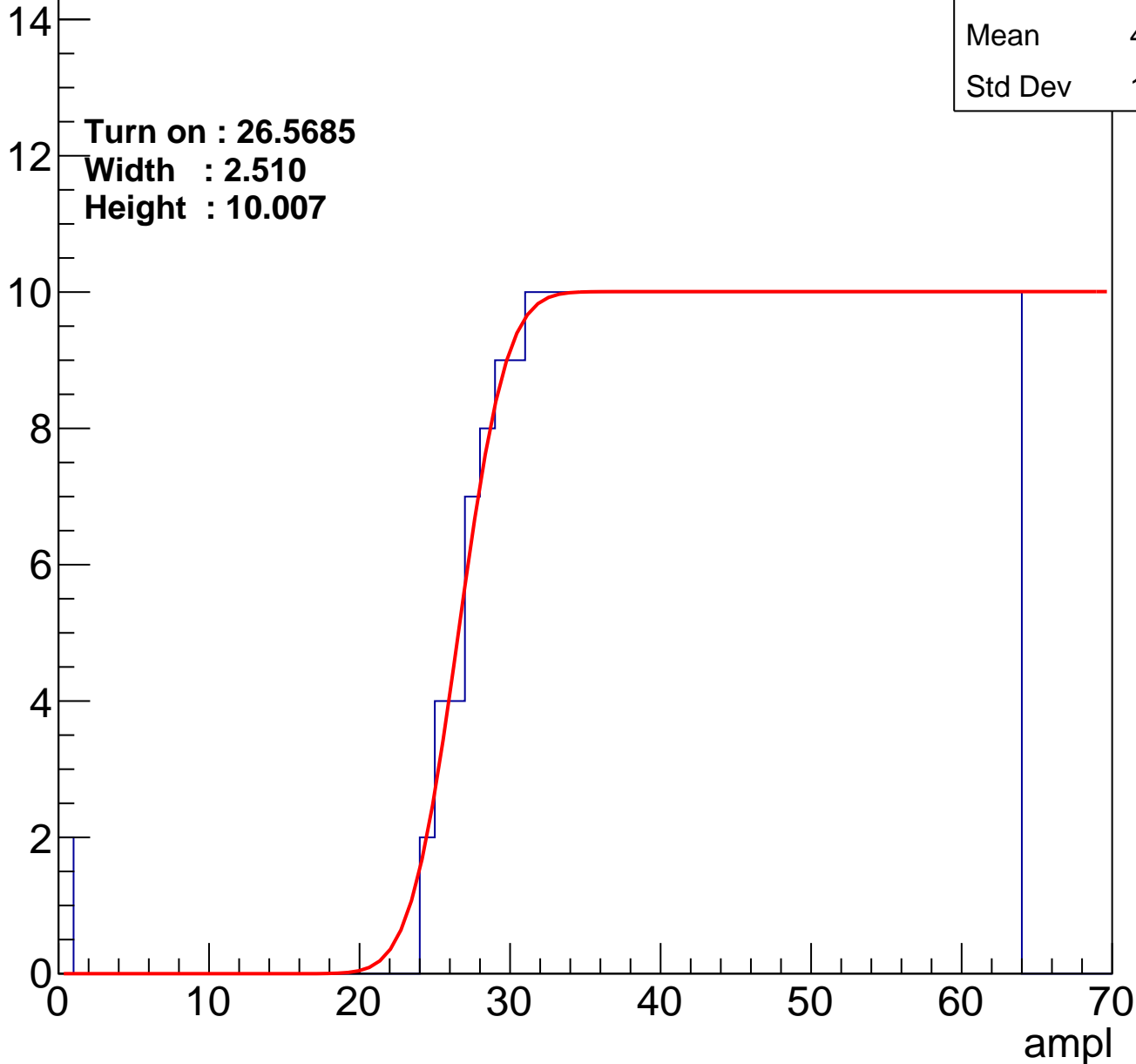
Entry

Entries	375
Mean	44.55
Std Dev	11.32

Turn on : 26.5685

Width : 2.510

Height : 10.007



B0L002S, U6-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.71
Std Dev	11.23

Turn on : 26.7174

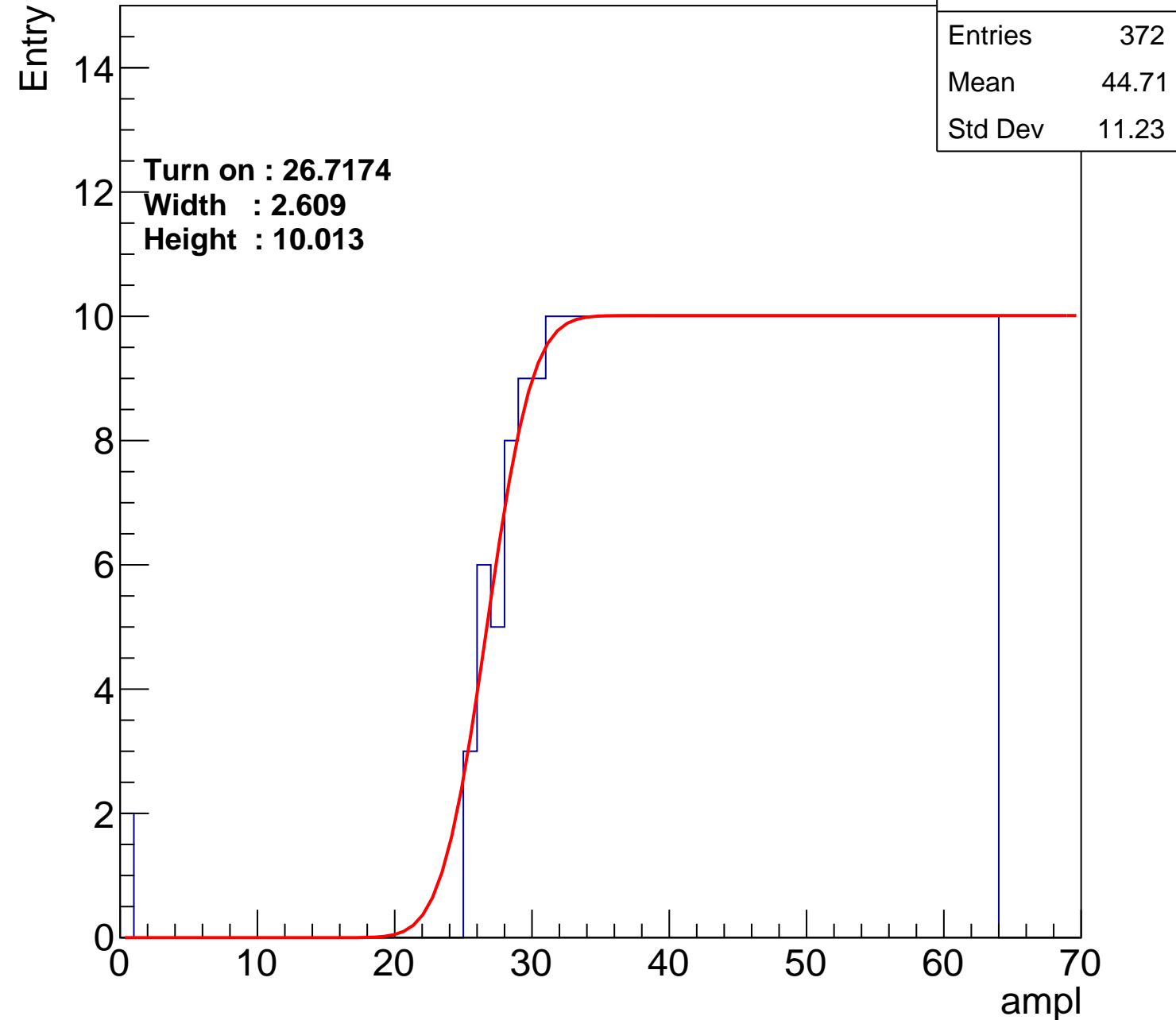
Width : 2.609

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch48

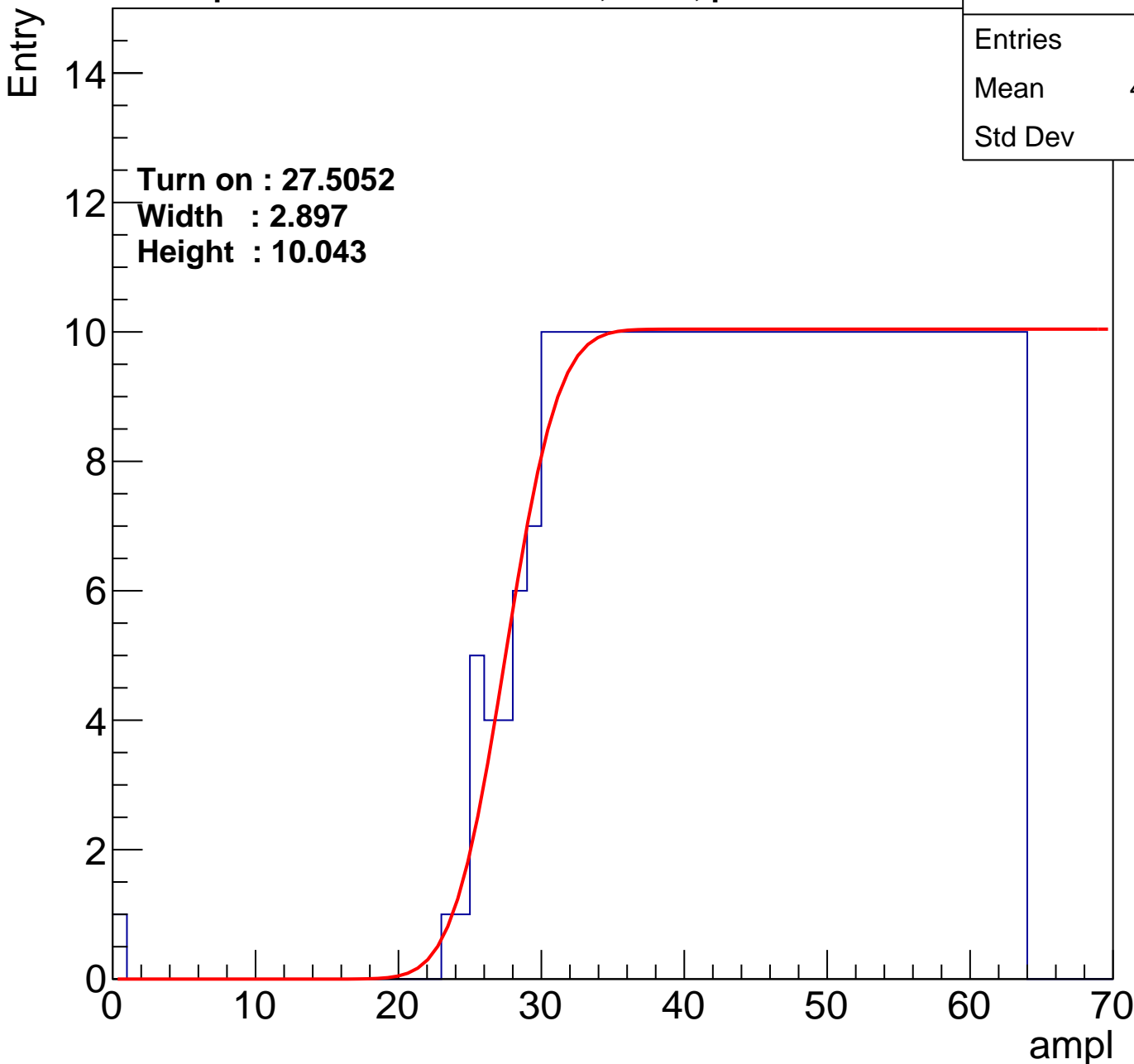
calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.5052

Width : 2.897

Height : 10.043



B0L002S, U6-ch49

calib_packv5_042523_0143.root, FC#8, port C1

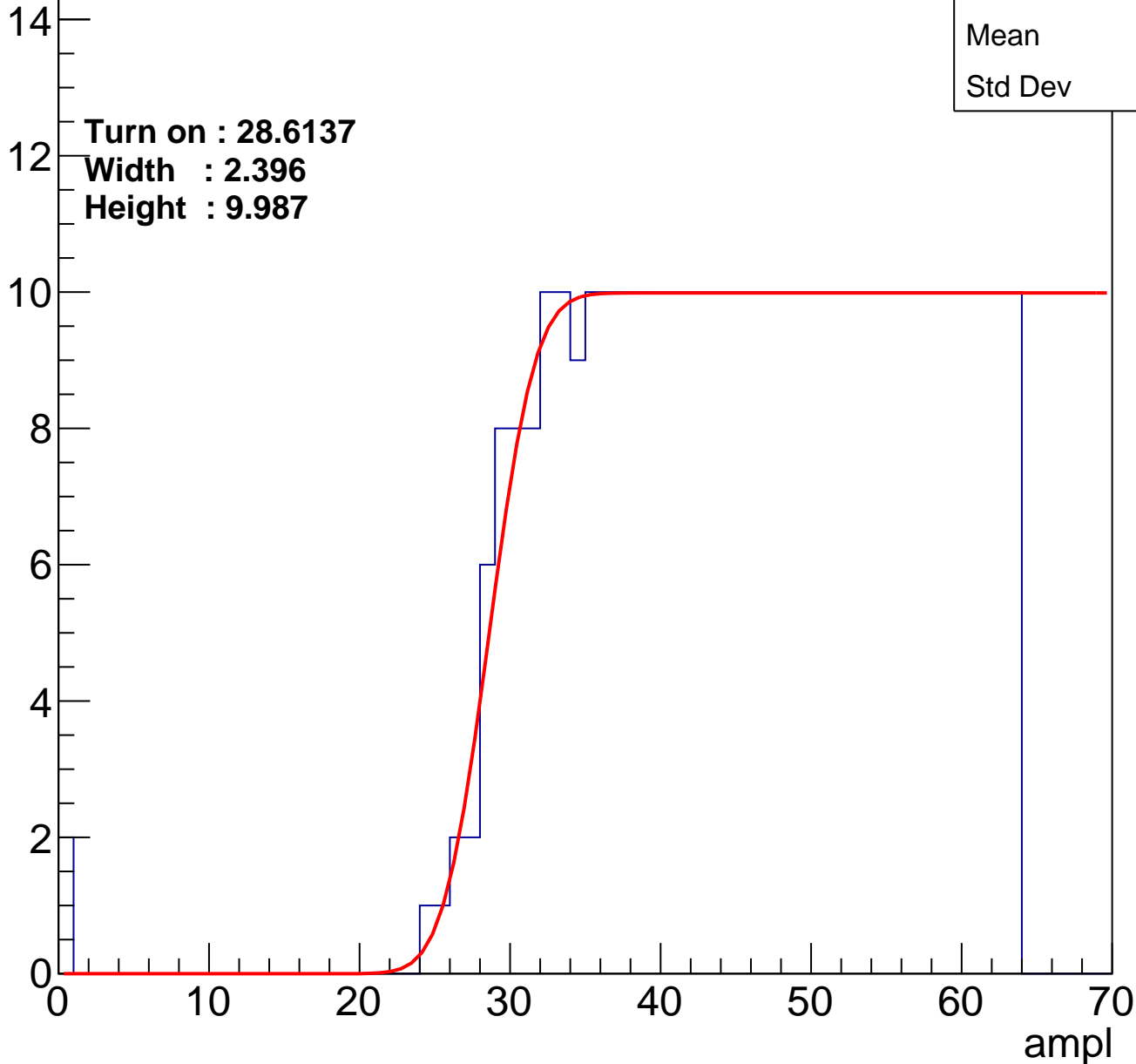
Entry

Entries	357
Mean	45.4
Std Dev	10.91

Turn on : 28.6137

Width : 2.396

Height : 9.987



B0L002S, U6-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	45.01
Std Dev	10.92

Turn on : 27.6531

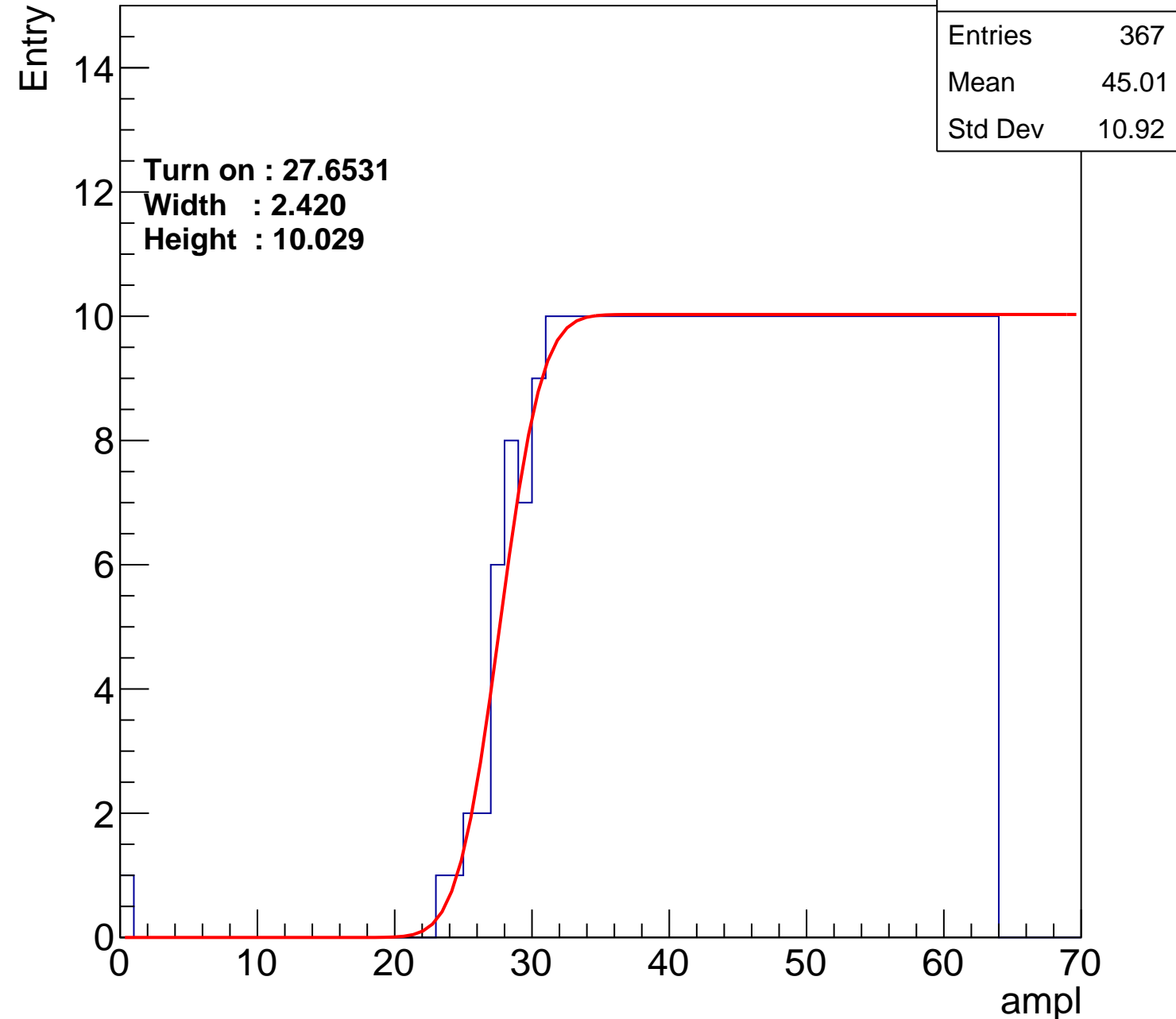
Width : 2.420

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.48
Std Dev	11.56

Turn on : 27.3202

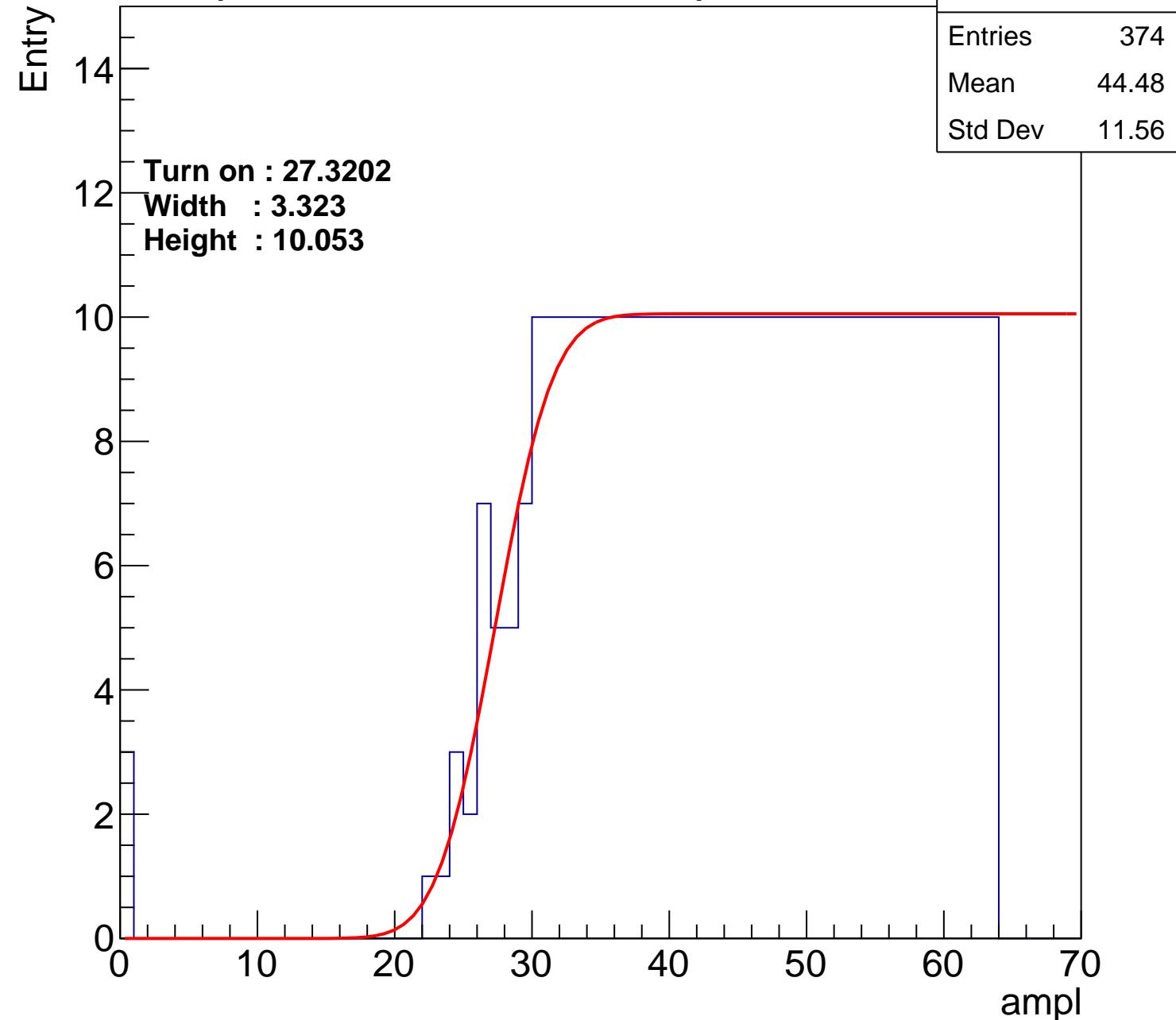
Width : 3.323

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.4193

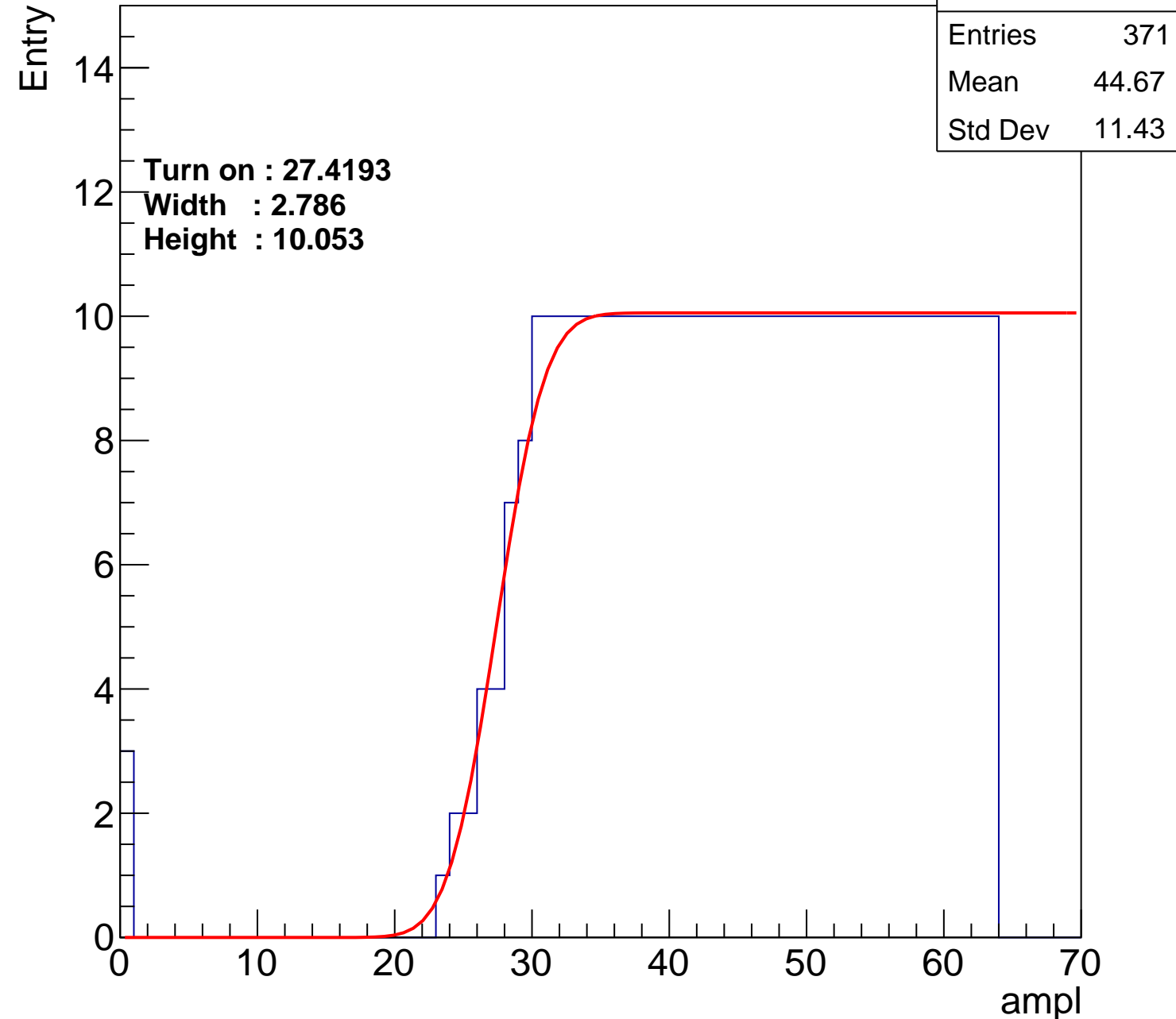
Width : 2.786

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.9
Std Dev	10.44

Turn on : 29.6004

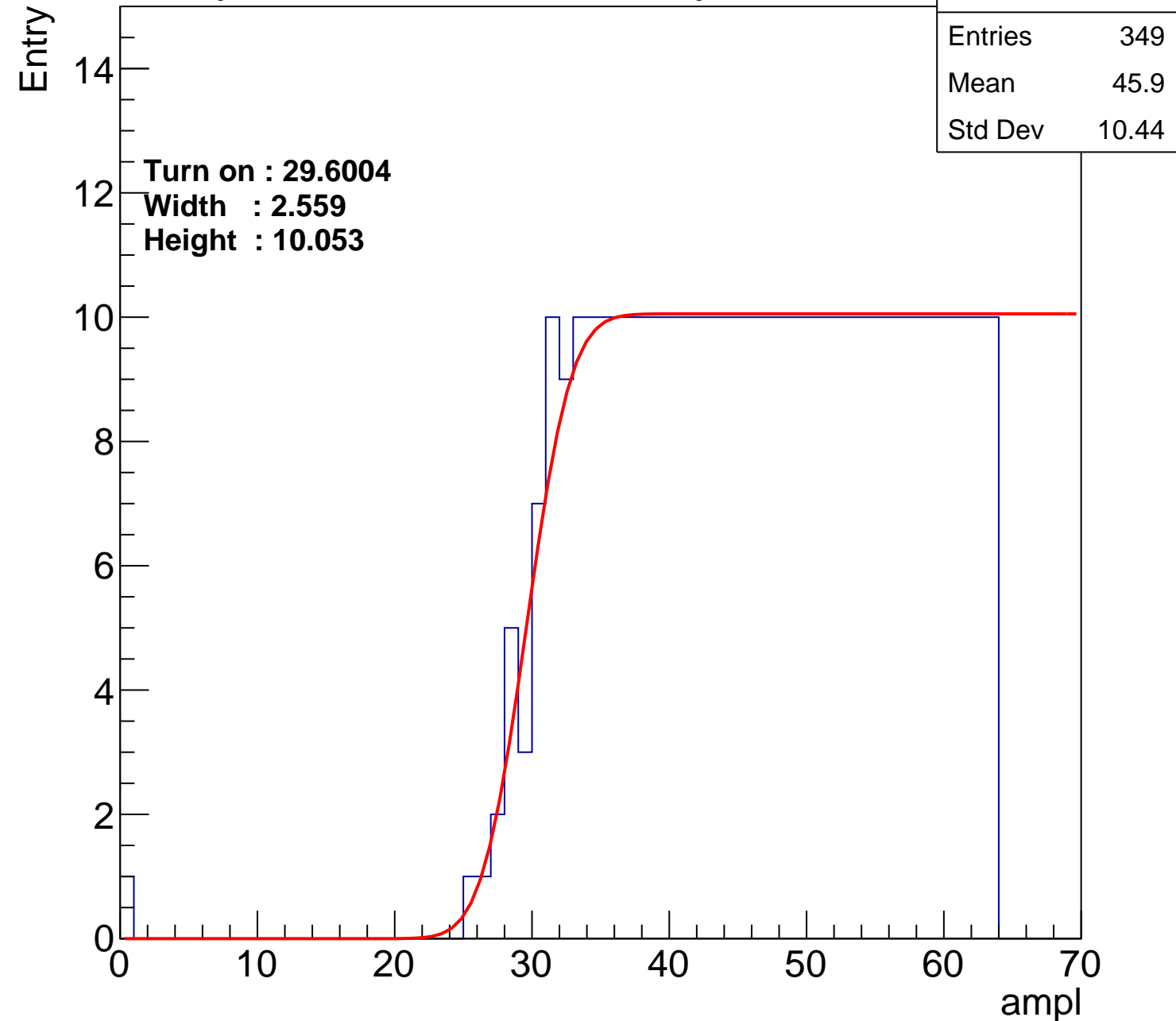
Width : 2.559

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.2622

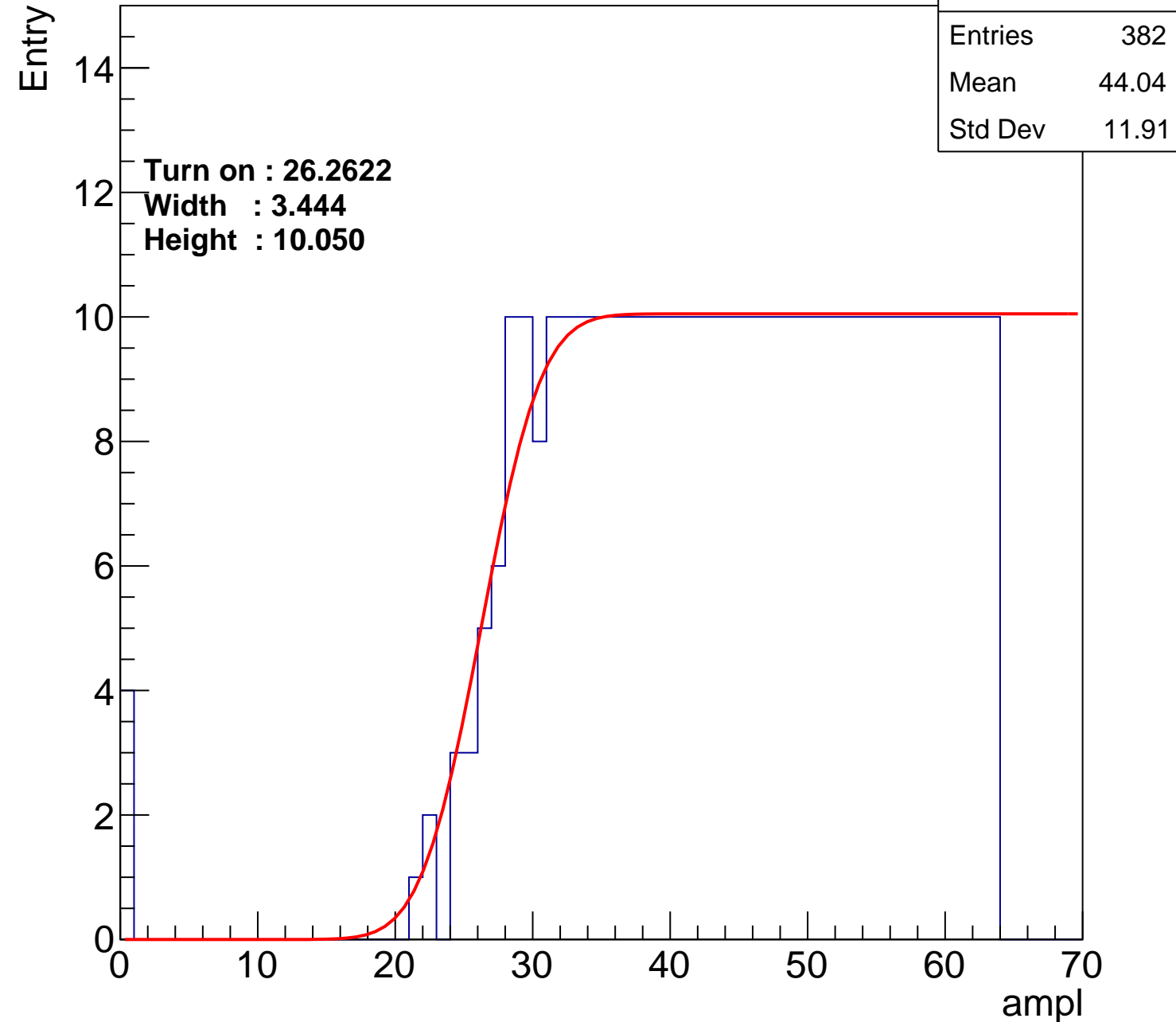
Width : 3.444

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.04
Std Dev	11.28

Turn on : 28.3307

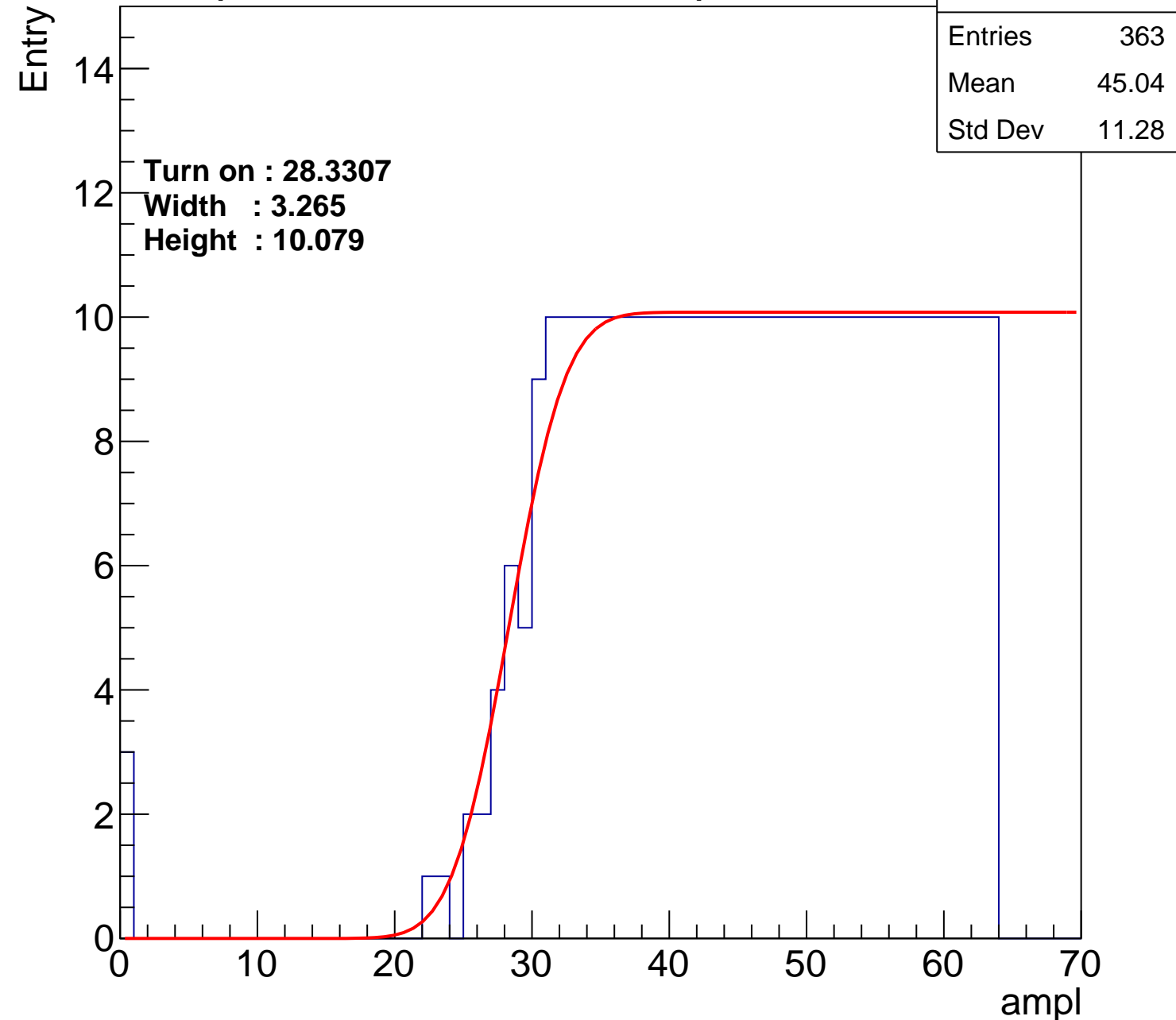
Width : 3.265

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.35
Std Dev	11.43

Turn on : 26.2266

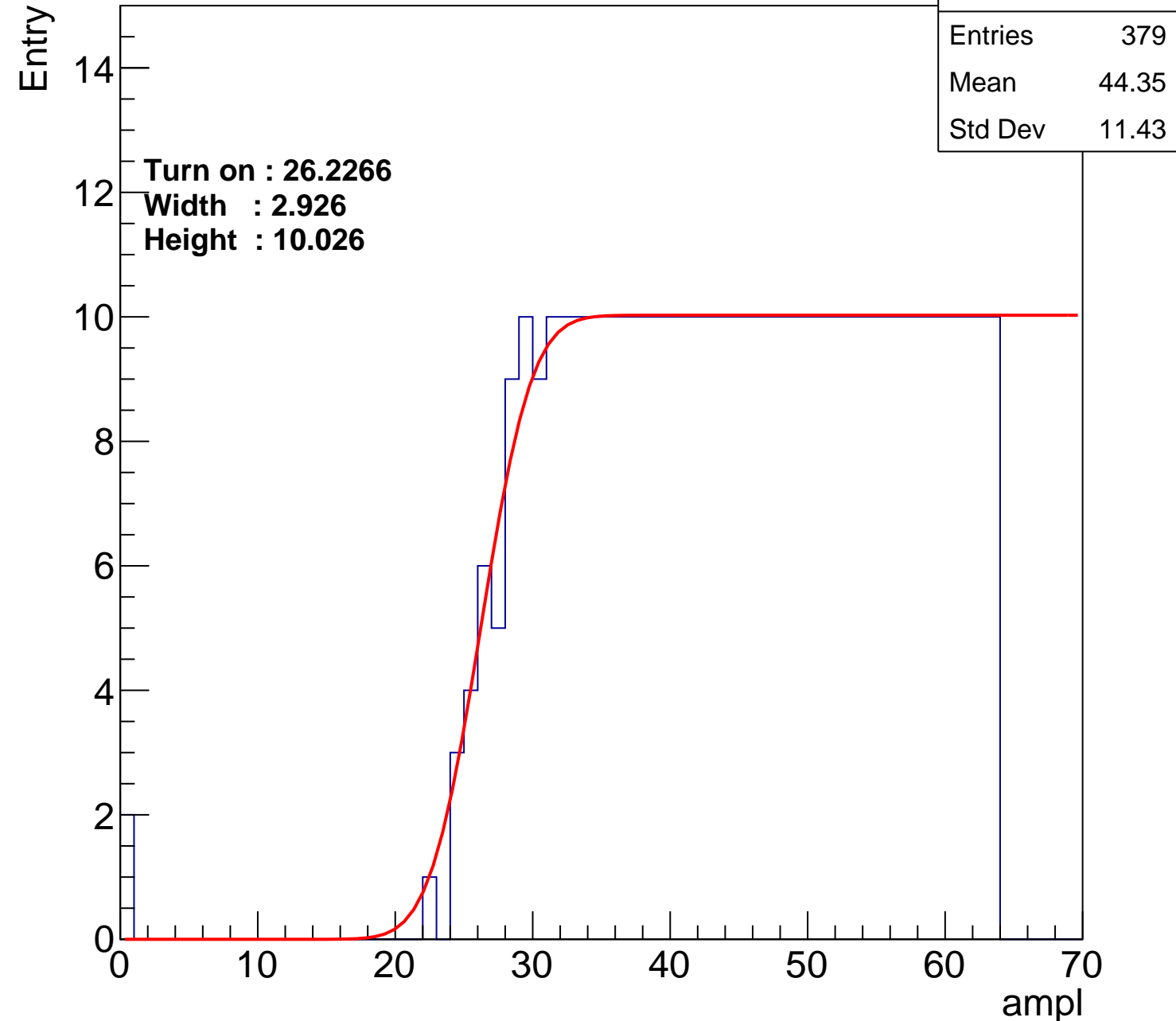
Width : 2.926

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.97
Std Dev	11.19

Turn on : 28.3018

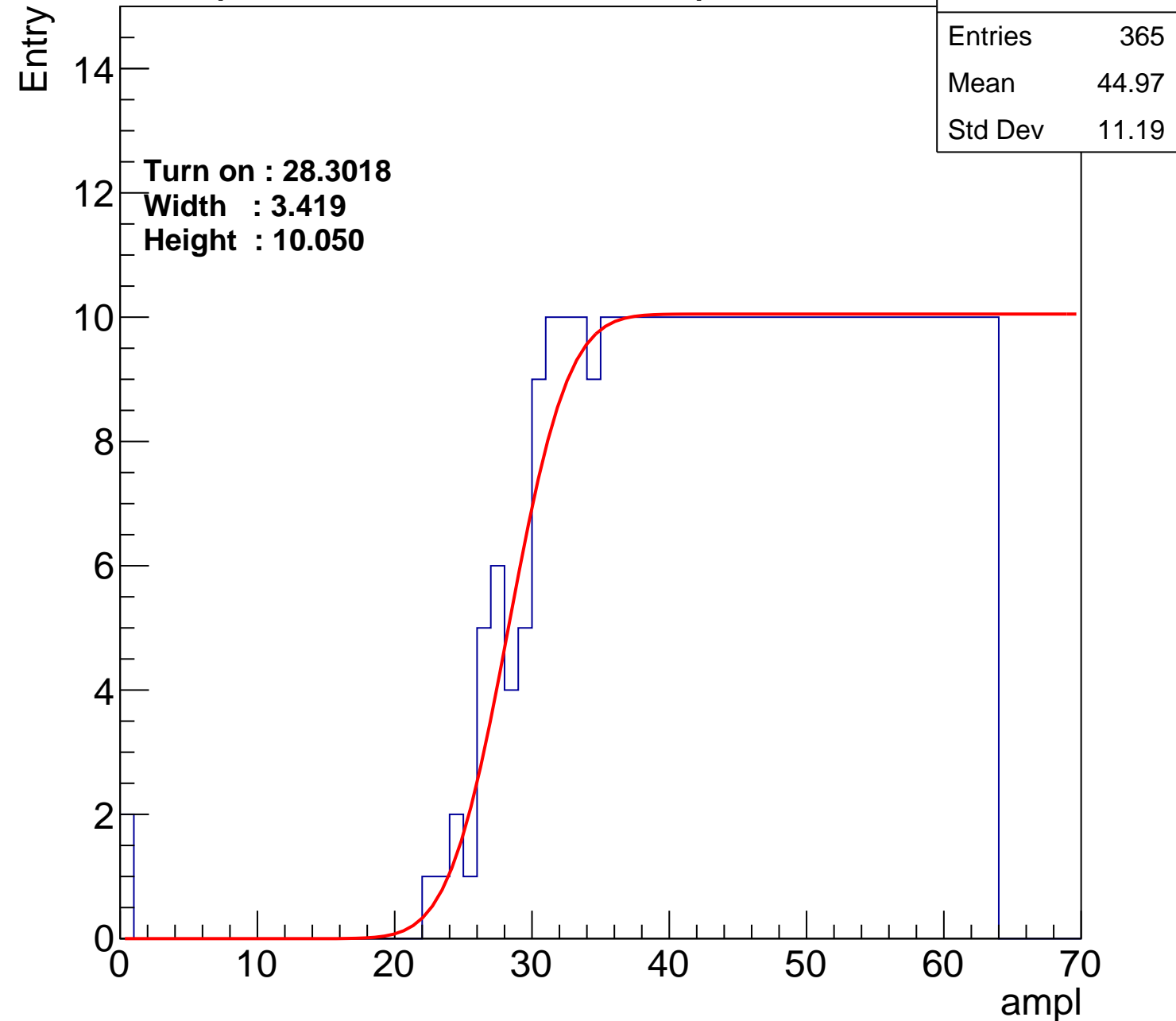
Width : 3.419

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.04
Std Dev	11.28

Turn on : 28.5241

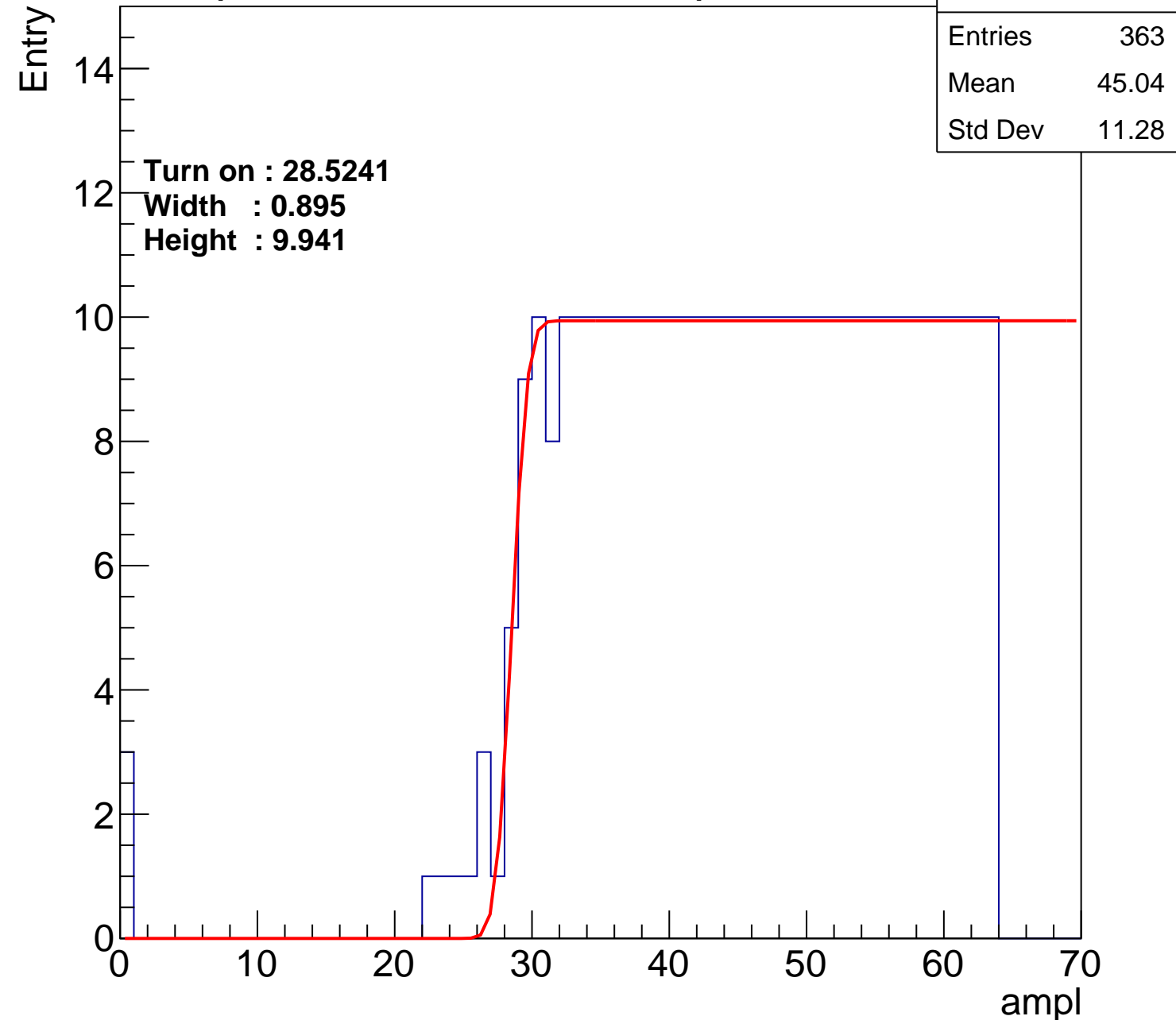
Width : 0.895

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.89
Std Dev	11.4

Turn on : 28.2040

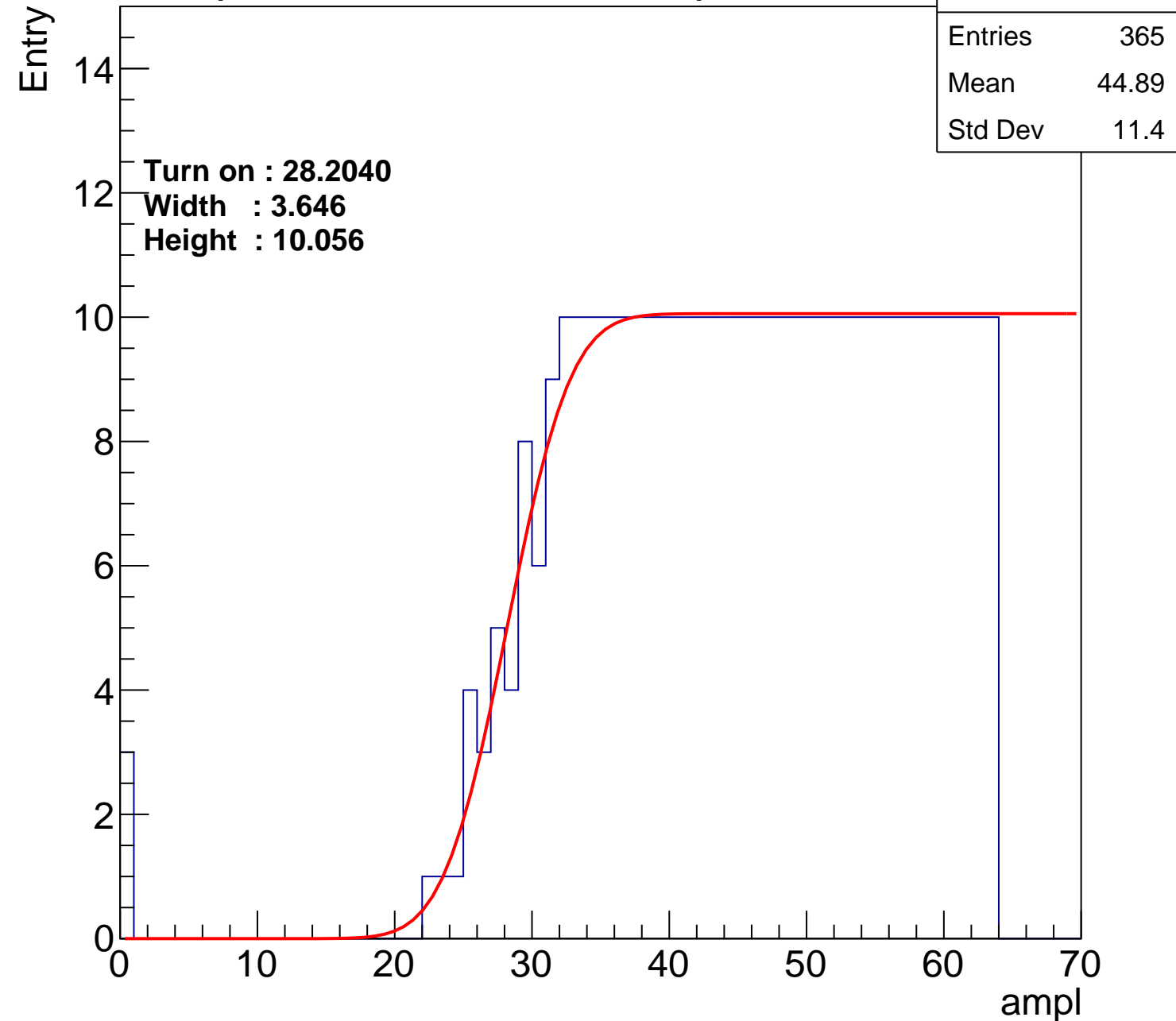
Width : 3.646

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch60

calib_packv5_042523_0143.root, FC#8, port C1

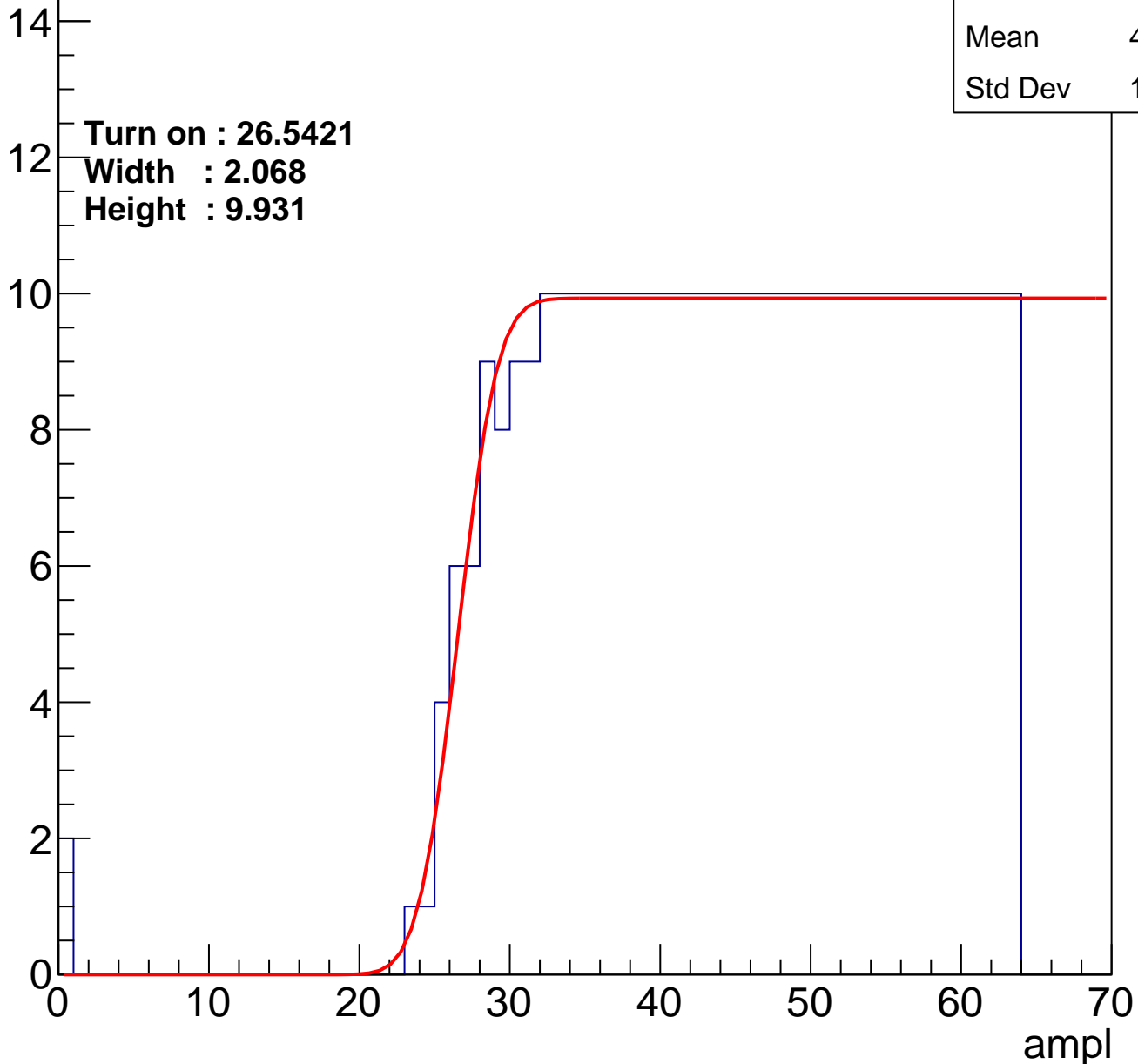
Entry

Entries	375
Mean	44.53
Std Dev	11.35

Turn on : 26.5421

Width : 2.068

Height : 9.931



B0L002S, U6-ch61

calib_packv5_042523_0143.root, FC#8, port C1

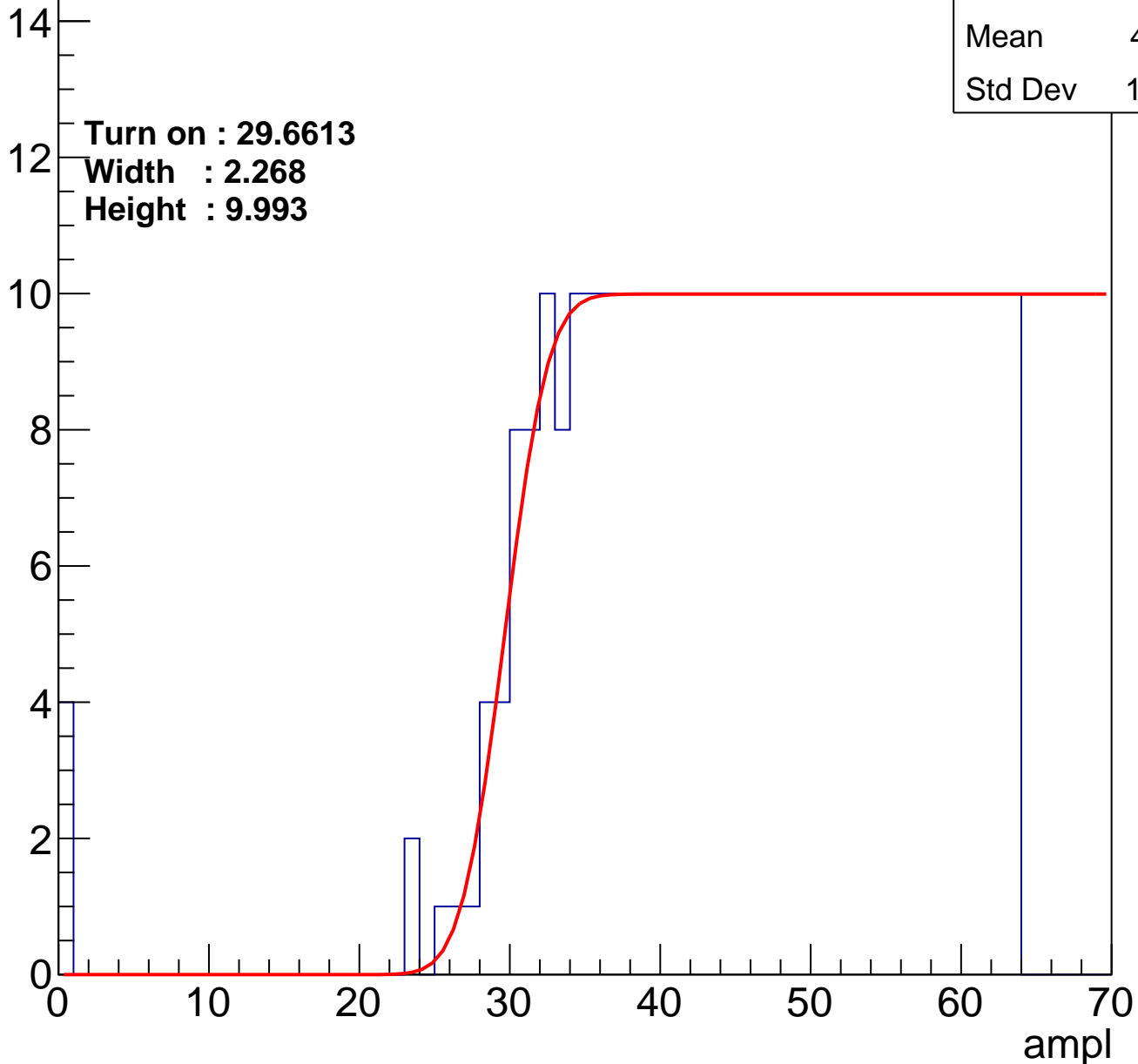
Entries	351
Mean	45.51
Std Dev	11.27

Turn on : 29.6613

Width : 2.268

Height : 9.993

Entry



B0L002S, U6-ch62

calib_packv5_042523_0143.root, FC#8, port C1

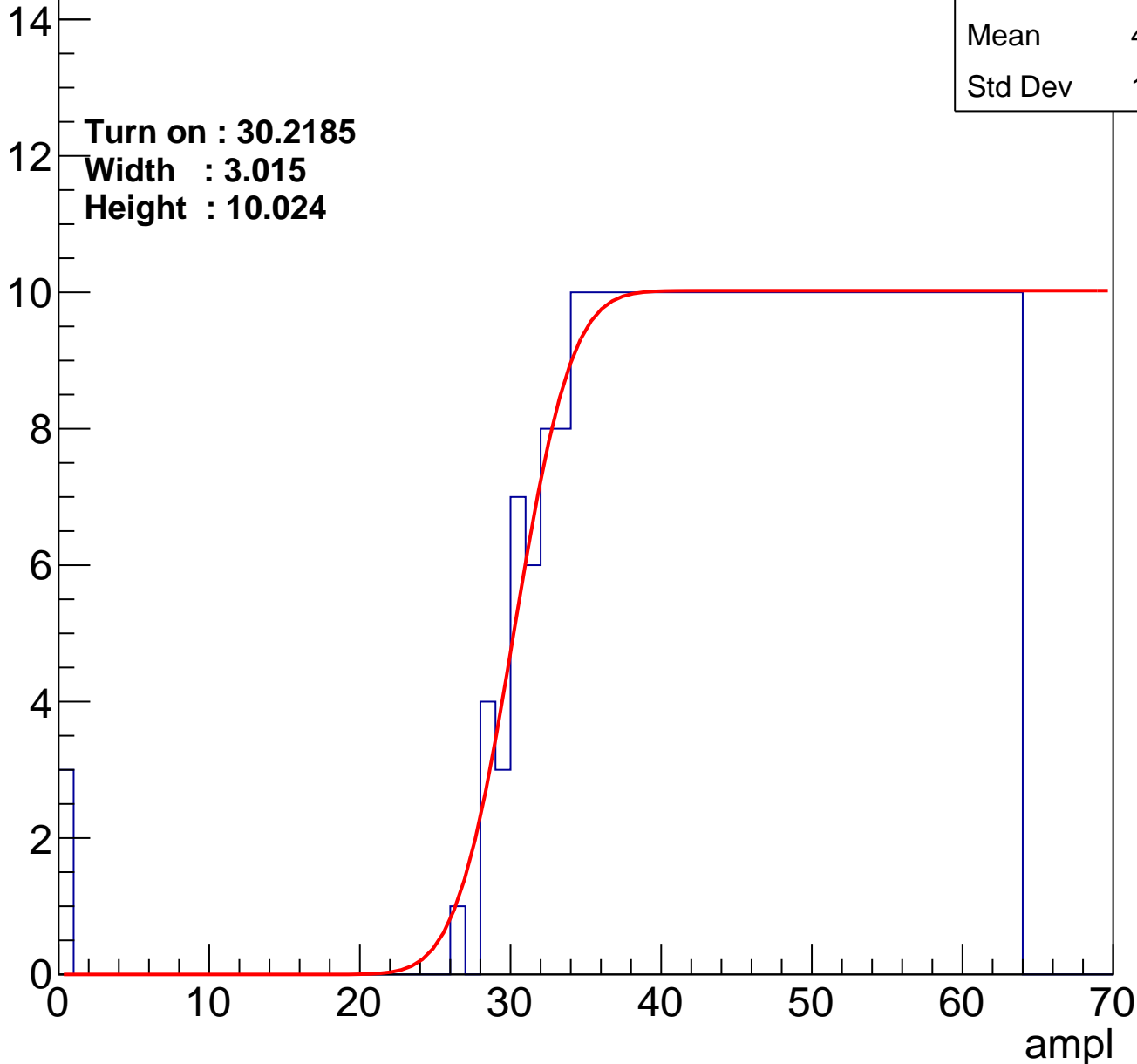
Entry

Entries	340
Mean	46.15
Std Dev	10.75

Turn on : 30.2185

Width : 3.015

Height : 10.024



B0L002S, U6-ch63

calib_packv5_042523_0143.root, FC#8, port C1

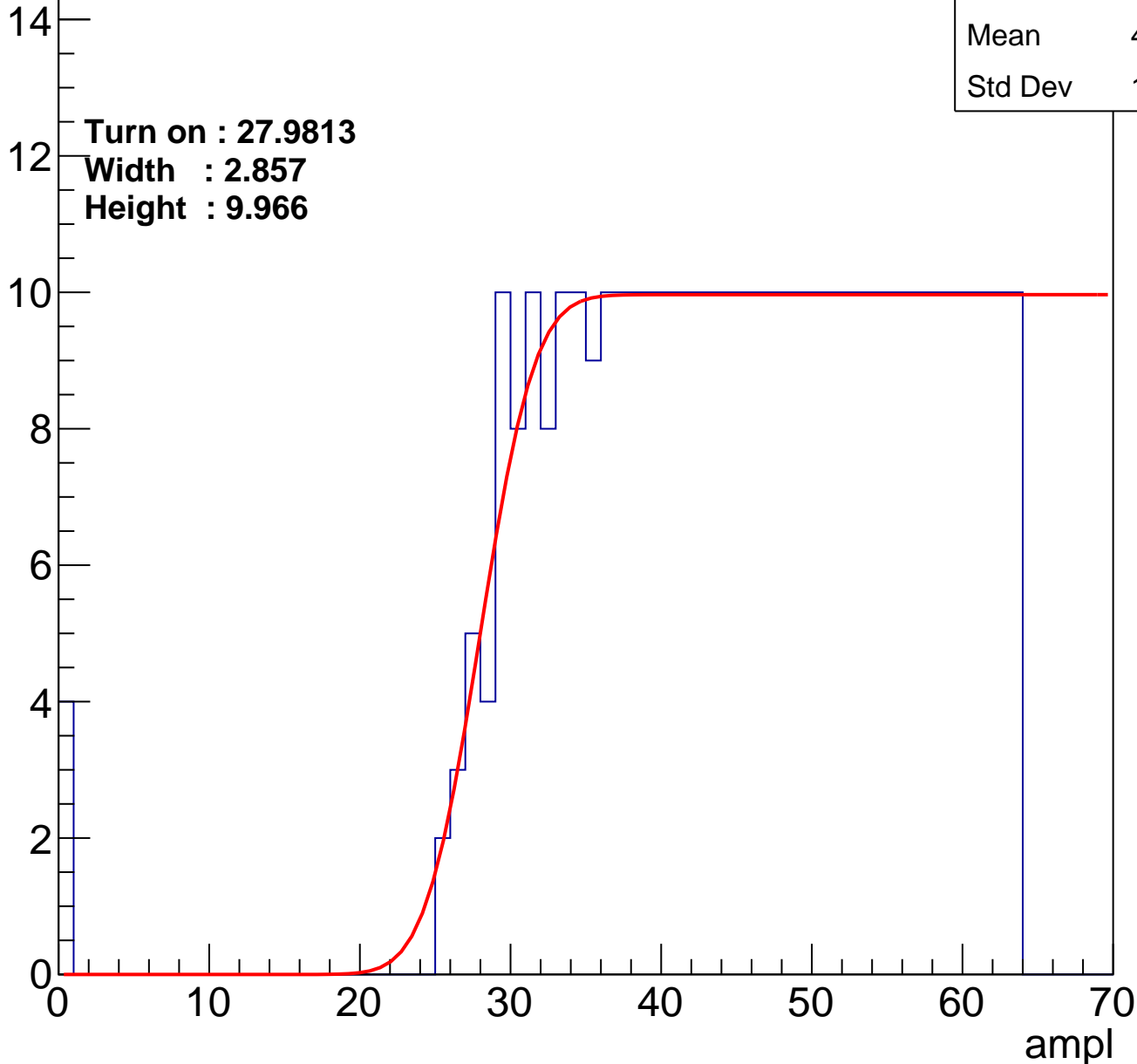
Entry

Entries	363
Mean	44.95
Std Dev	11.49

Turn on : 27.9813

Width : 2.857

Height : 9.966



B0L002S, U6-ch64

calib_packv5_042523_0143.root, FC#8, port C1

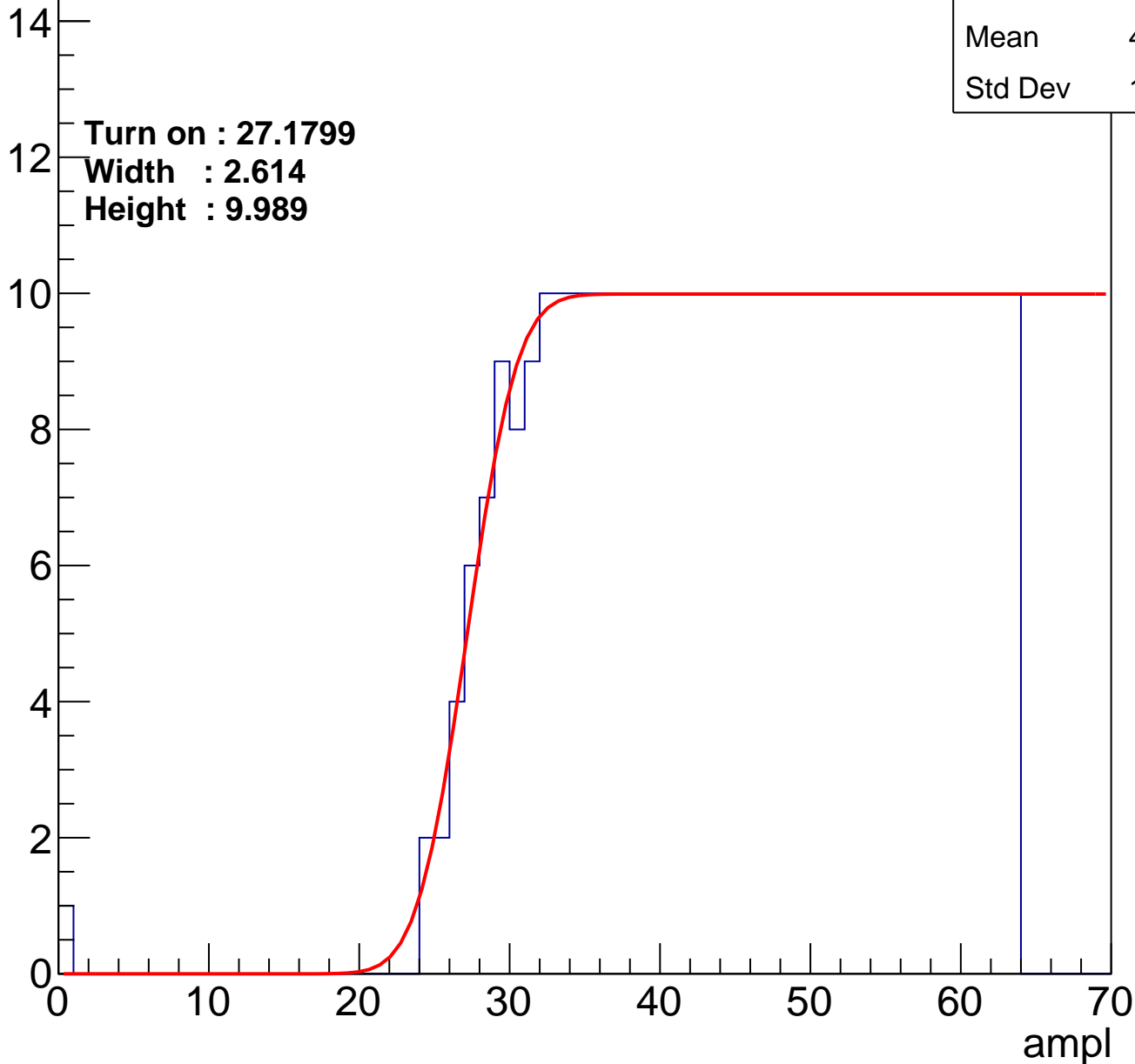
Entry

Entries	368
Mean	44.95
Std Dev	10.97

Turn on : 27.1799

Width : 2.614

Height : 9.989



B0L002S, U6-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.75
Std Dev	10.74

Turn on : 29.4011

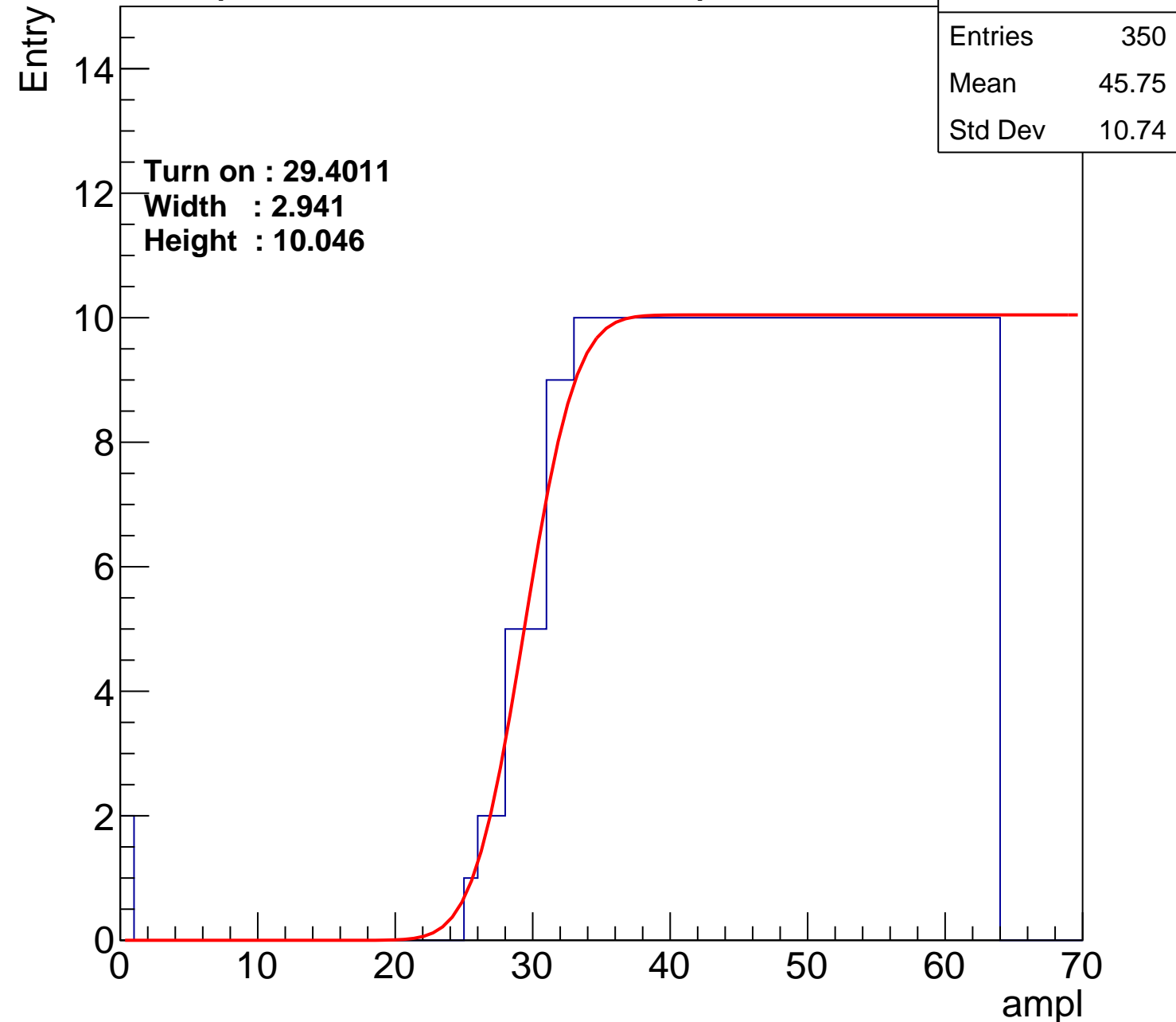
Width : 2.941

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.11
Std Dev	11.07

Turn on : 27.6092

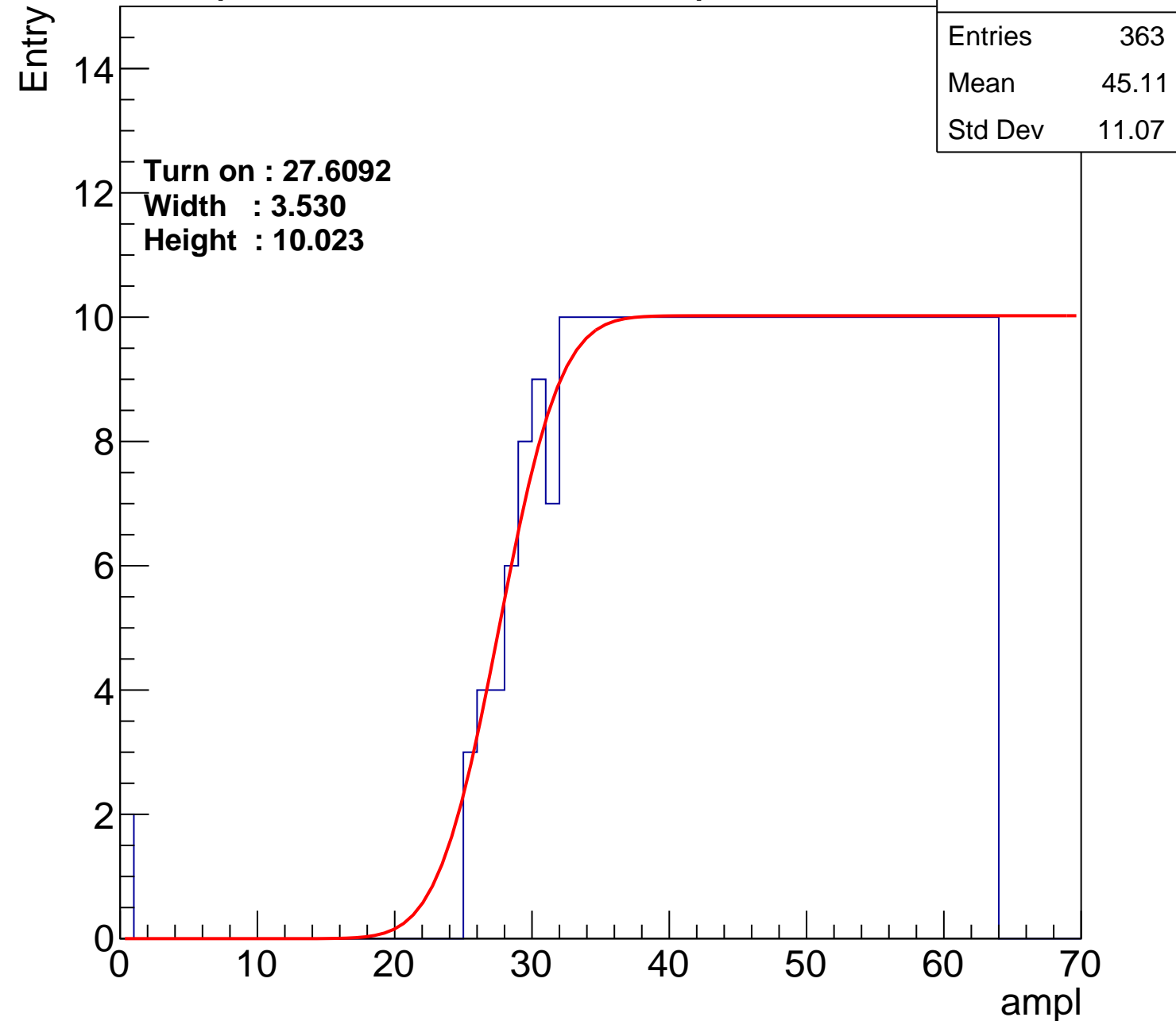
Width : 3.530

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.19
Std Dev	11.04

Turn on : 28.2520

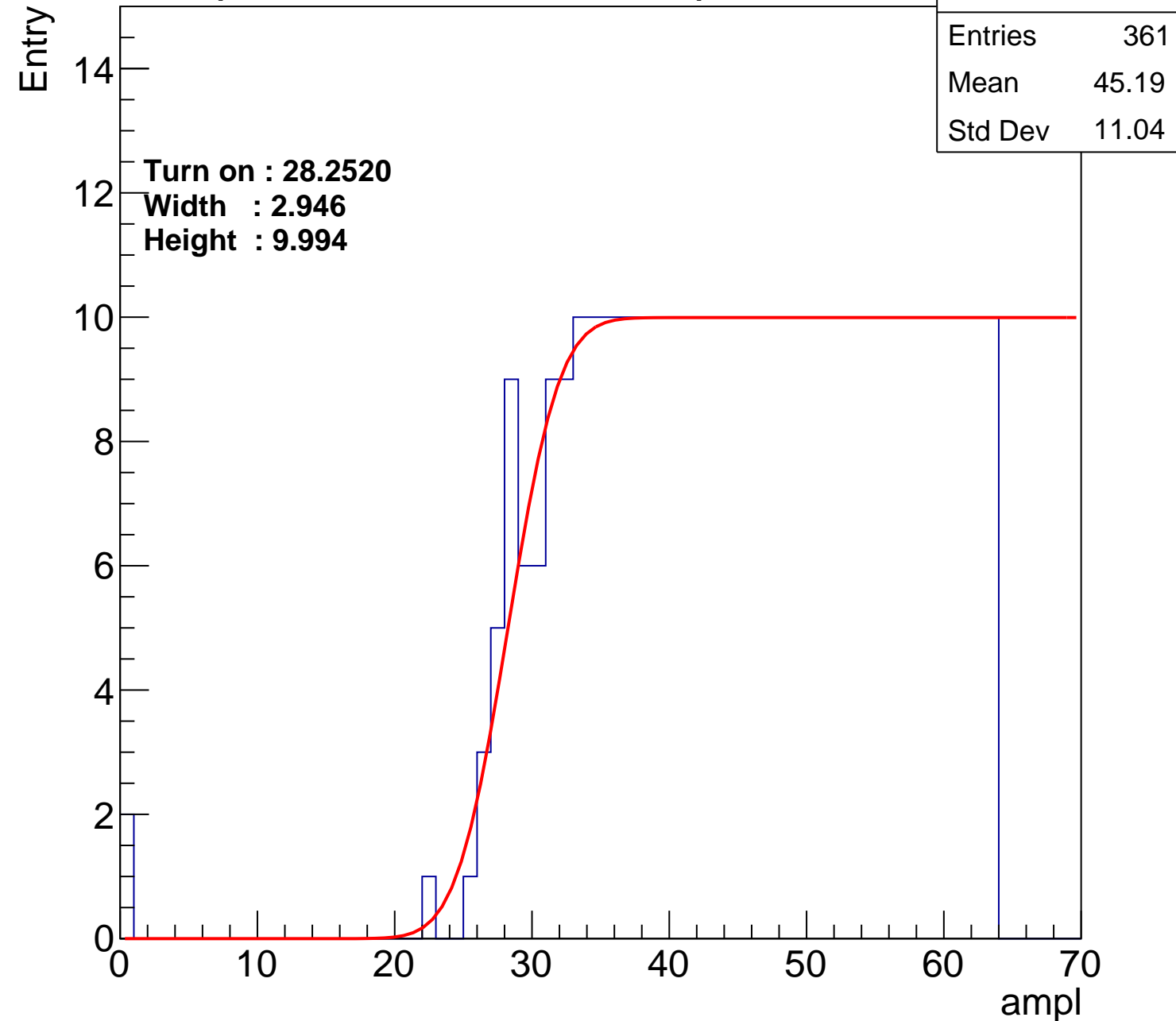
Width : 2.946

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	43.95
Std Dev	12.01

Turn on : 27.0037

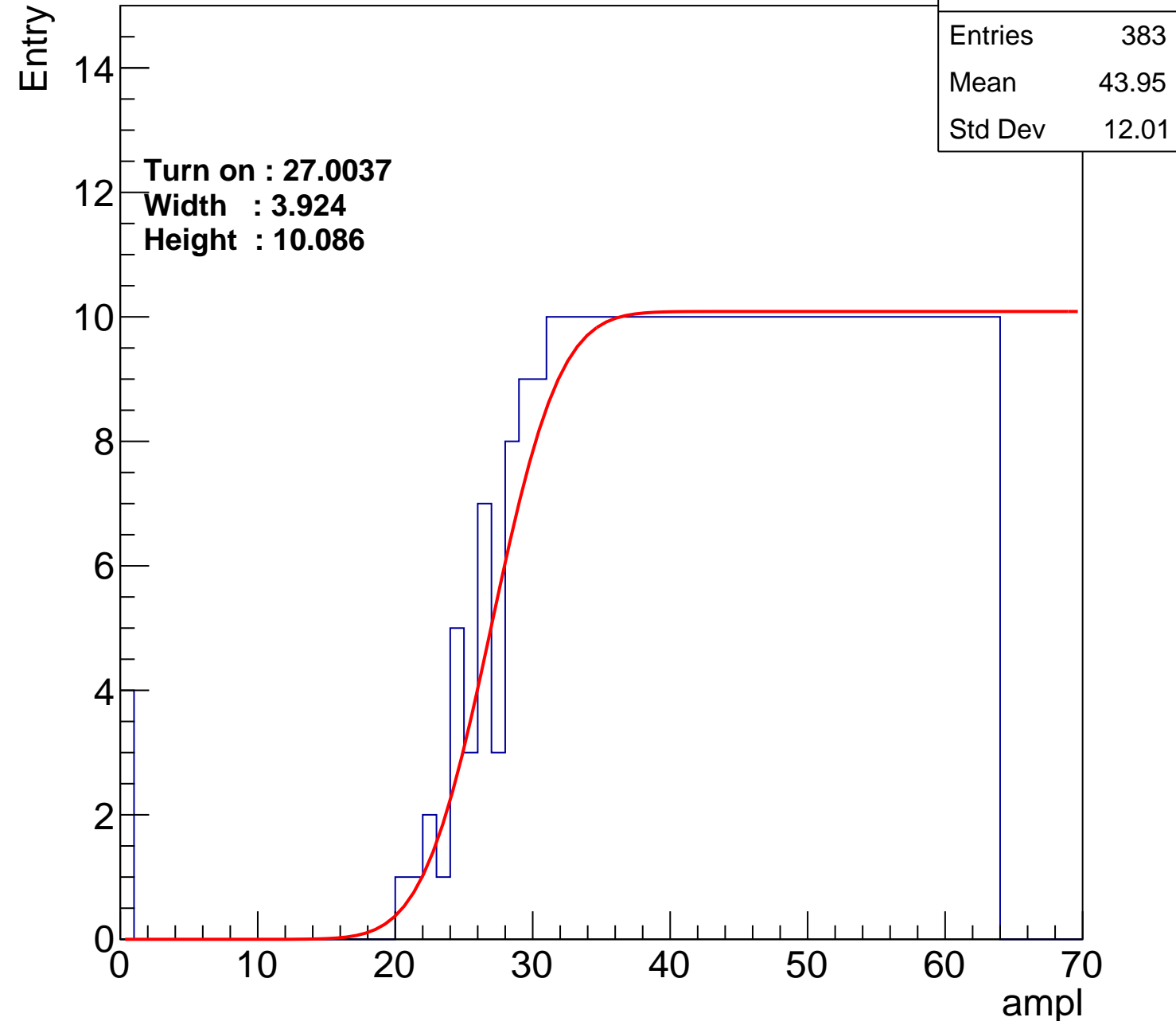
Width : 3.924

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.96
Std Dev	11.33

Turn on : 27.4825

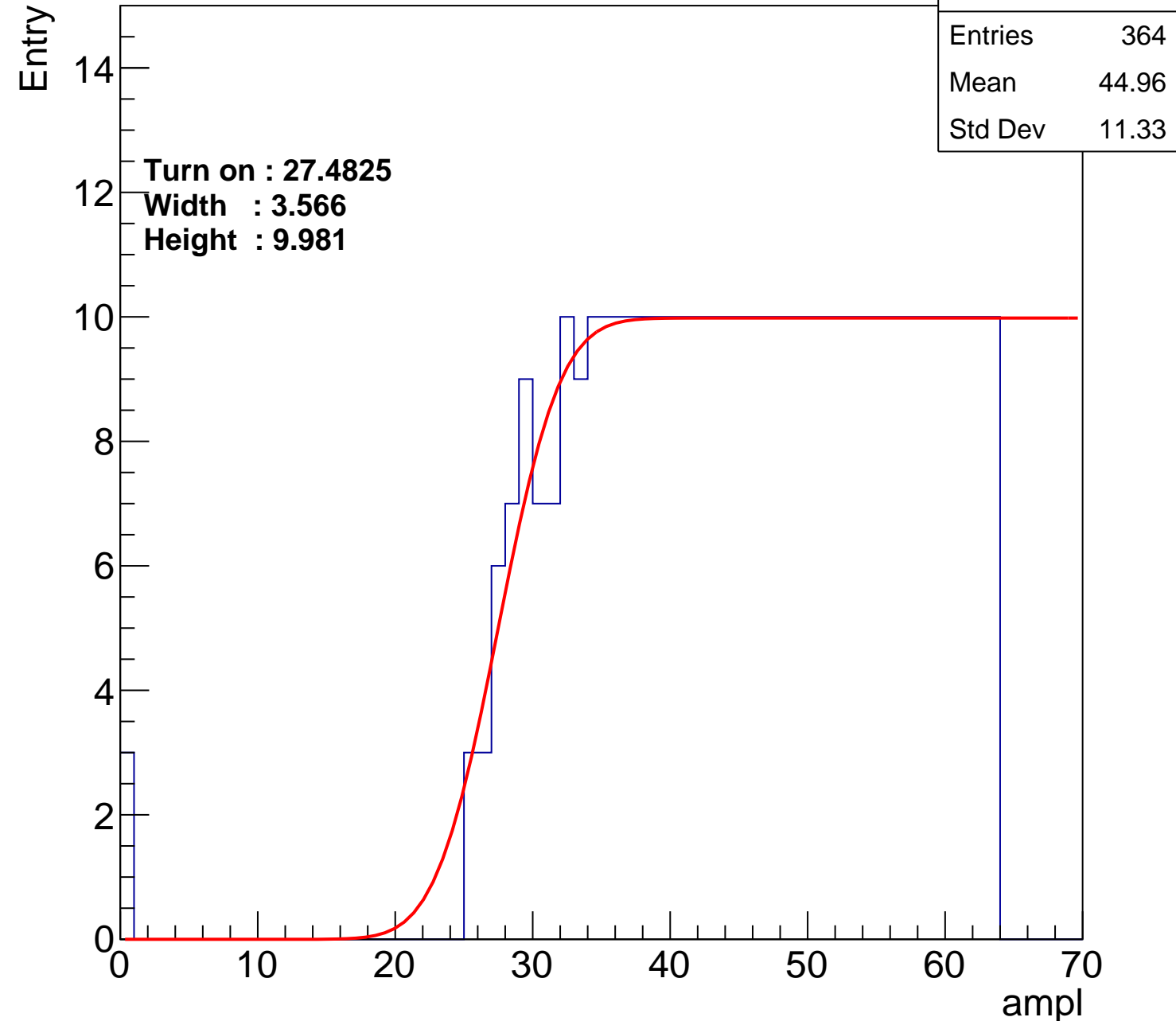
Width : 3.566

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.34
Std Dev	11.01

Turn on : 28.4575

Width : 3.525

Height : 10.010

Entry

14

12

10

8

6

4

2

0

0

10

20

30

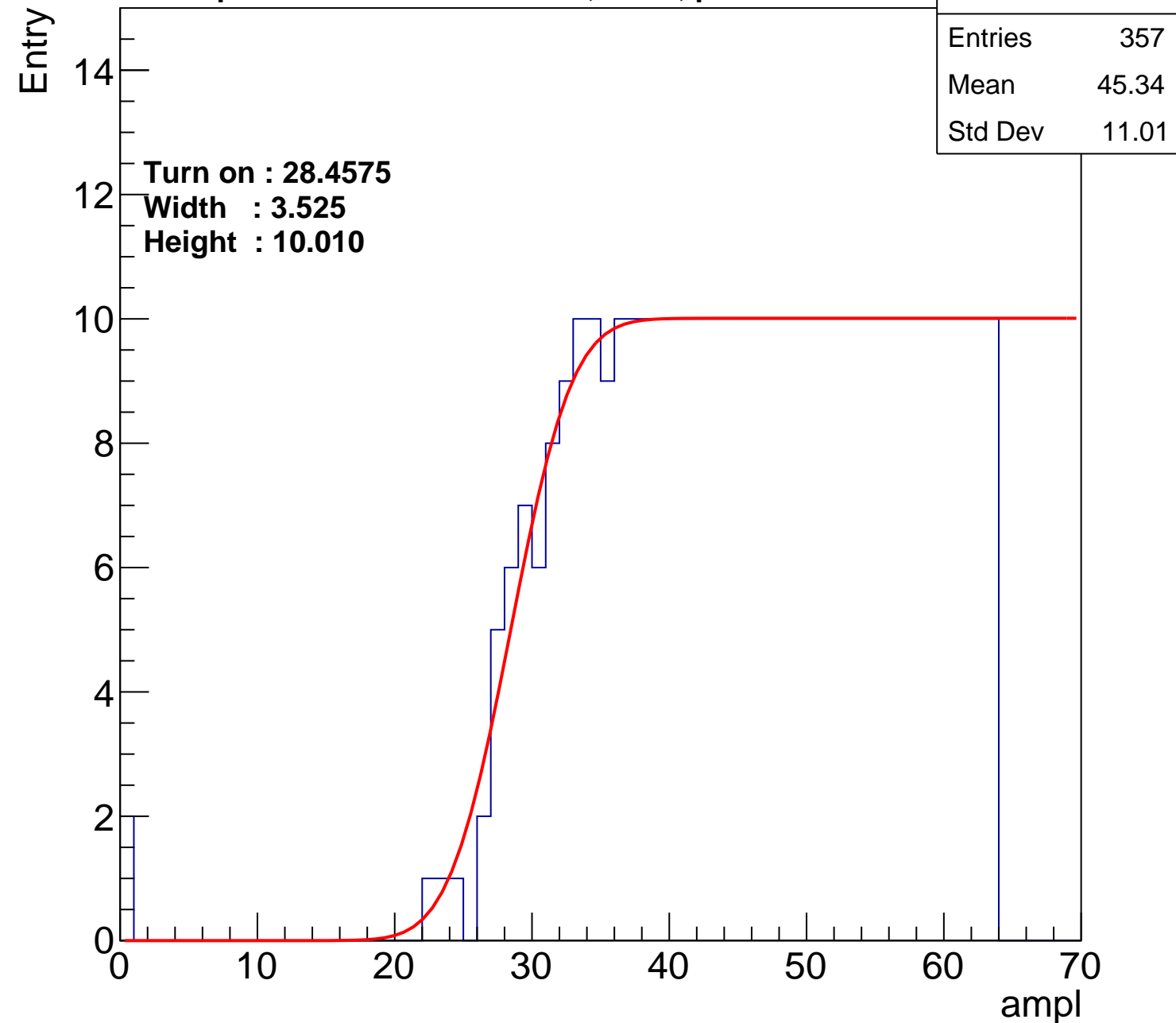
40

50

60

70

ampl



B0L002S, U6-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.35
Std Dev	11.43

Turn on : 26.6182

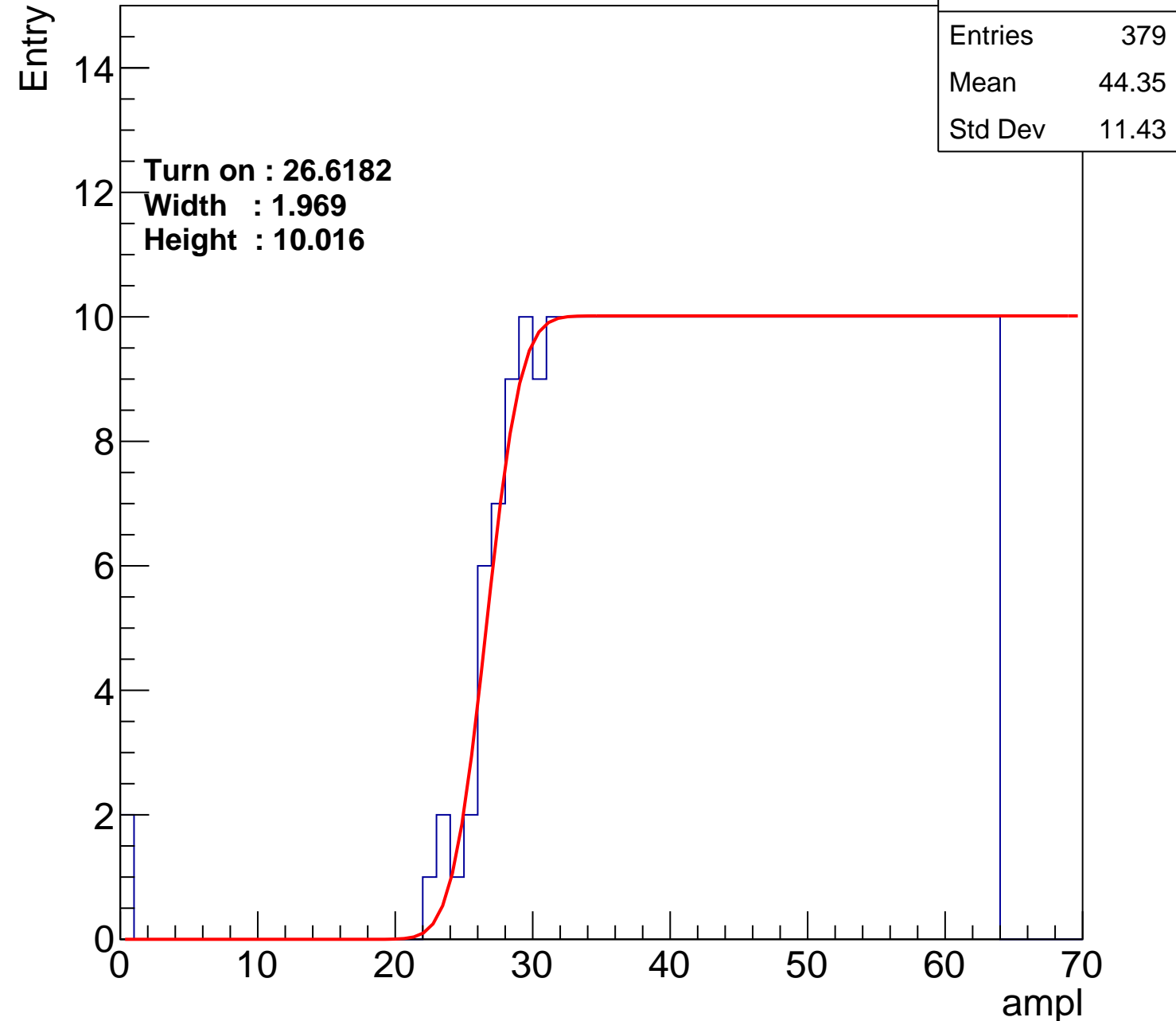
Width : 1.969

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch72

calib_packv5_042523_0143.root, FC#8, port C1

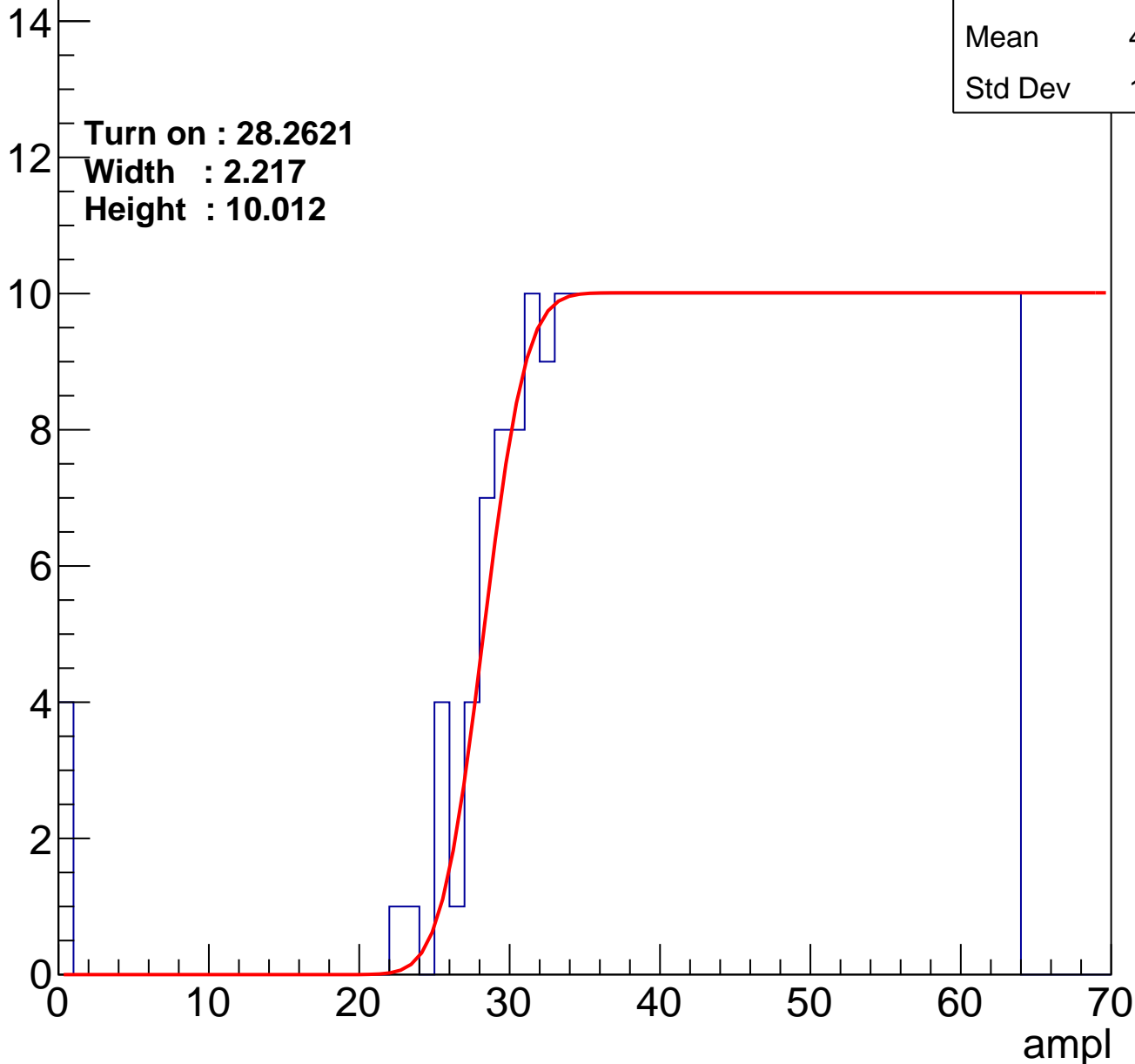
Entry

Entries	367
Mean	44.75
Std Dev	11.59

Turn on : 28.2621

Width : 2.217

Height : 10.012



B0L002S, U6-ch73

calib_packv5_042523_0143.root, FC#8, port C1

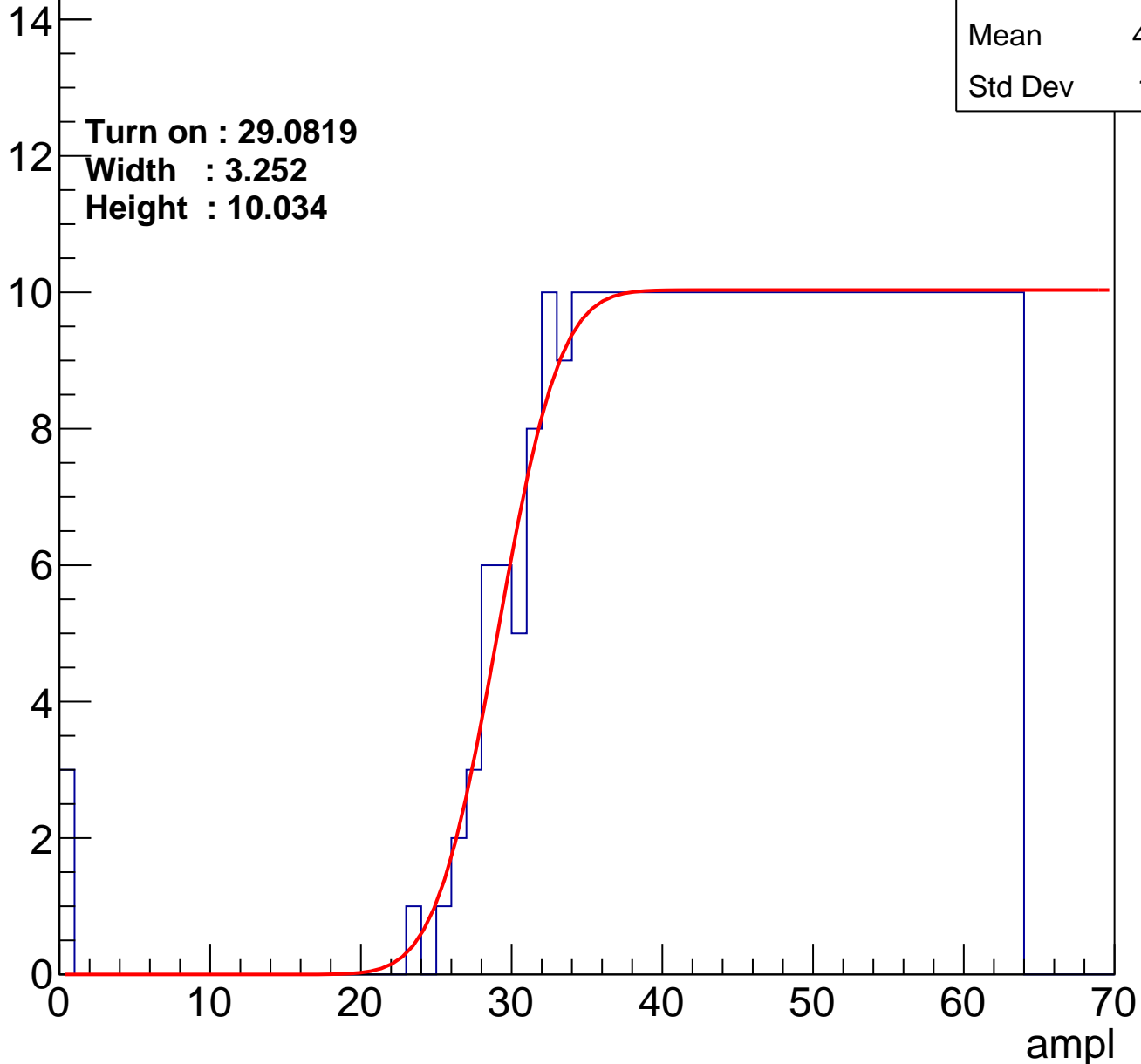
Entries	354
Mean	45.45
Std Dev	11.11

Turn on : 29.0819

Width : 3.252

Height : 10.034

Entry



B0L002S, U6-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.76
Std Dev	11.44

Turn on : 28.1557

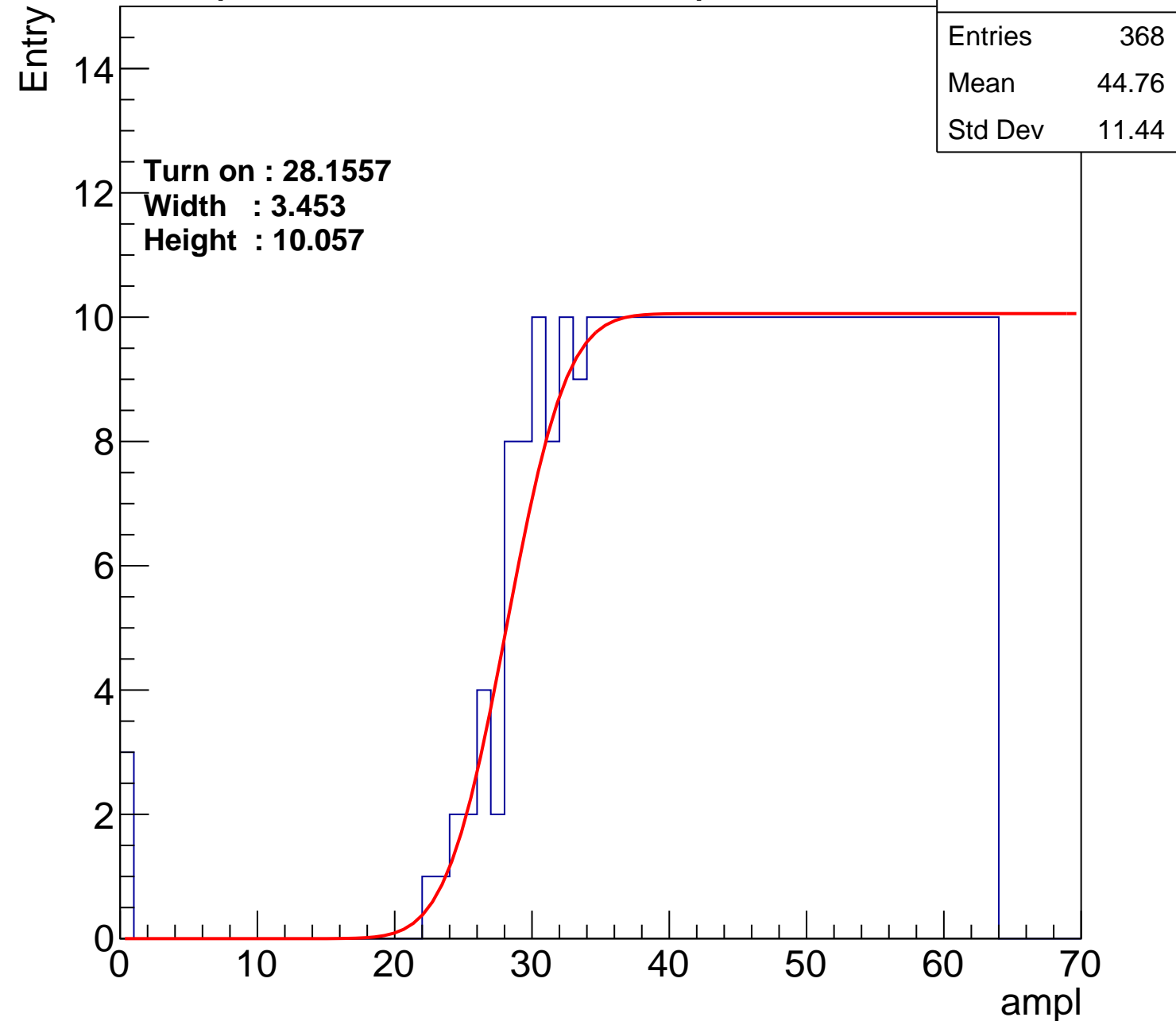
Width : 3.453

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch75

calib_packv5_042523_0143.root, FC#8, port C1

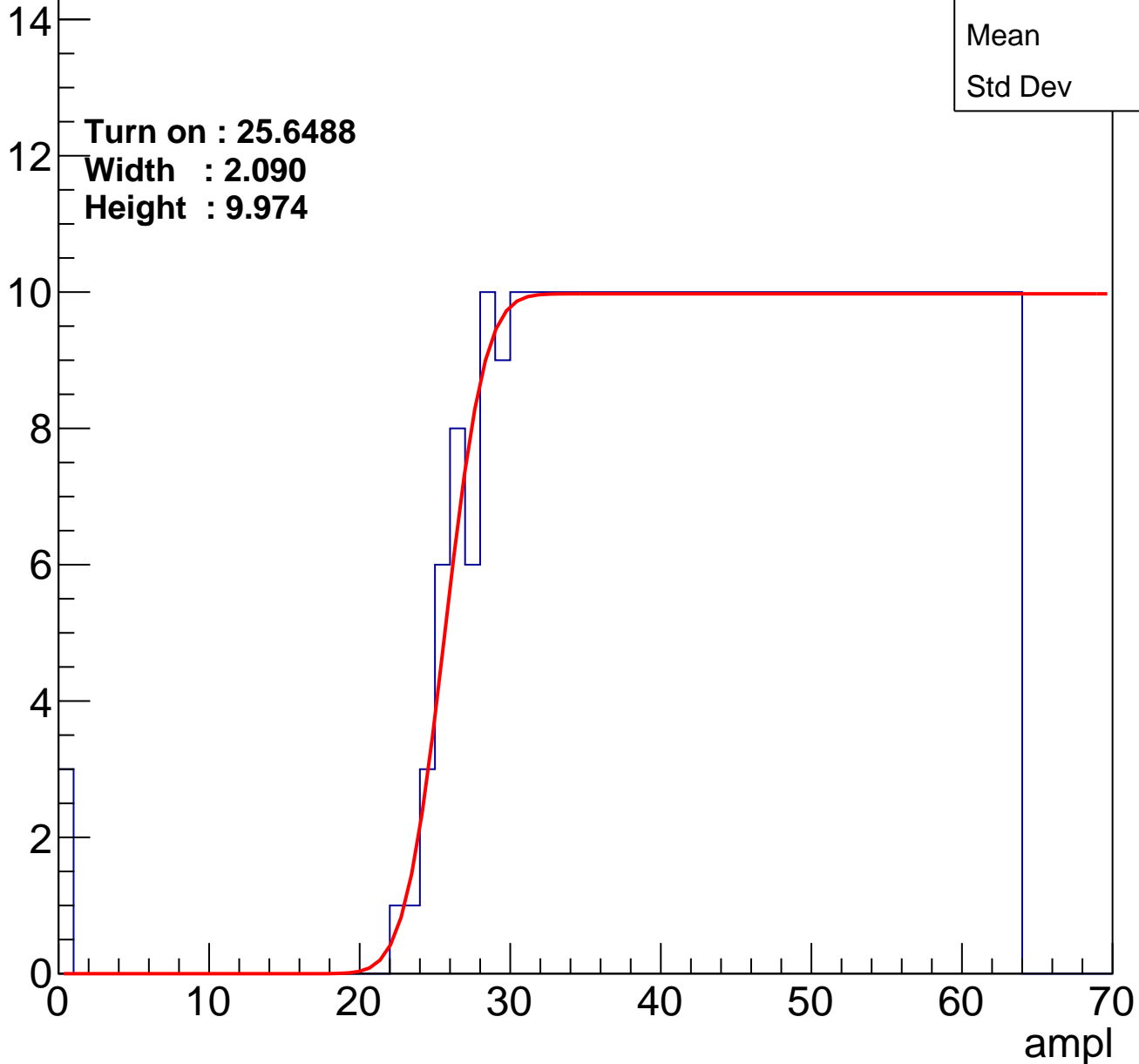
Entry

Entries	387
Mean	43.9
Std Dev	11.8

Turn on : 25.6488

Width : 2.090

Height : 9.974



B0L002S, U6-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.81
Std Dev	10.55

Turn on : 29.5595

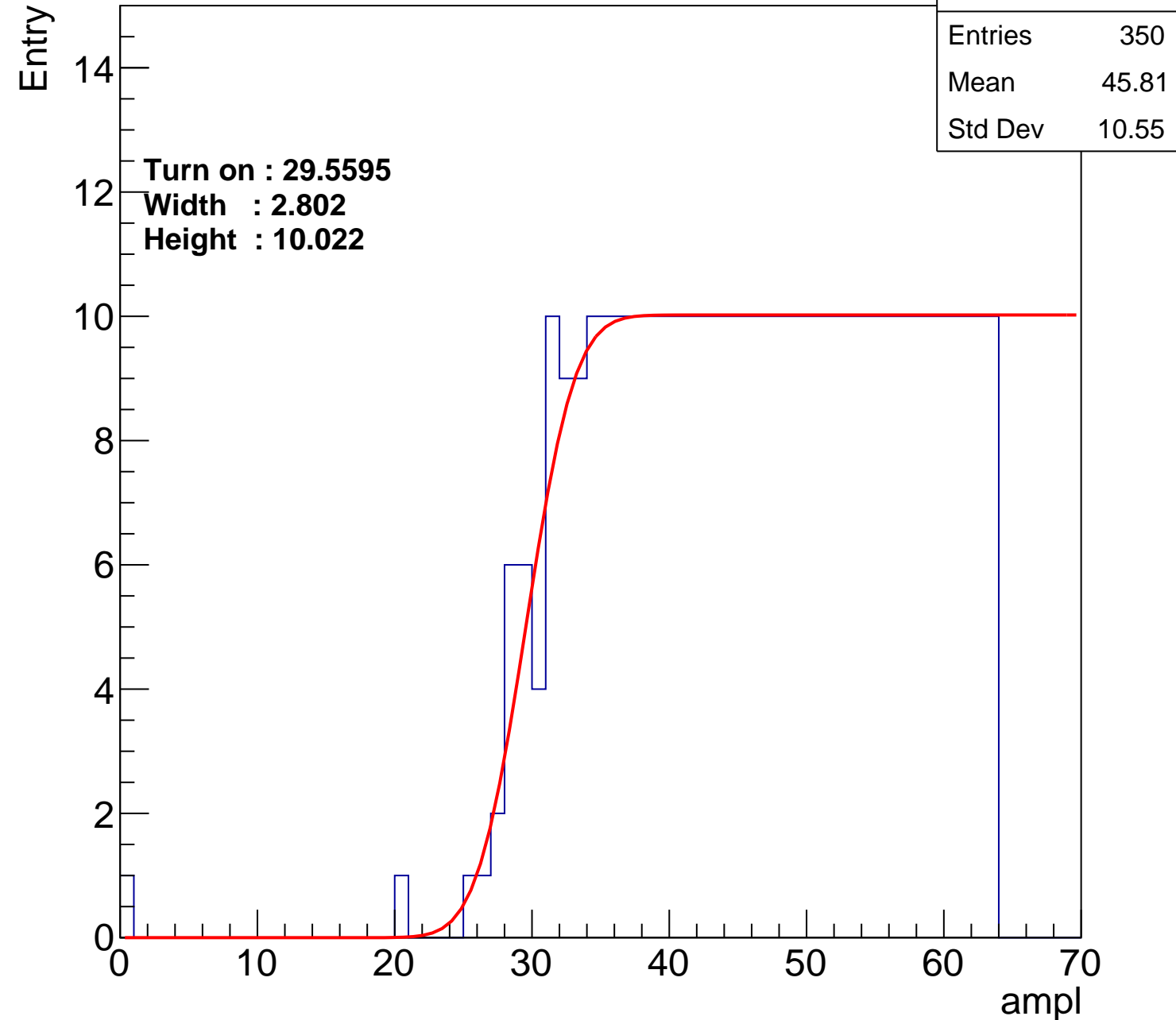
Width : 2.802

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.3
Std Dev	10.81

Turn on : 28.0546

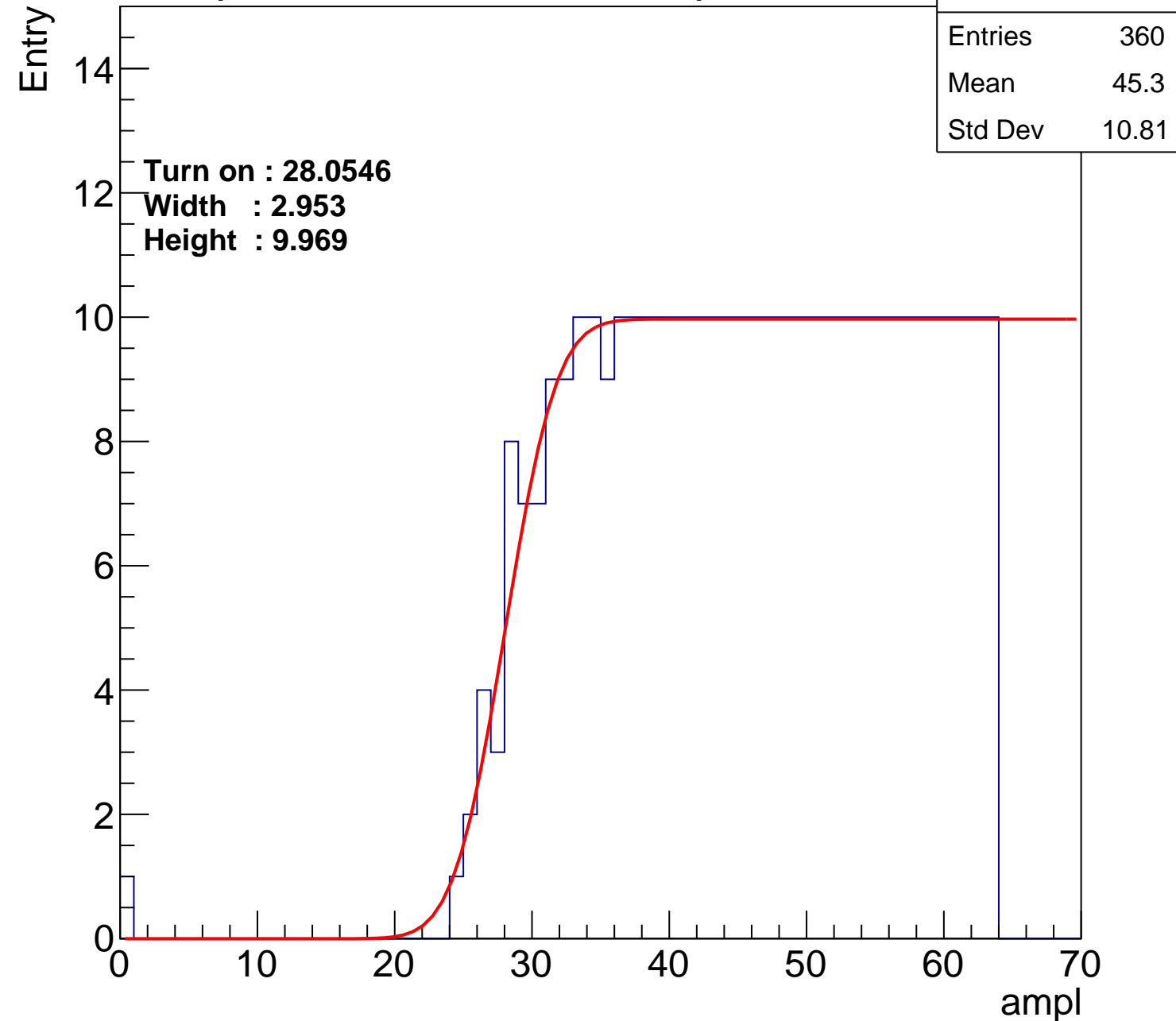
Width : 2.953

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.1
Std Dev	10.93

Turn on : 28.1678

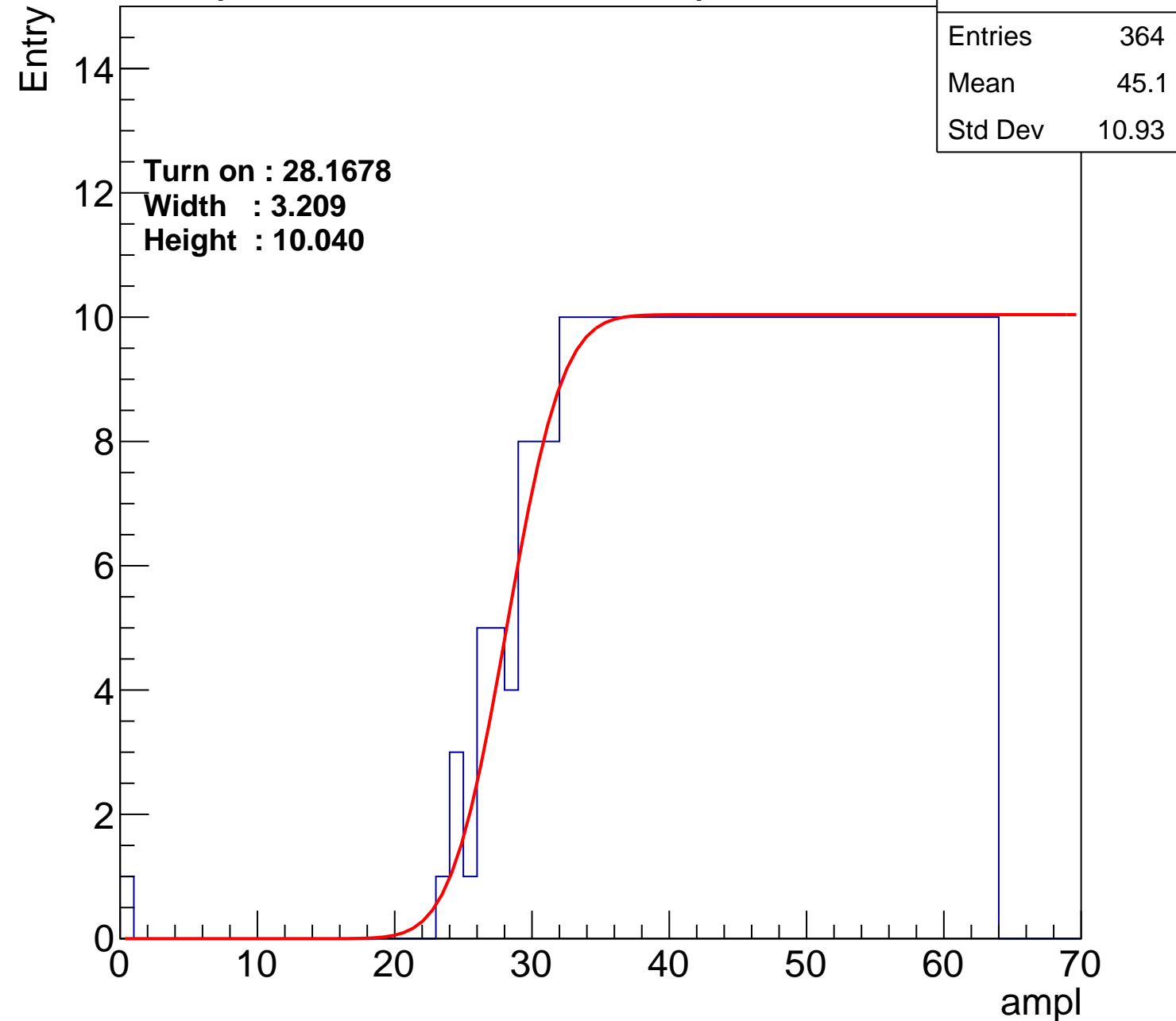
Width : 3.209

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.36
Std Dev	11.53

Turn on : 27.1915

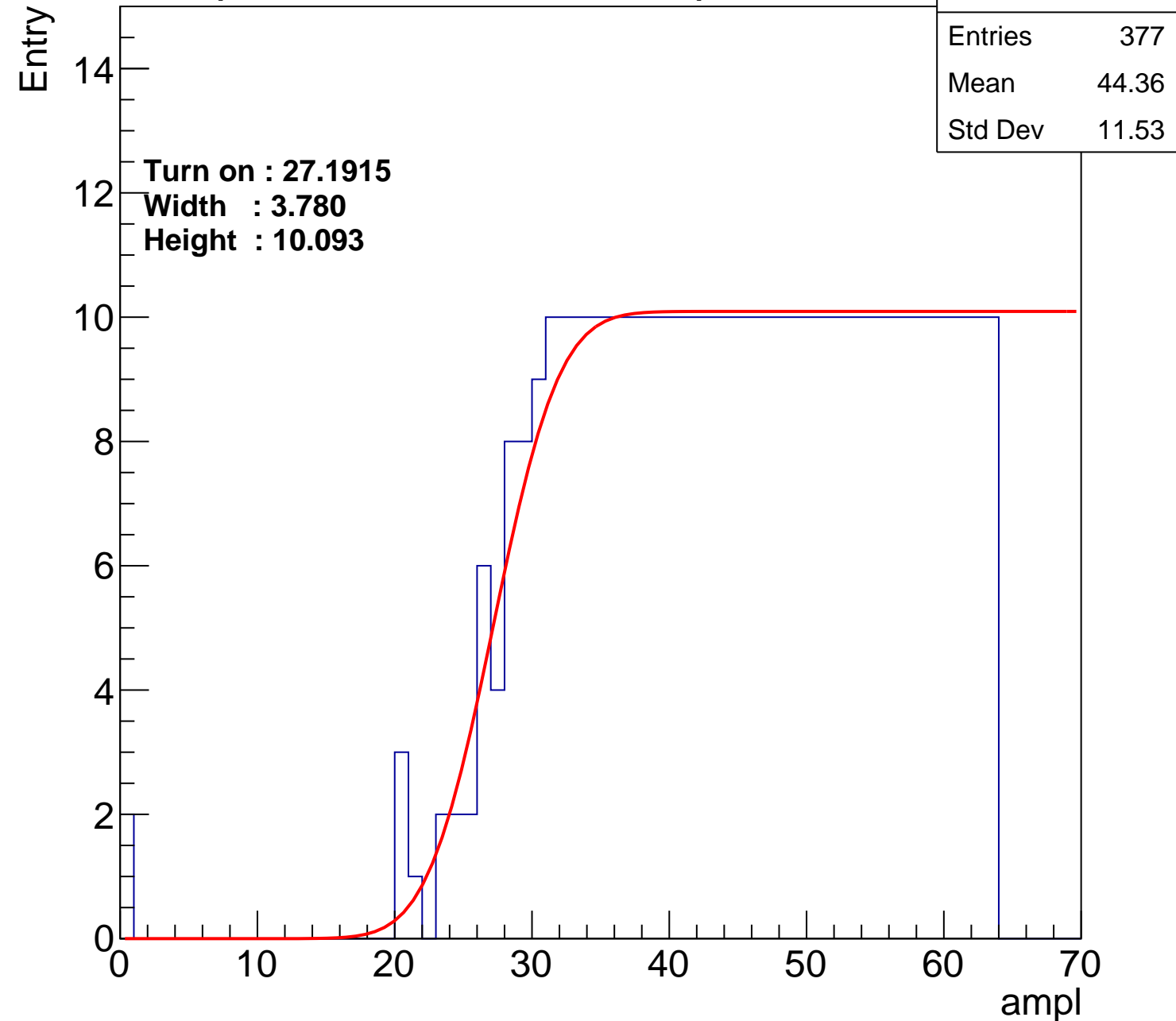
Width : 3.780

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.72
Std Dev	10.52

Turn on : 29.3097

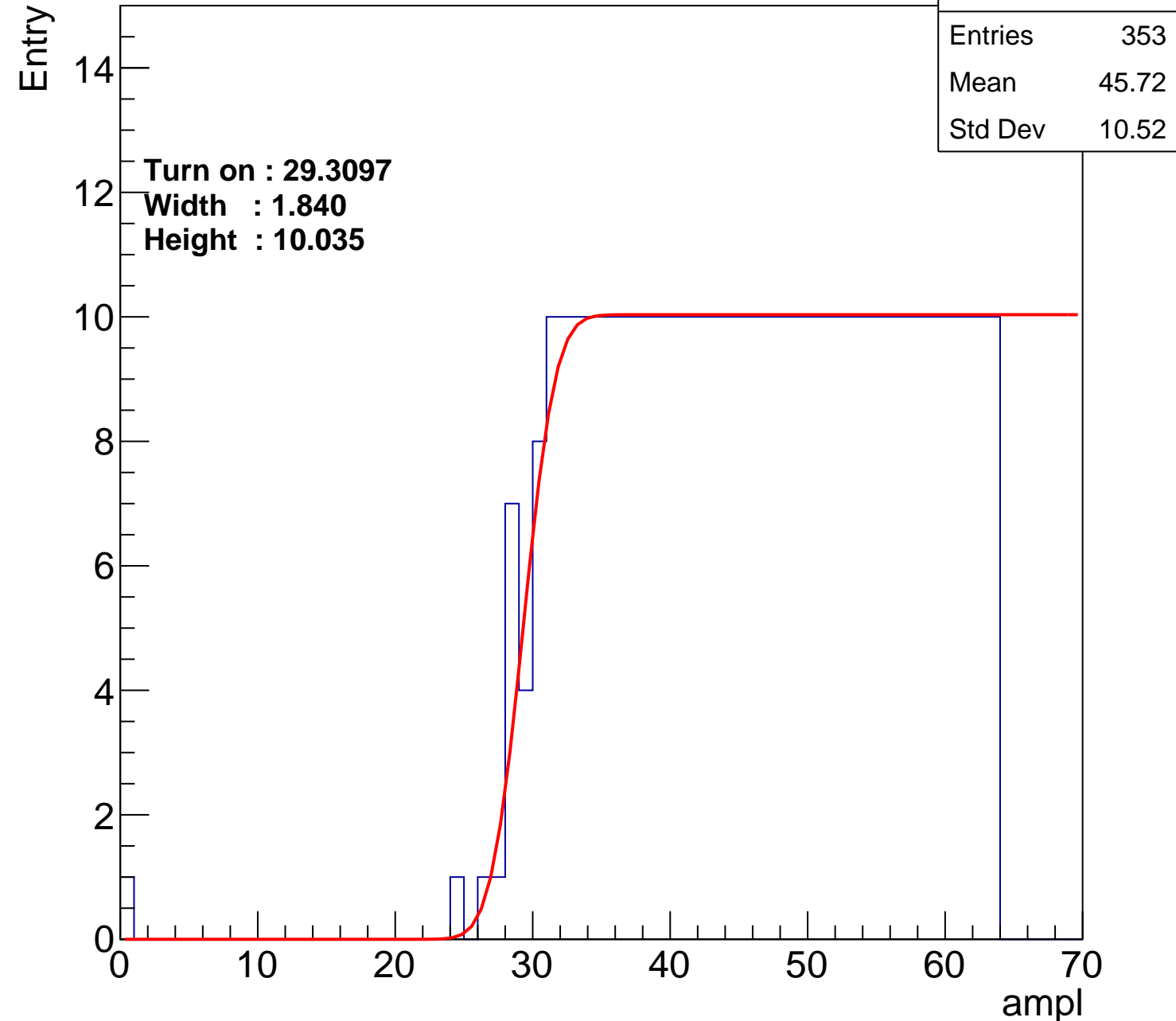
Width : 1.840

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.35
Std Dev	11.54

Turn on : 26.8722

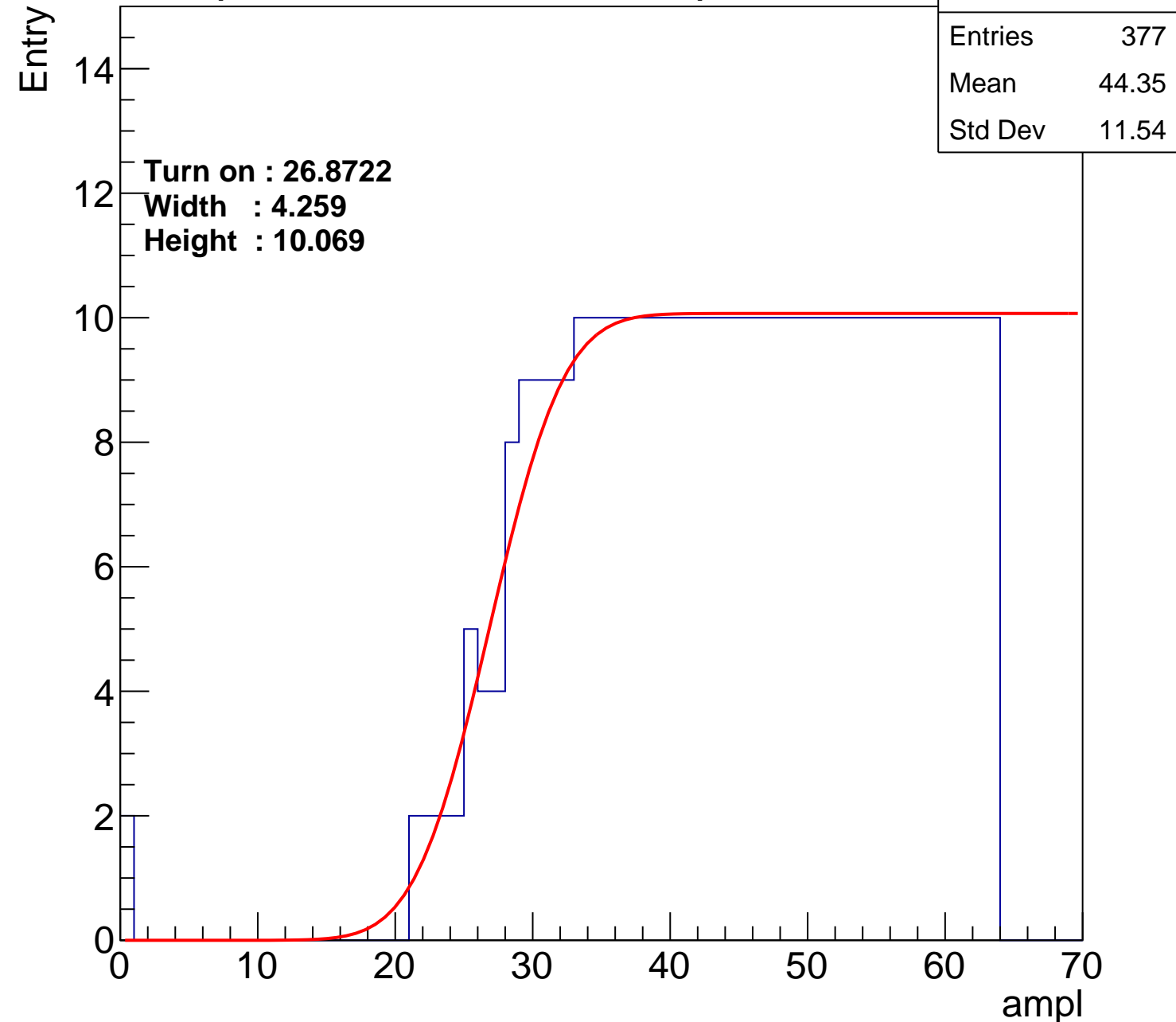
Width : 4.259

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.0671

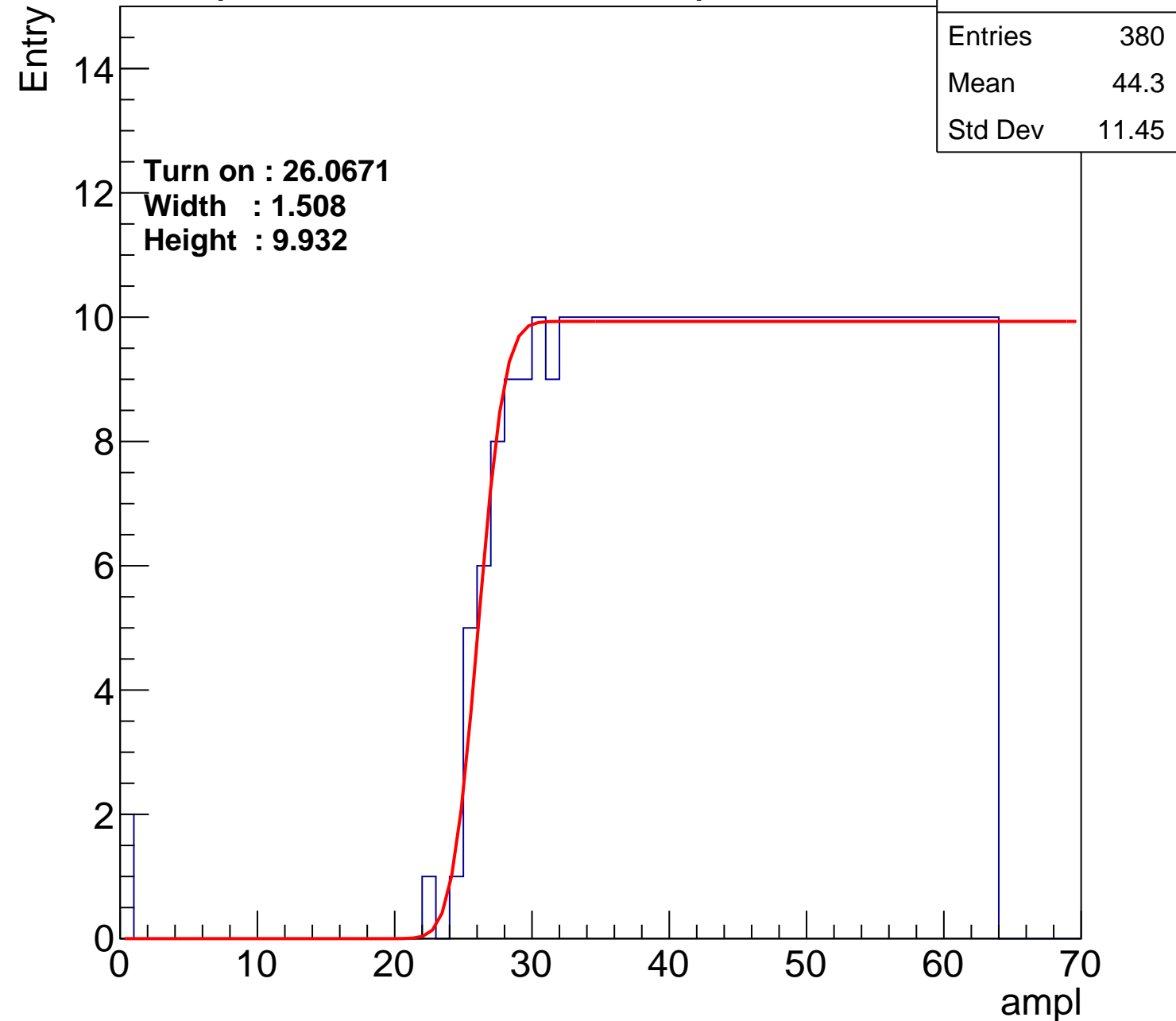
Width : 1.508

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.37
Std Dev	10.77

Turn on : 28.3698

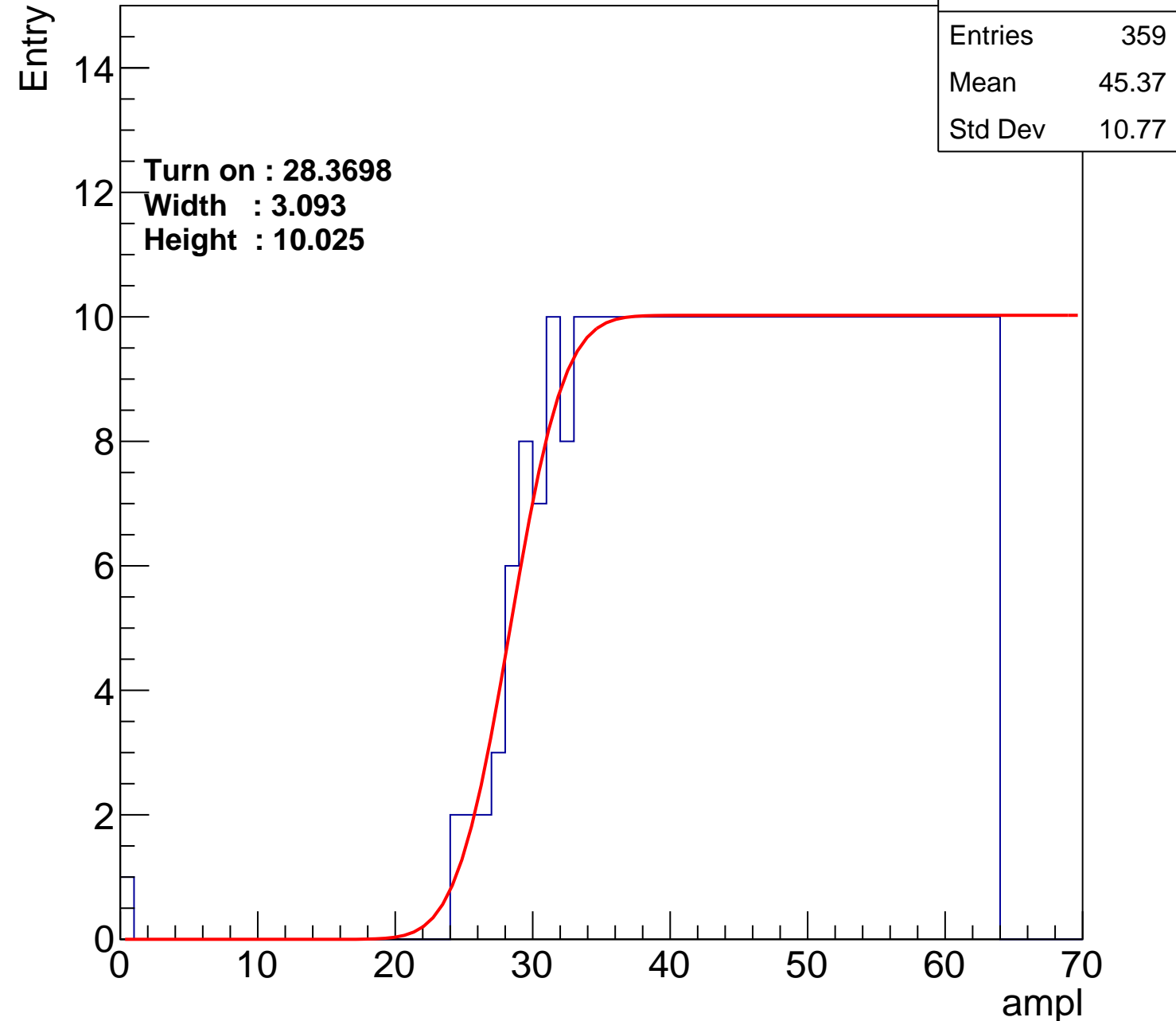
Width : 3.093

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.69
Std Dev	11.31

Turn on : 27.6405

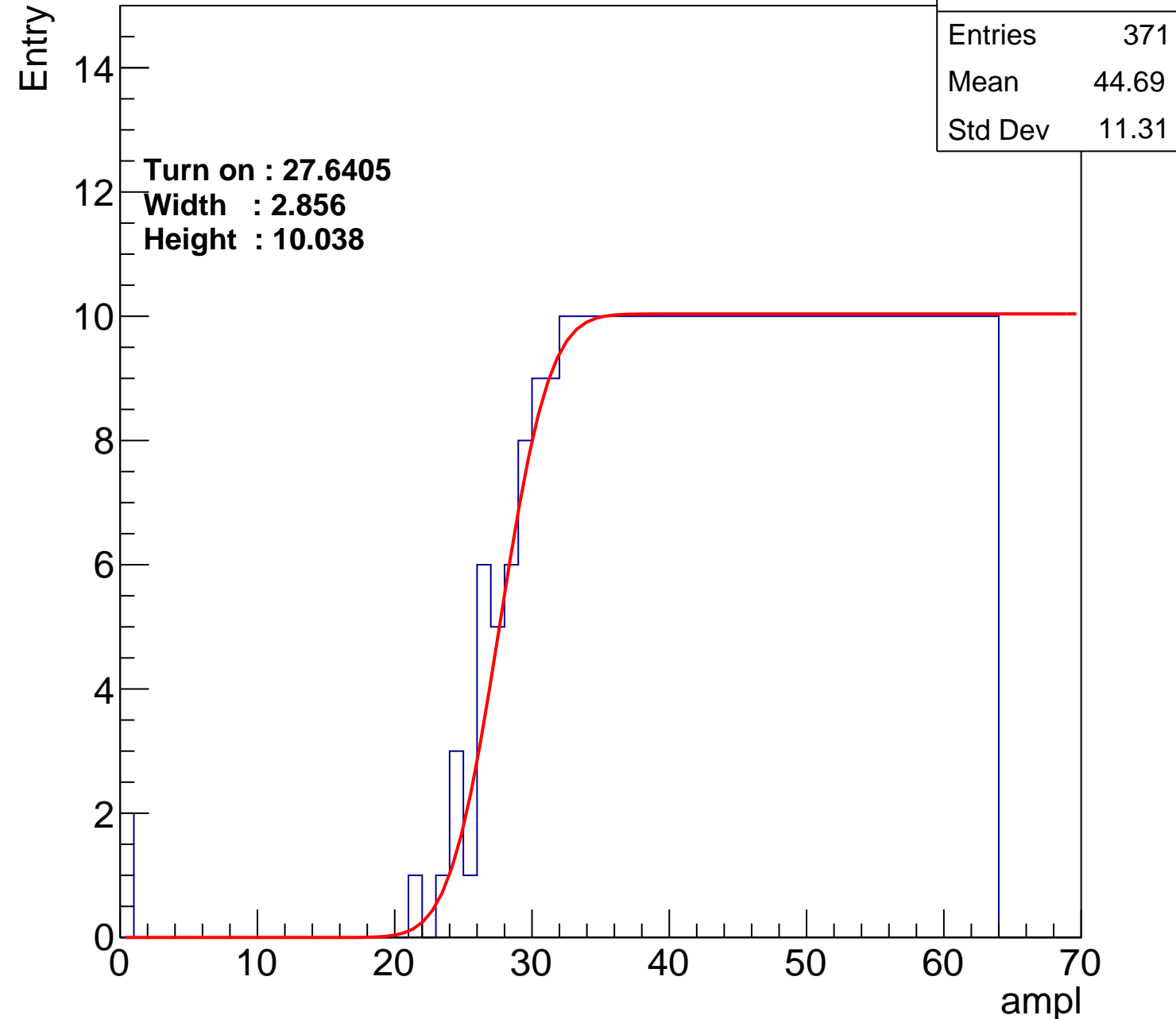
Width : 2.856

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.1
Std Dev	11.55

Turn on : 25.5415

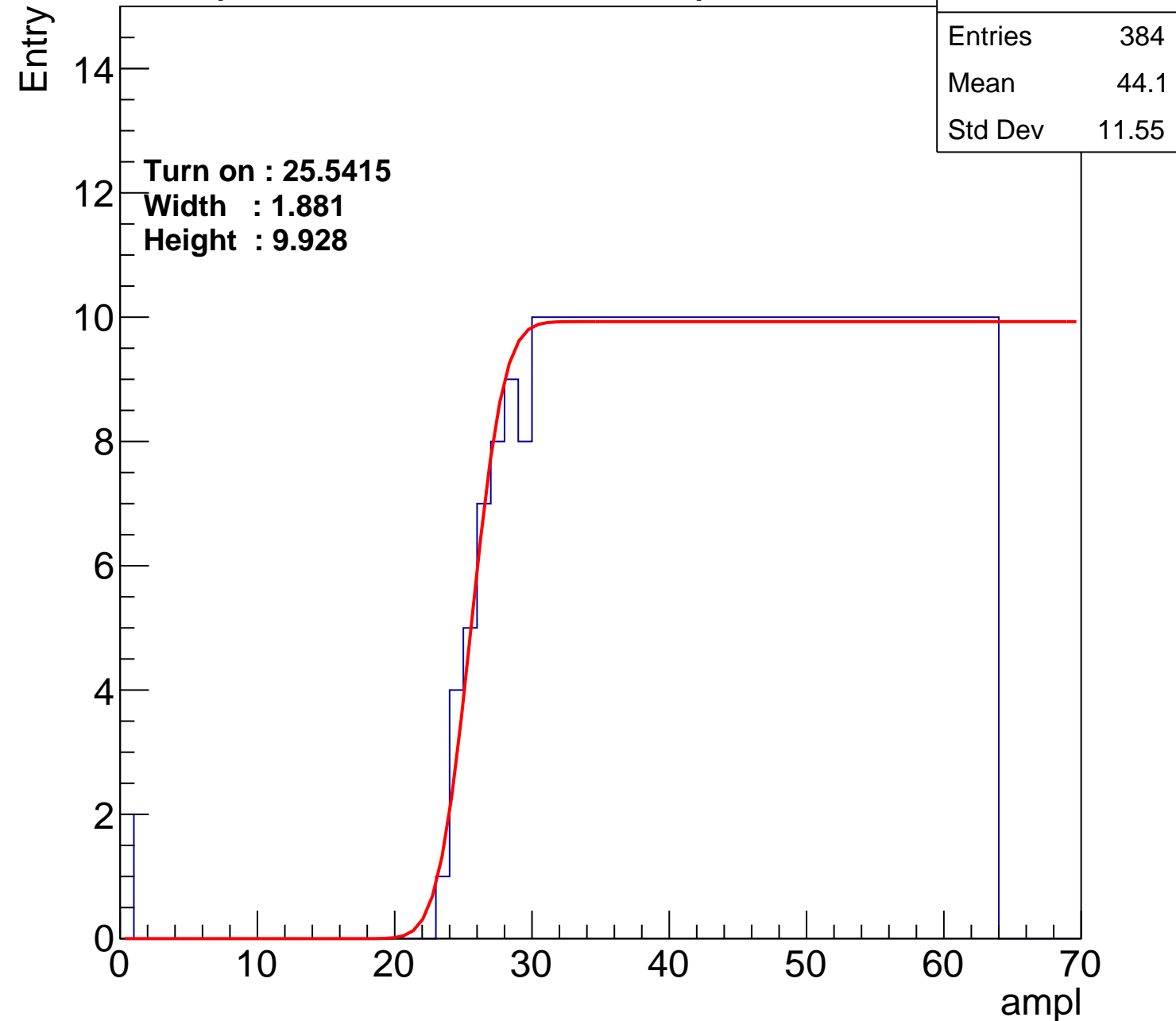
Width : 1.881

Height : 9.928

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.7018

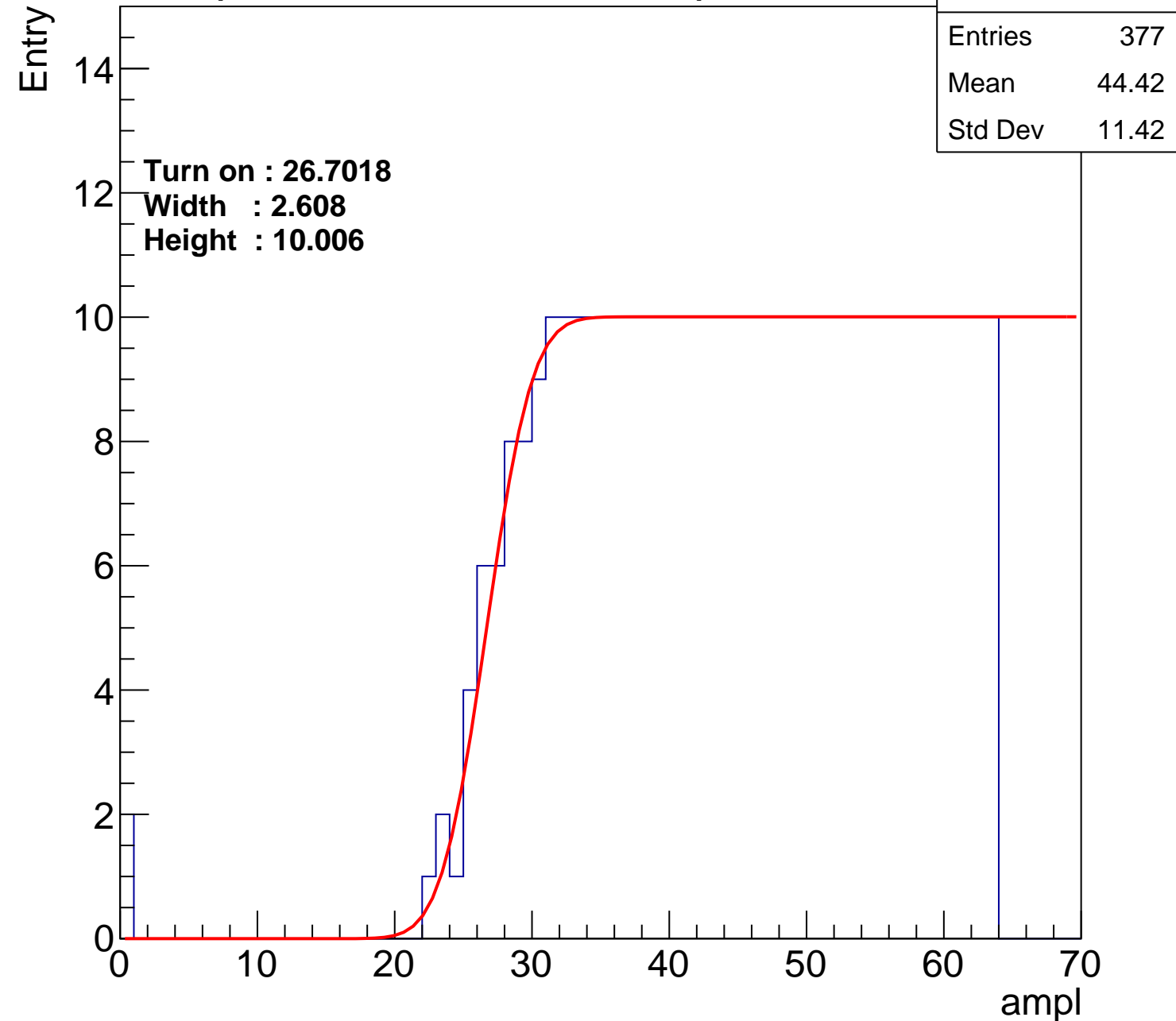
Width : 2.608

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.44
Std Dev	11.44

Turn on : 26.8852

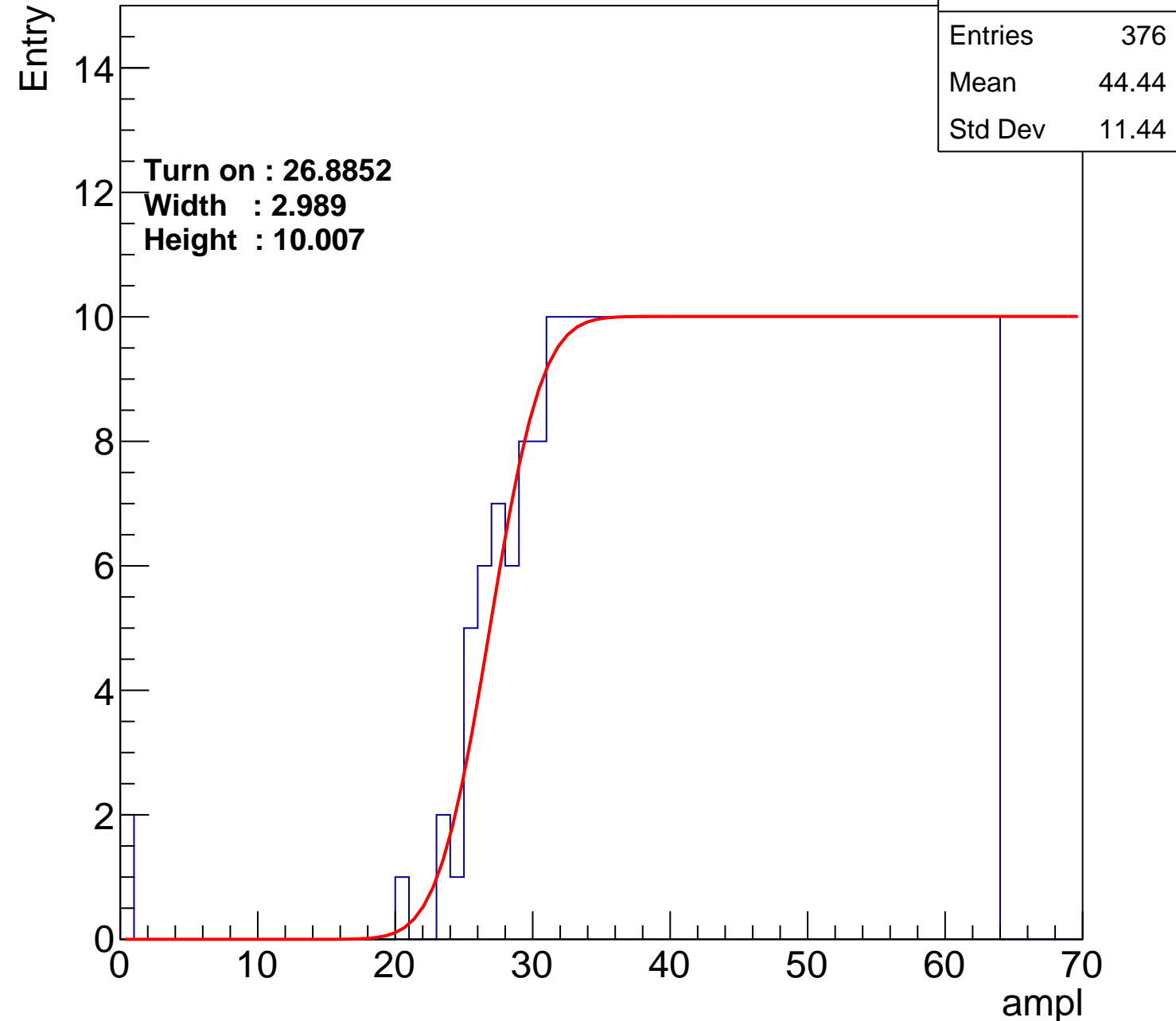
Width : 2.989

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.91
Std Dev	11.22

Turn on : 28.1336

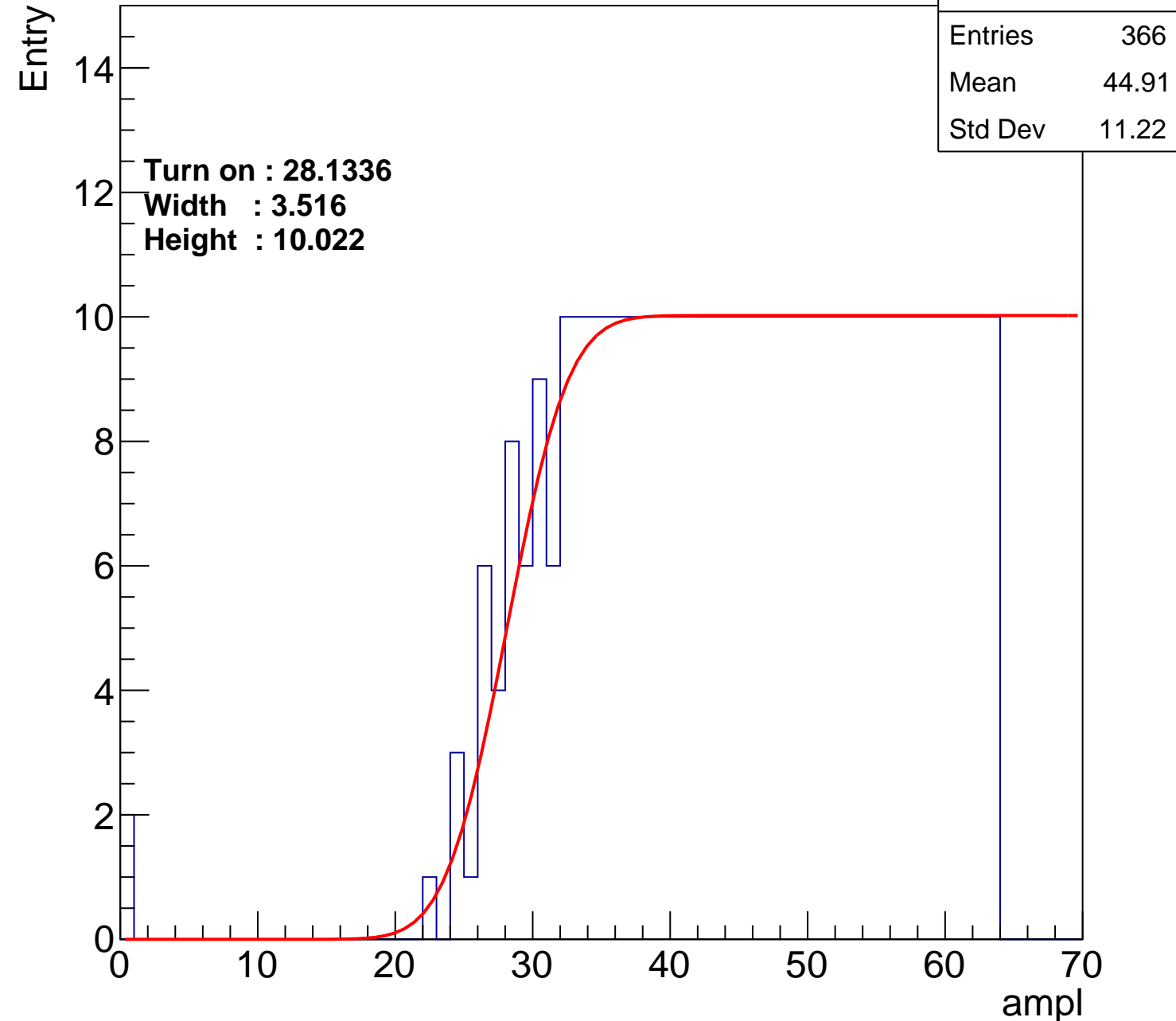
Width : 3.516

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.54
Std Dev	11.85

Turn on : 28.1155

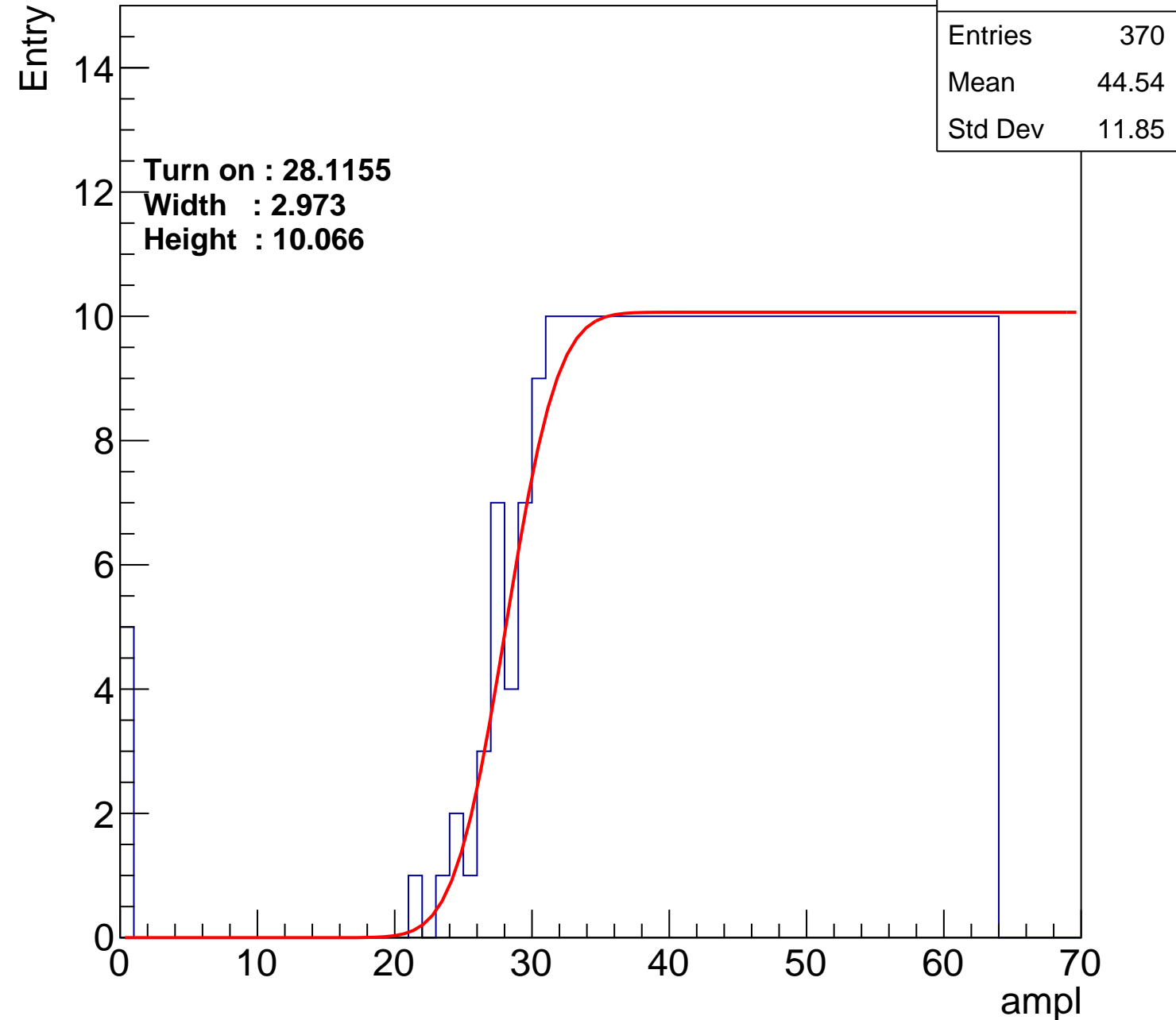
Width : 2.973

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.68
Std Dev	12.05

Turn on : 26.1361

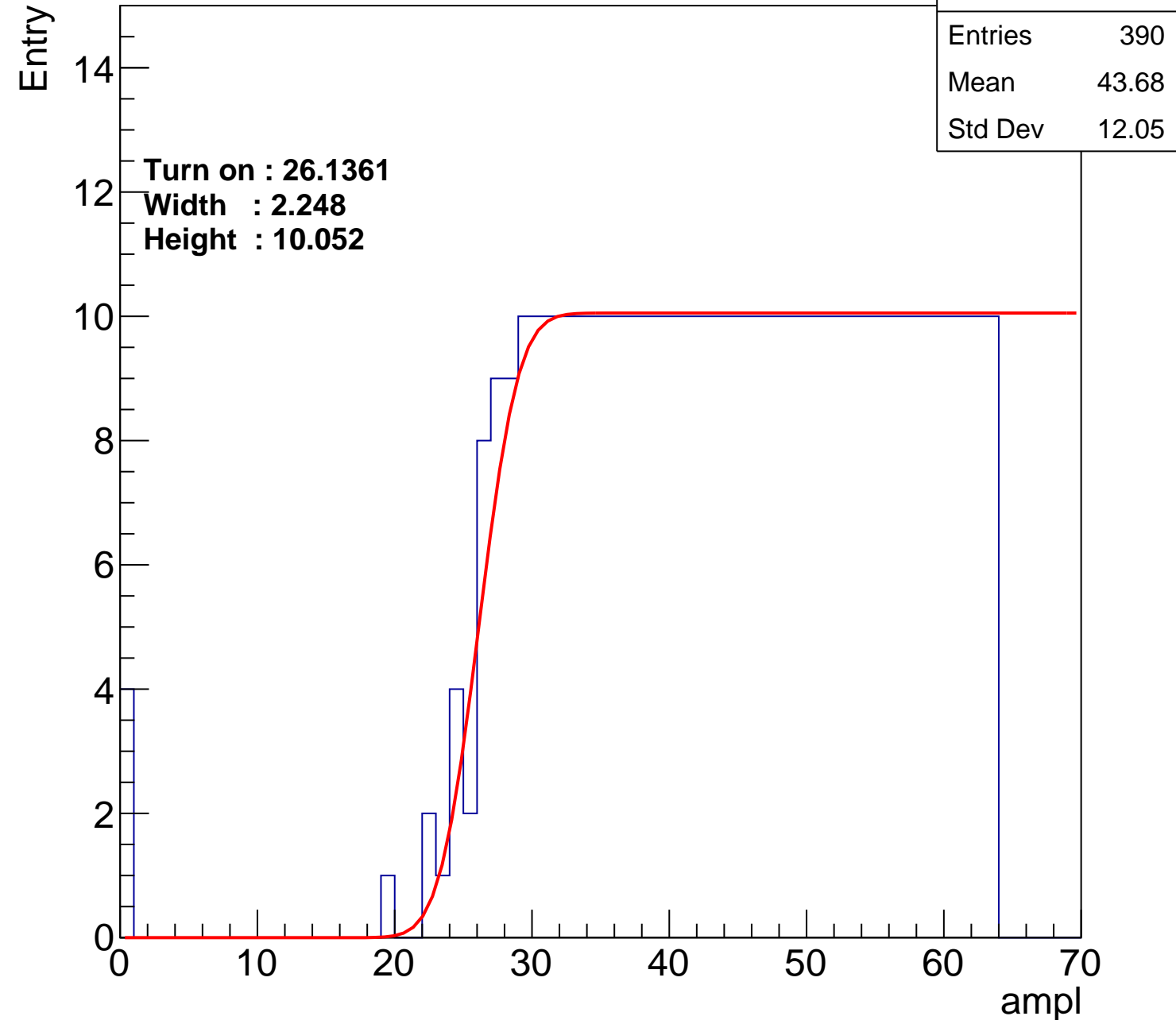
Width : 2.248

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.11
Std Dev	11.72

Turn on : 26.8471

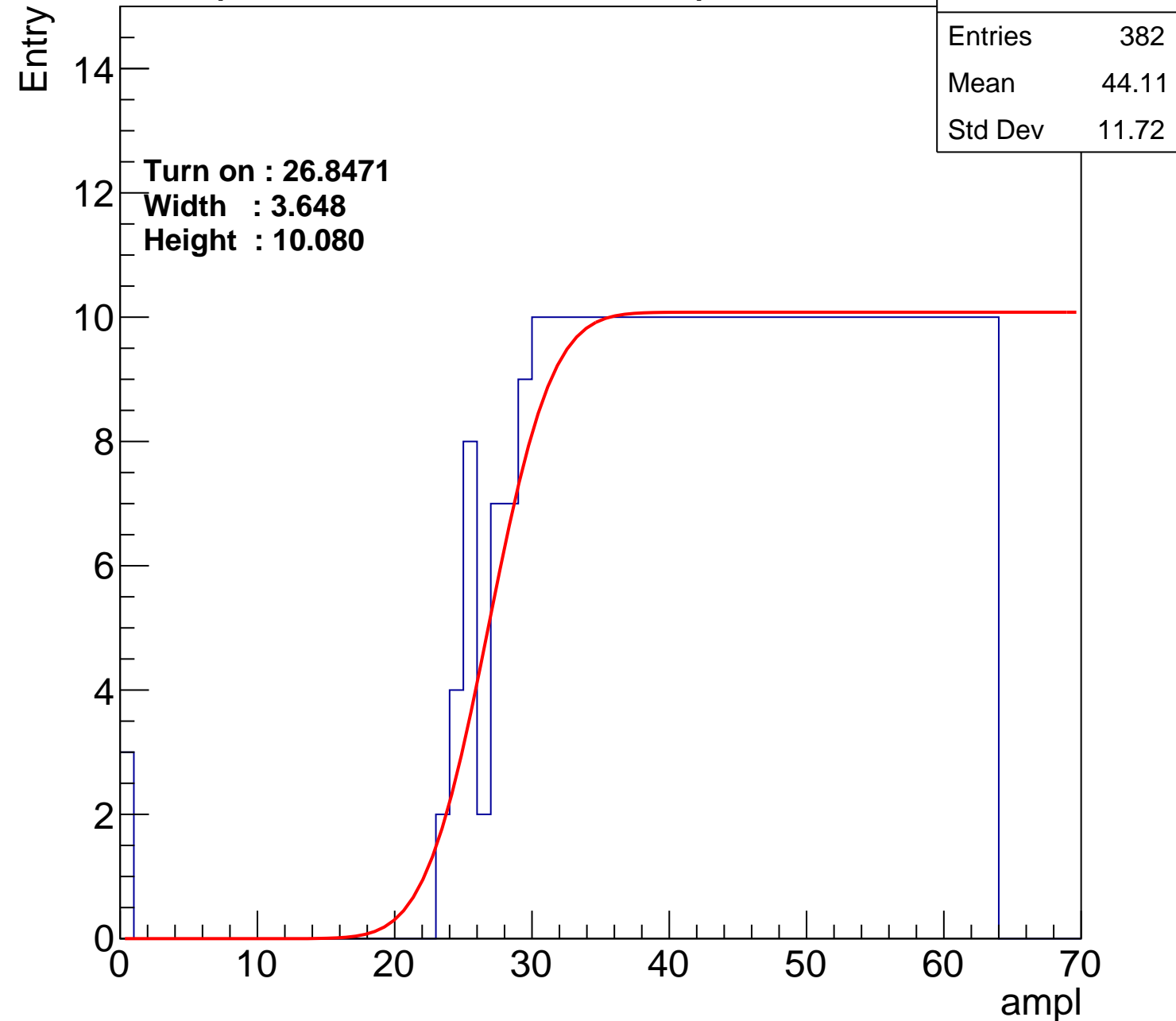
Width : 3.648

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.76
Std Dev	10.78

Turn on : 28.5275

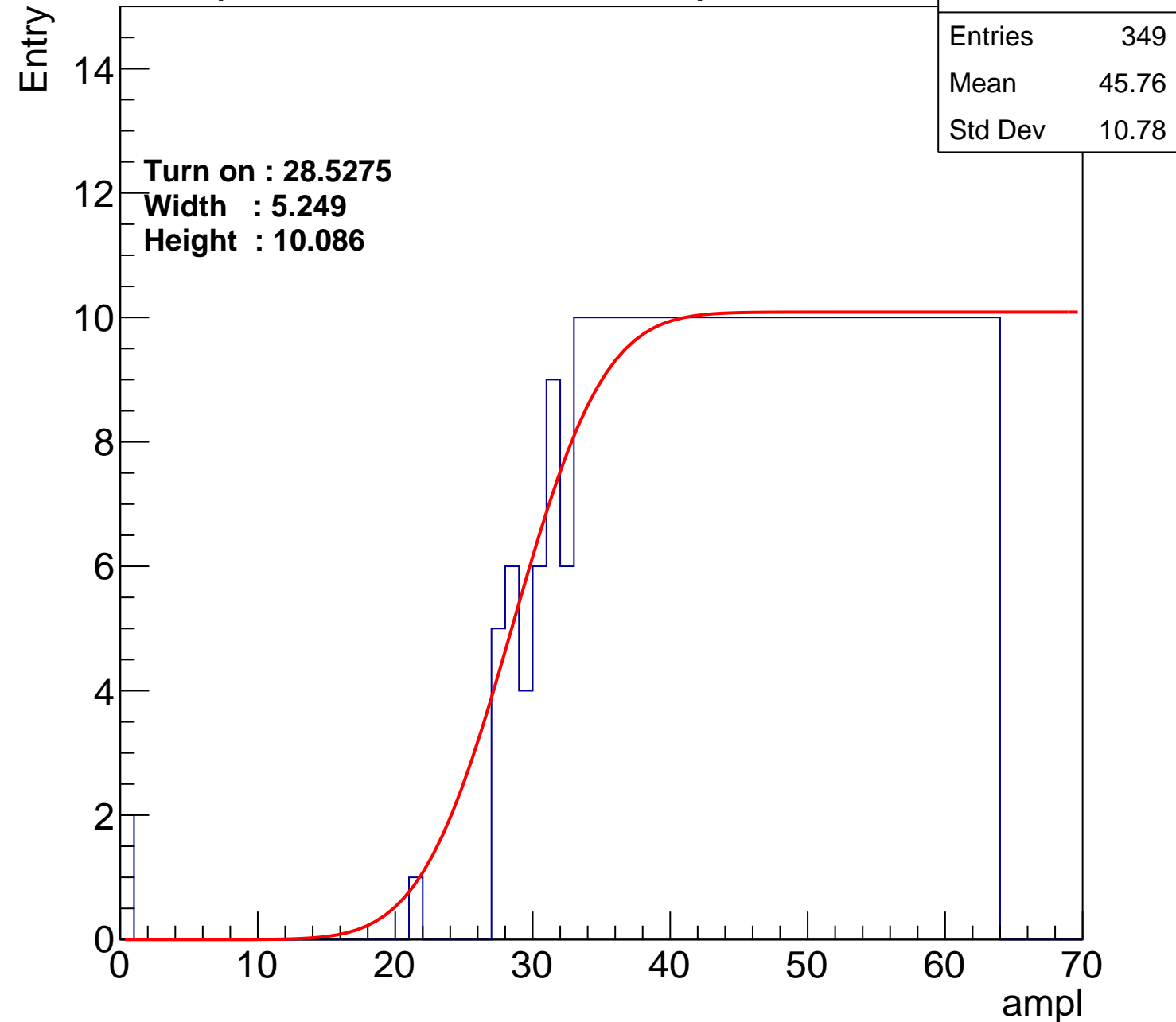
Width : 5.249

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.37
Std Dev	10.78

Turn on : 28.6257

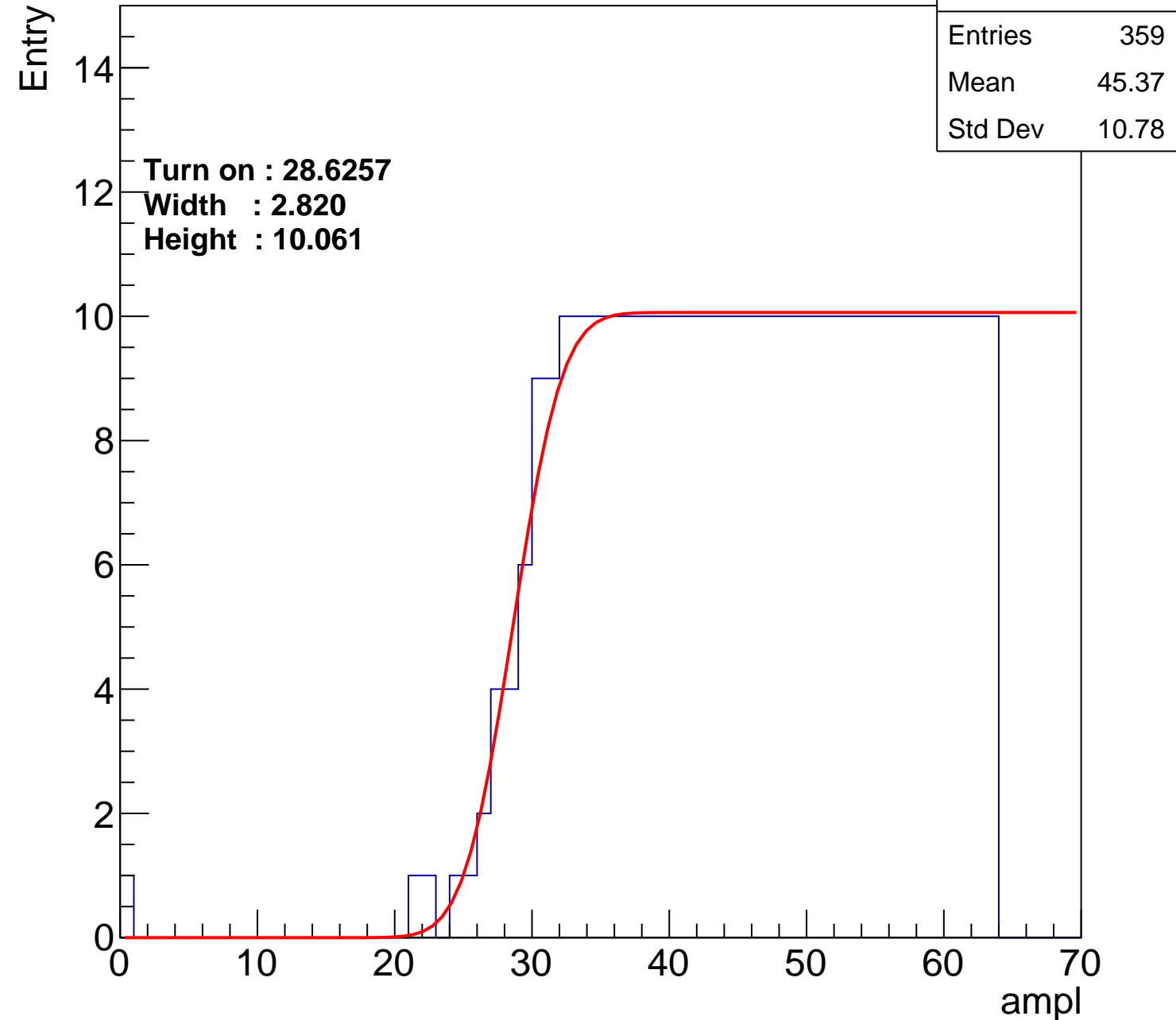
Width : 2.820

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 27.7980

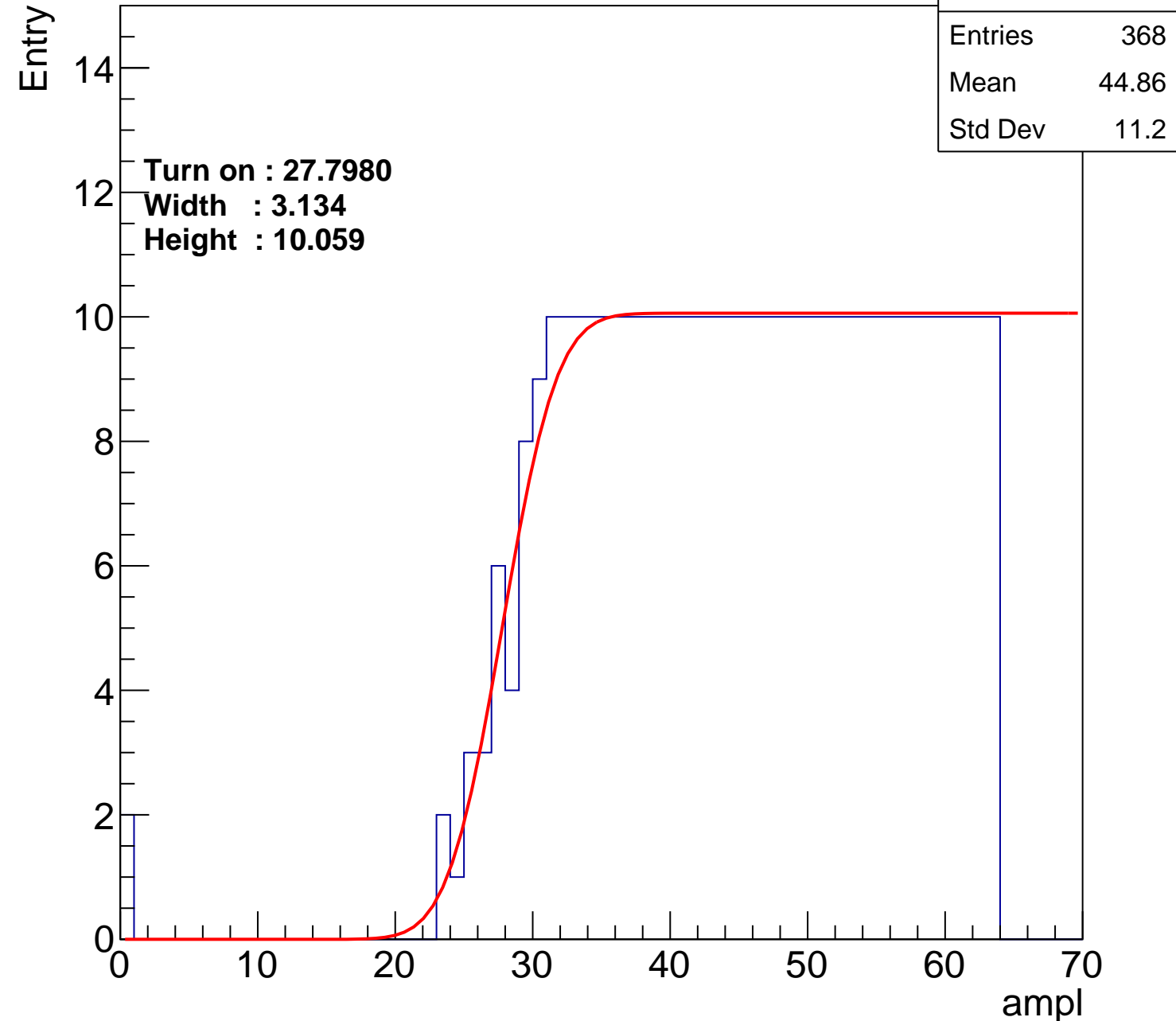
Width : 3.134

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 25.6349

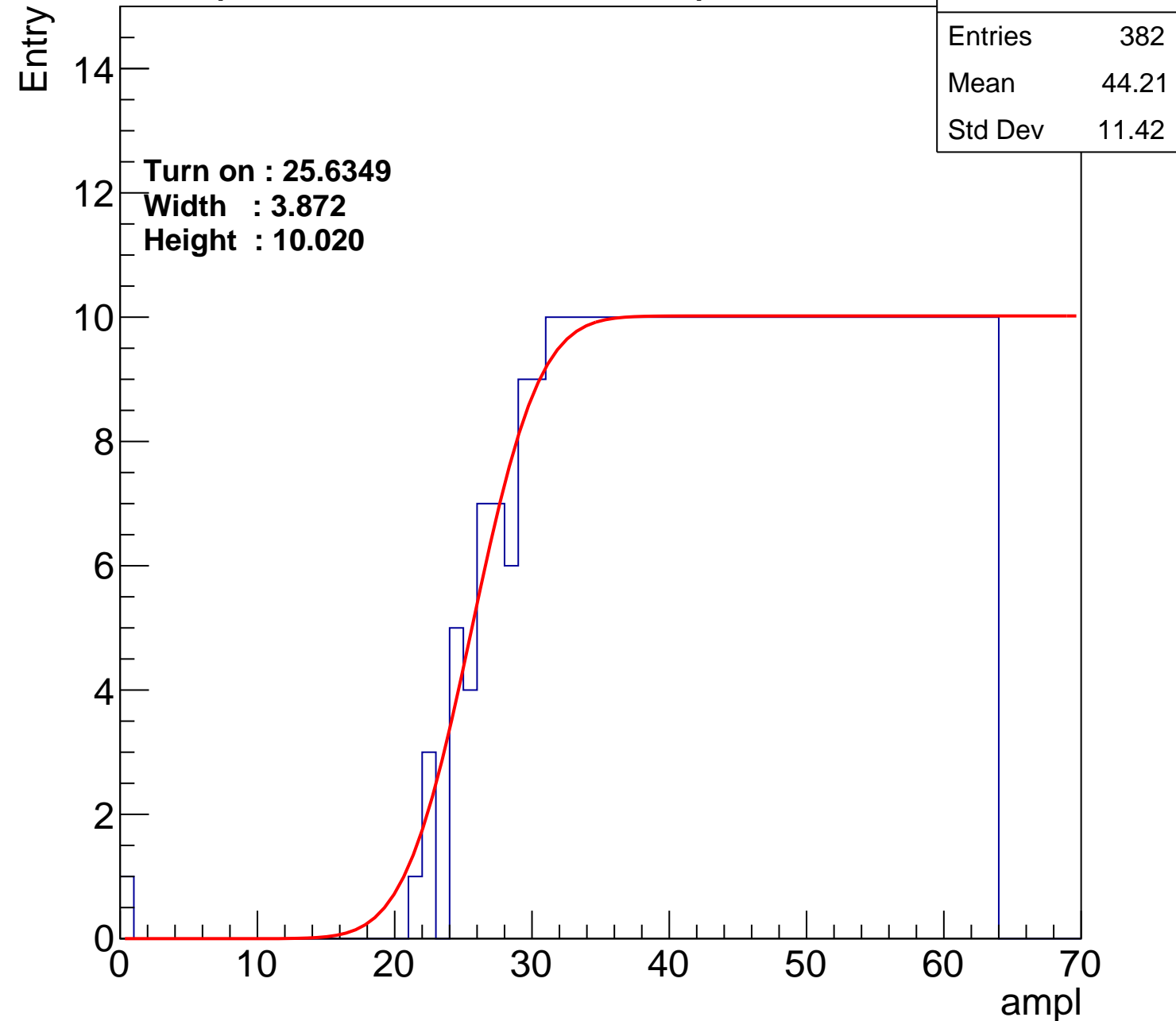
Width : 3.872

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.14
Std Dev	11.44

Turn on : 26.2539

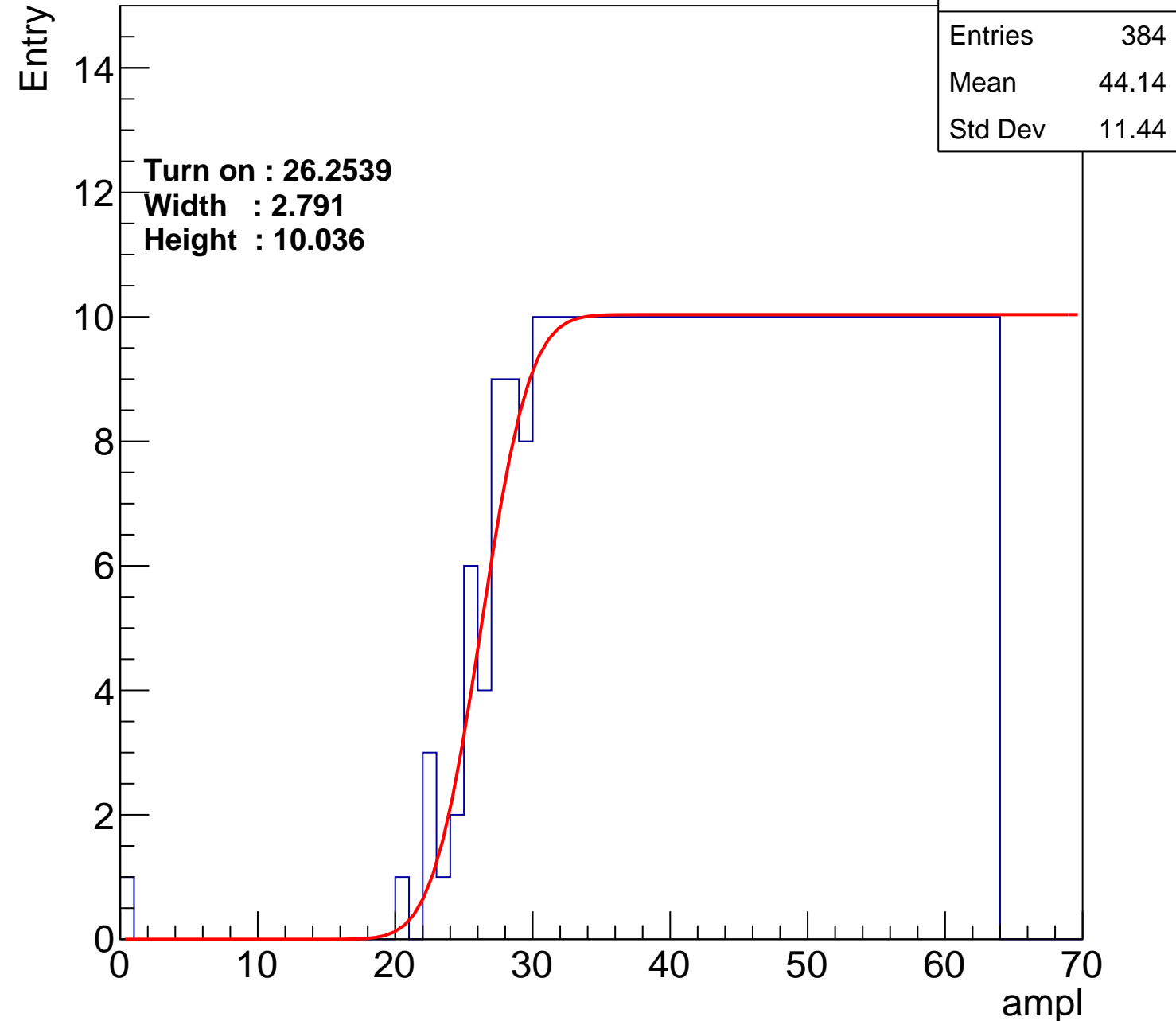
Width : 2.791

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.33
Std Dev	10.82

Turn on : 28.4205

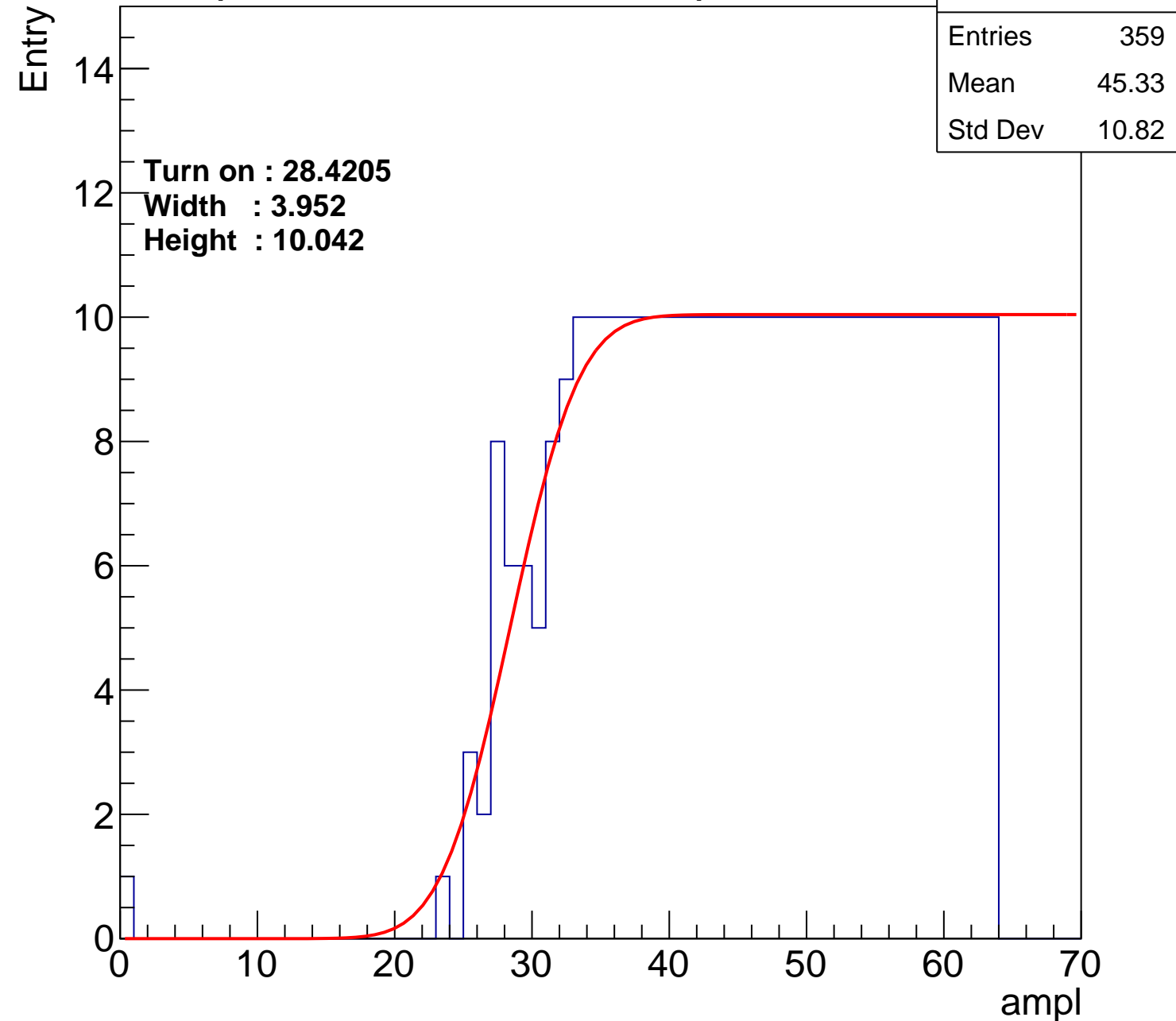
Width : 3.952

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 28.0933

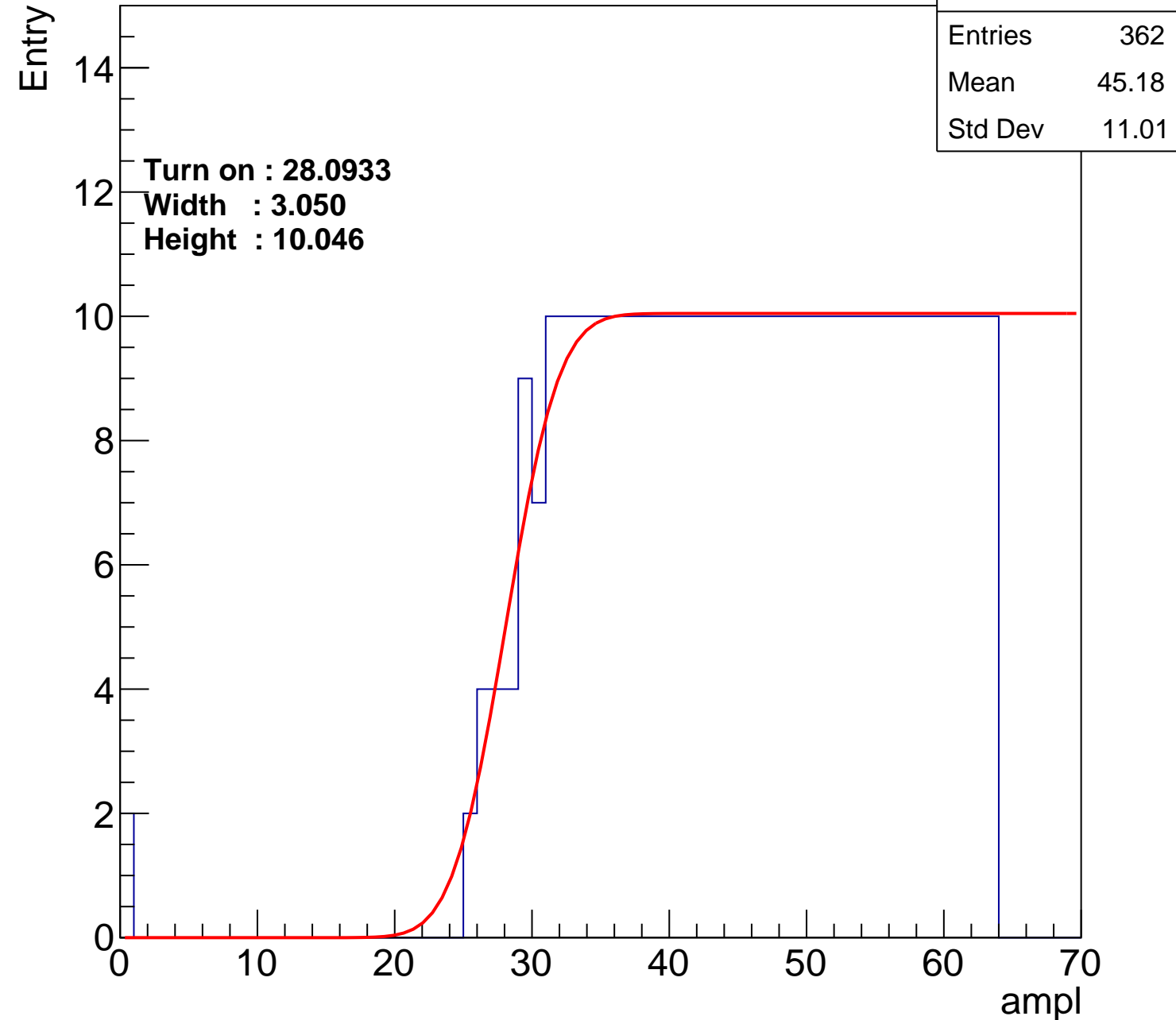
Width : 3.050

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries 344

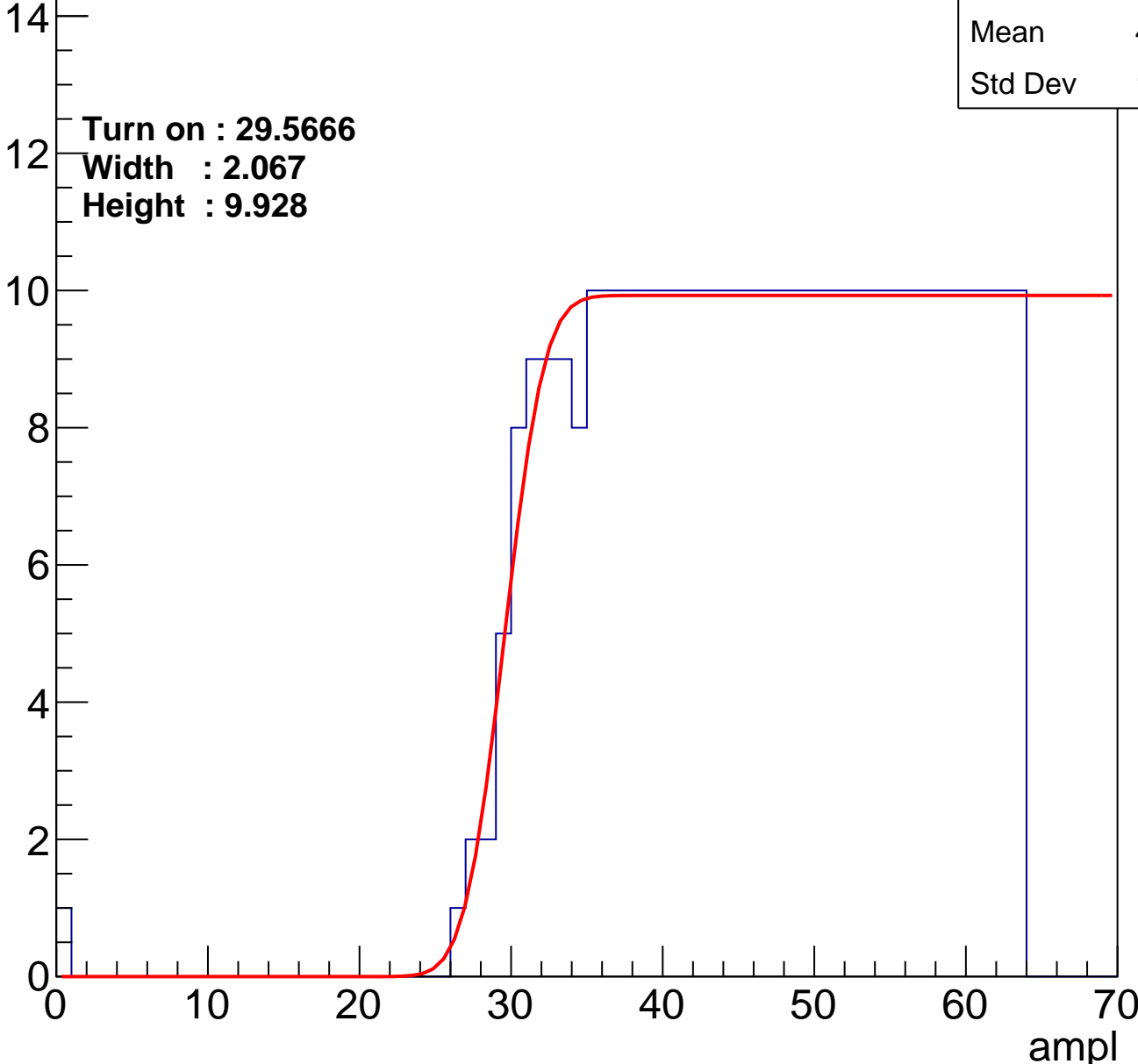
Mean 46.12

Std Dev 10.34

Turn on : 29.5666

Width : 2.067

Height : 9.928



B0L002S, U6-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.54
Std Dev	10.87

Turn on : 29.4143

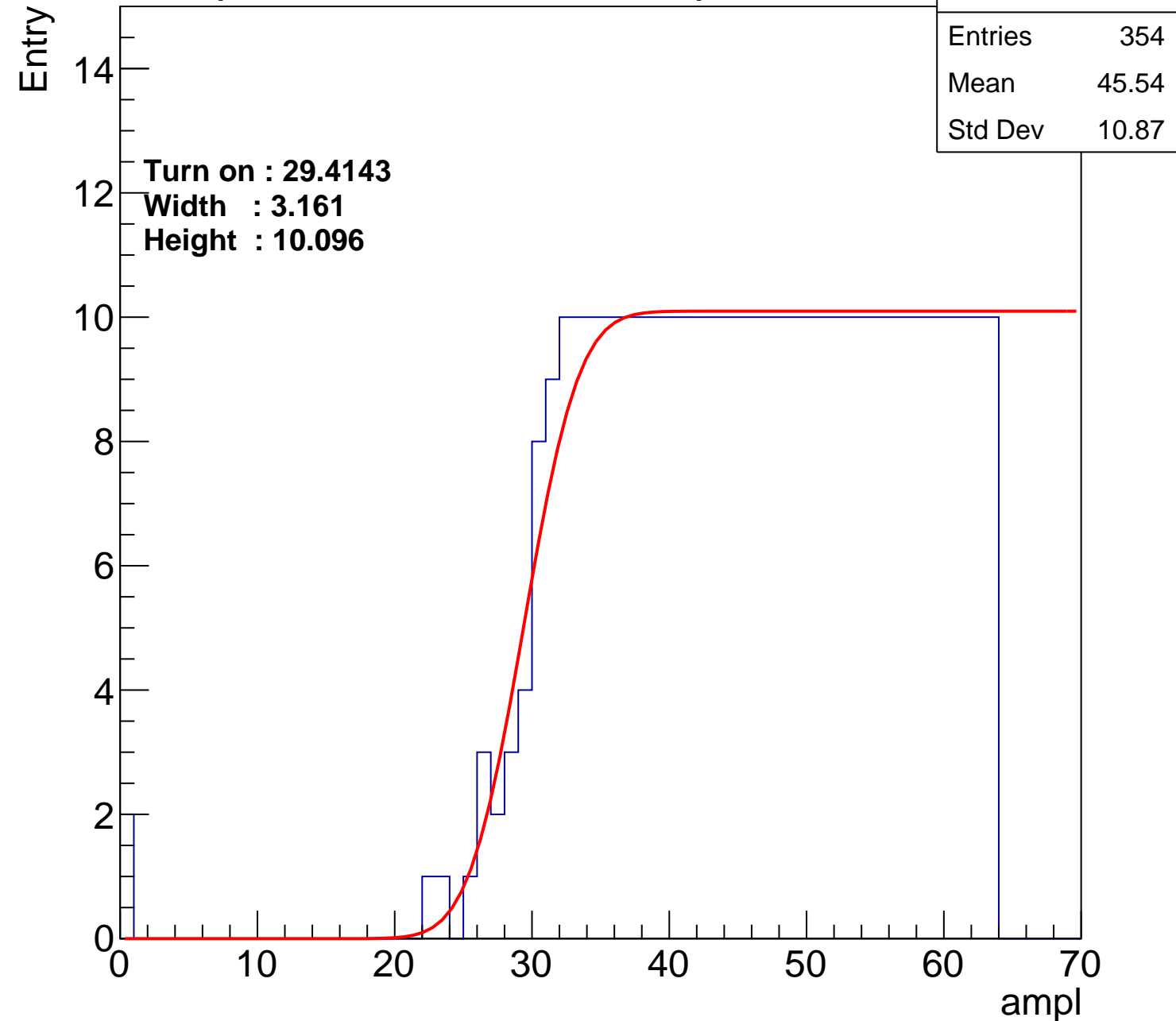
Width : 3.161

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch101

calib_packv5_042523_0143.root, FC#8, port C1

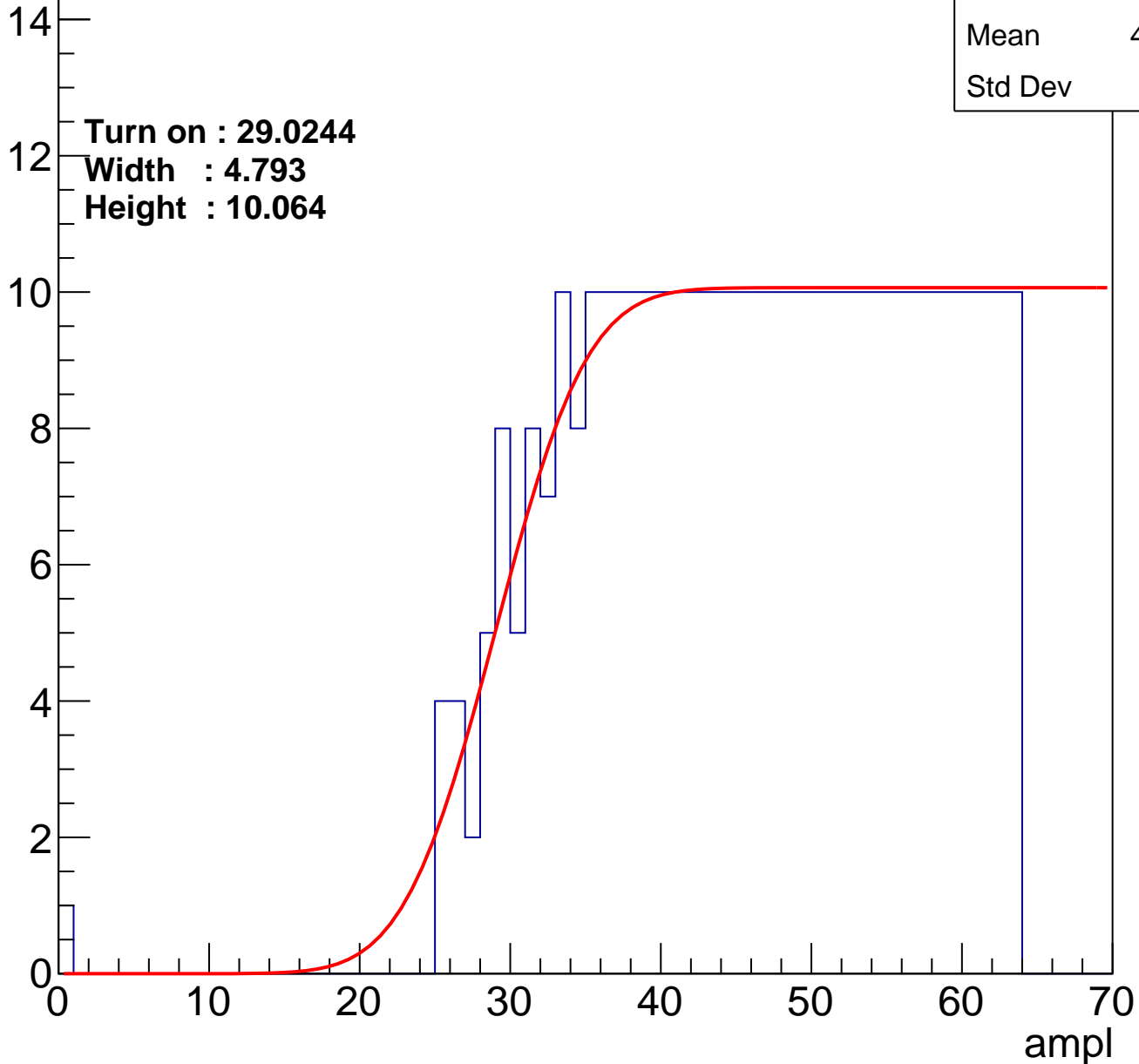
Entries	352
Mean	45.64
Std Dev	10.7

Turn on : 29.0244

Width : 4.793

Height : 10.064

Entry



B0L002S, U6-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.7782

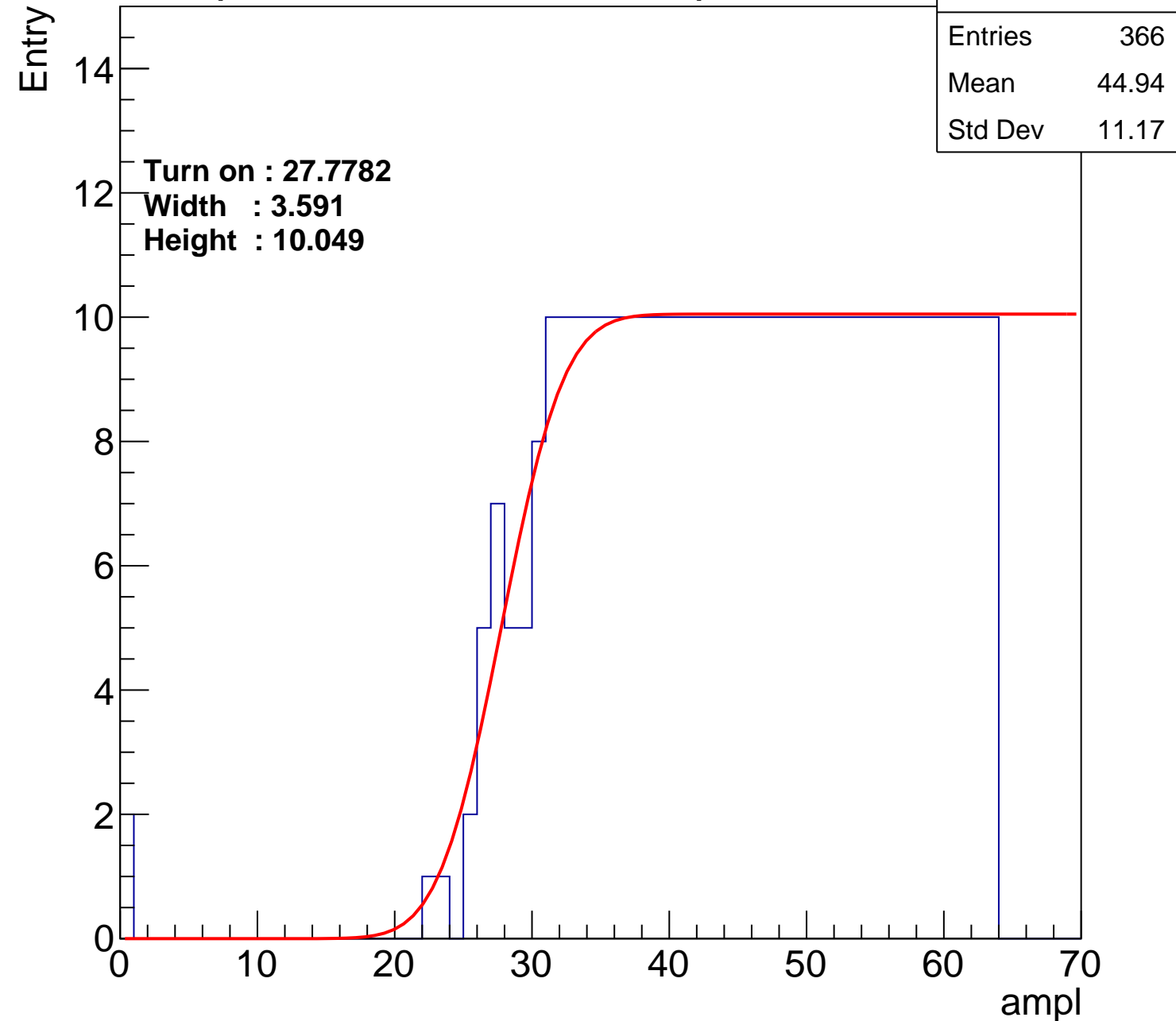
Width : 3.591

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.93
Std Dev	11.48

Turn on : 28.2784

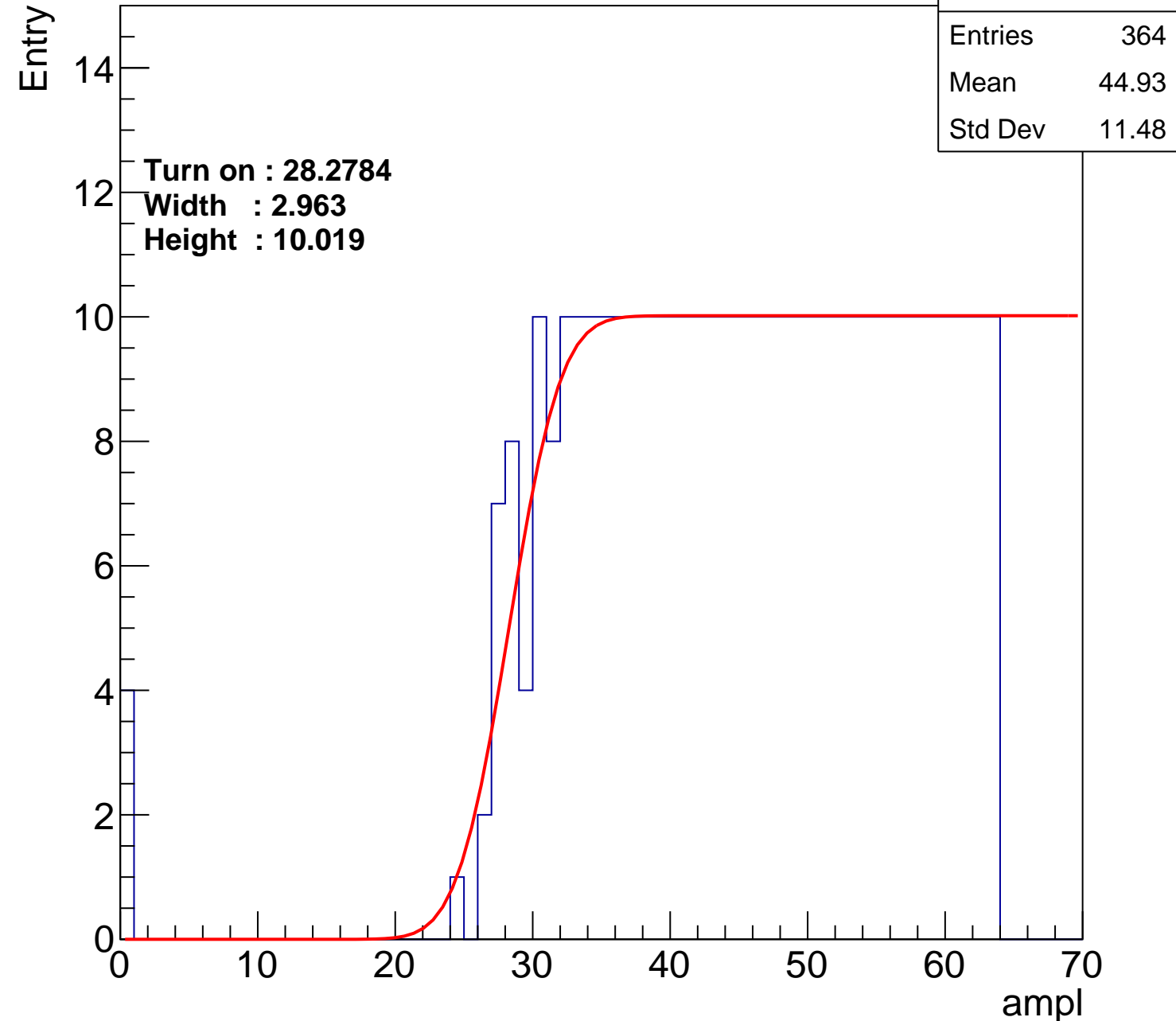
Width : 2.963

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.22
Std Dev	10.79

Turn on : 27.8361

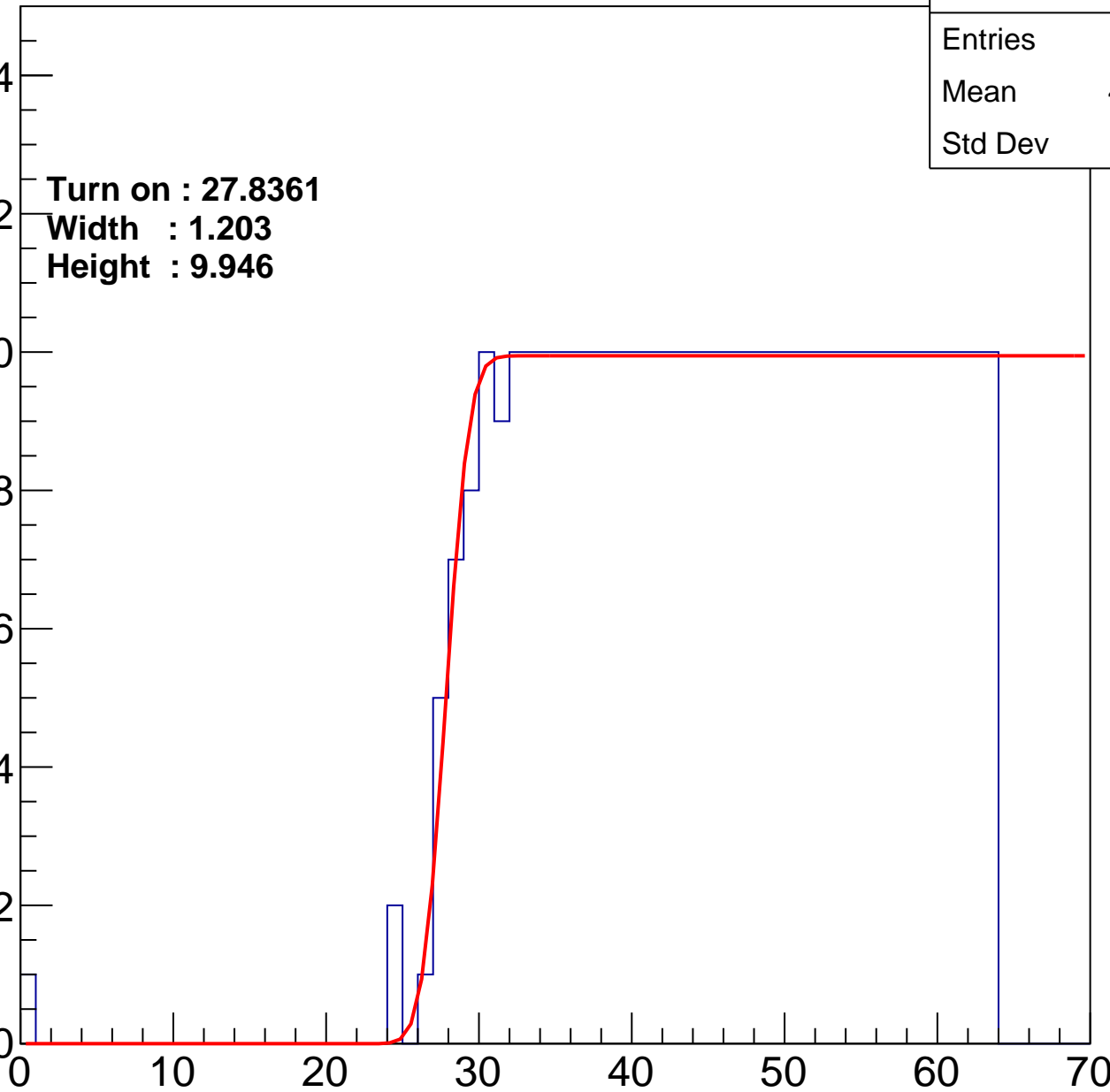
Width : 1.203

Height : 9.946

Entry

14
12
10
8
6
4
2
0

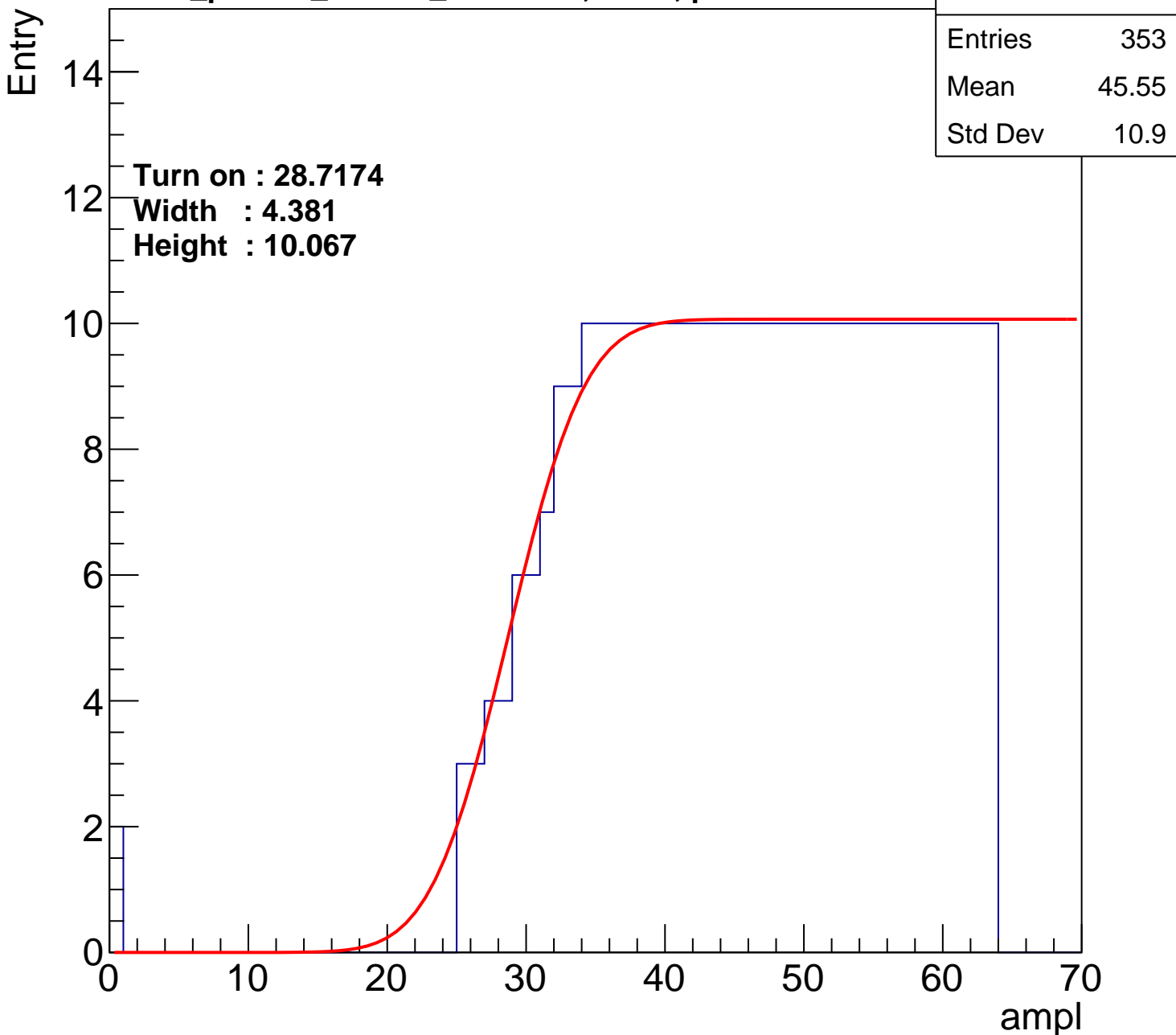
ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.7174
Width : 4.381
Height : 10.067



B0L002S, U6-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.14
Std Dev	11.01

Turn on : 27.8564

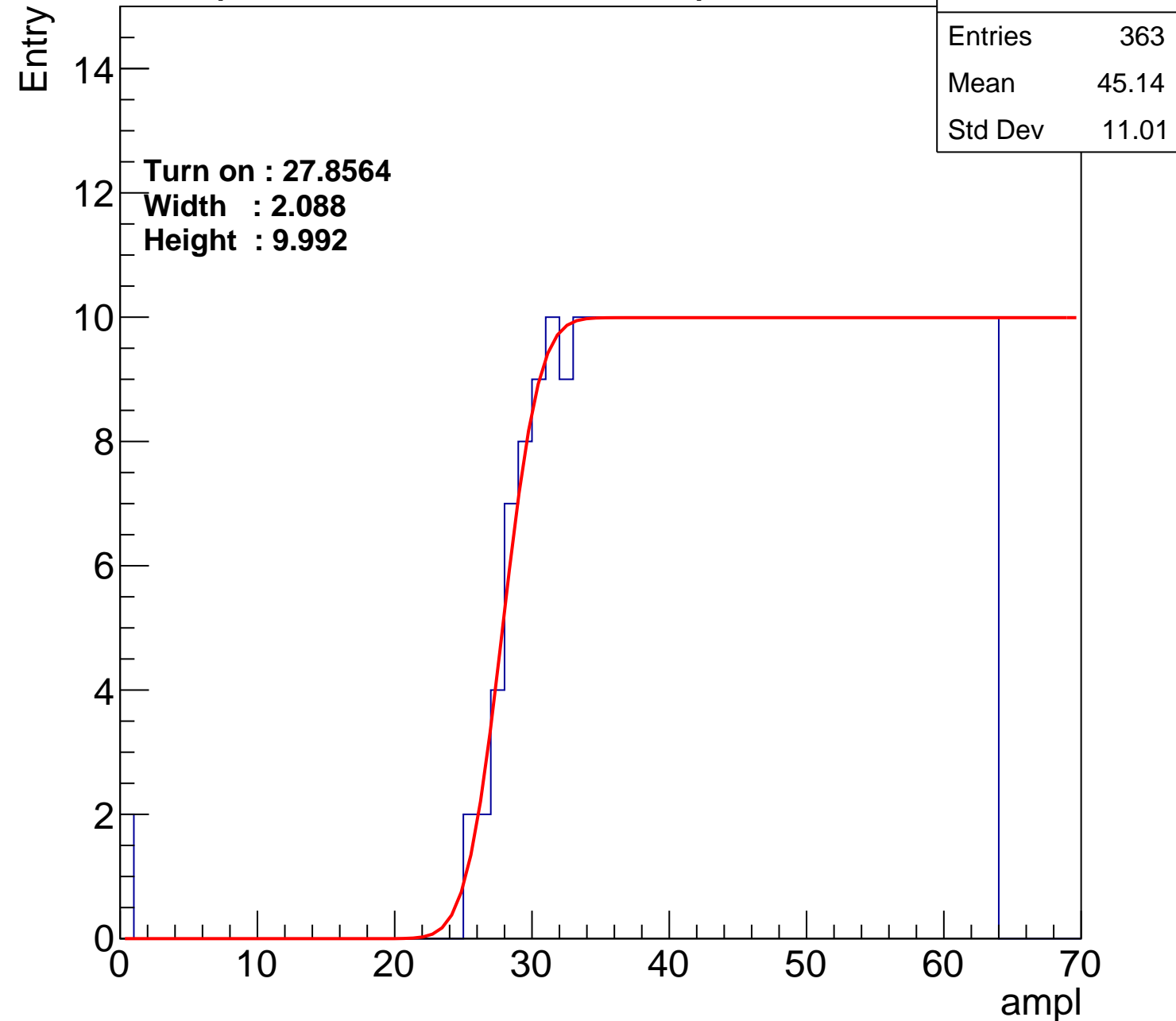
Width : 2.088

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.56
Std Dev	11.25

Turn on : 29.6707

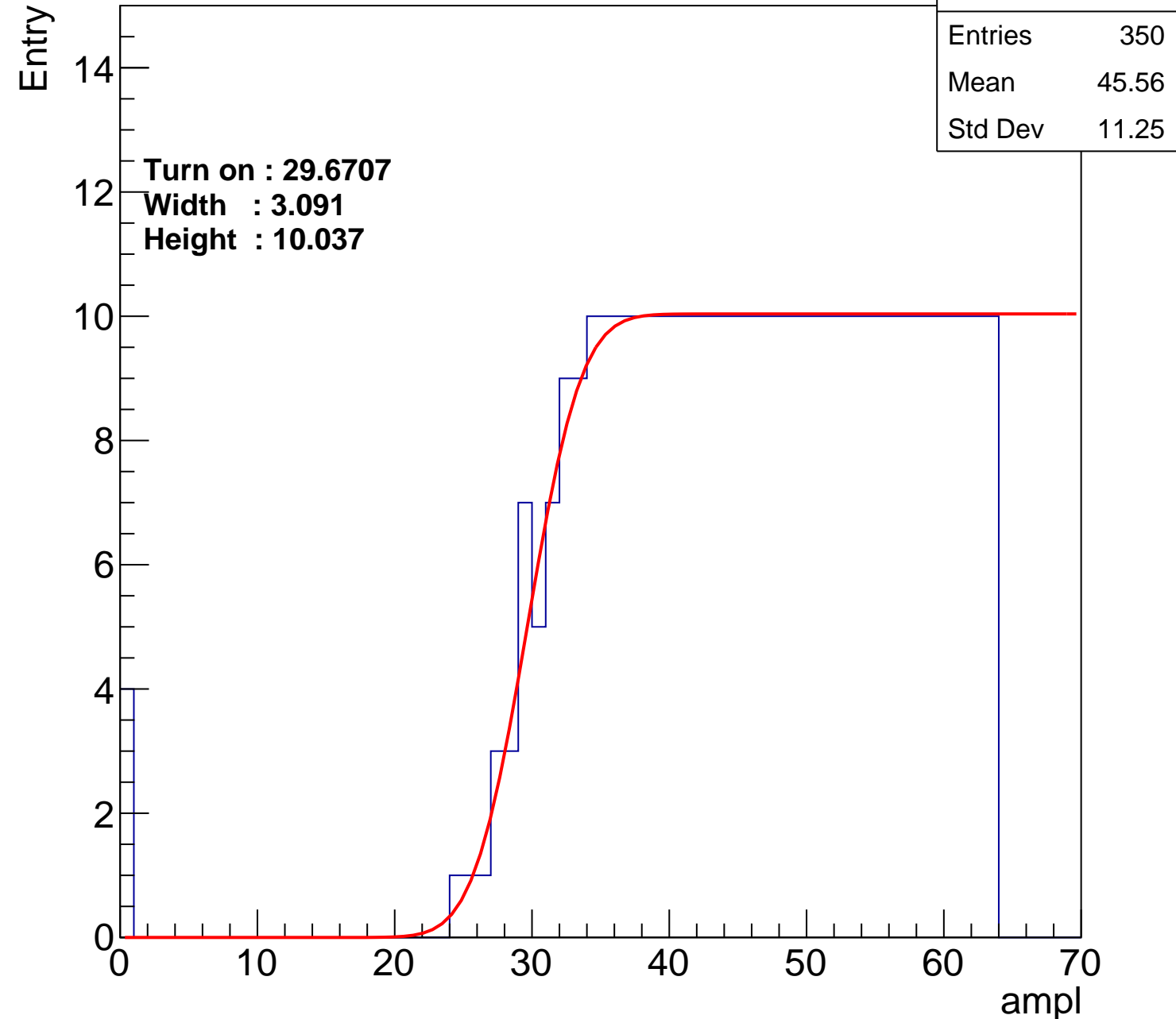
Width : 3.091

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch108

calib_packv5_042523_0143.root, FC#8, port C1

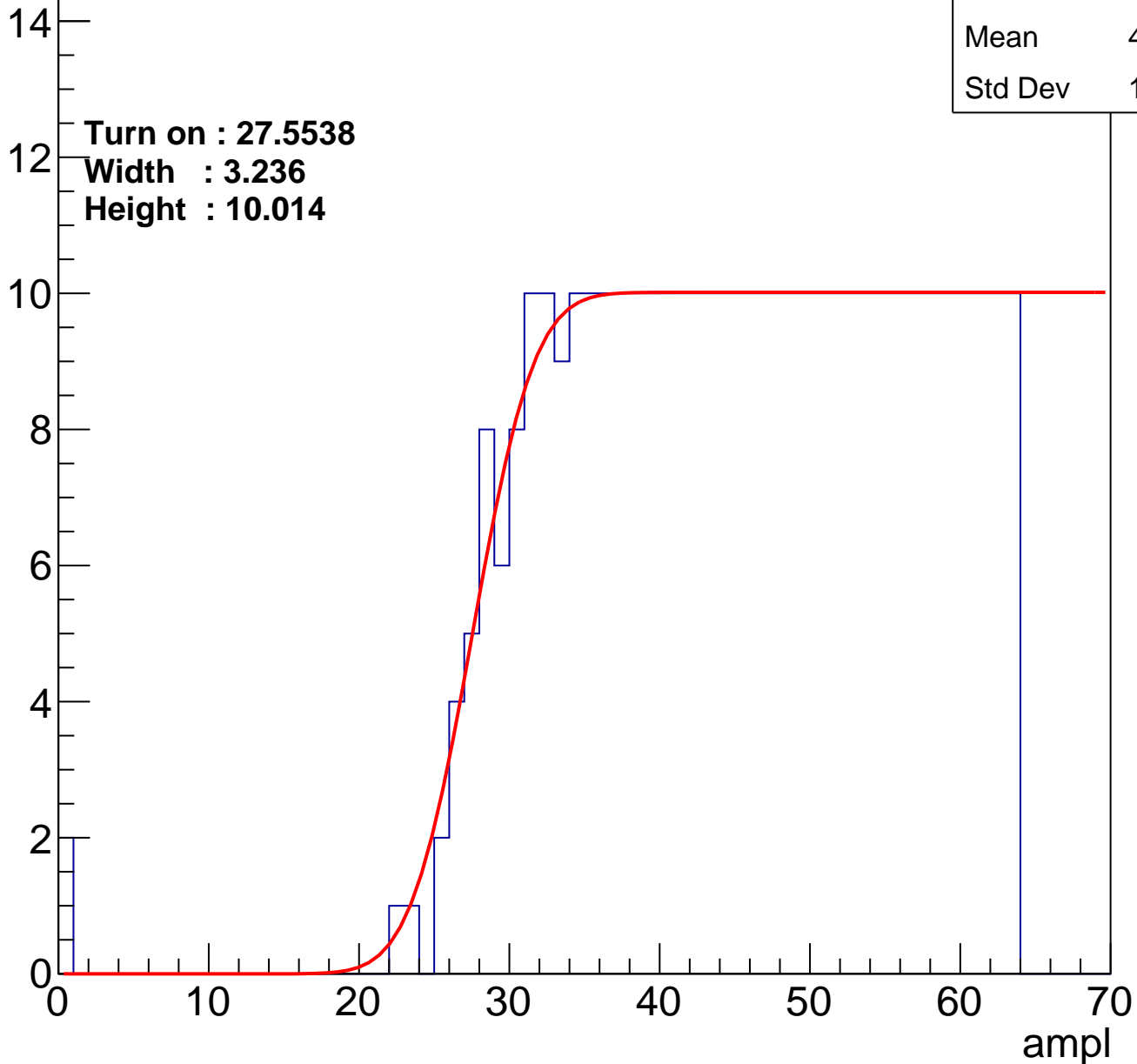
Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.5538

Width : 3.236

Height : 10.014

Entry



B0L002S, U6-ch109

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.49
Std Dev	10.89

Turn on : 29.2536

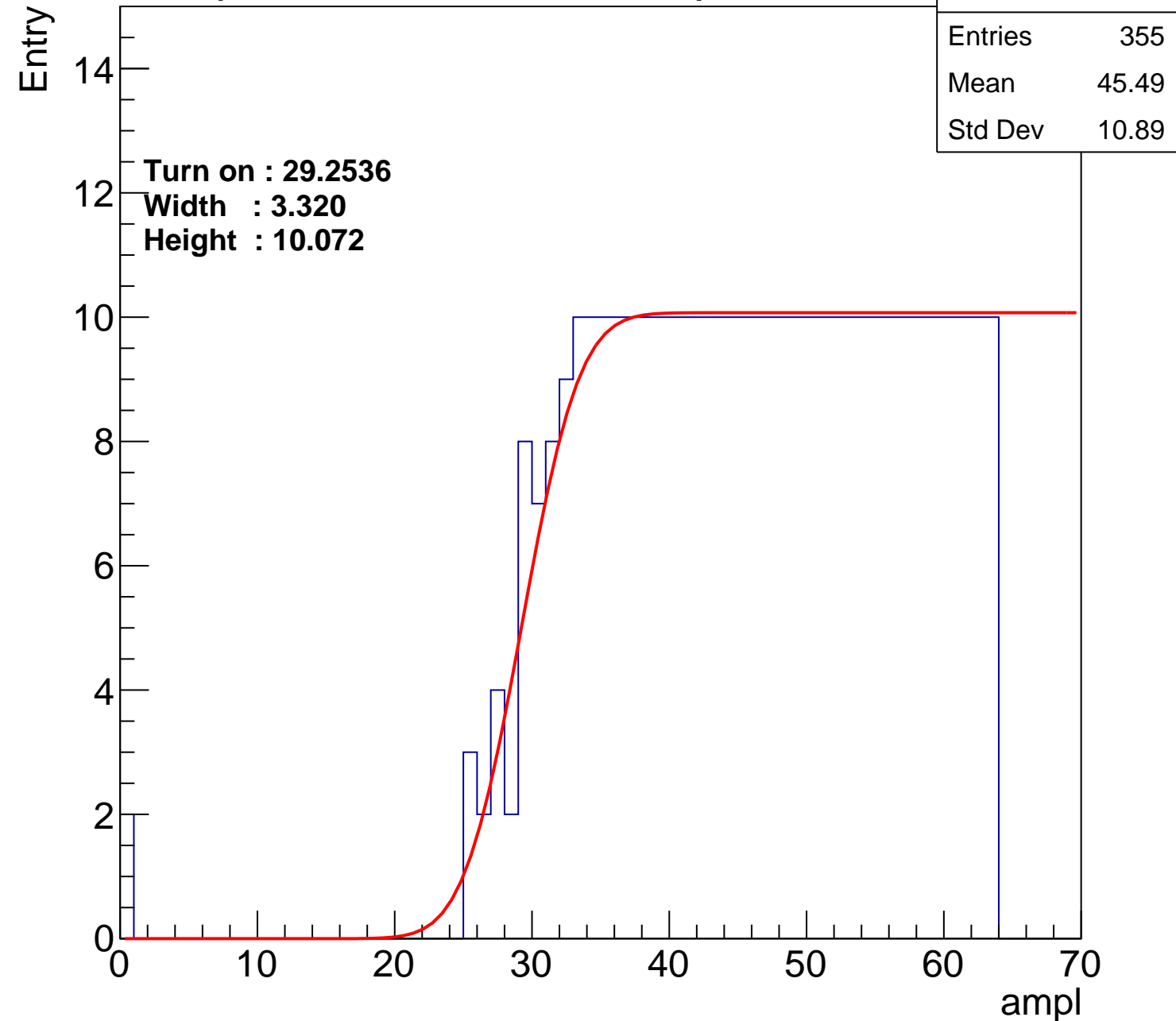
Width : 3.320

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.95
Std Dev	11.59

Turn on : 25.6904

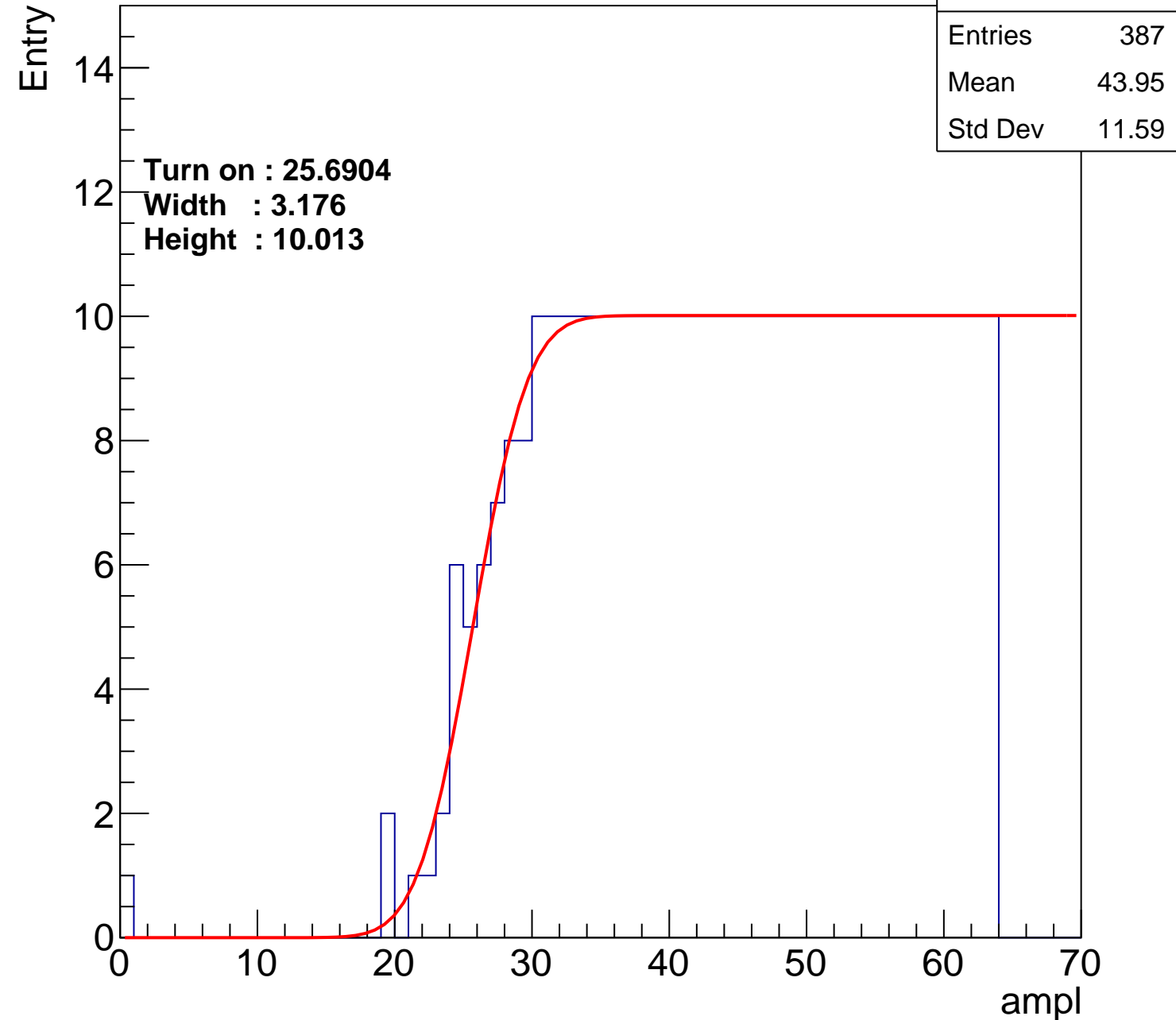
Width : 3.176

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.12
Std Dev	11.19

Turn on : 28.3951

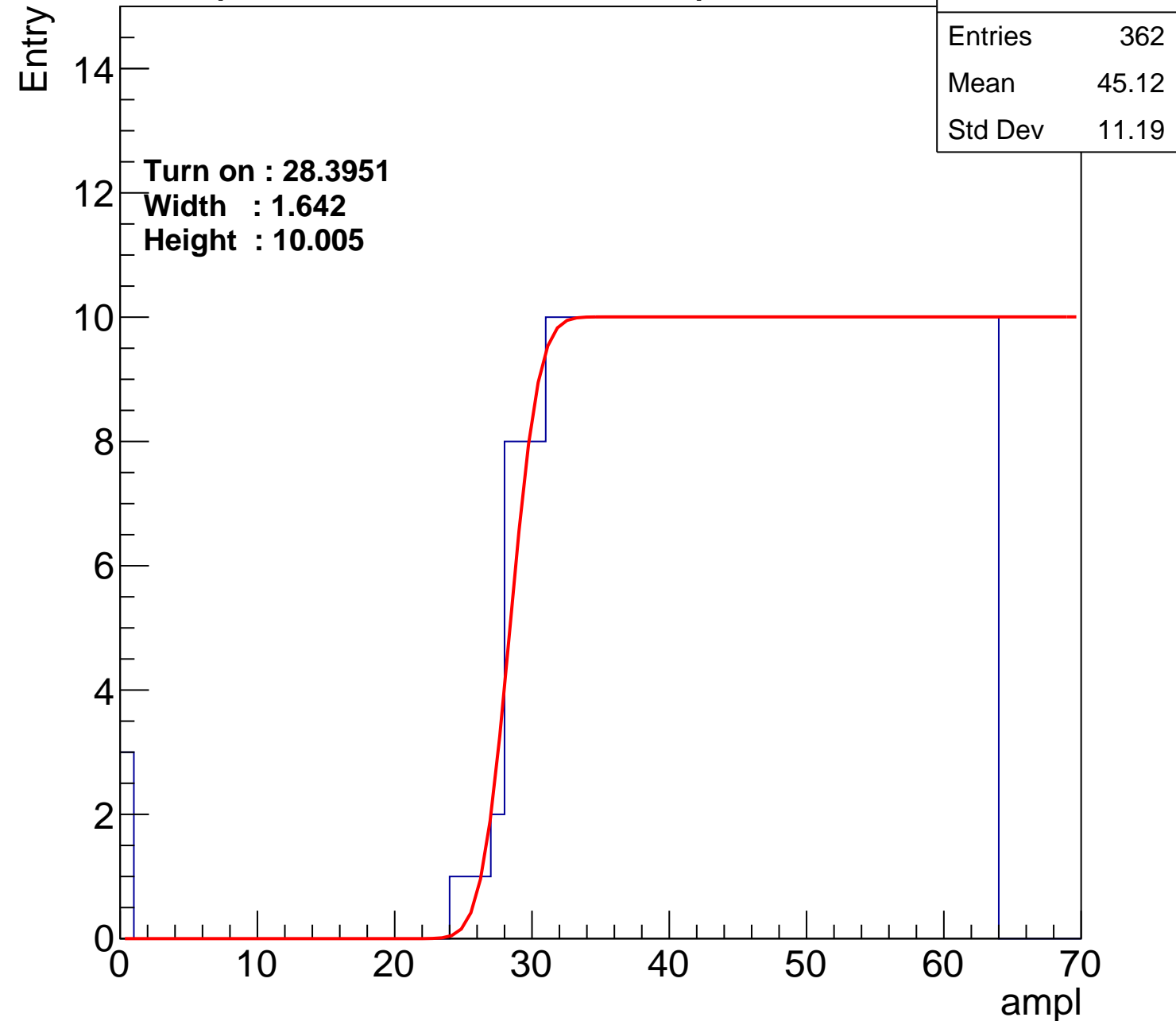
Width : 1.642

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.1787

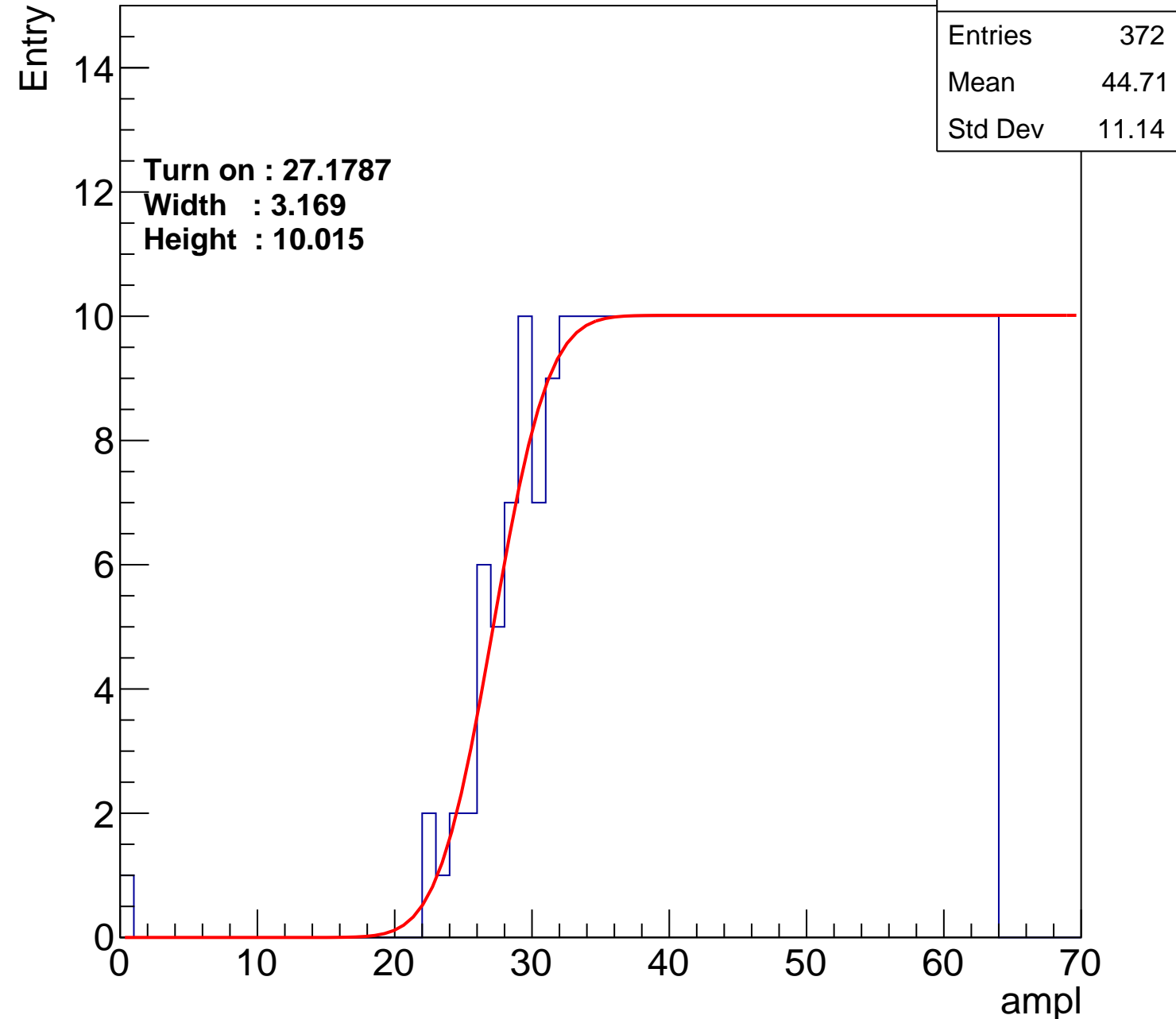
Width : 3.169

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.55
Std Dev	11.7

Turn on : 27.7969

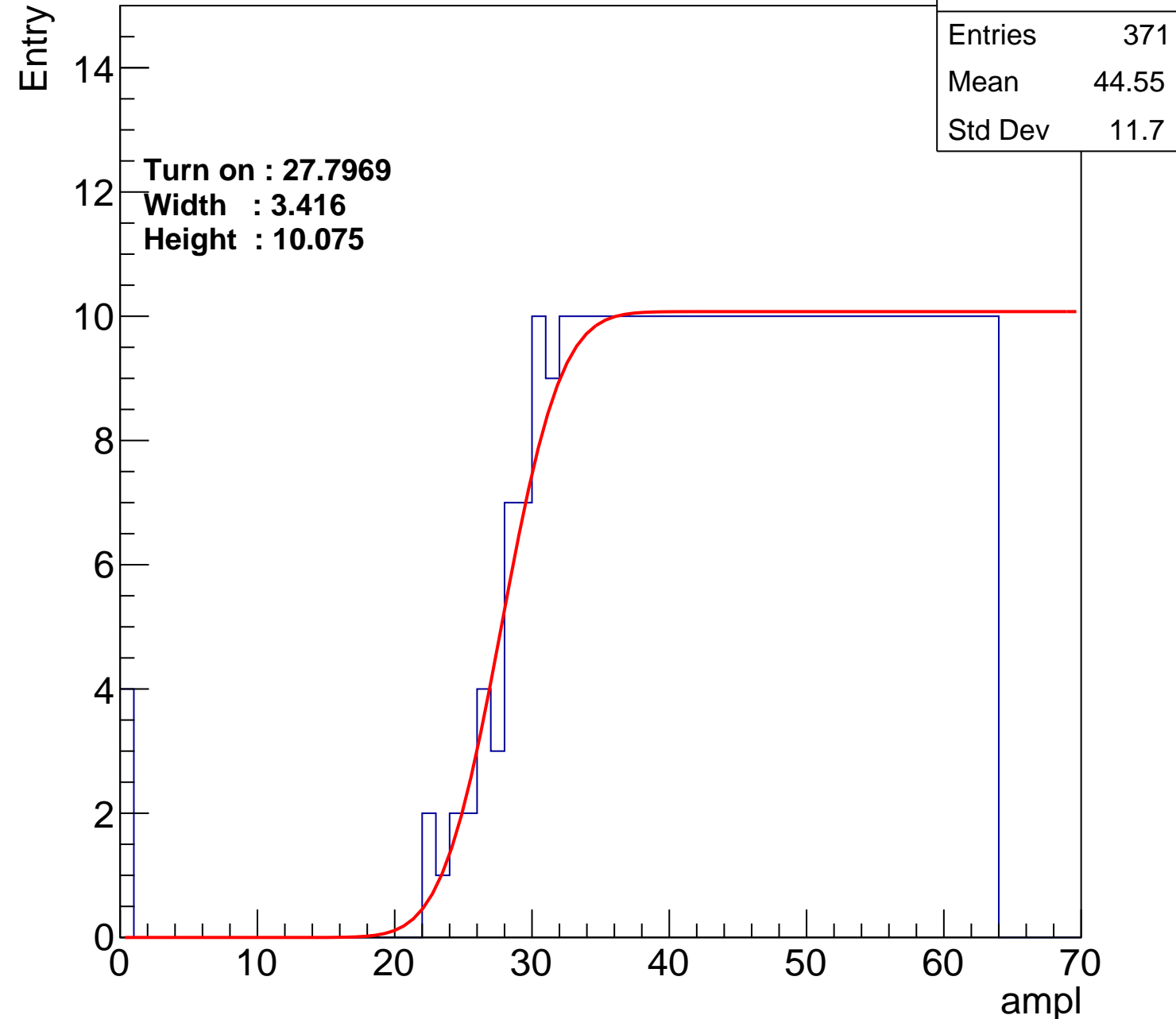
Width : 3.416

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch114

calib_packv5_042523_0143.root, FC#8, port C1

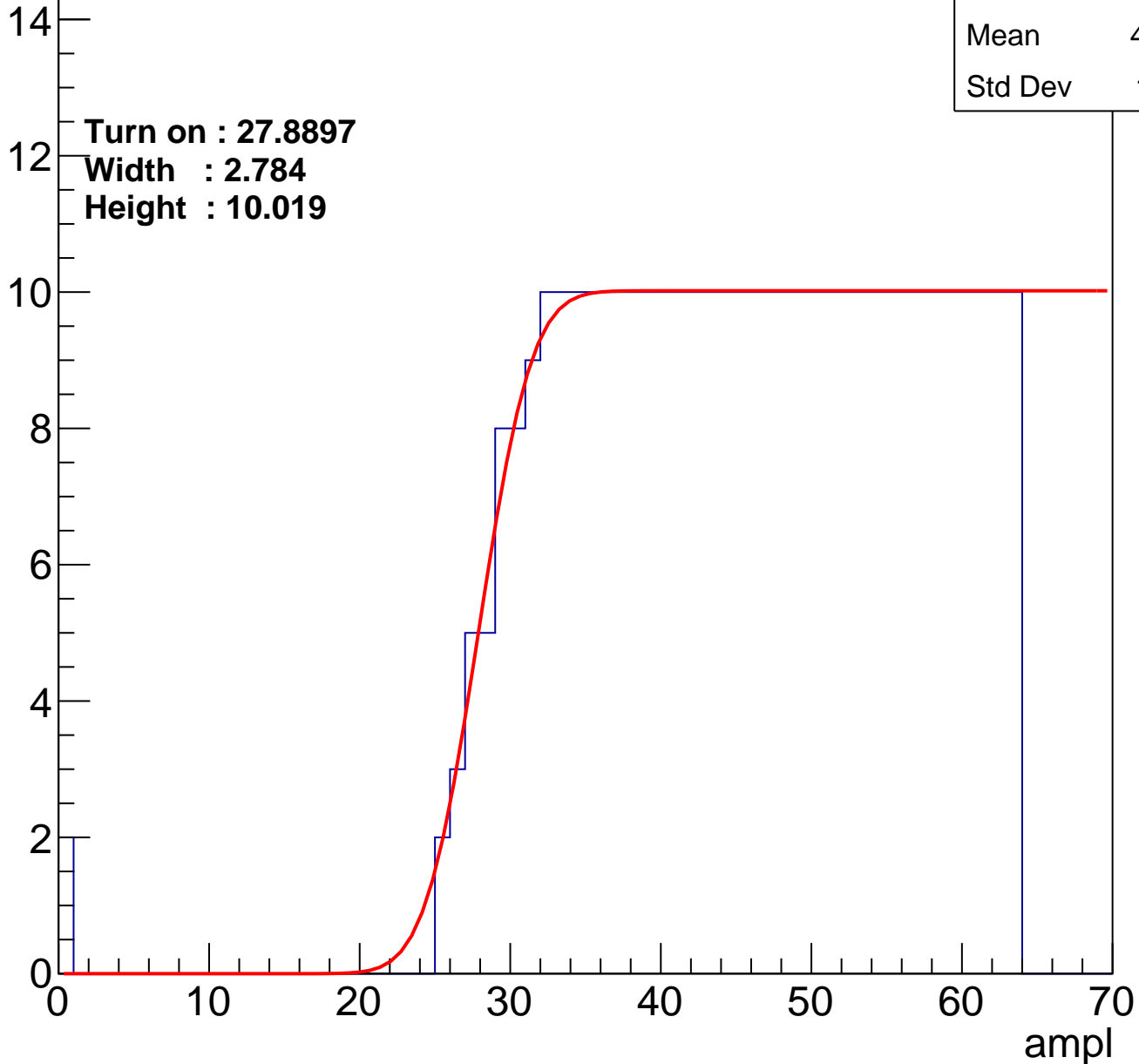
Entries	362
Mean	45.18
Std Dev	11.01

Turn on : 27.8897

Width : 2.784

Height : 10.019

Entry



B0L002S, U6-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.76
Std Dev	11.39

Turn on : 27.6910

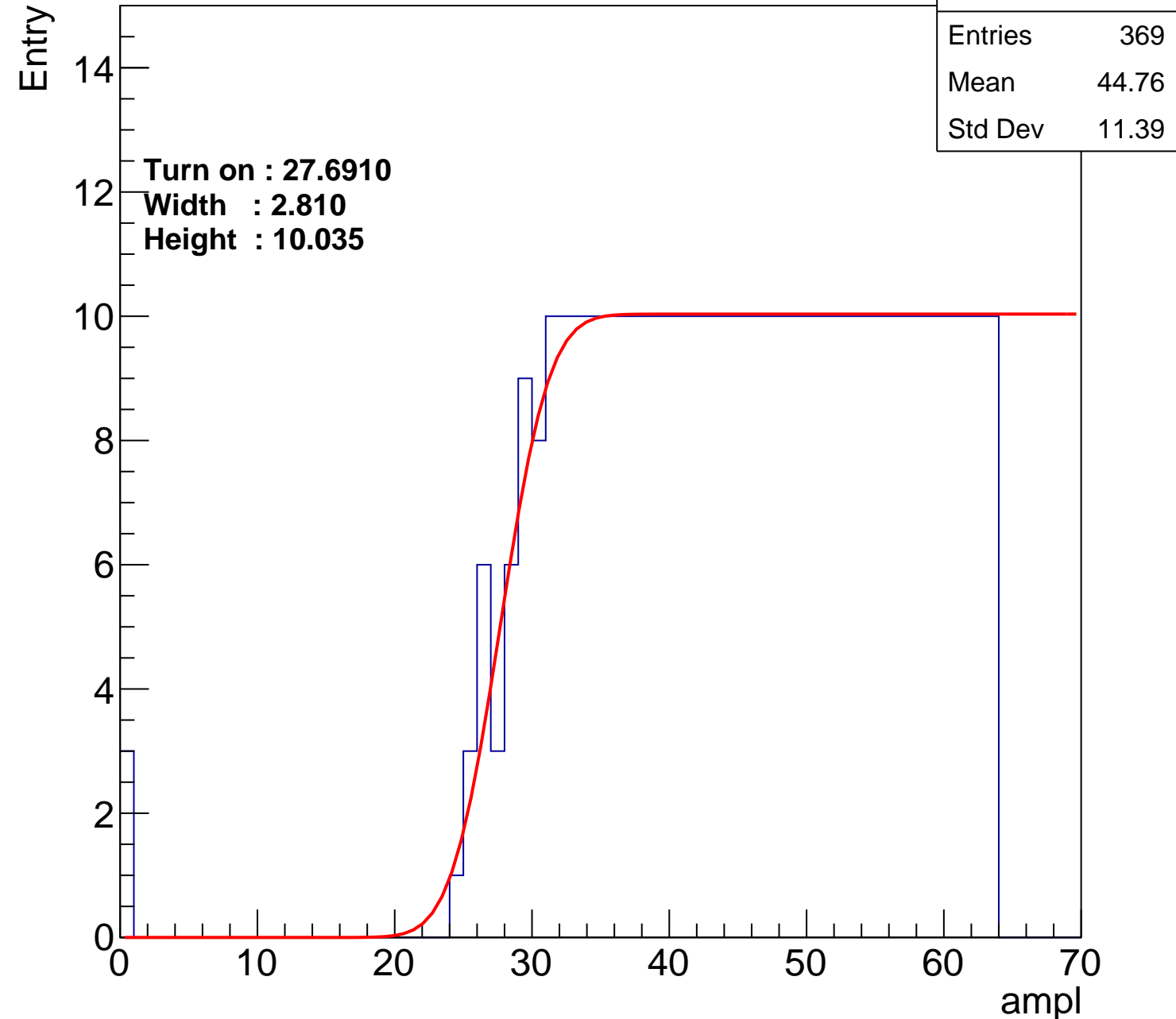
Width : 2.810

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch116

calib_packv5_042523_0143.root, FC#8, port C1

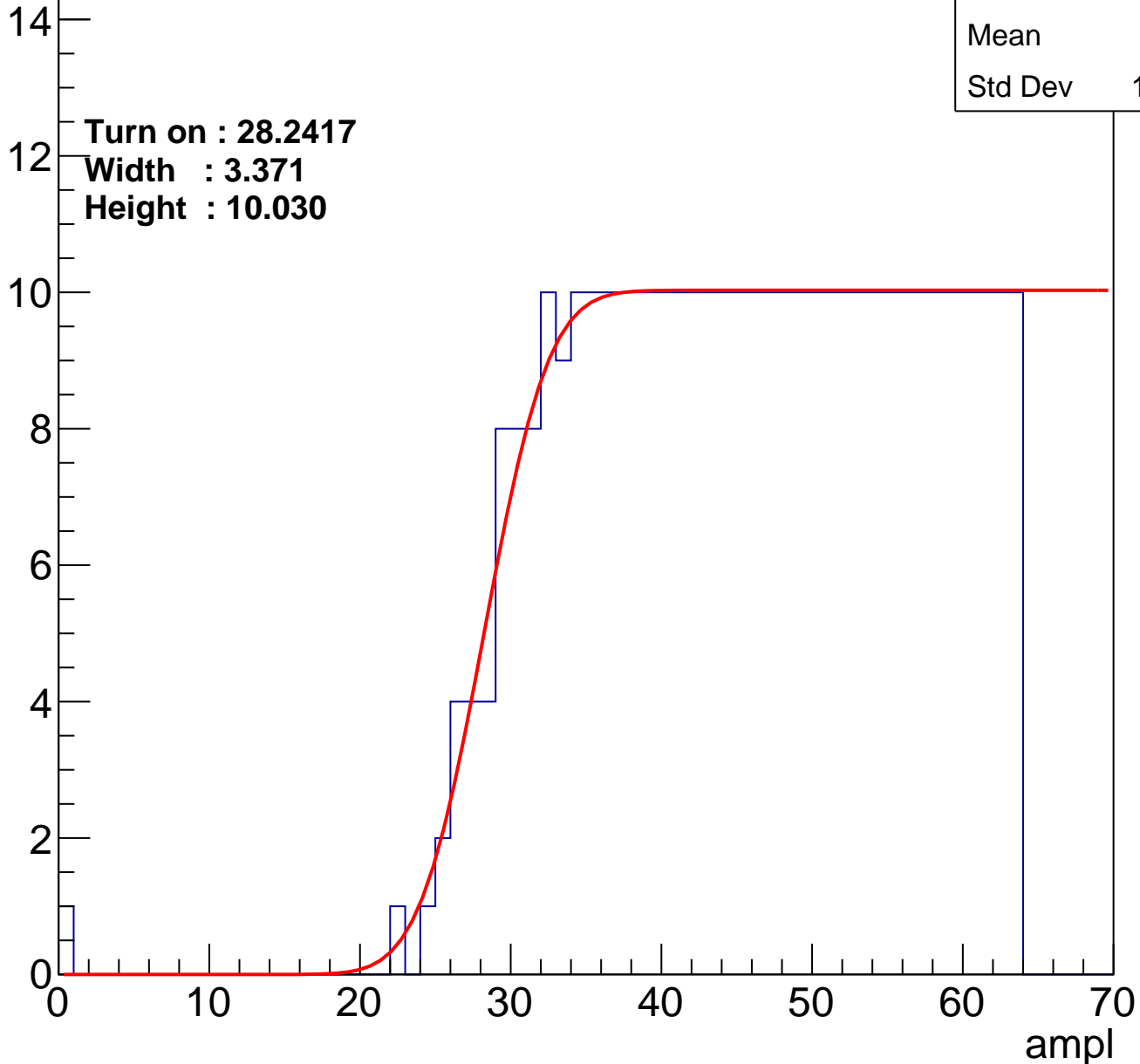
Entries	360
Mean	45.3
Std Dev	10.83

Turn on : 28.2417

Width : 3.371

Height : 10.030

Entry



B0L002S, U6-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.43
Std Dev	11.34

Turn on : 26.4441

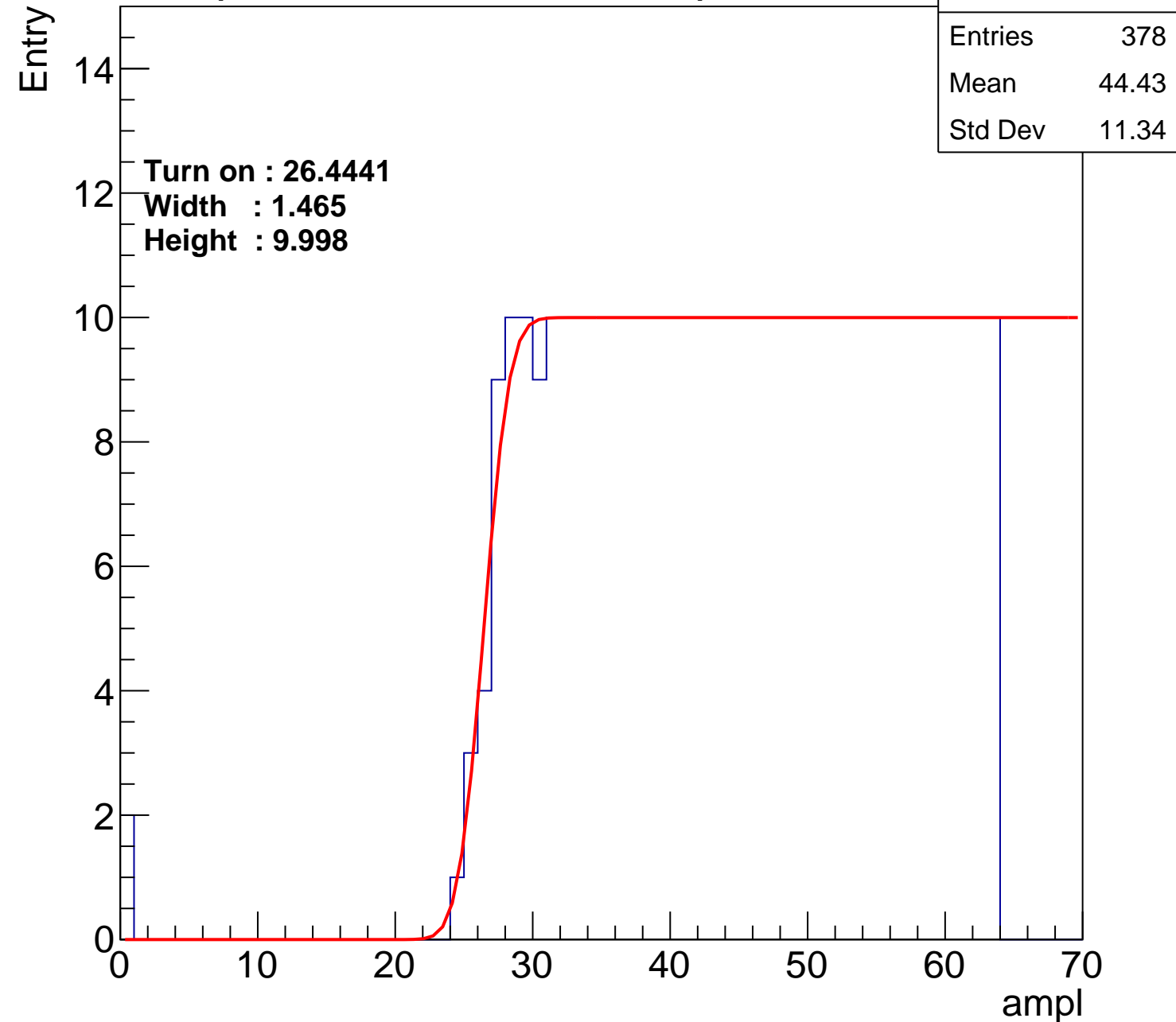
Width : 1.465

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch118

calib_packv5_042523_0143.root, FC#8, port C1

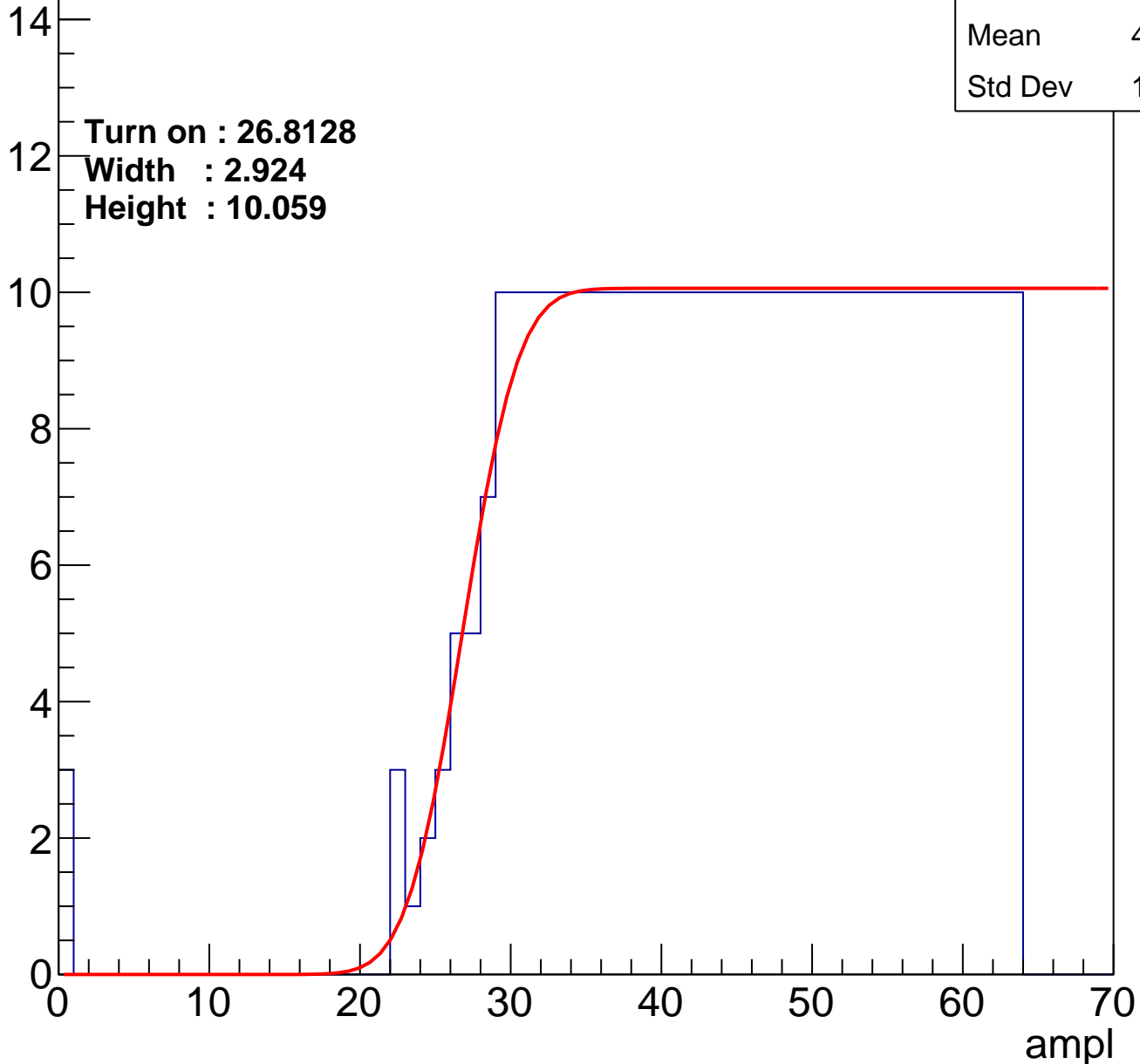
Entries	379
Mean	44.26
Std Dev	11.66

Turn on : 26.8128

Width : 2.924

Height : 10.059

Entry



B0L002S, U6-ch119

calib_packv5_042523_0143.root, FC#8, port C1

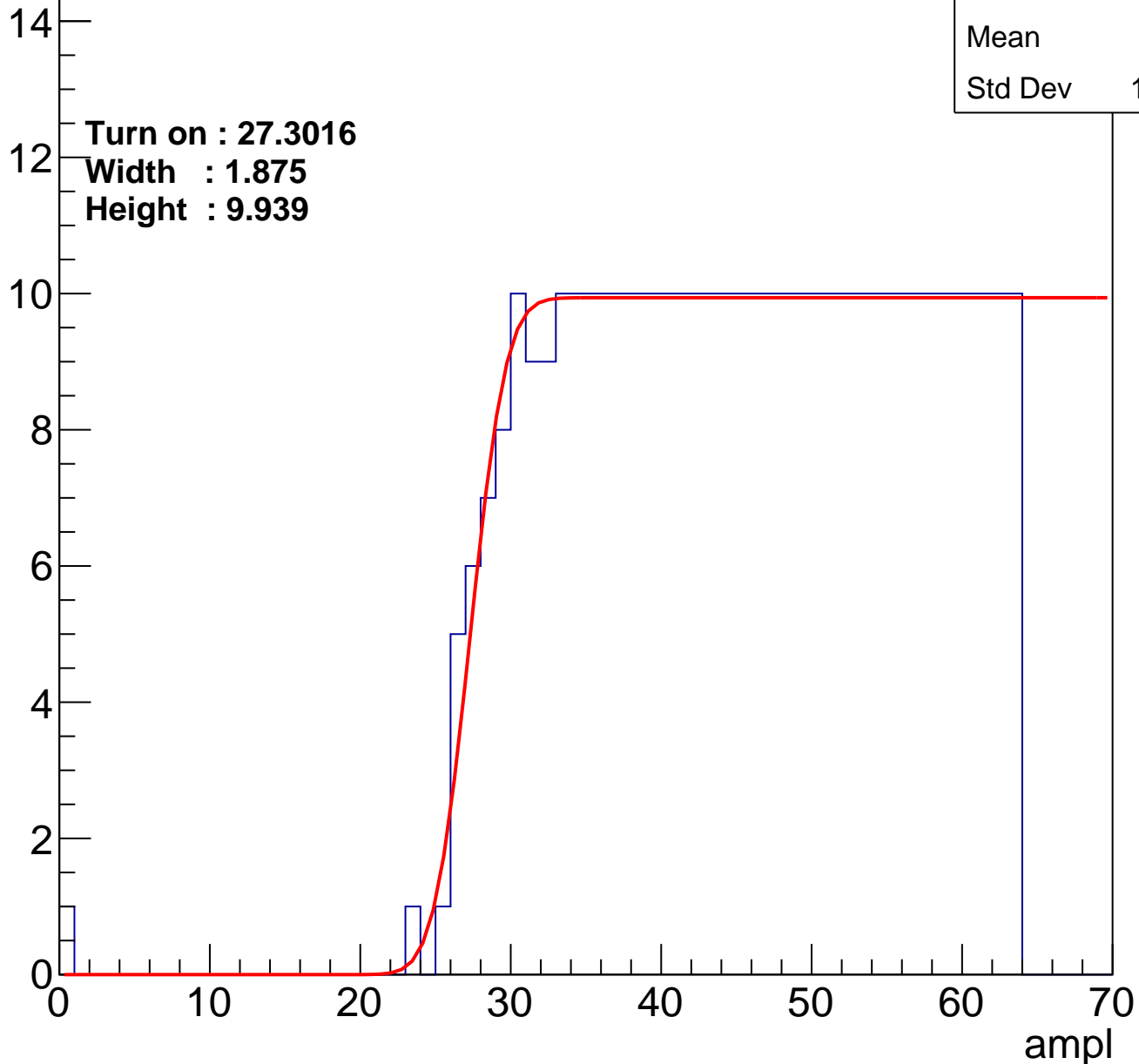
Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.3016

Width : 1.875

Height : 9.939

Entry



B0L002S, U6-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.67
Std Dev	11.42

Turn on : 26.5759

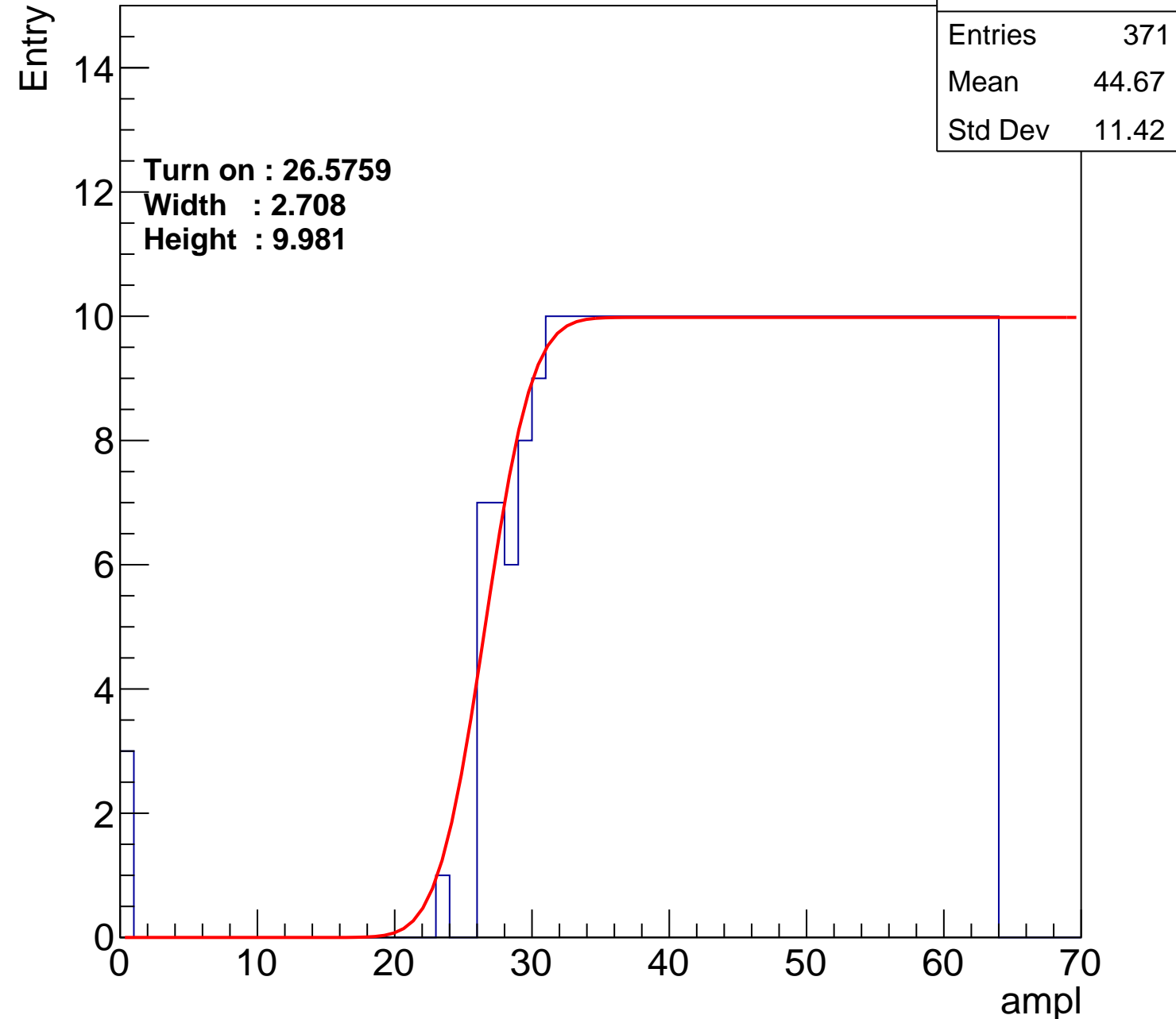
Width : 2.708

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch121

calib_packv5_042523_0143.root, FC#8, port C1

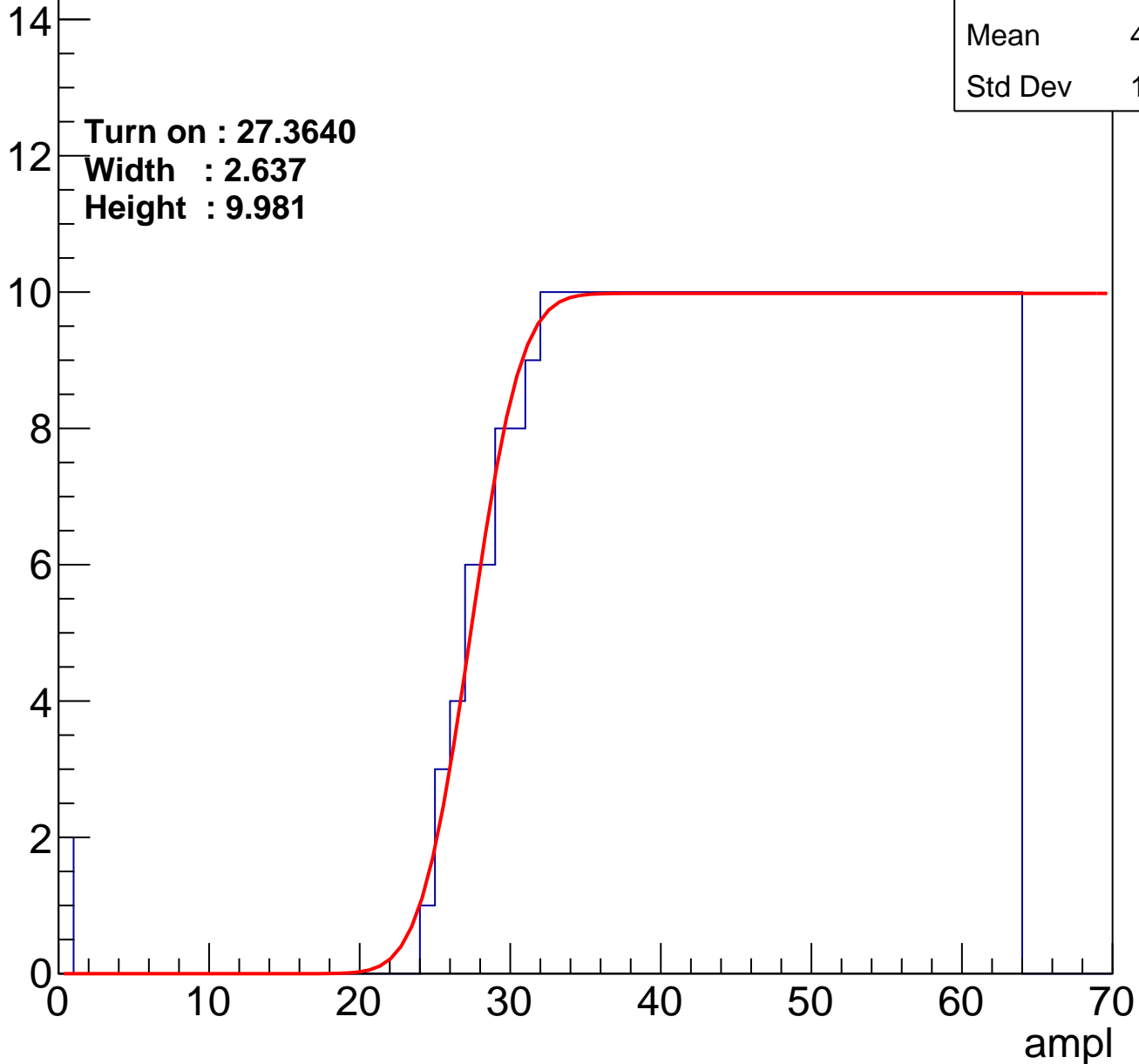
Entry

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.3640

Width : 2.637

Height : 9.981



B0L002S, U6-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.14
Std Dev	11.27

Turn on : 28.3986

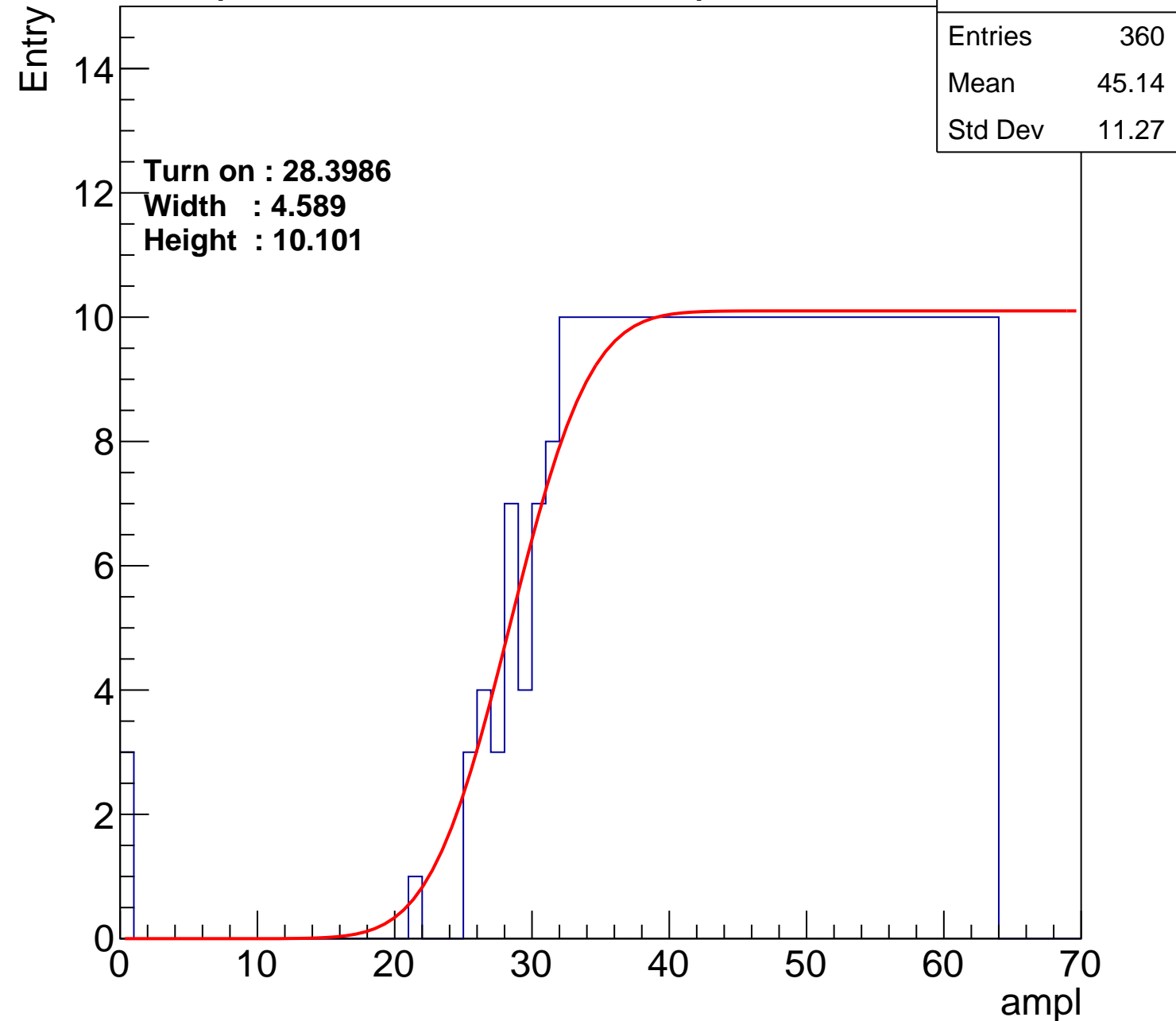
Width : 4.589

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.96
Std Dev	11.2

Turn on : 28.1556

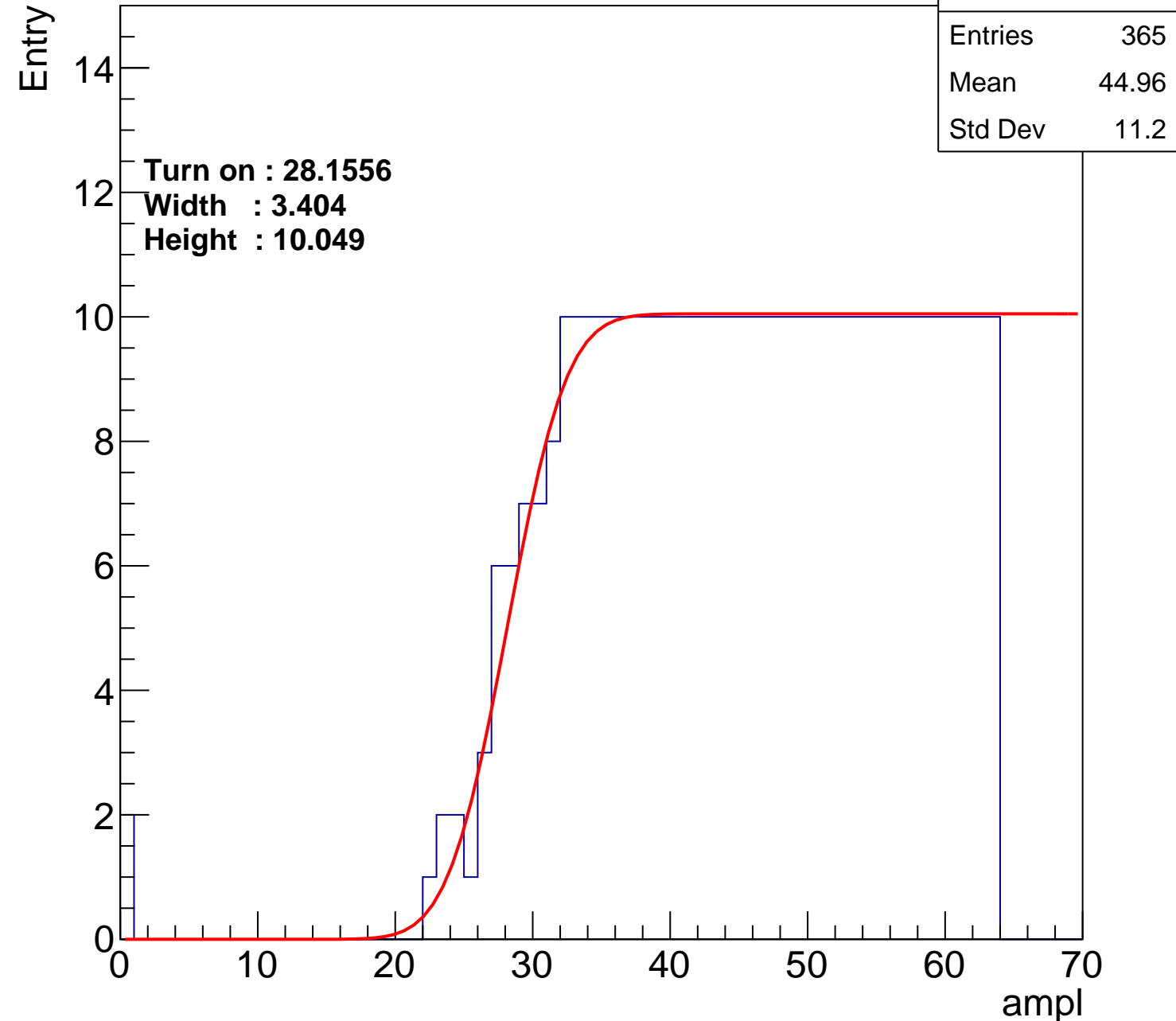
Width : 3.404

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.5

Turn on : 27.3597

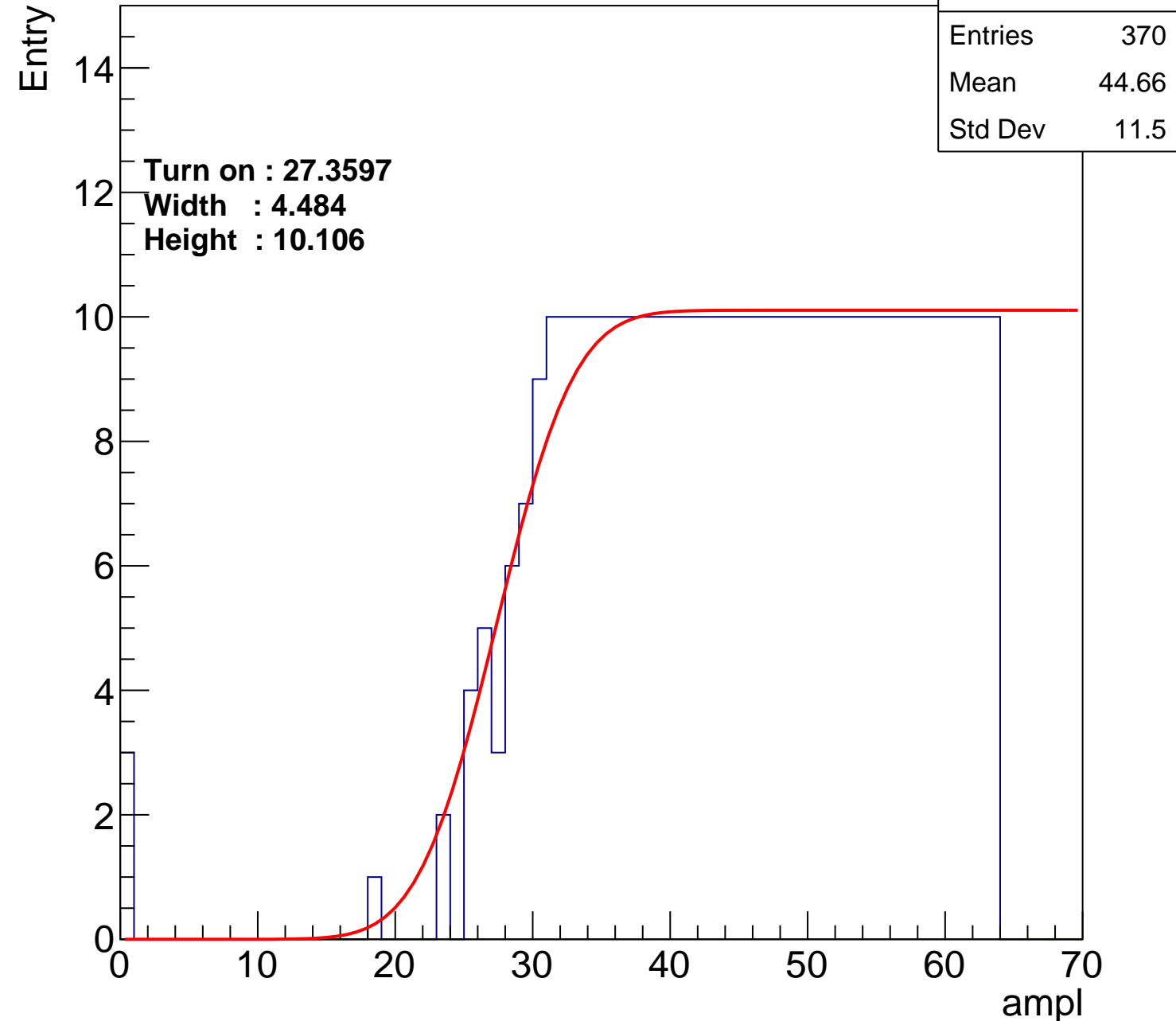
Width : 4.484

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.65
Std Dev	10.63

Turn on : 28.6942

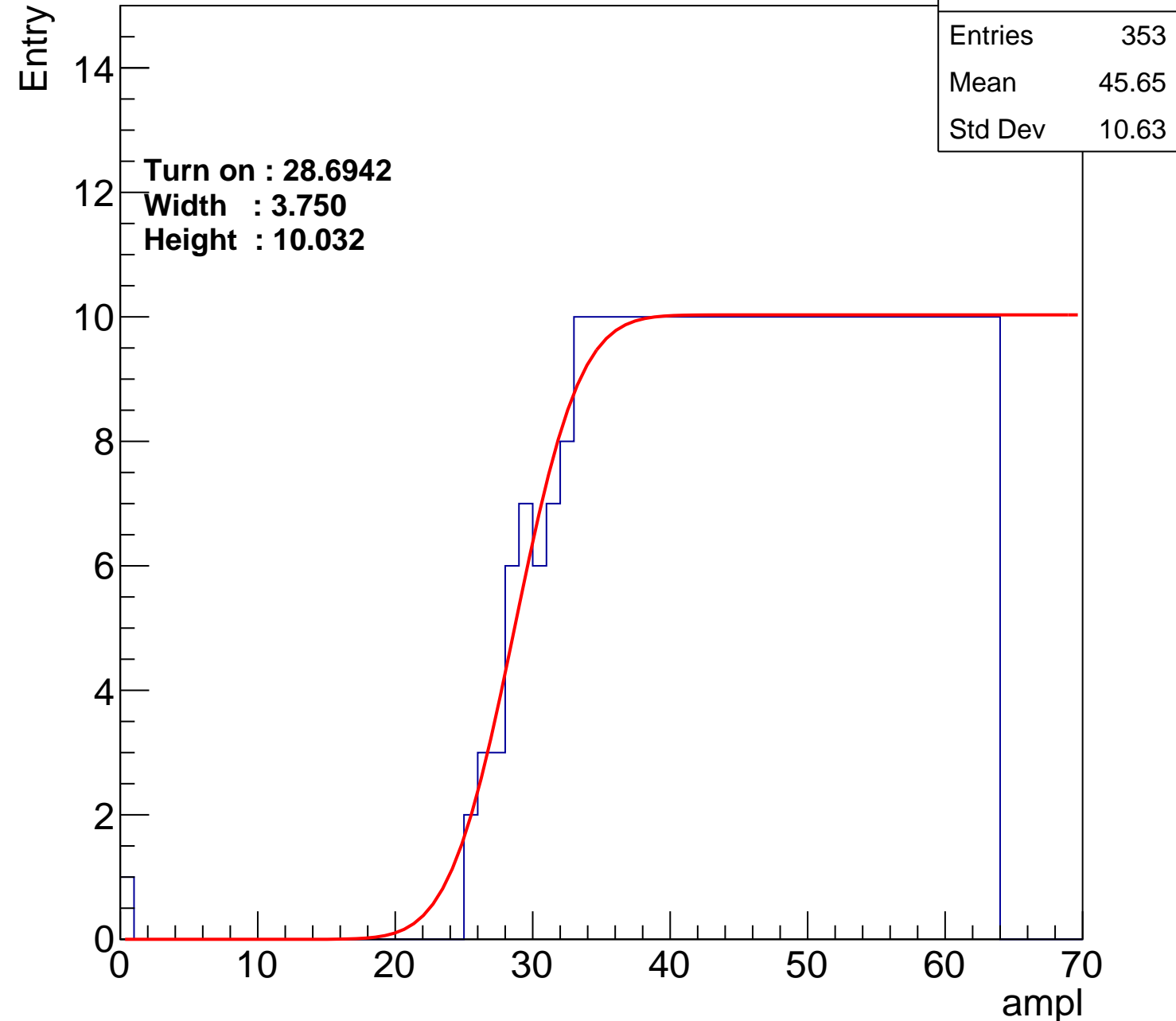
Width : 3.750

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	43.89
Std Dev	12.28

Turn on : 26.5787

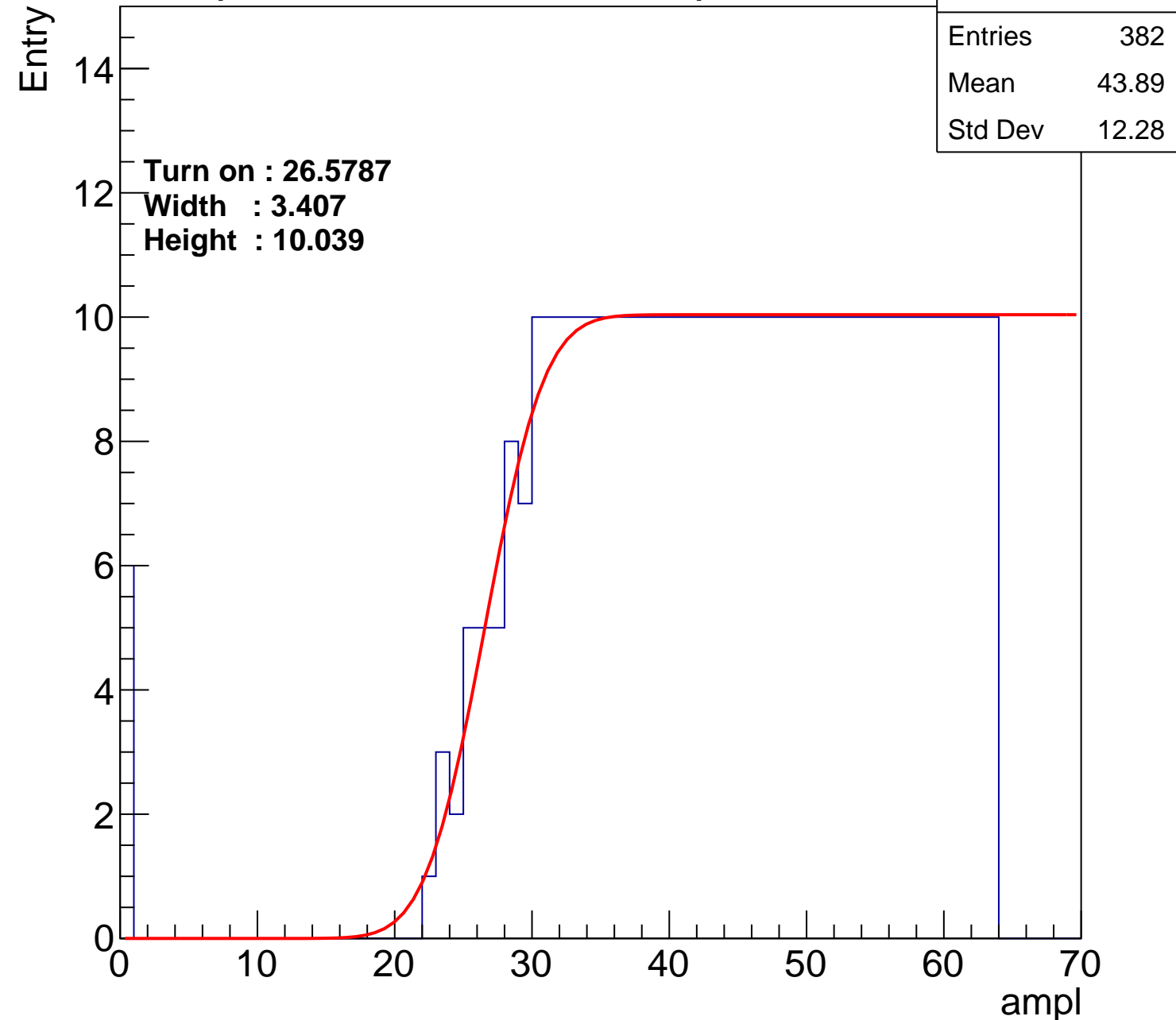
Width : 3.407

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U6-ch127

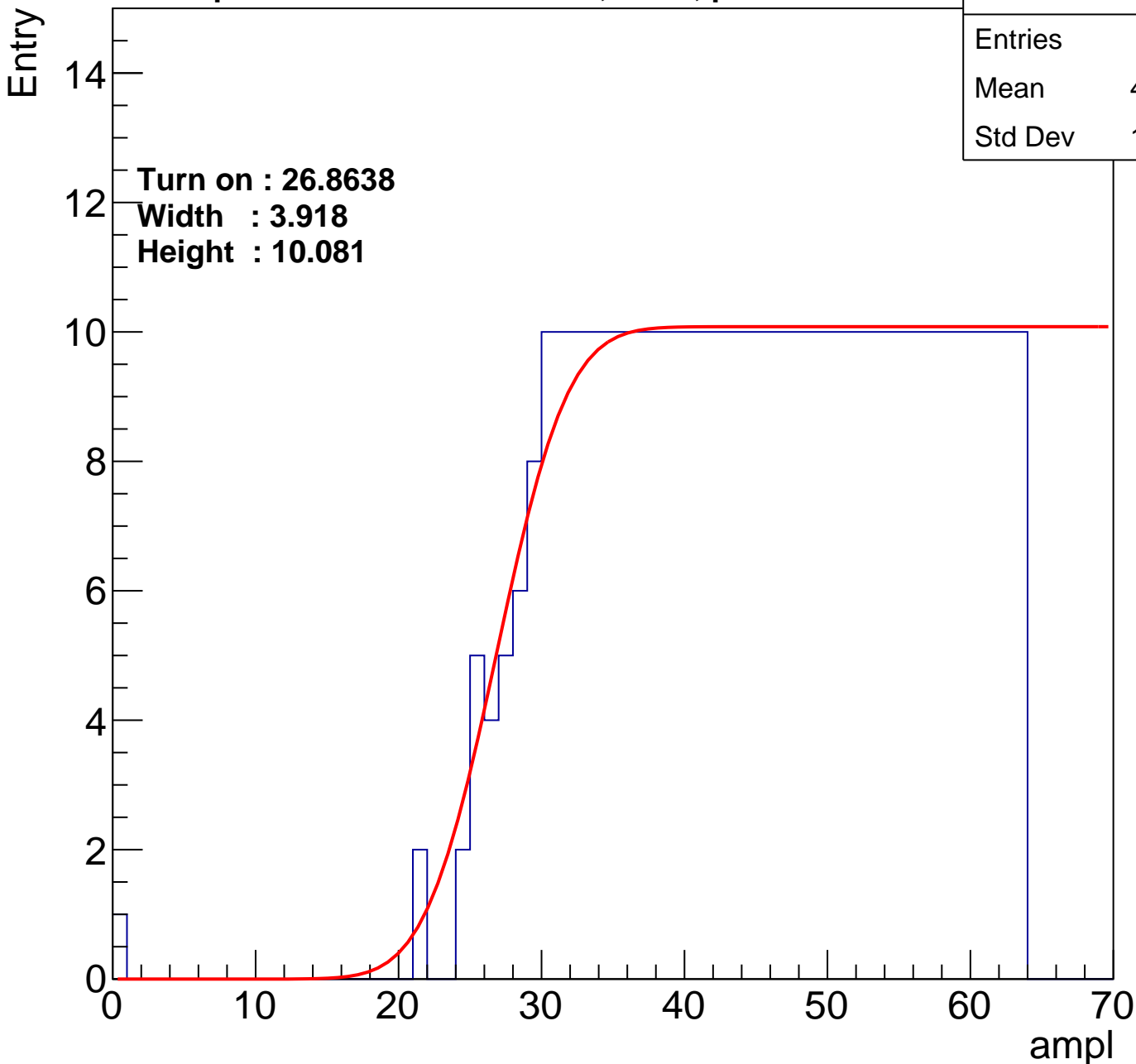
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.68
Std Dev	11.15

Turn on : 26.8638

Width : 3.918

Height : 10.081



B0L002S, U6-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.68
Std Dev	11.15

Turn on : 26.8638

Width : 3.918

Height : 10.081

