

B0L002S, U5-ch0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.79
Std Dev	11.26

Turn on : 27.0984

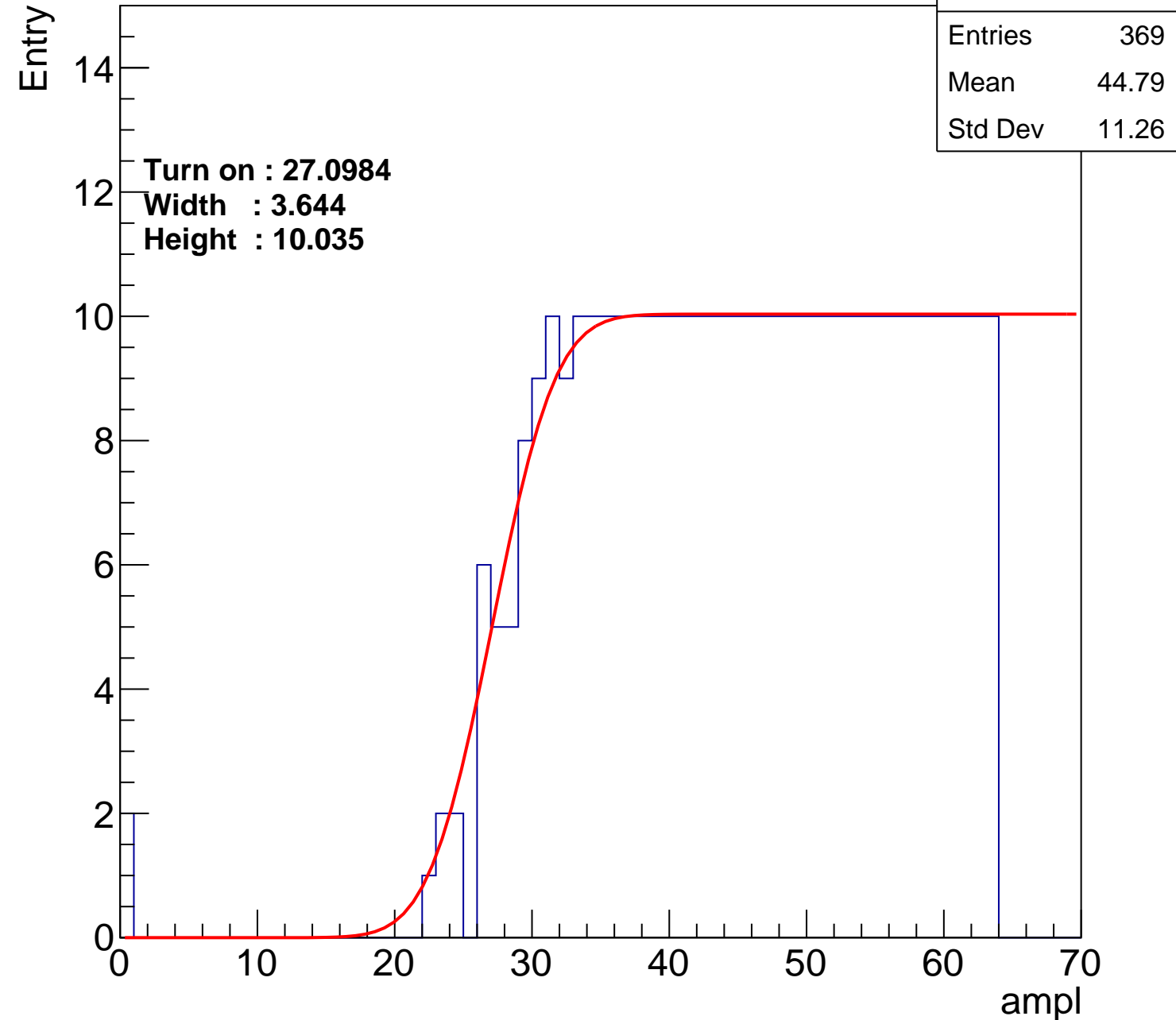
Width : 3.644

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch1

calib_packv5_042523_0143.root, FC#8, port C1

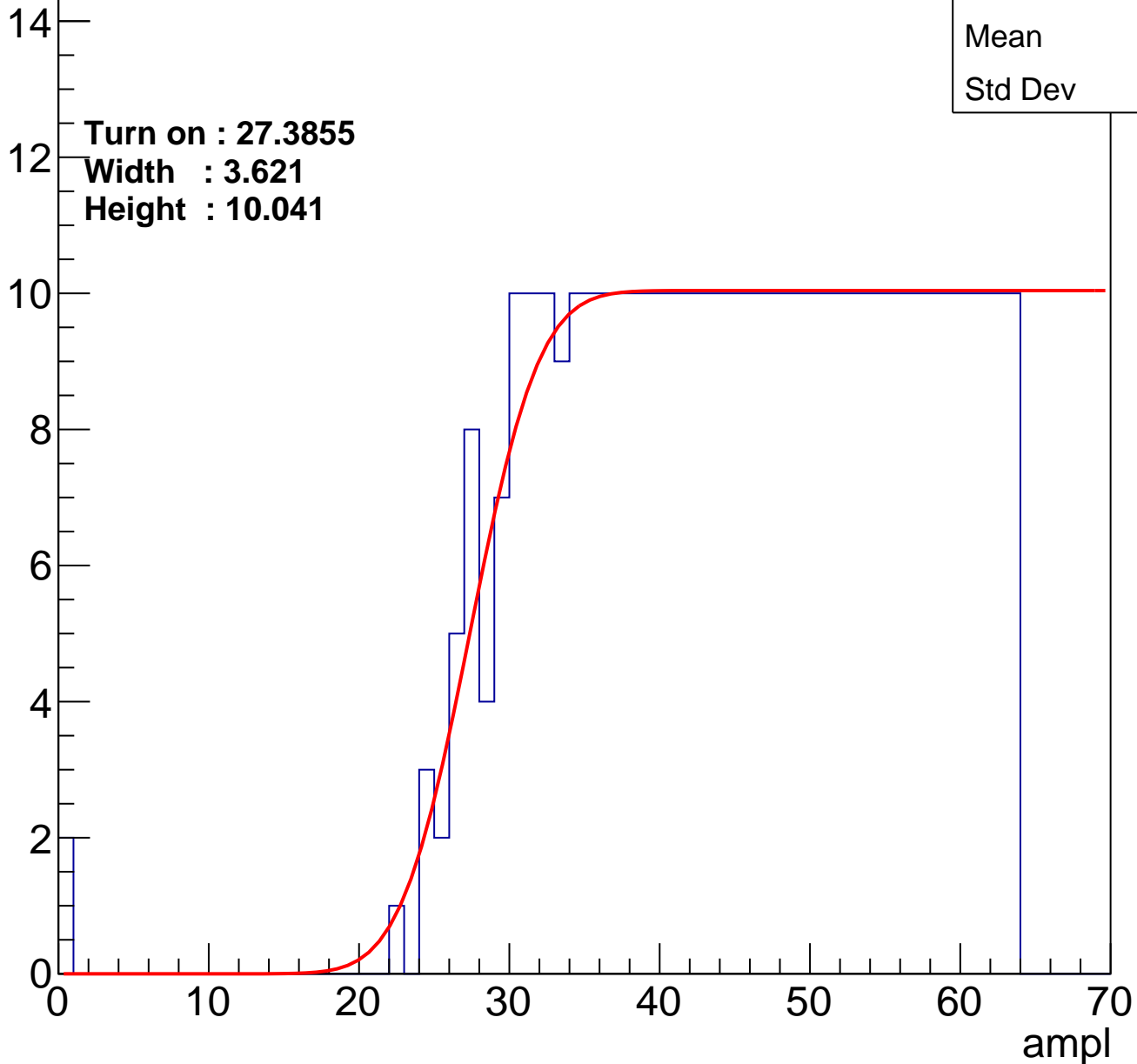
Entries	371
Mean	44.7
Std Dev	11.3

Turn on : 27.3855

Width : 3.621

Height : 10.041

Entry



B0L002S, U5-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.16
Std Dev	11.24

Turn on : 28.9227

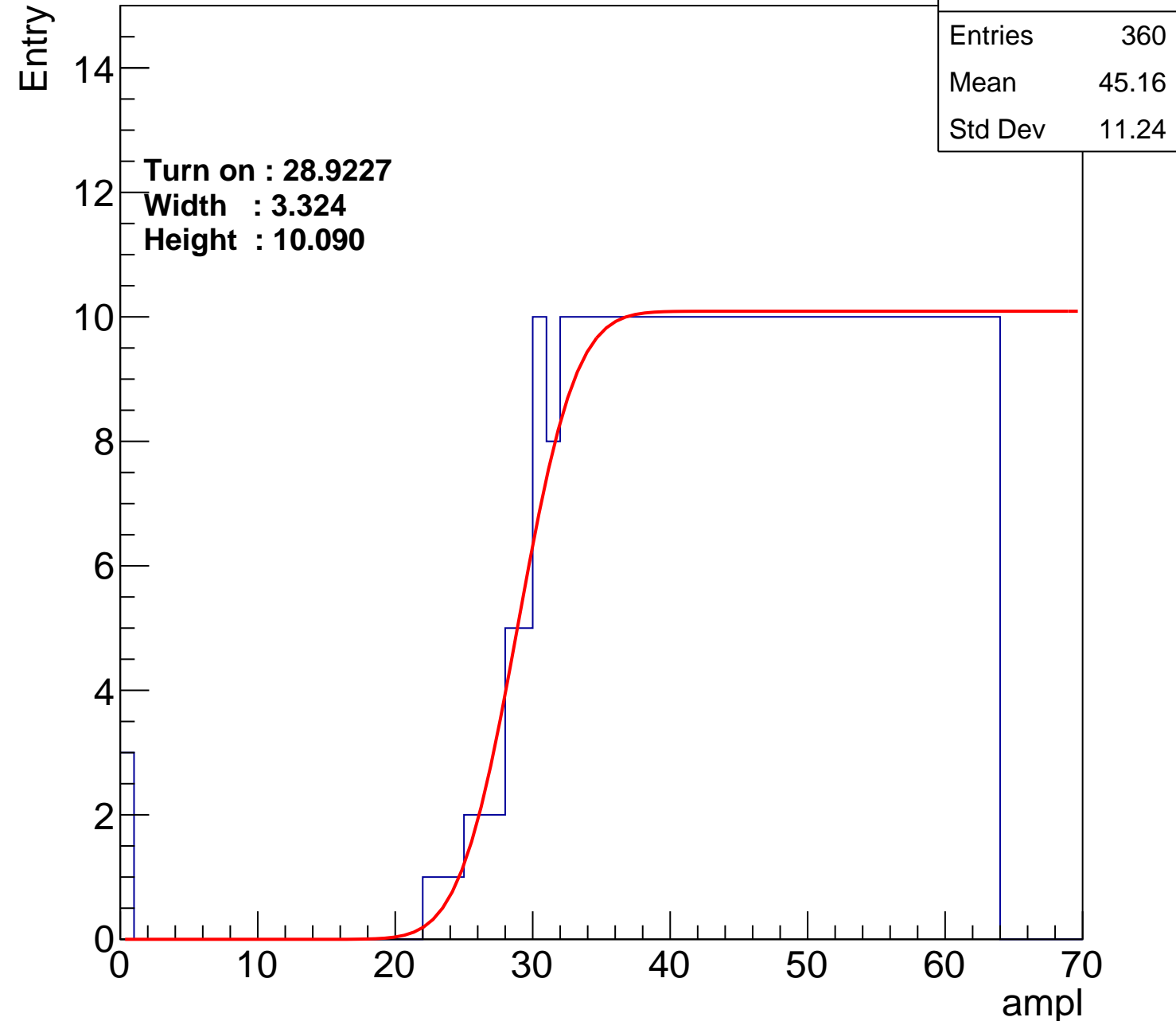
Width : 3.324

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.62
Std Dev	10.62

Turn on : 28.7429

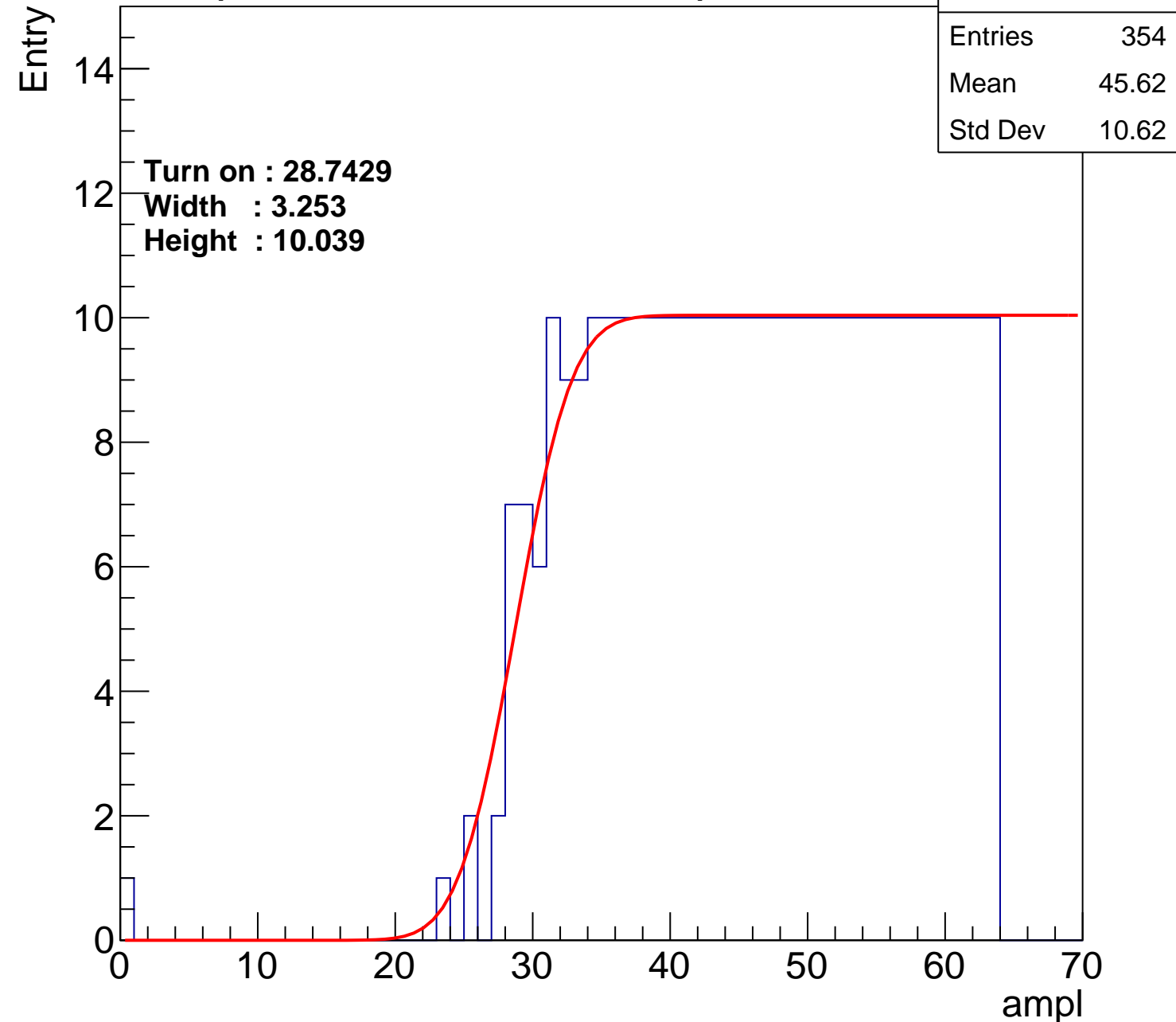
Width : 3.253

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.04
Std Dev	10.93

Turn on : 28.1632

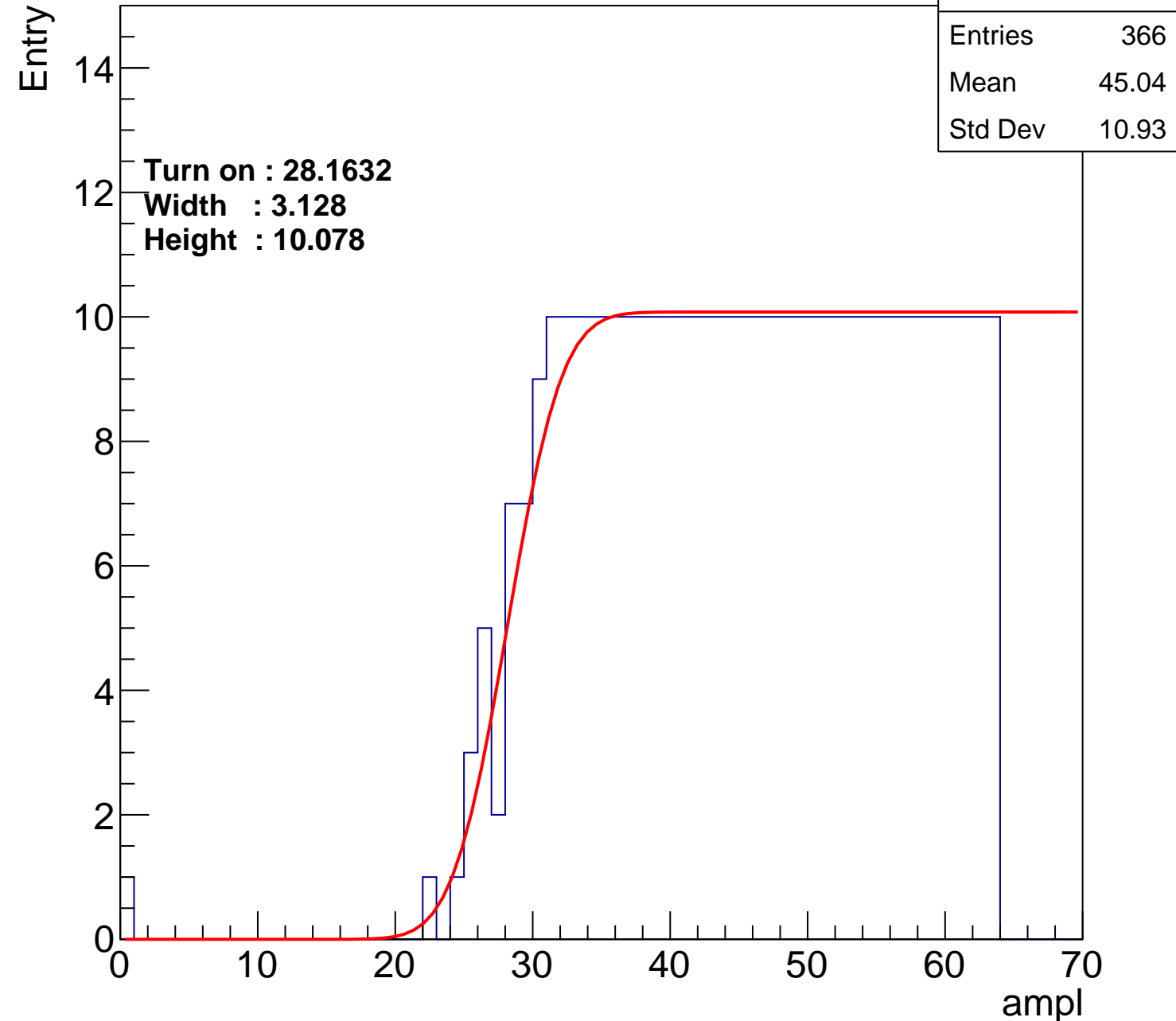
Width : 3.128

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.31
Std Dev	11.43

Turn on : 26.5345

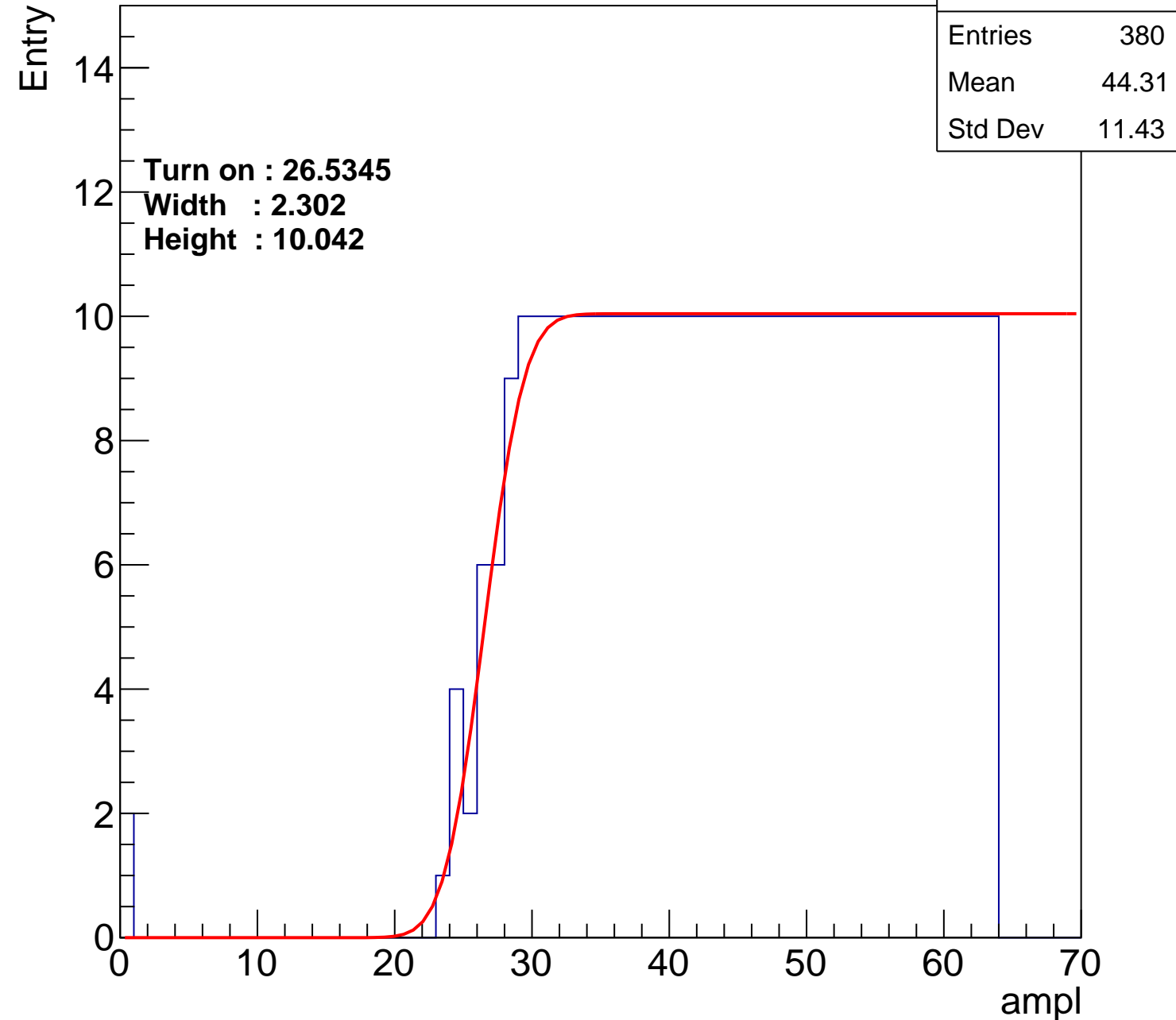
Width : 2.302

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch6

calib_packv5_042523_0143.root, FC#8, port C1

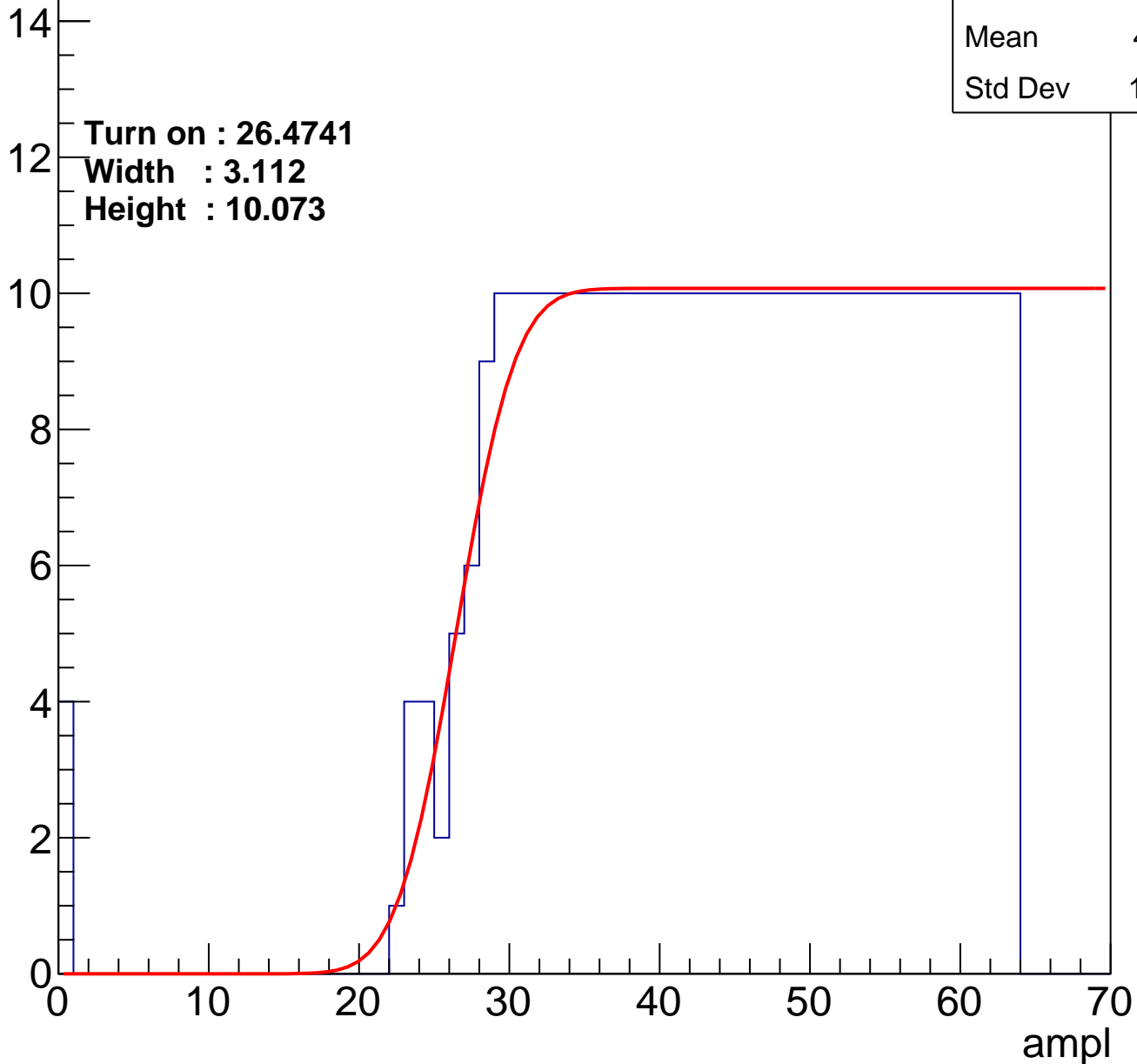
Entries	385
Mean	43.91
Std Dev	11.96

Turn on : 26.4741

Width : 3.112

Height : 10.073

Entry



B0L002S, U5-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.2985

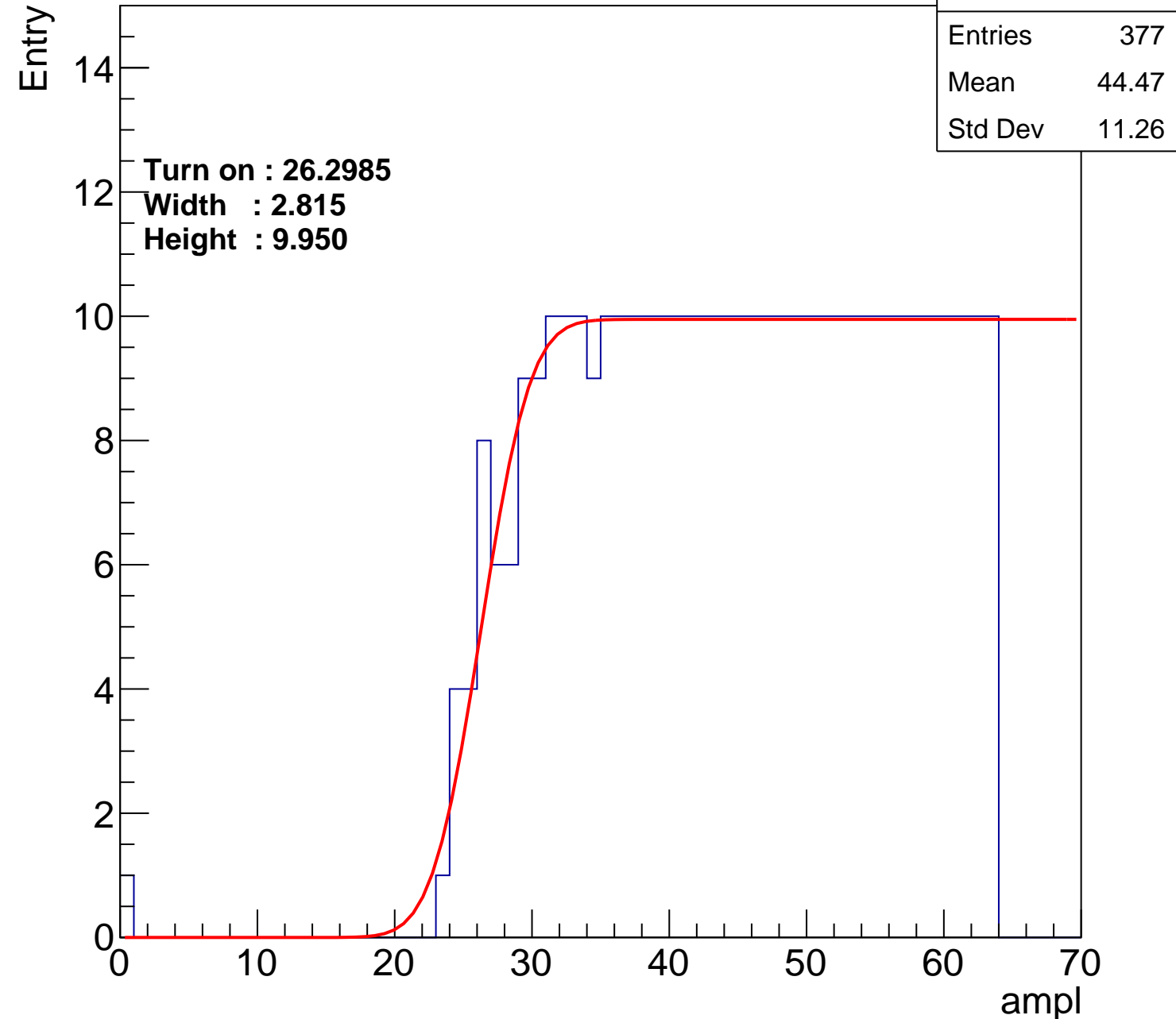
Width : 2.815

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch8

calib_packv5_042523_0143.root, FC#8, port C1

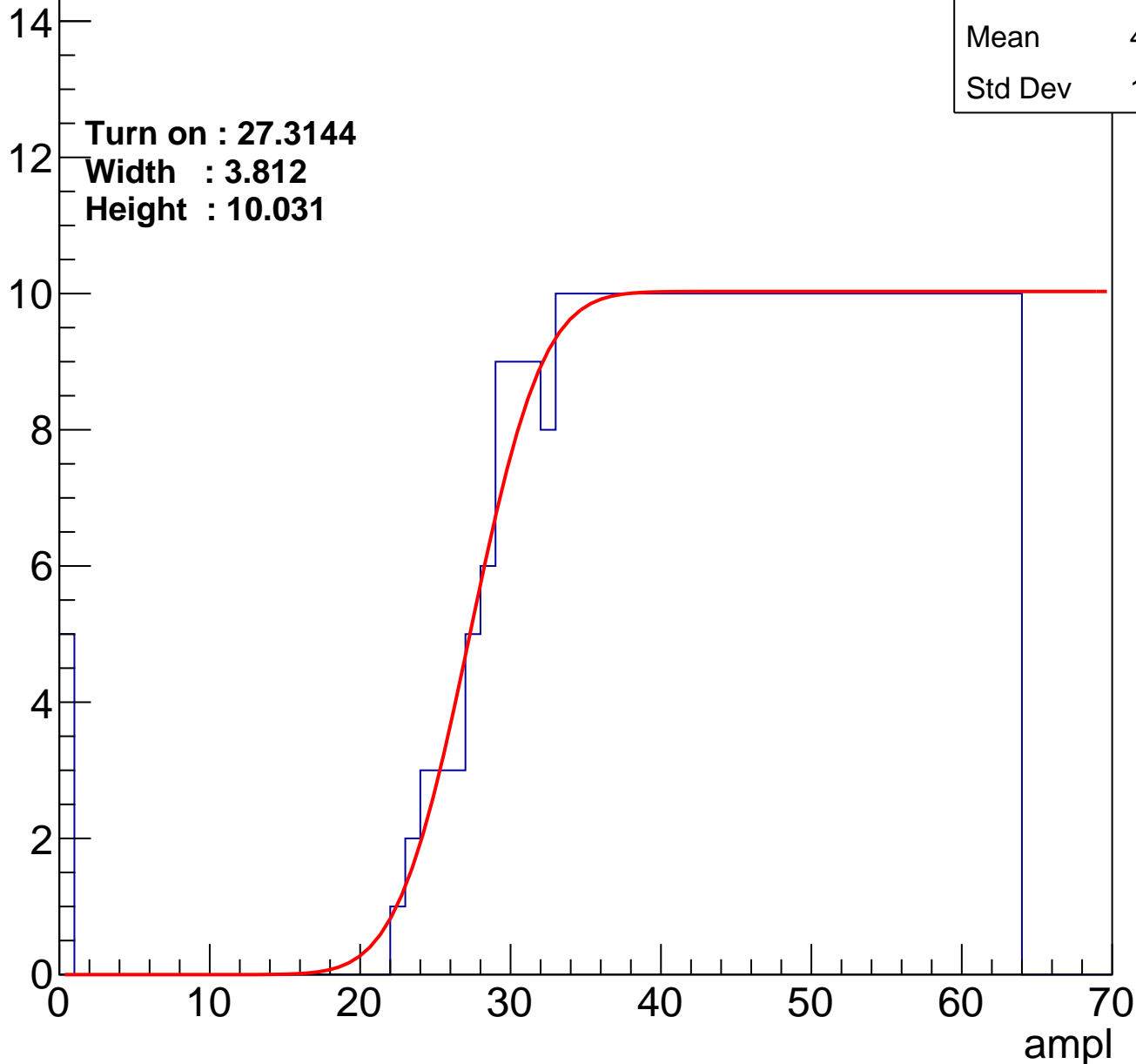
Entries	373
Mean	44.35
Std Dev	11.97

Turn on : 27.3144

Width : 3.812

Height : 10.031

Entry



B0L002S, U5-ch9

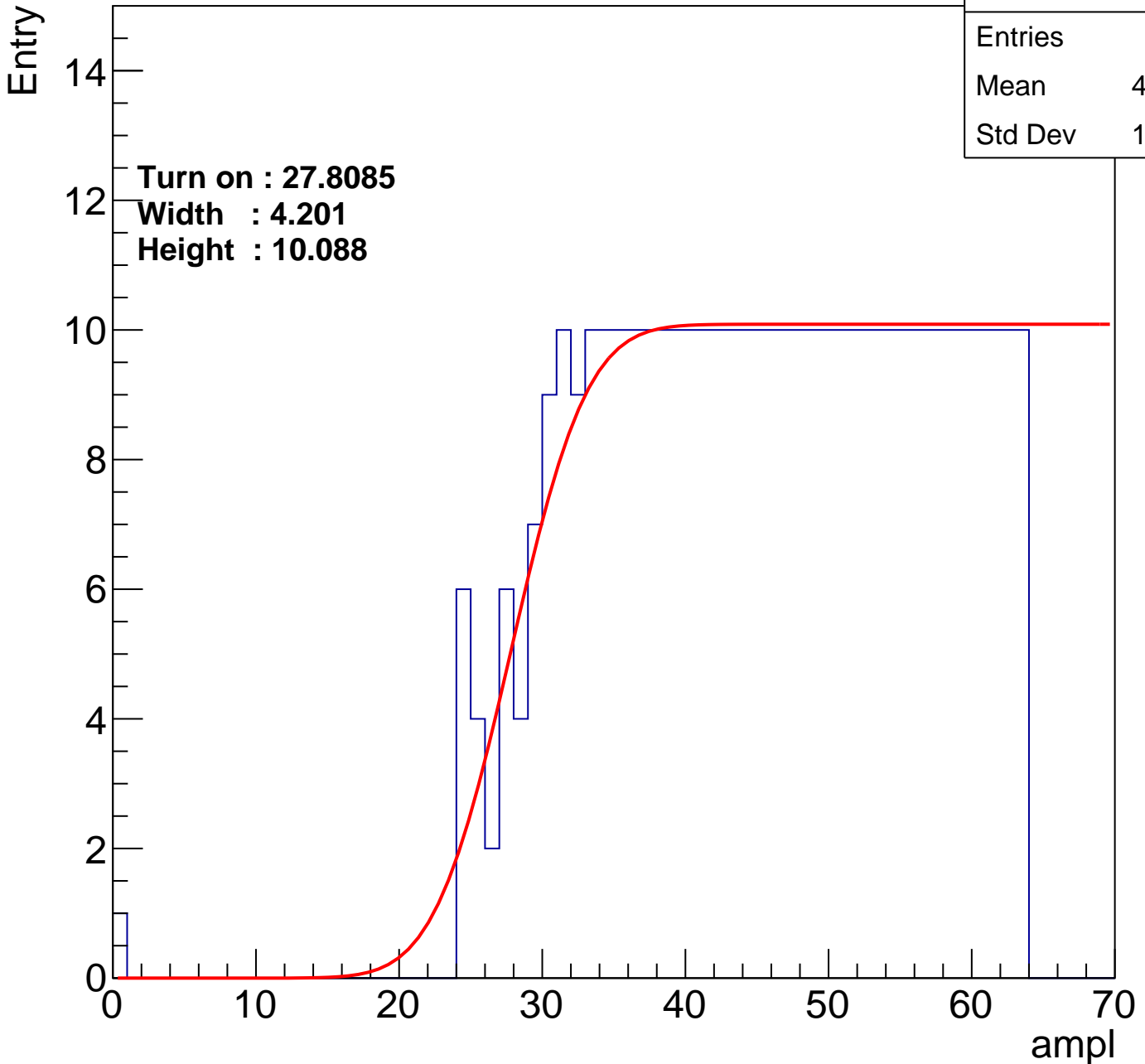
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.06

Turn on : 27.8085

Width : 4.201

Height : 10.088



B0L002S, U5-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.96
Std Dev	11.81

Turn on : 26.0439

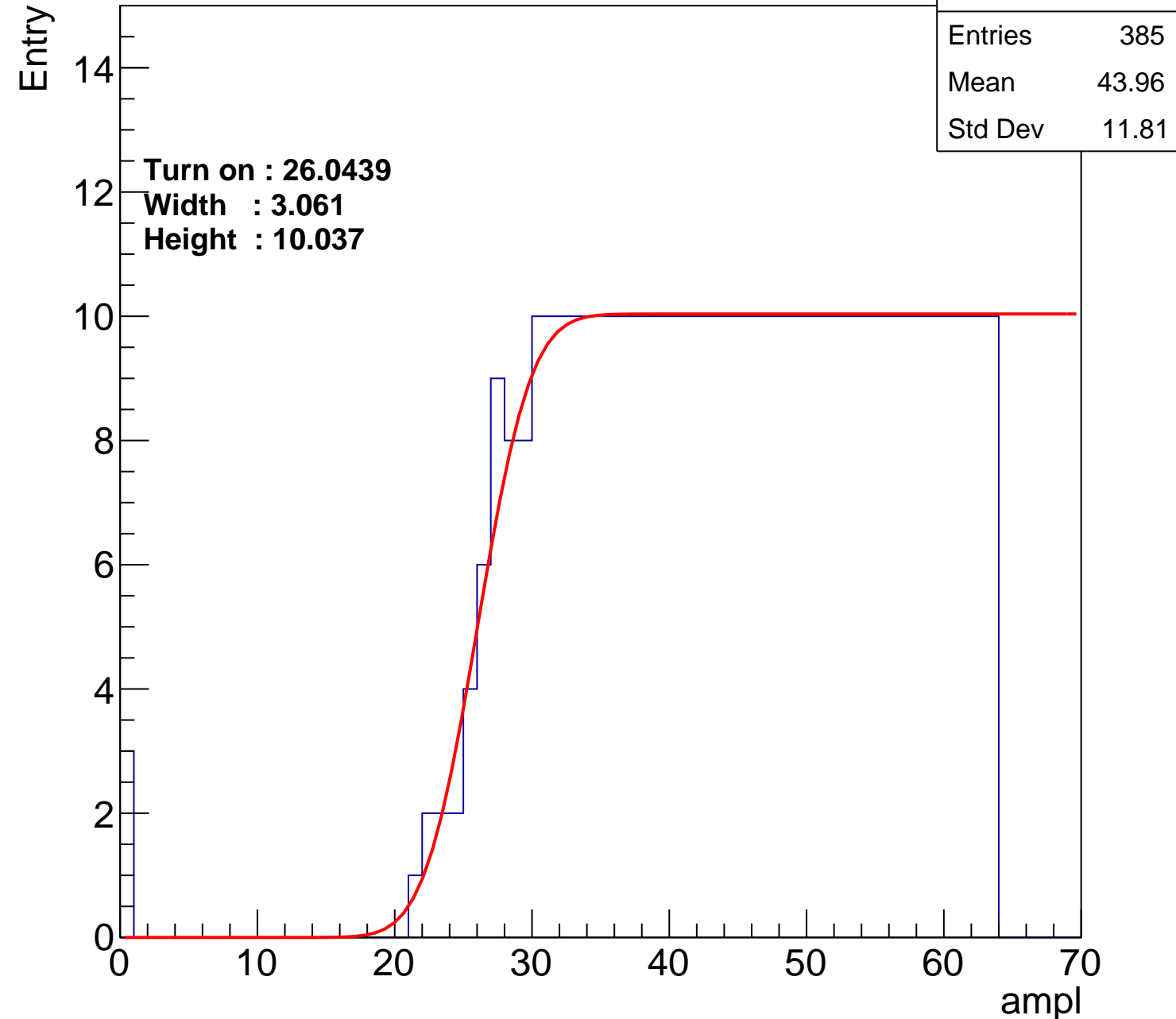
Width : 3.061

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.28
Std Dev	11.97

Turn on : 27.6356

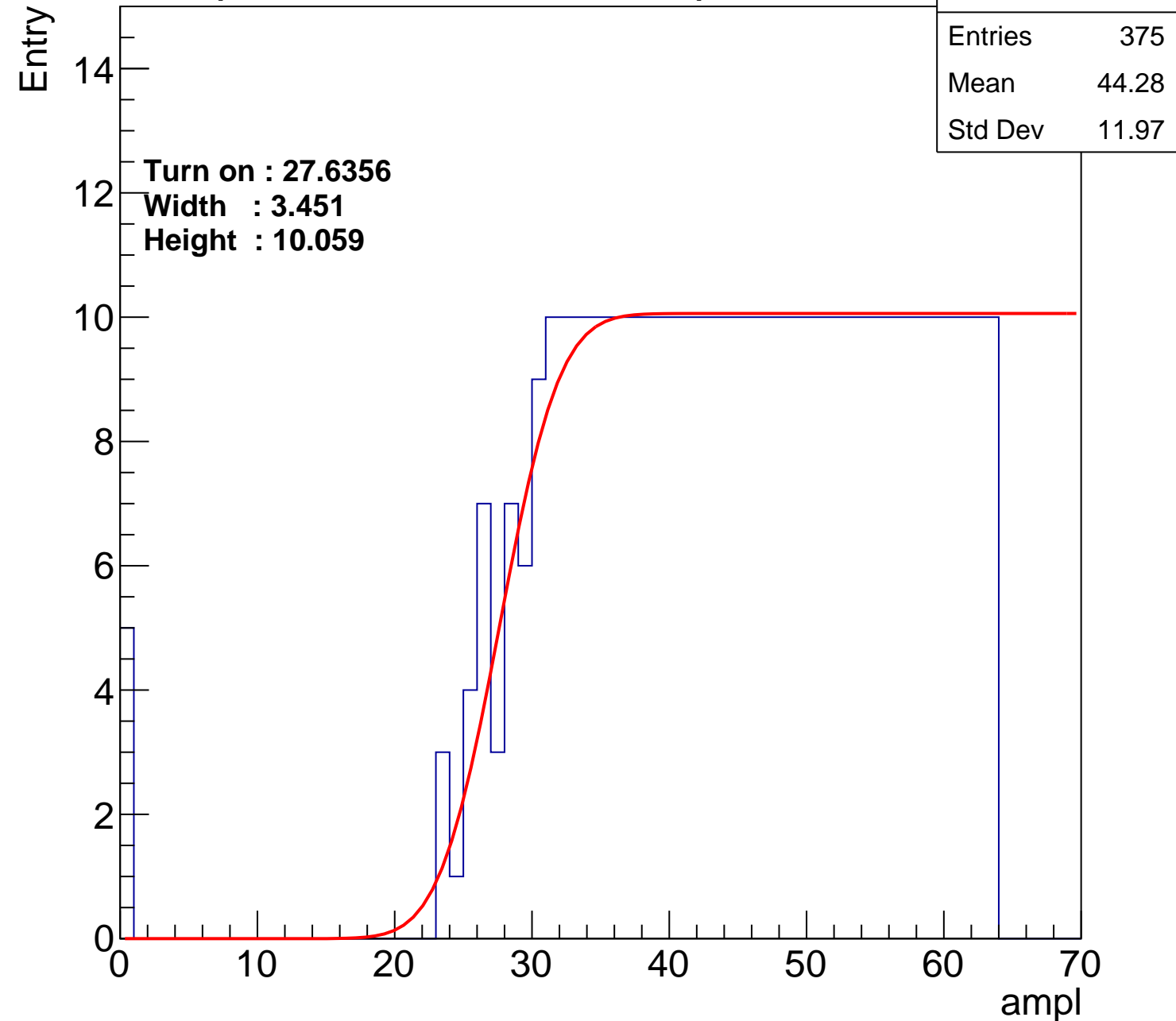
Width : 3.451

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.67
Std Dev	10.76

Turn on : 29.1109

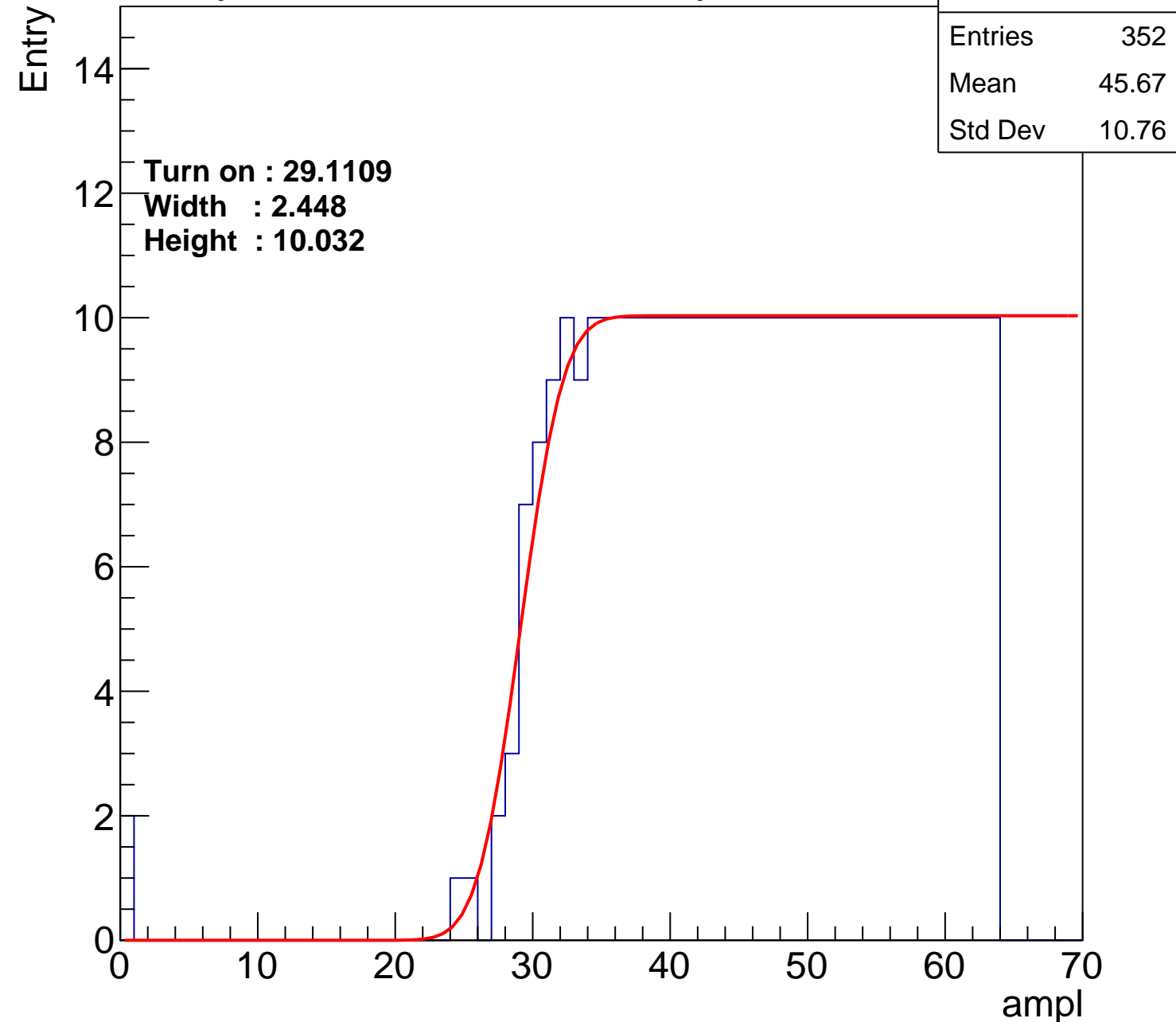
Width : 2.448

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.38
Std Dev	11.61

Turn on : 27.0907

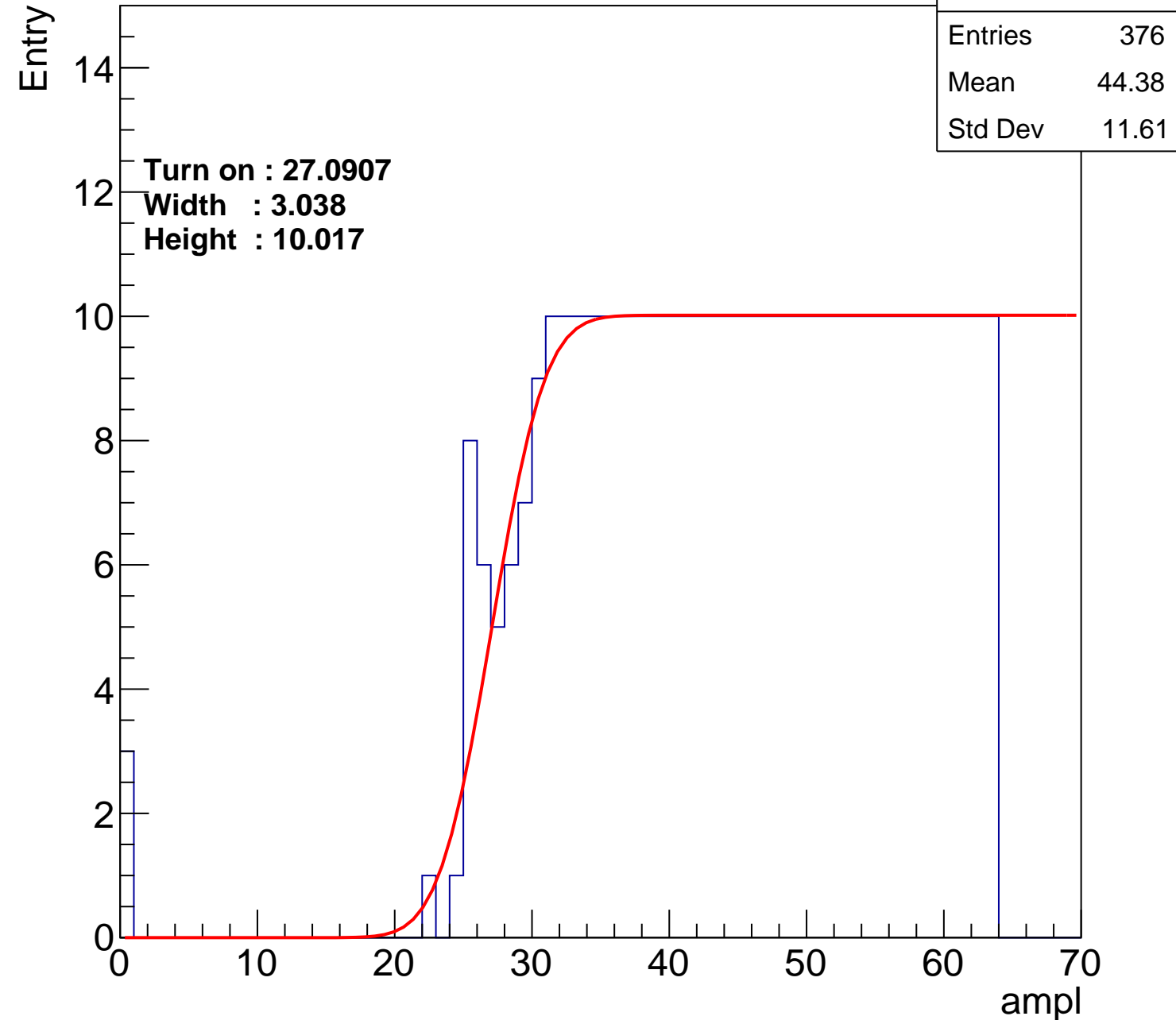
Width : 3.038

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch14

calib_packv5_042523_0143.root, FC#8, port C1

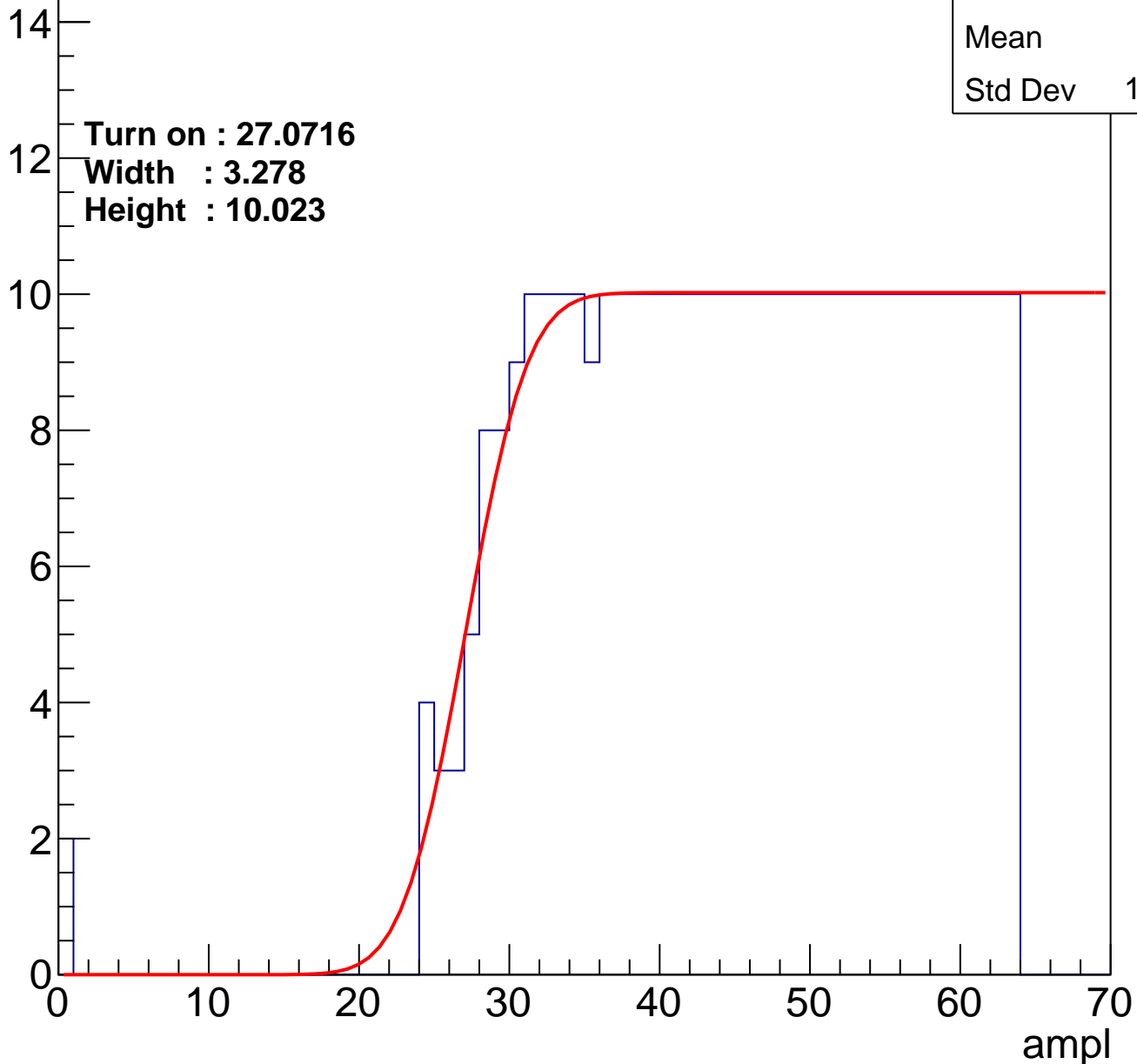
Entries	371
Mean	44.7
Std Dev	11.28

Turn on : 27.0716

Width : 3.278

Height : 10.023

Entry



B0L002S, U5-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.32
Std Dev	11.36

Turn on : 26.4391

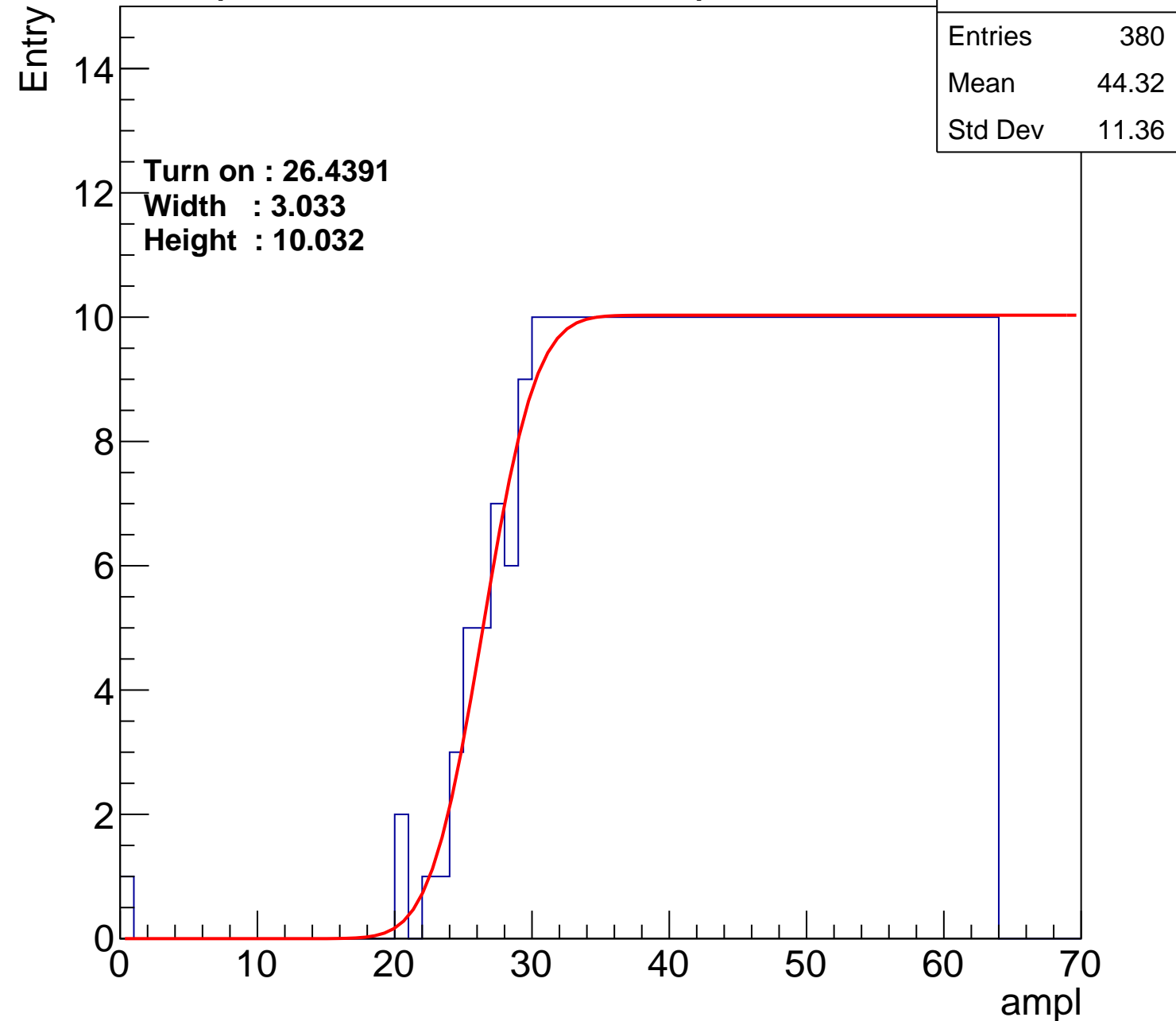
Width : 3.033

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	44.05
Std Dev	11.46

Turn on : 25.6772

Width : 2.823

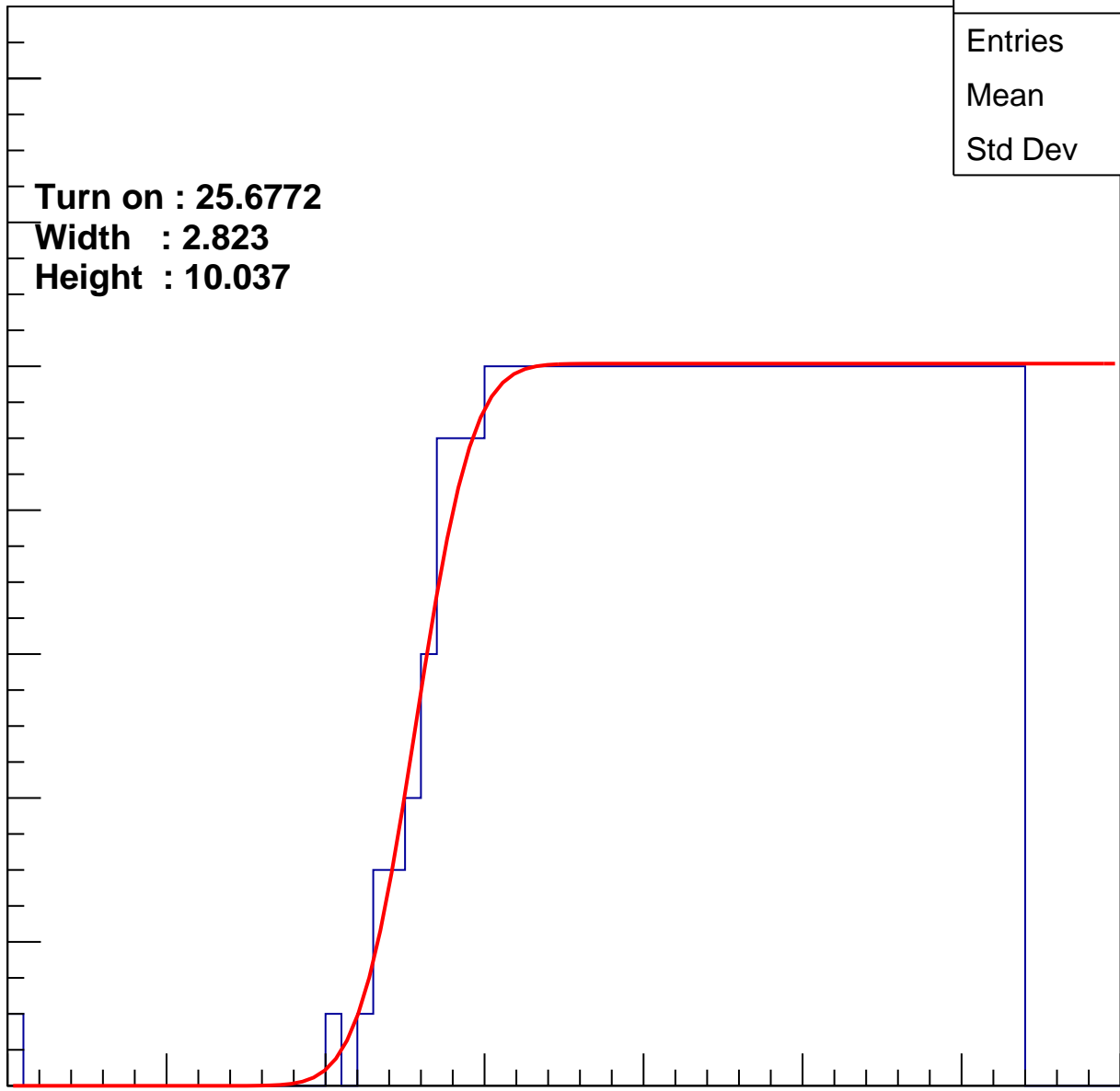
Height : 10.037

Entry

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U5-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.16
Std Dev	10.89

Turn on : 27.6923

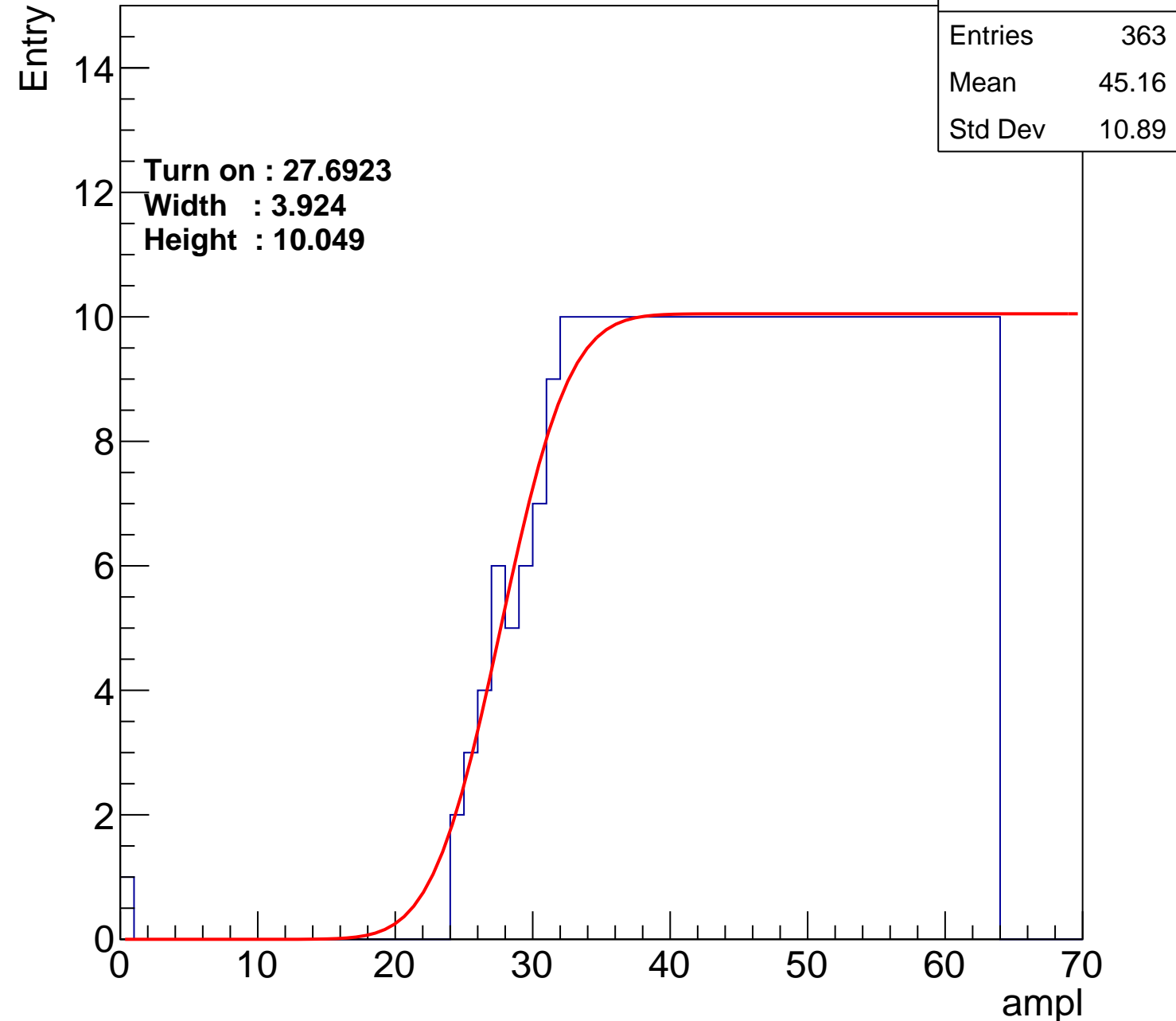
Width : 3.924

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.66
Std Dev	11.5

Turn on : 27.5731

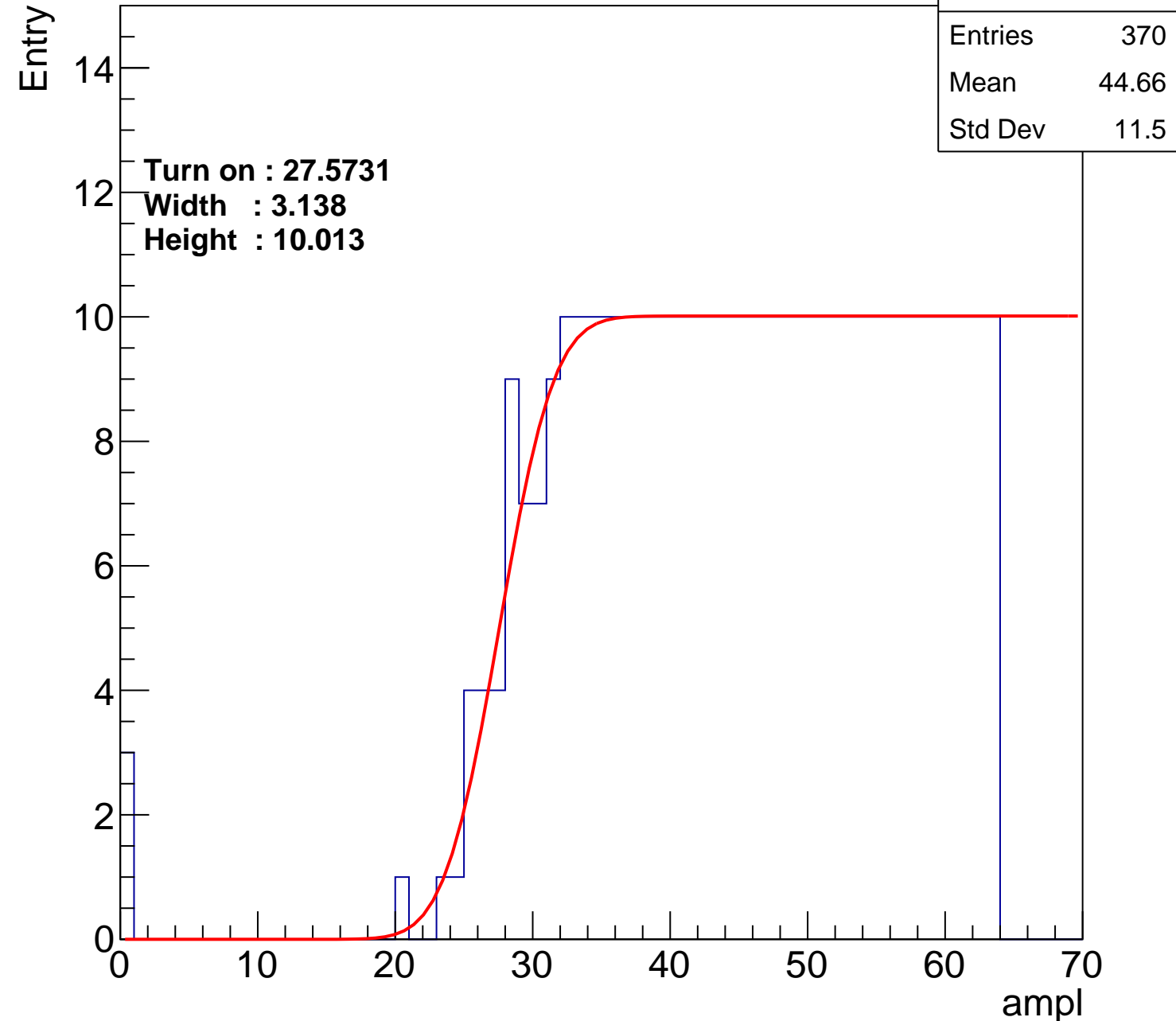
Width : 3.138

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch19

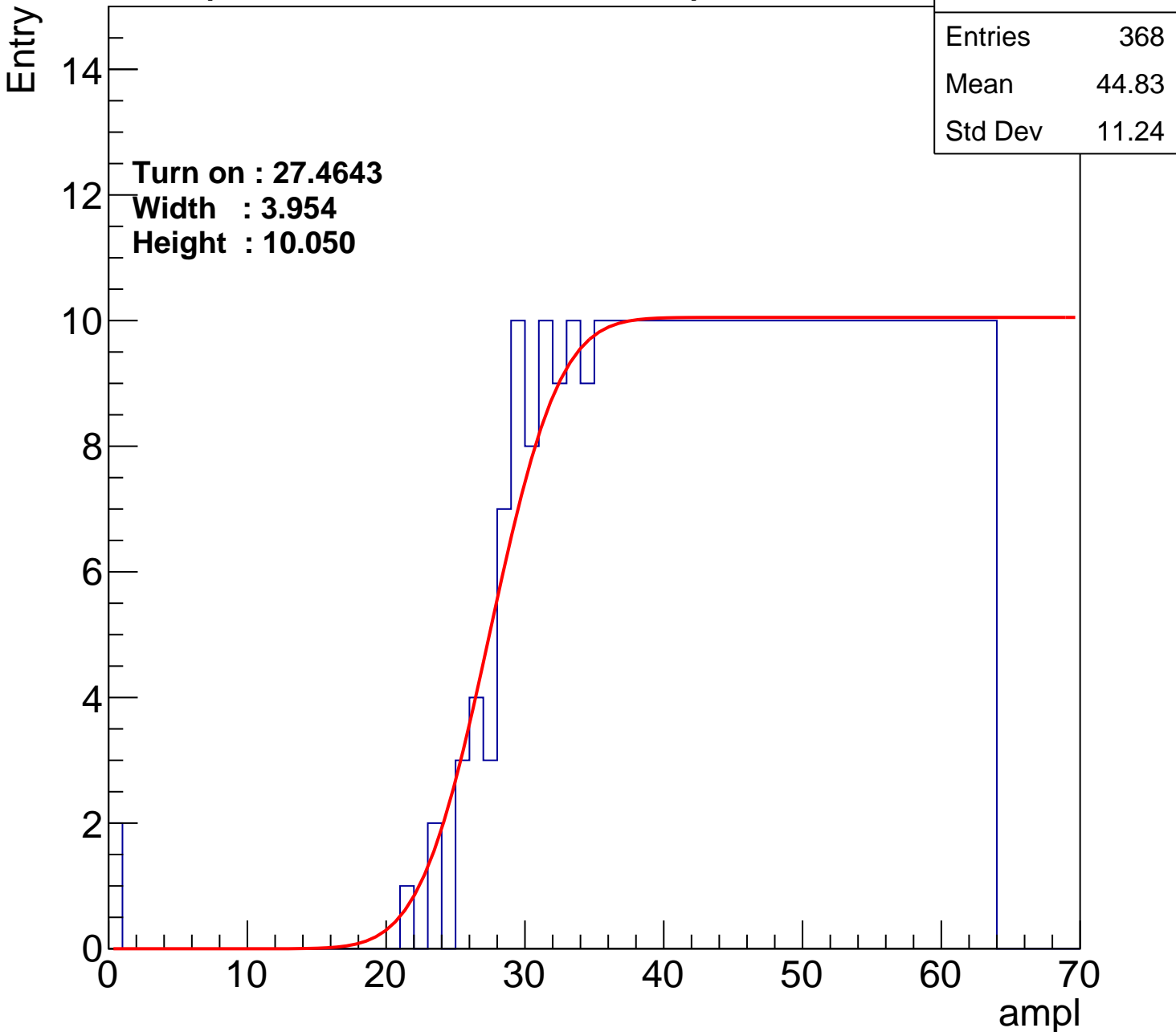
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.4643

Width : 3.954

Height : 10.050

Entries	368
Mean	44.83
Std Dev	11.24



B0L002S, U5-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.8
Std Dev	10.56

Turn on : 29.4587

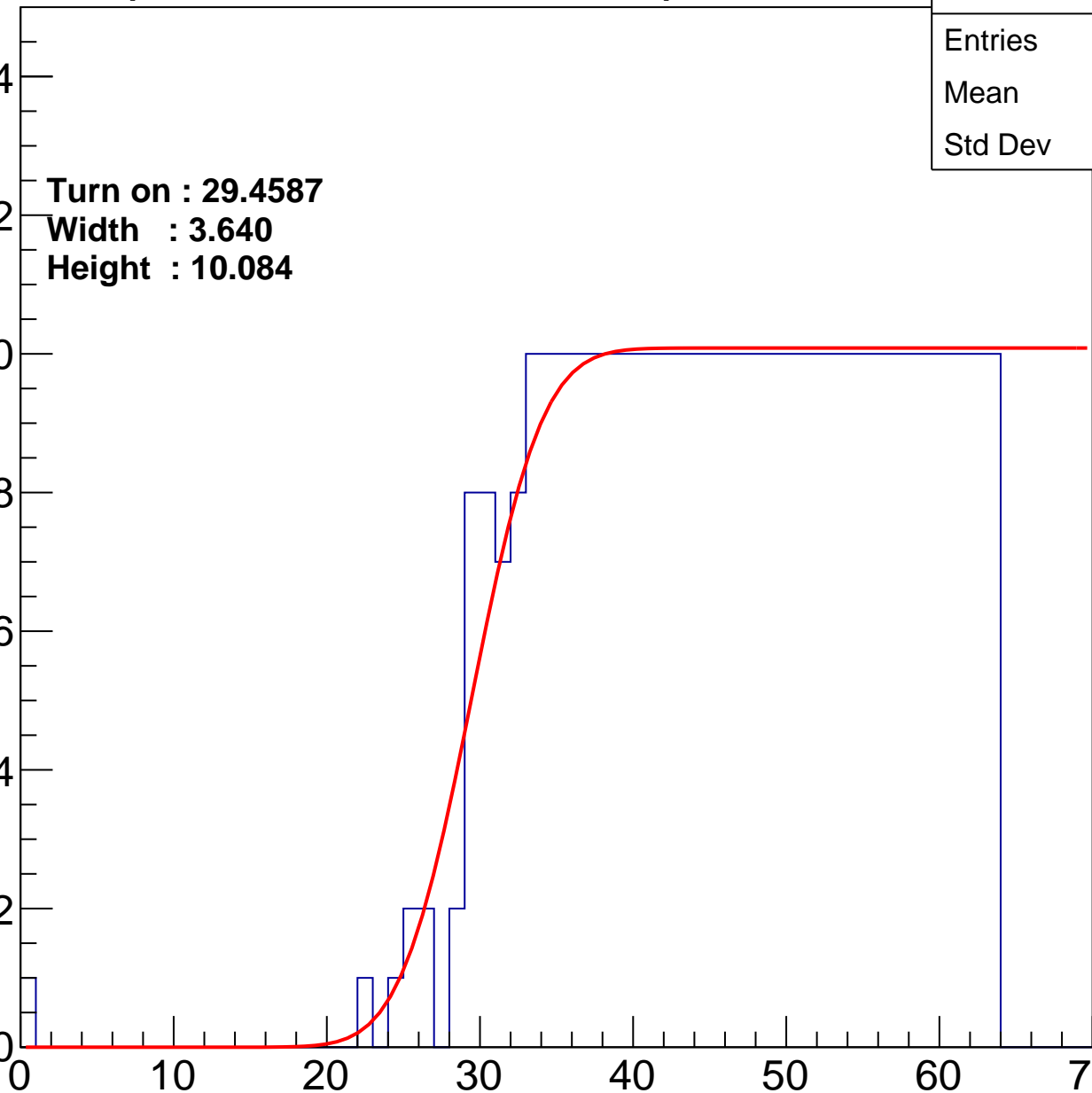
Width : 3.640

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.82
Std Dev	11.26

Turn on : 27.7788

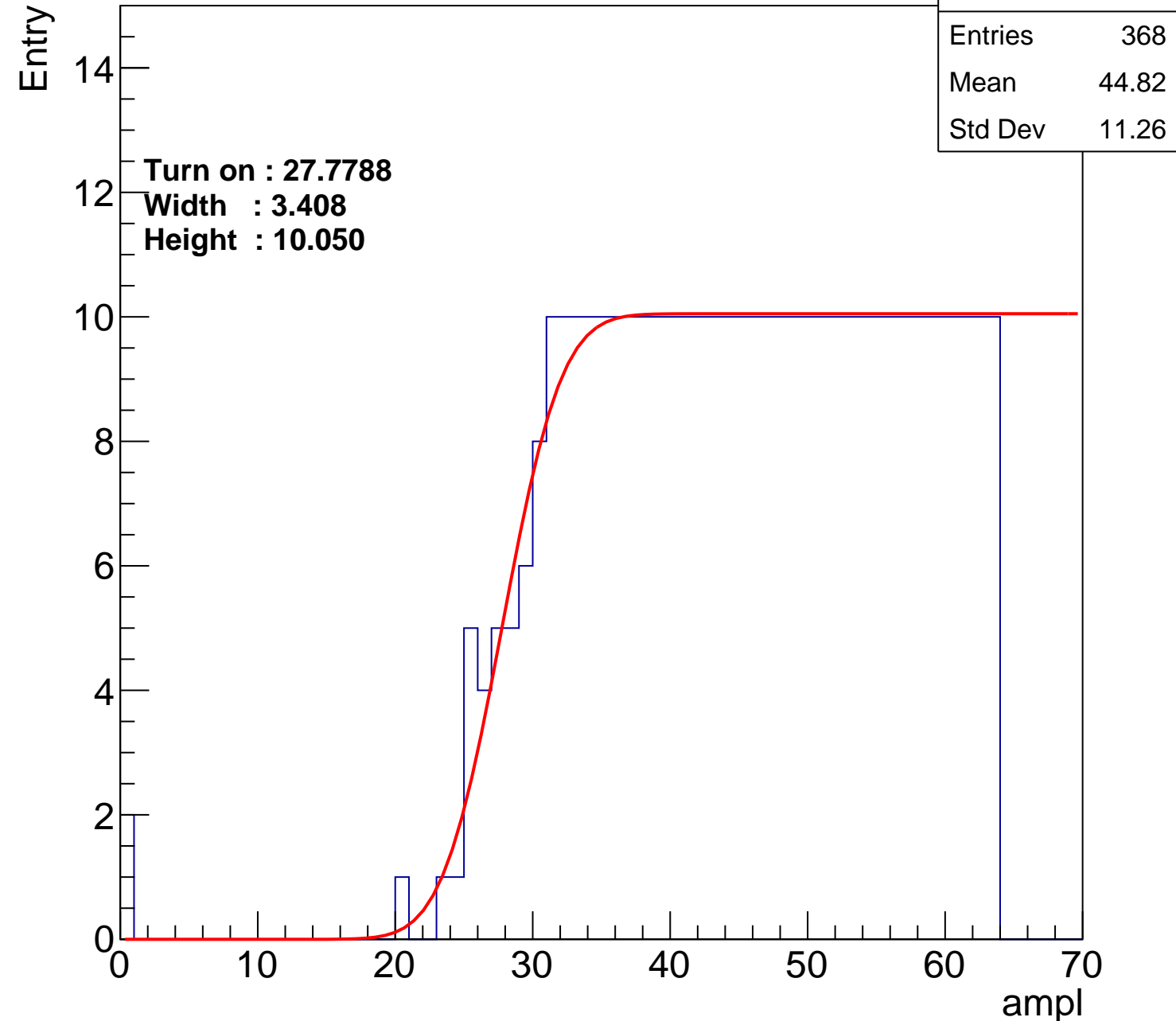
Width : 3.408

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.5278

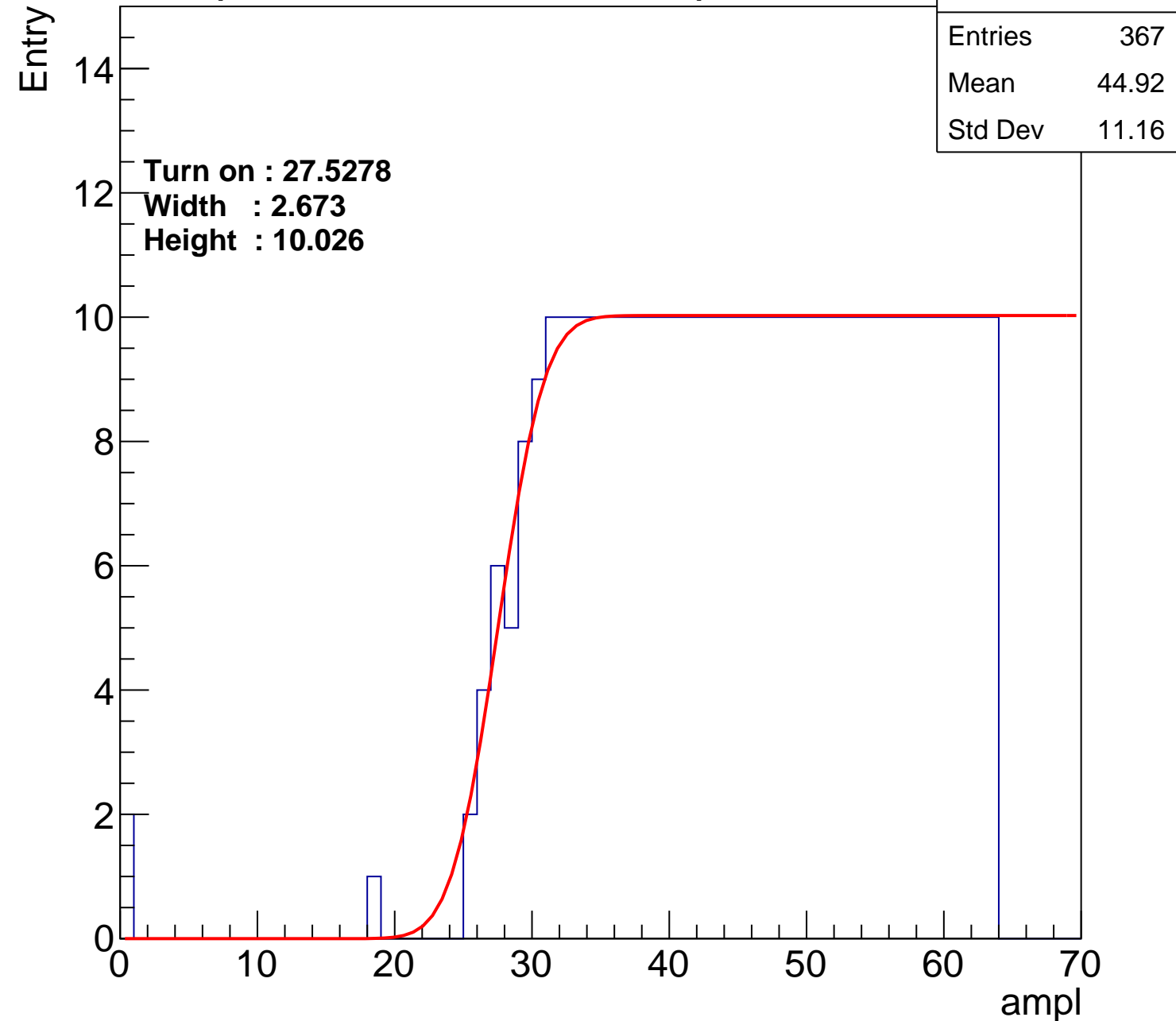
Width : 2.673

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.64
Std Dev	11.48

Turn on : 27.4512

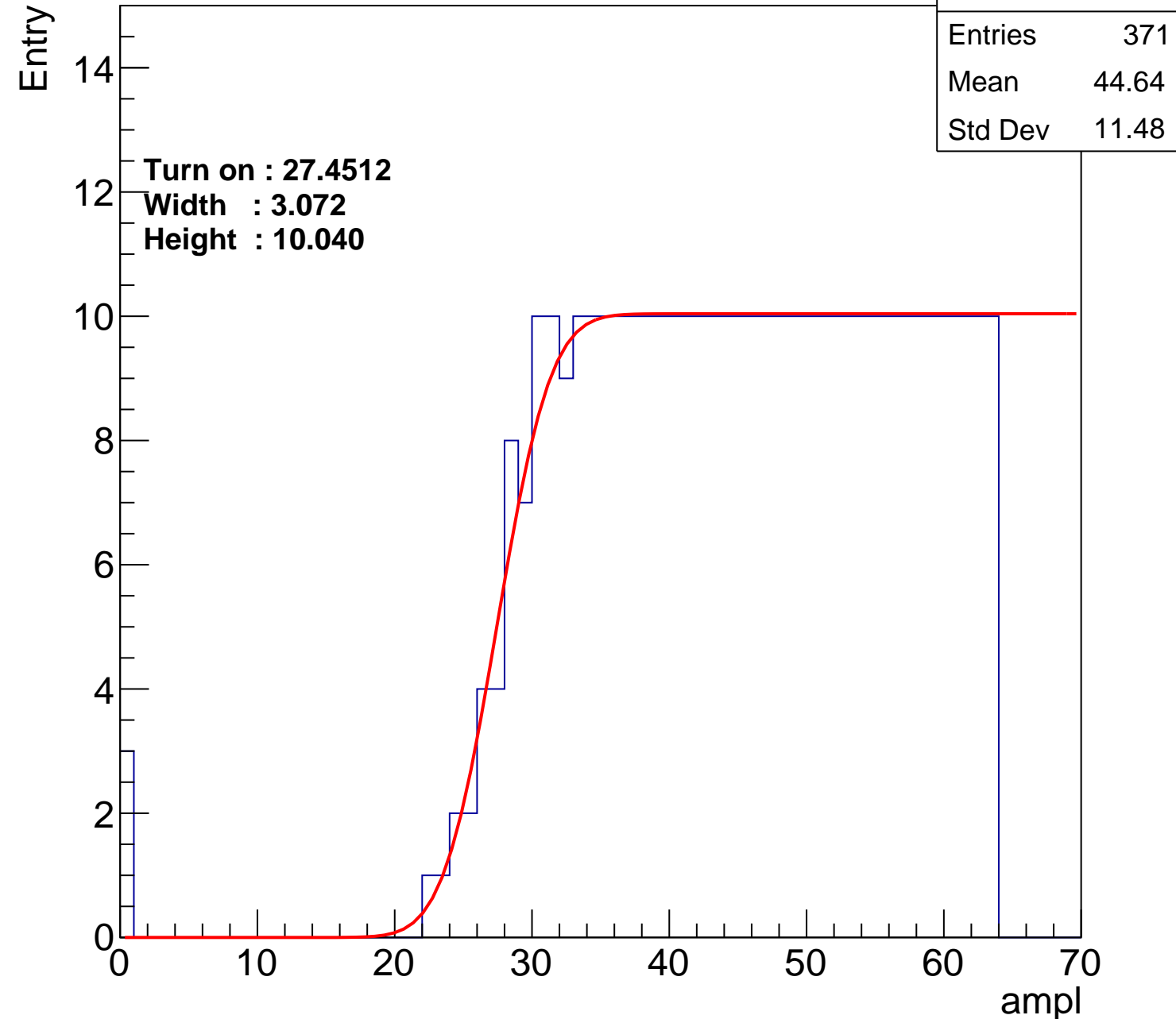
Width : 3.072

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.35
Std Dev	10.95

Turn on : 28.8119

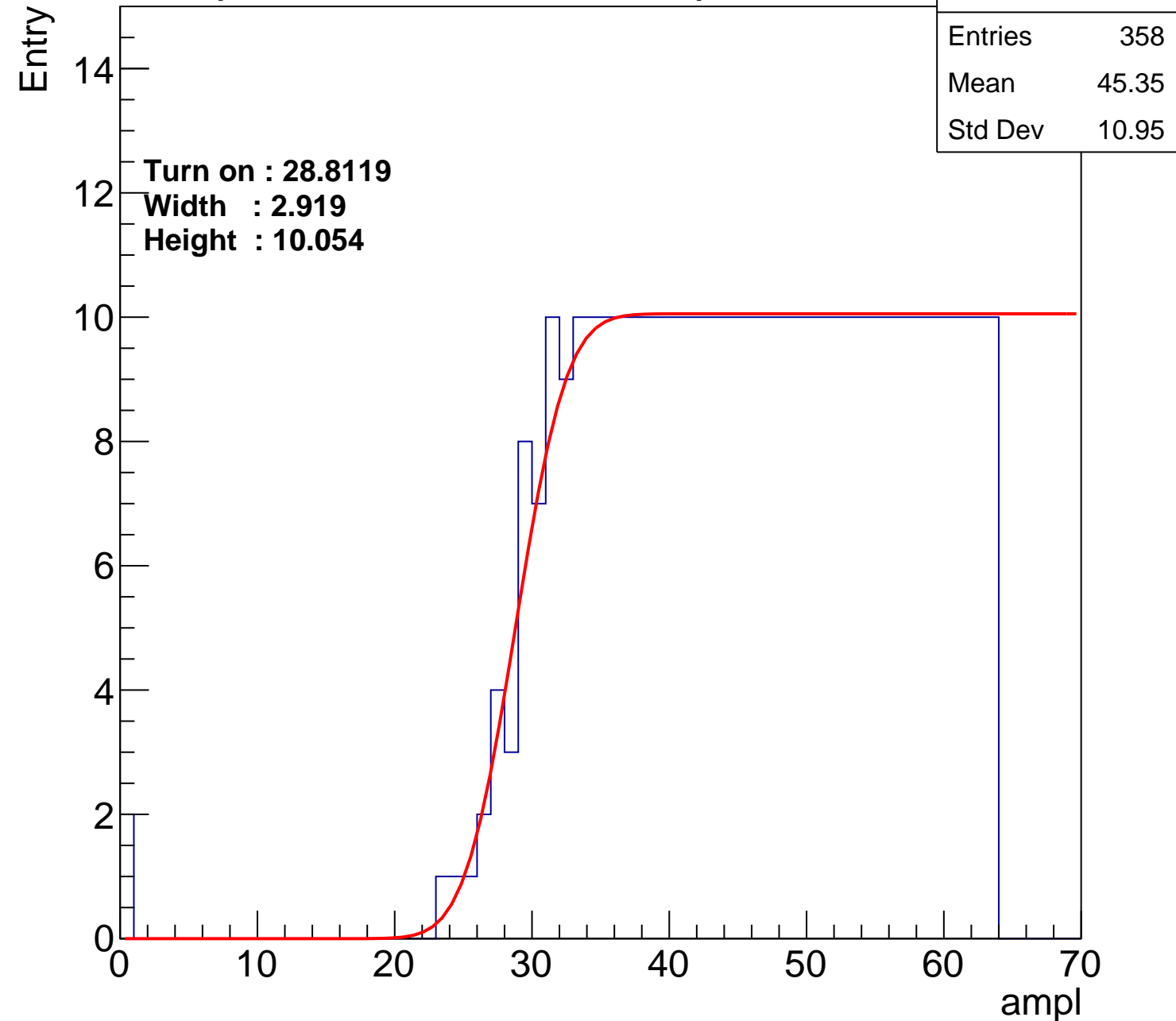
Width : 2.919

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.62
Std Dev	10.68

Turn on : 29.1162

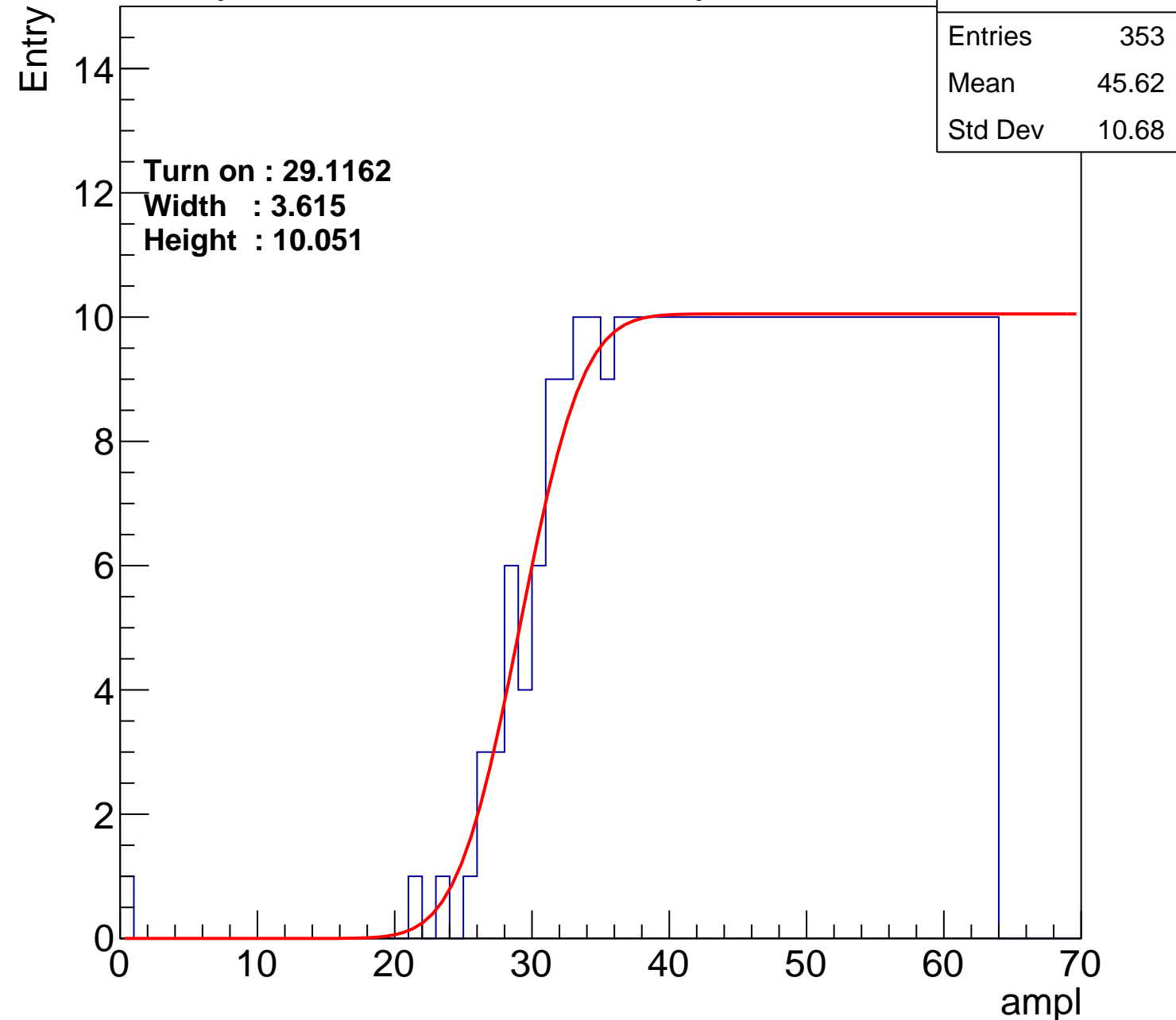
Width : 3.615

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch26

calib_packv5_042523_0143.root, FC#8, port C1

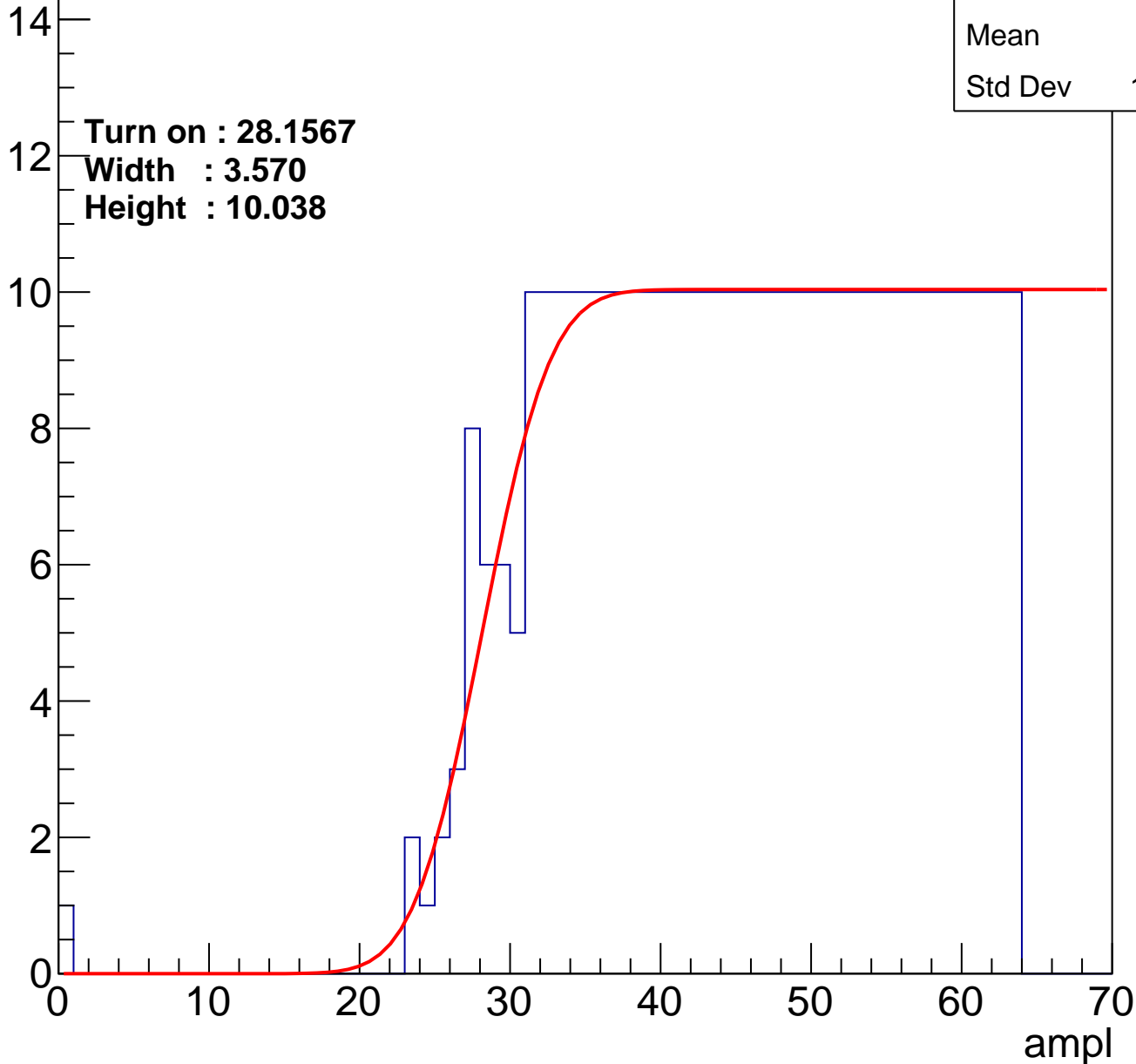
Entries	364
Mean	45.1
Std Dev	10.94

Turn on : 28.1567

Width : 3.570

Height : 10.038

Entry



B0L002S, U5-ch27

calib_packv5_042523_0143.root, FC#8, port C1

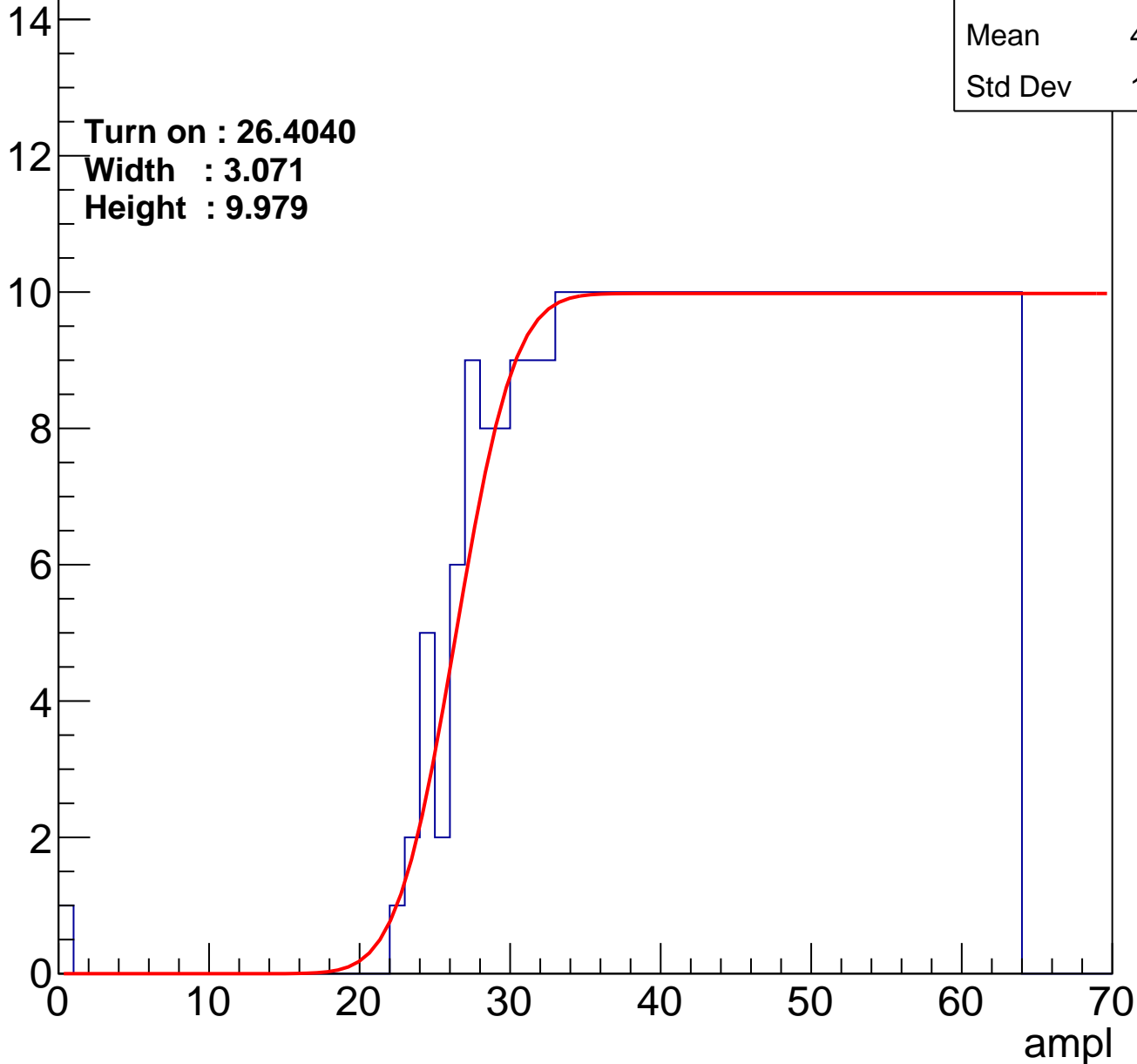
Entry

Entries	379
Mean	44.35
Std Dev	11.34

Turn on : 26.4040

Width : 3.071

Height : 9.979



B0L002S, U5-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	395
Mean	43.48
Std Dev	12.04

Turn on : 24.8870

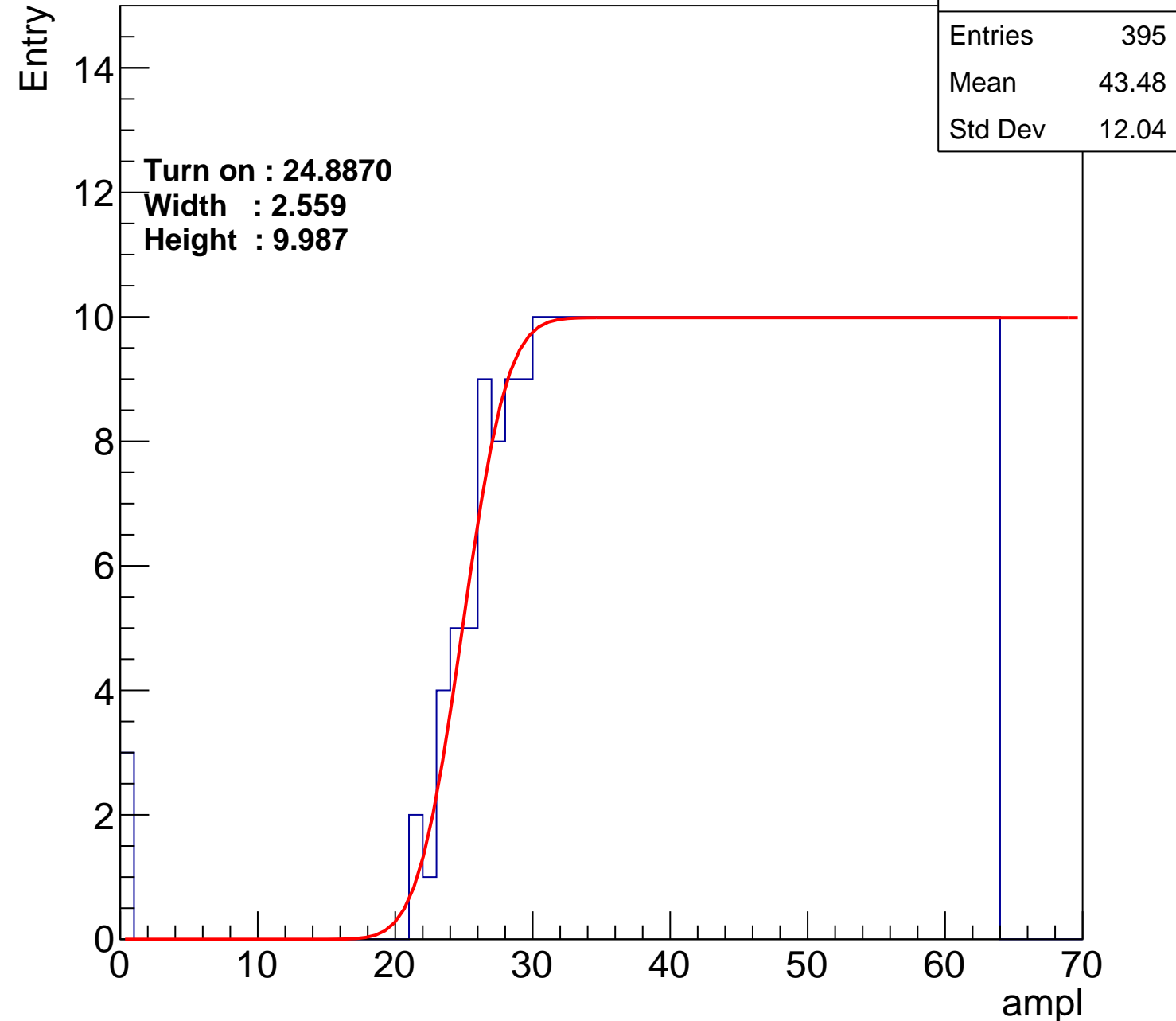
Width : 2.559

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch29

calib_packv5_042523_0143.root, FC#8, port C1

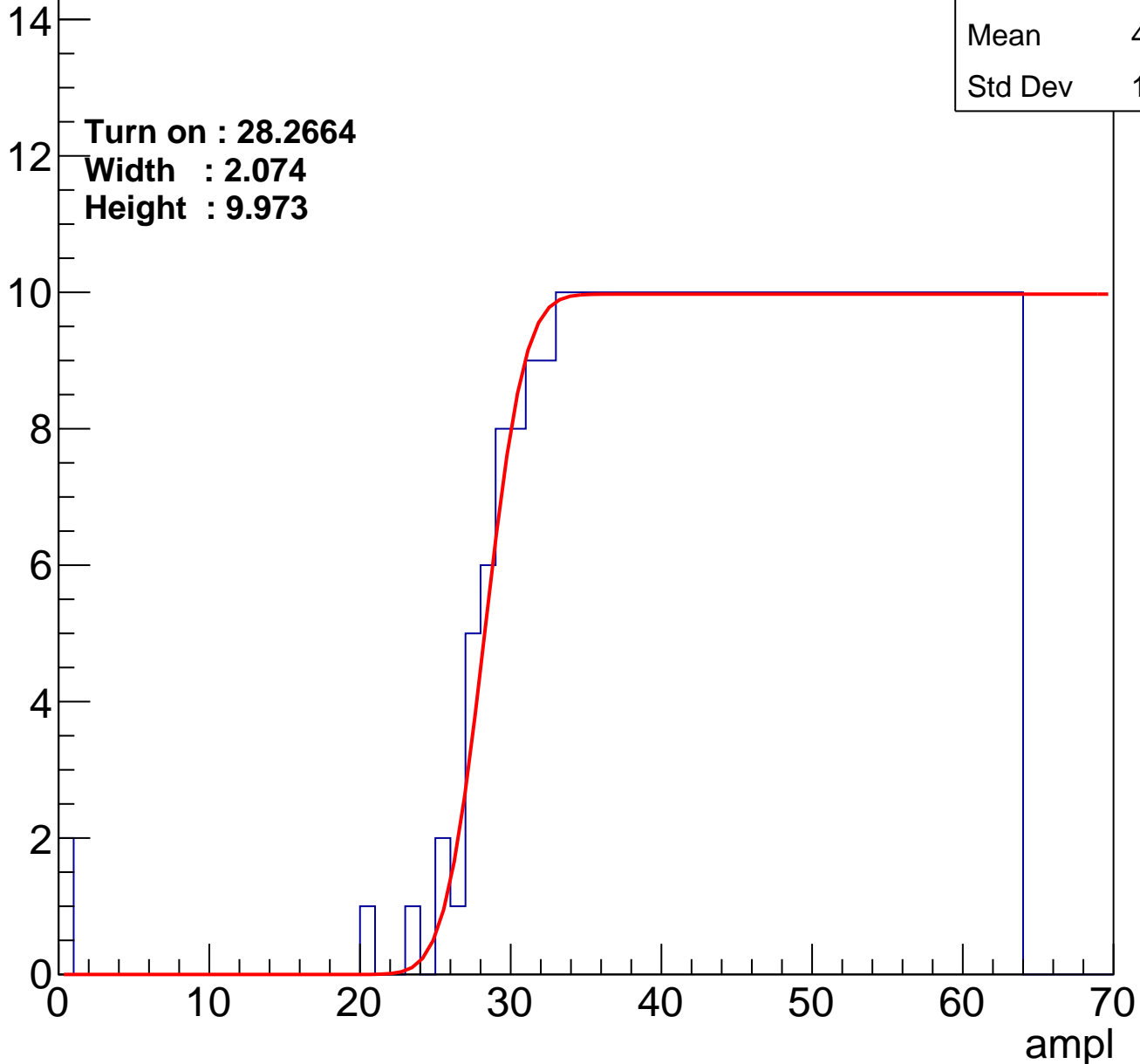
Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 28.2664

Width : 2.074

Height : 9.973

Entry



B0L002S, U5-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	45
Std Dev	10.93

Turn on : 27.6112

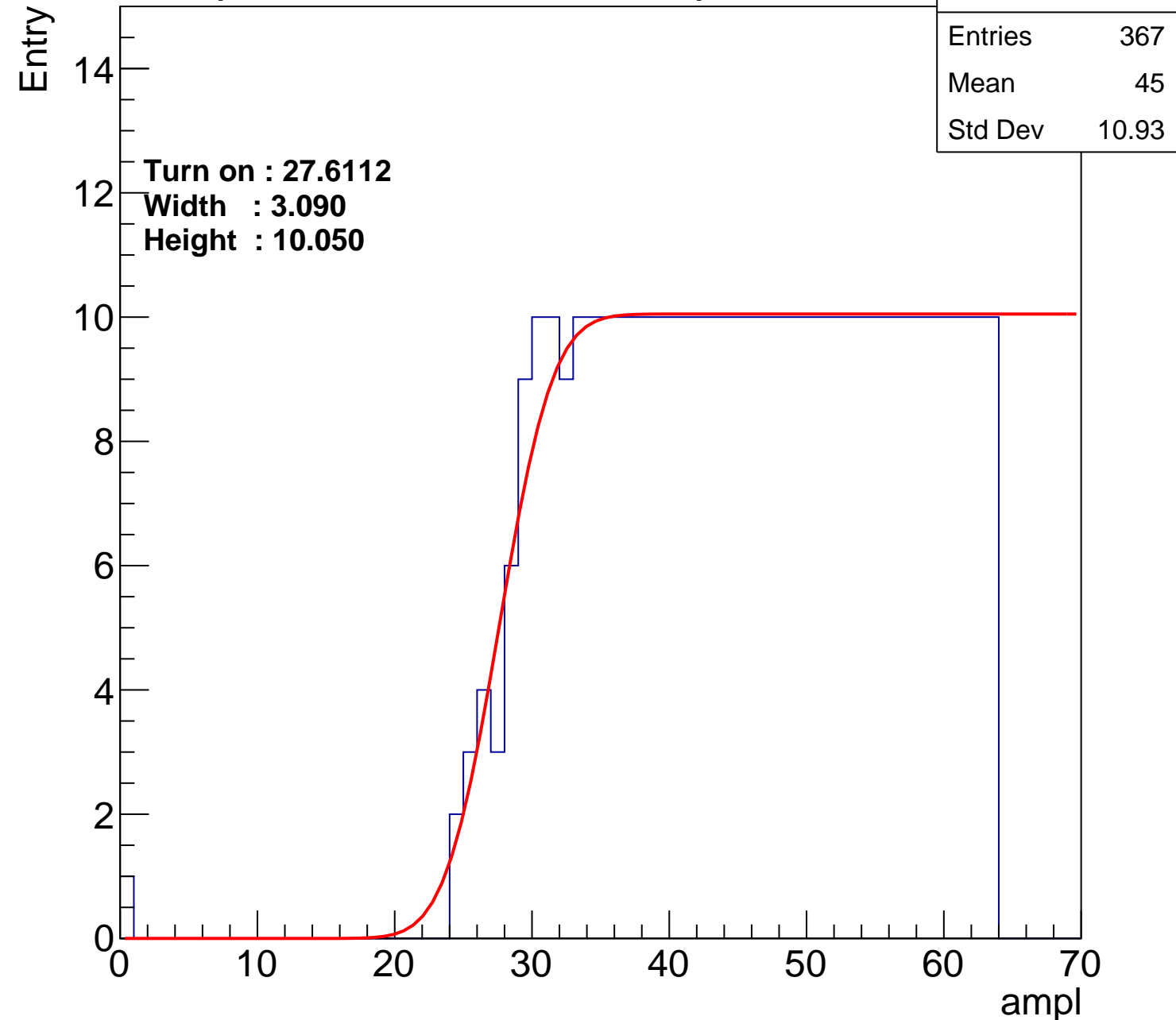
Width : 3.090

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.87
Std Dev	11.24

Turn on : 27.9882

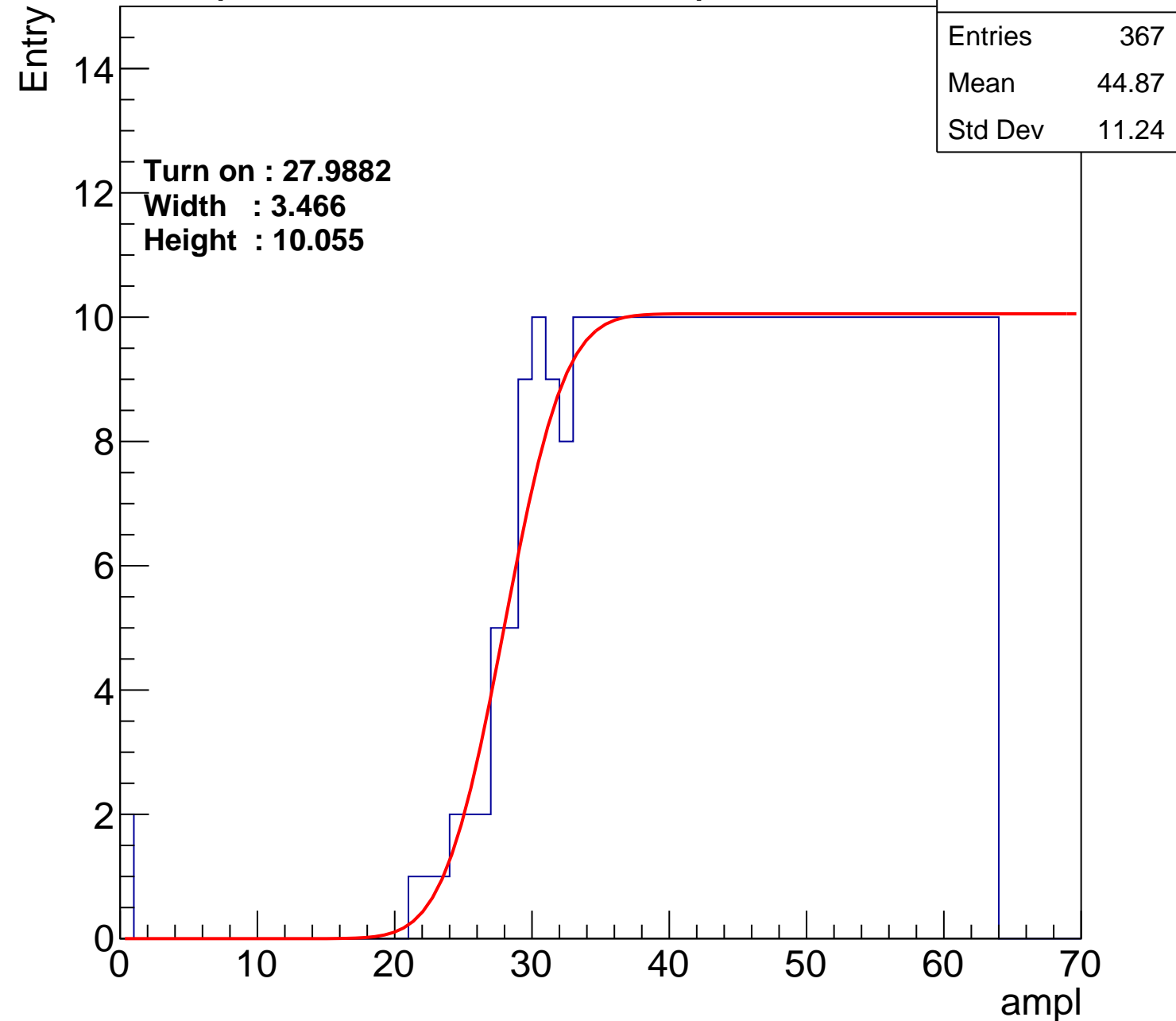
Width : 3.466

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.25
Std Dev	11.15

Turn on : 28.5148

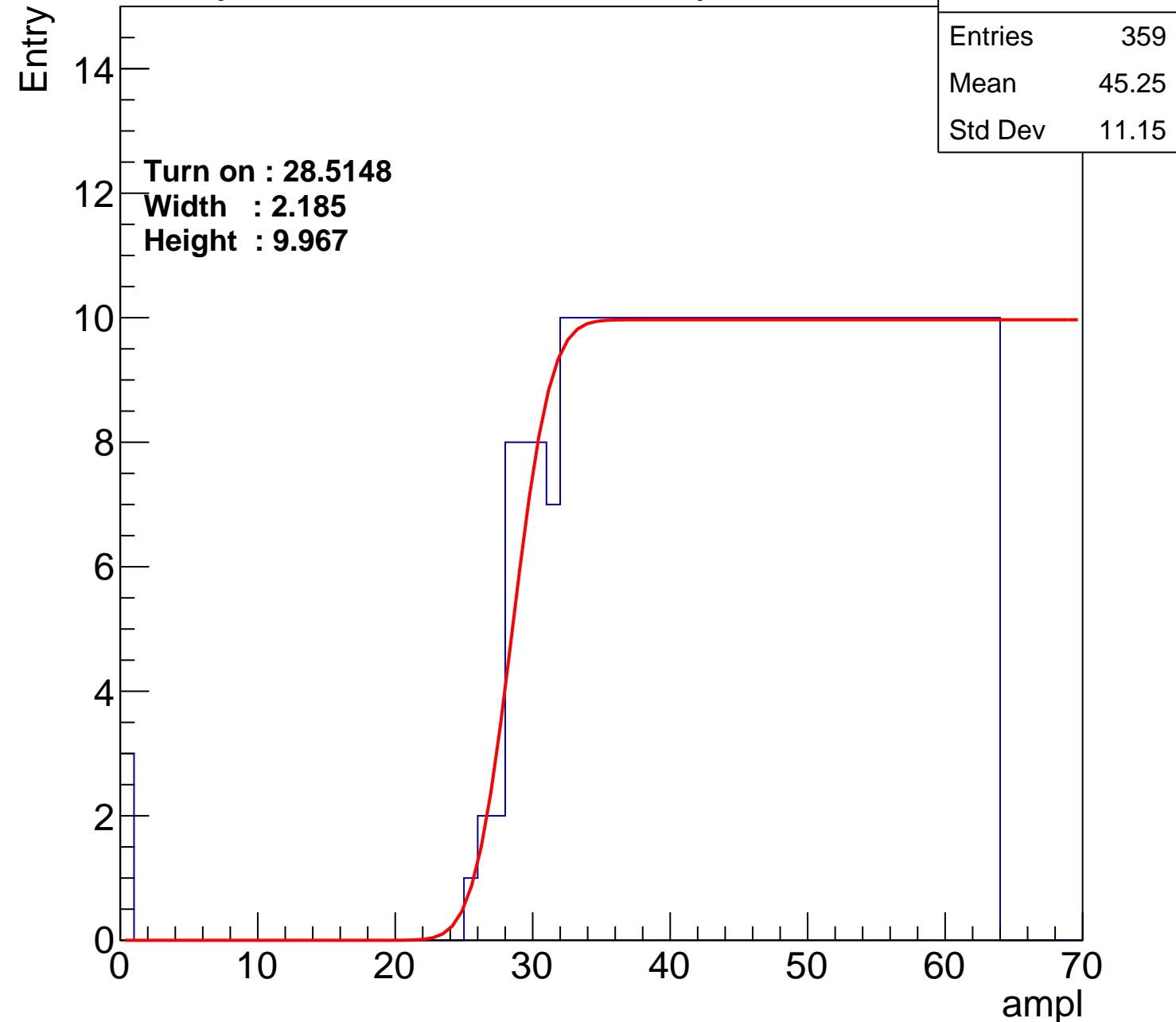
Width : 2.185

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.05
Std Dev	11.17

Turn on : 28.9327

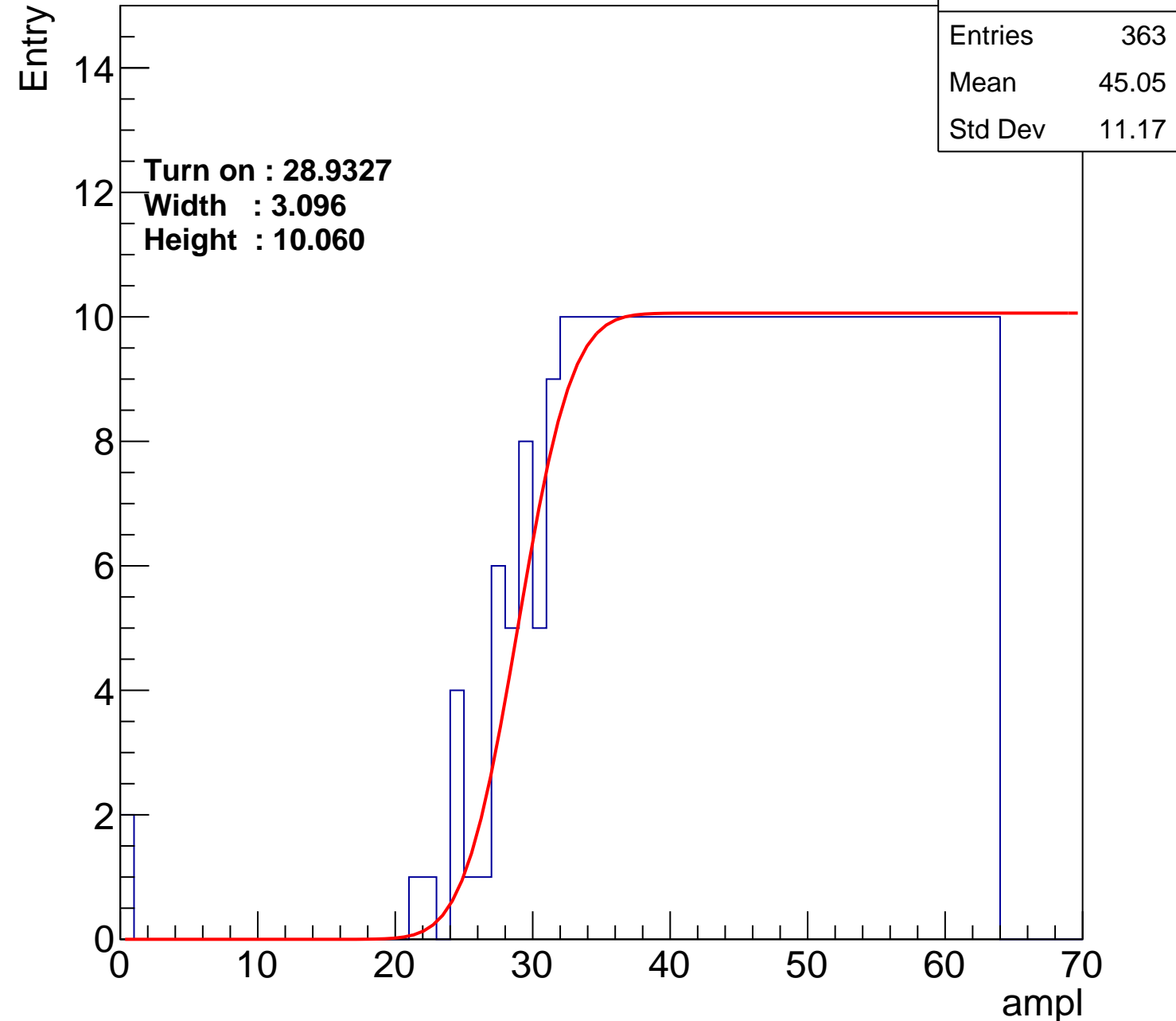
Width : 3.096

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.94
Std Dev	11.38

Turn on : 28.4411

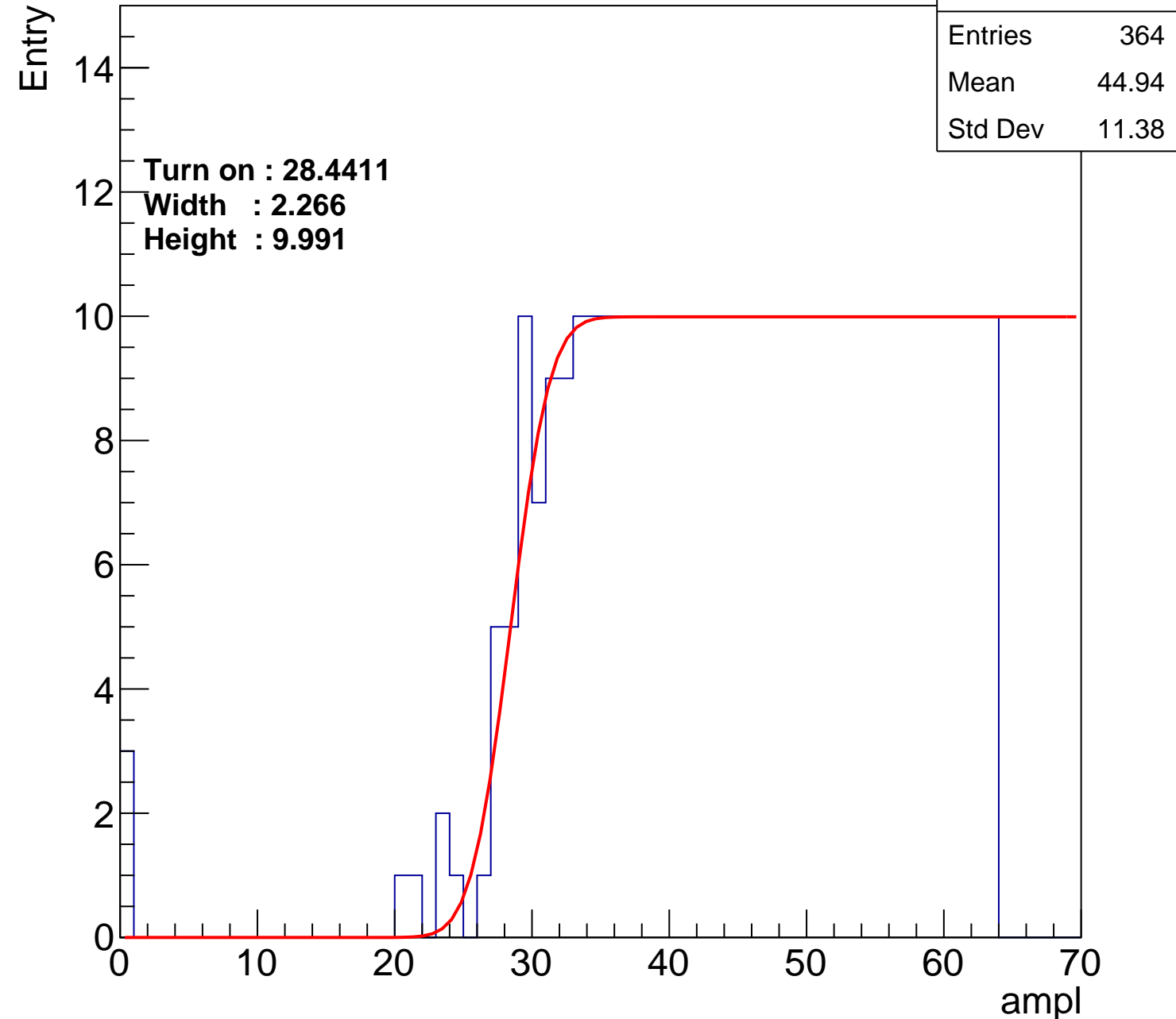
Width : 2.266

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.88
Std Dev	11.17

Turn on : 26.7771

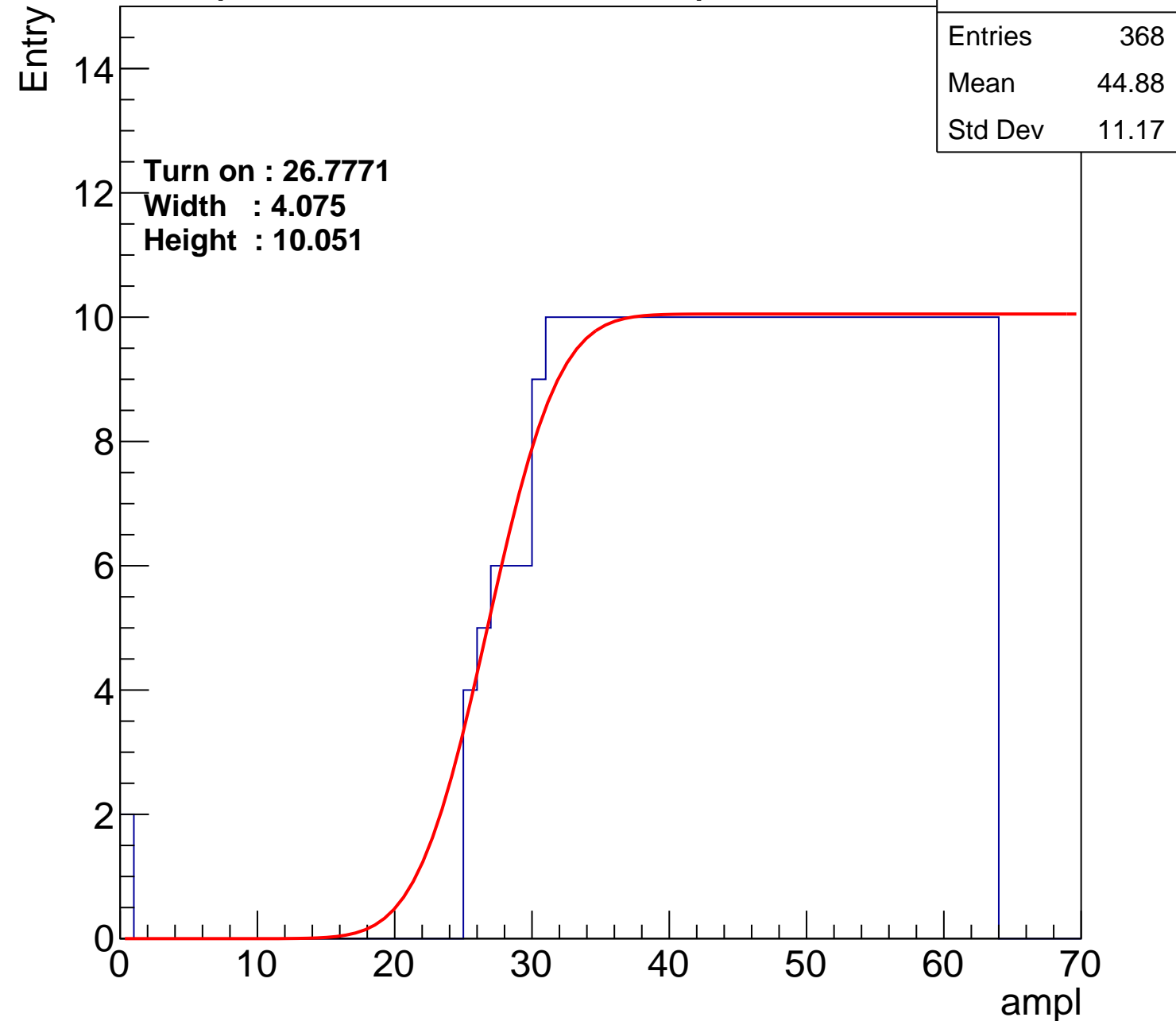
Width : 4.075

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.21
Std Dev	11.01

Turn on : 28.0297

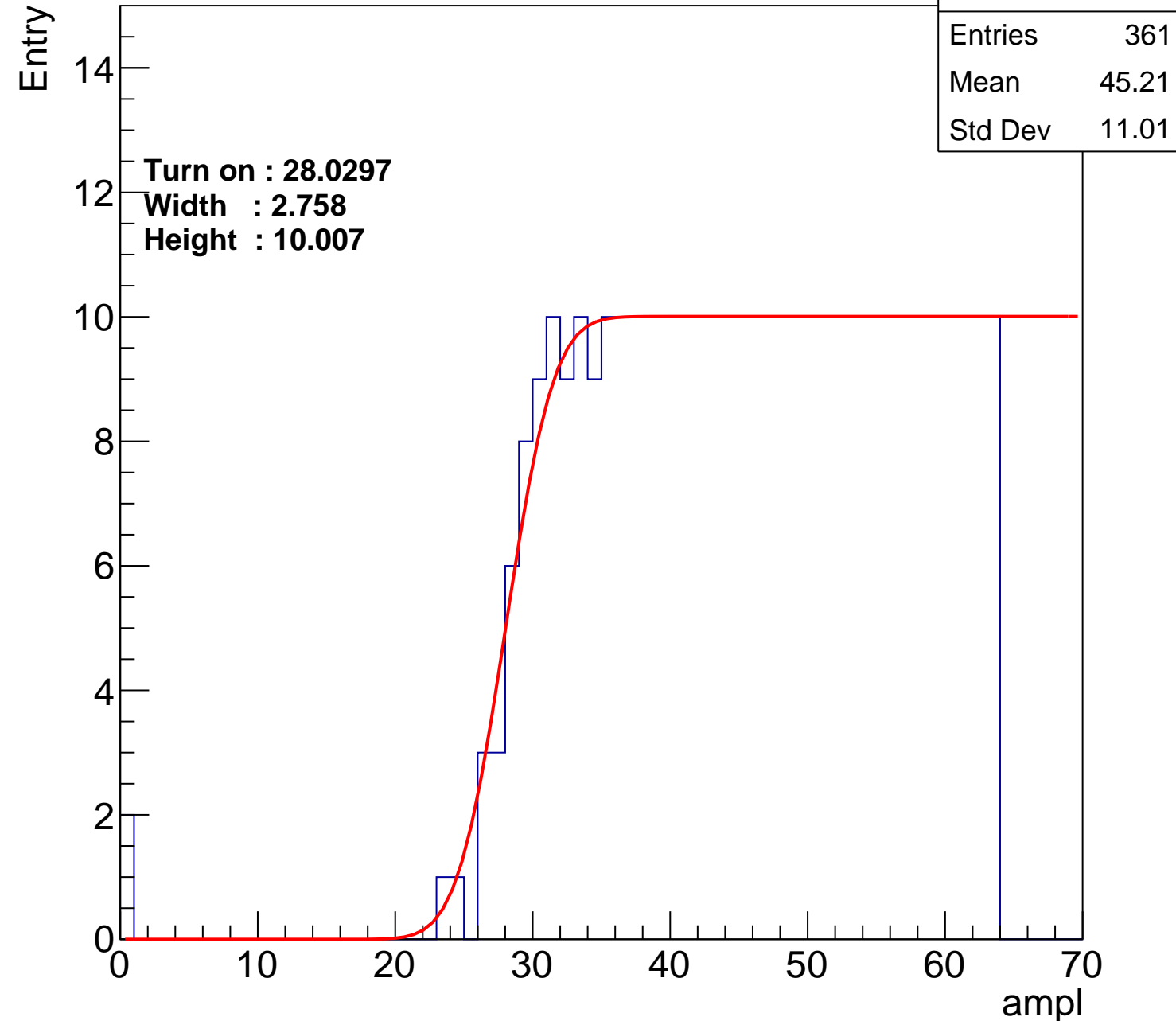
Width : 2.758

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.96
Std Dev	10.49

Turn on : 29.3443

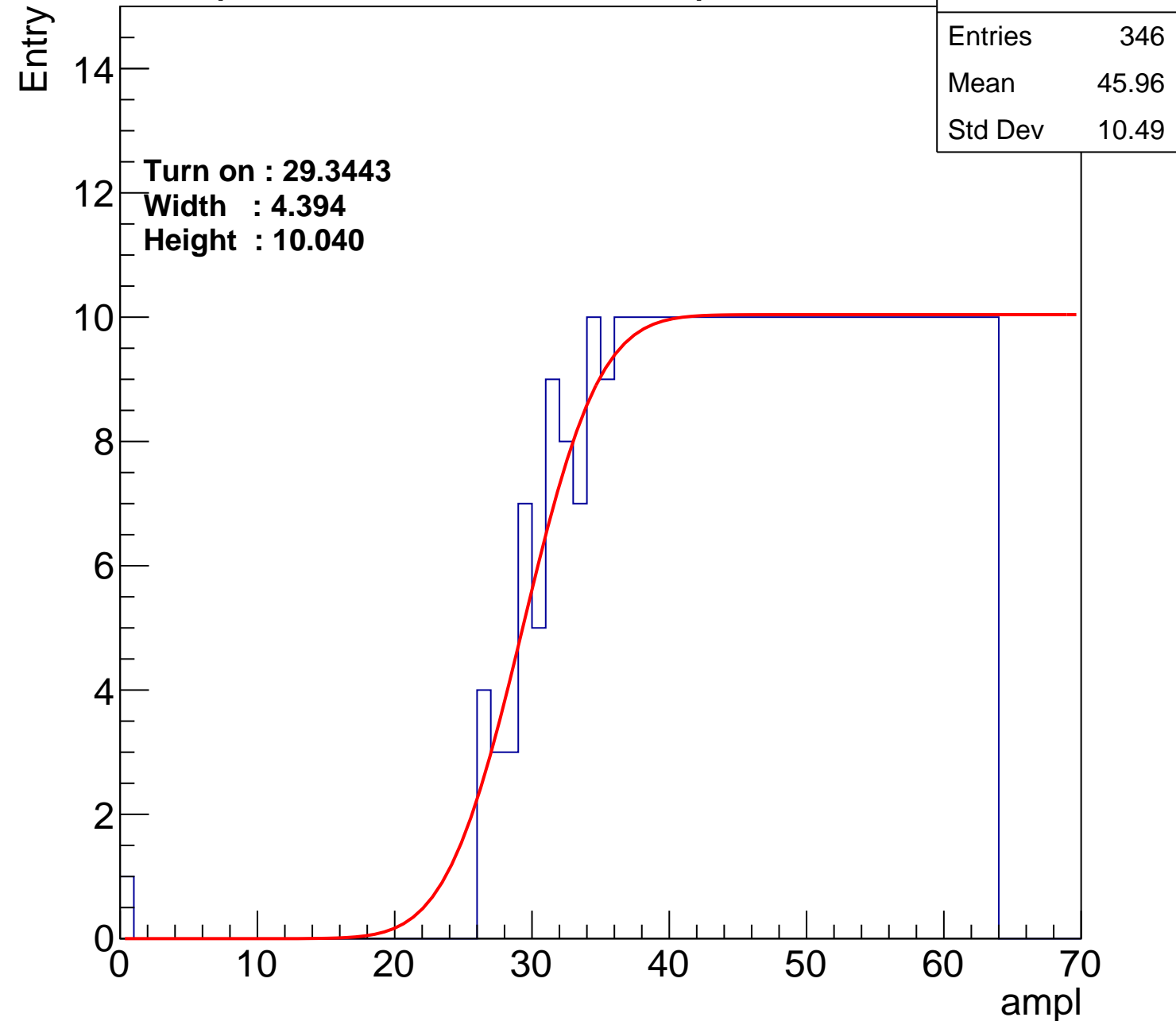
Width : 4.394

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 26.6355

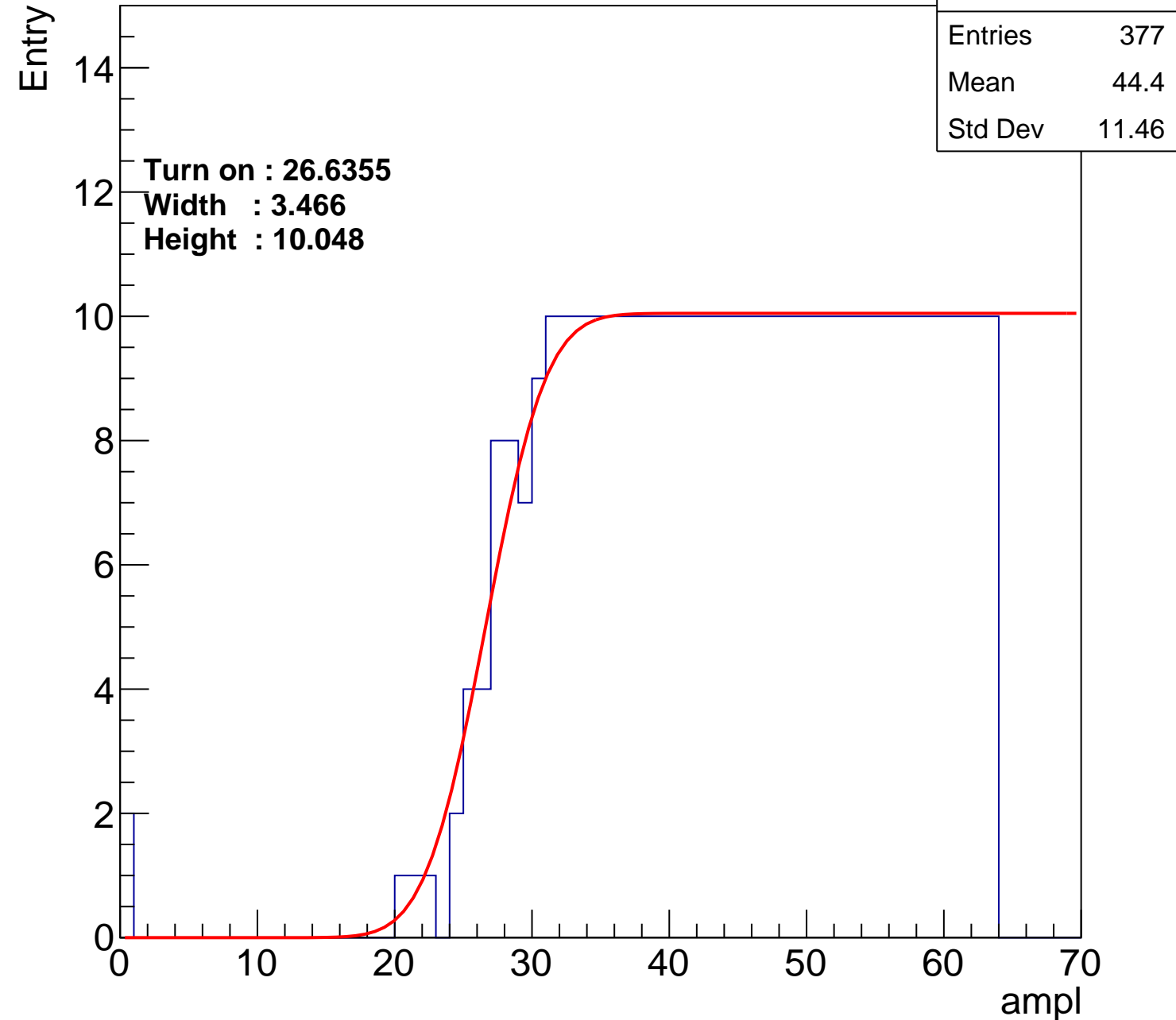
Width : 3.466

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.5639

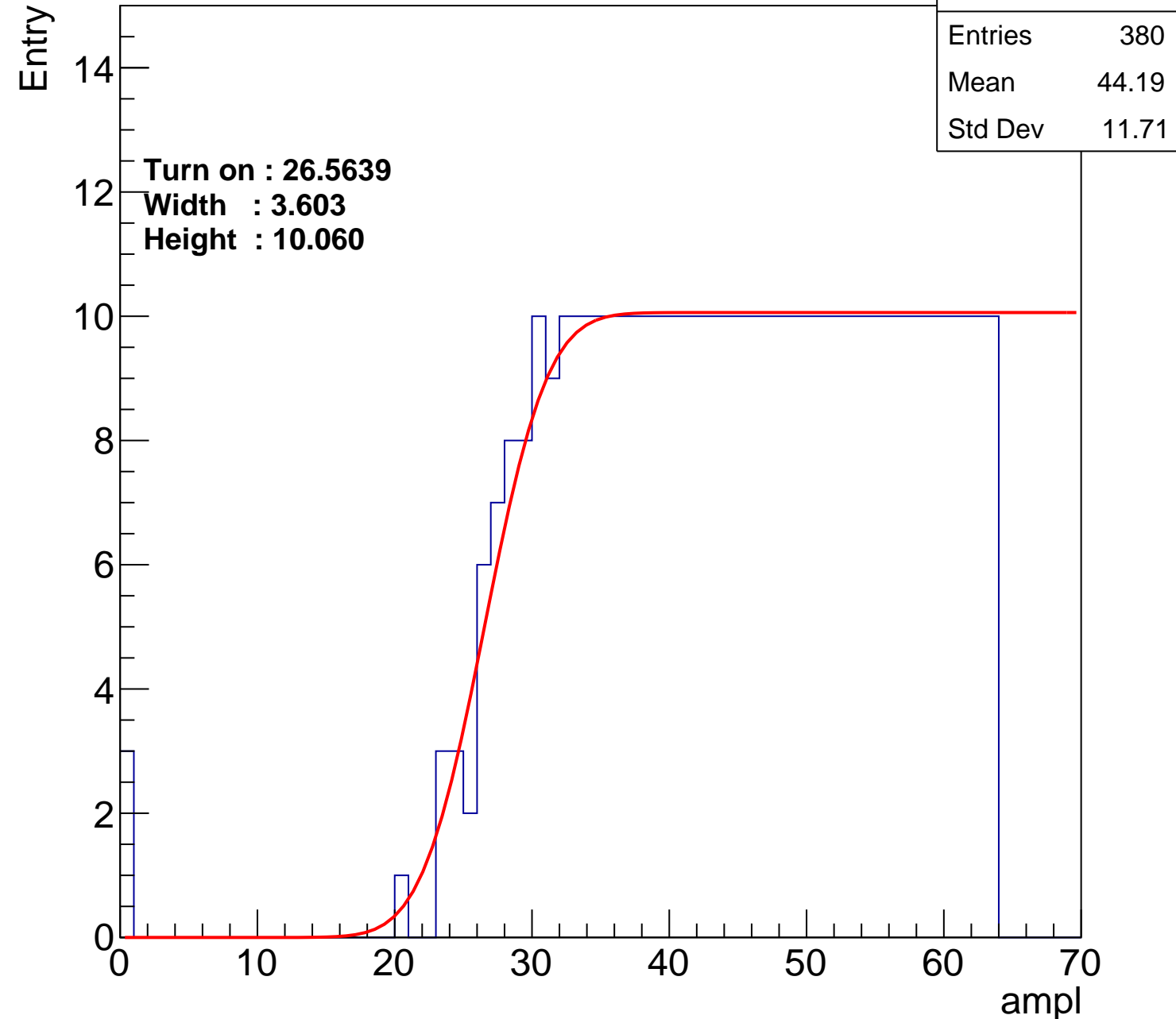
Width : 3.603

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.17
Std Dev	11.07

Turn on : 28.3019

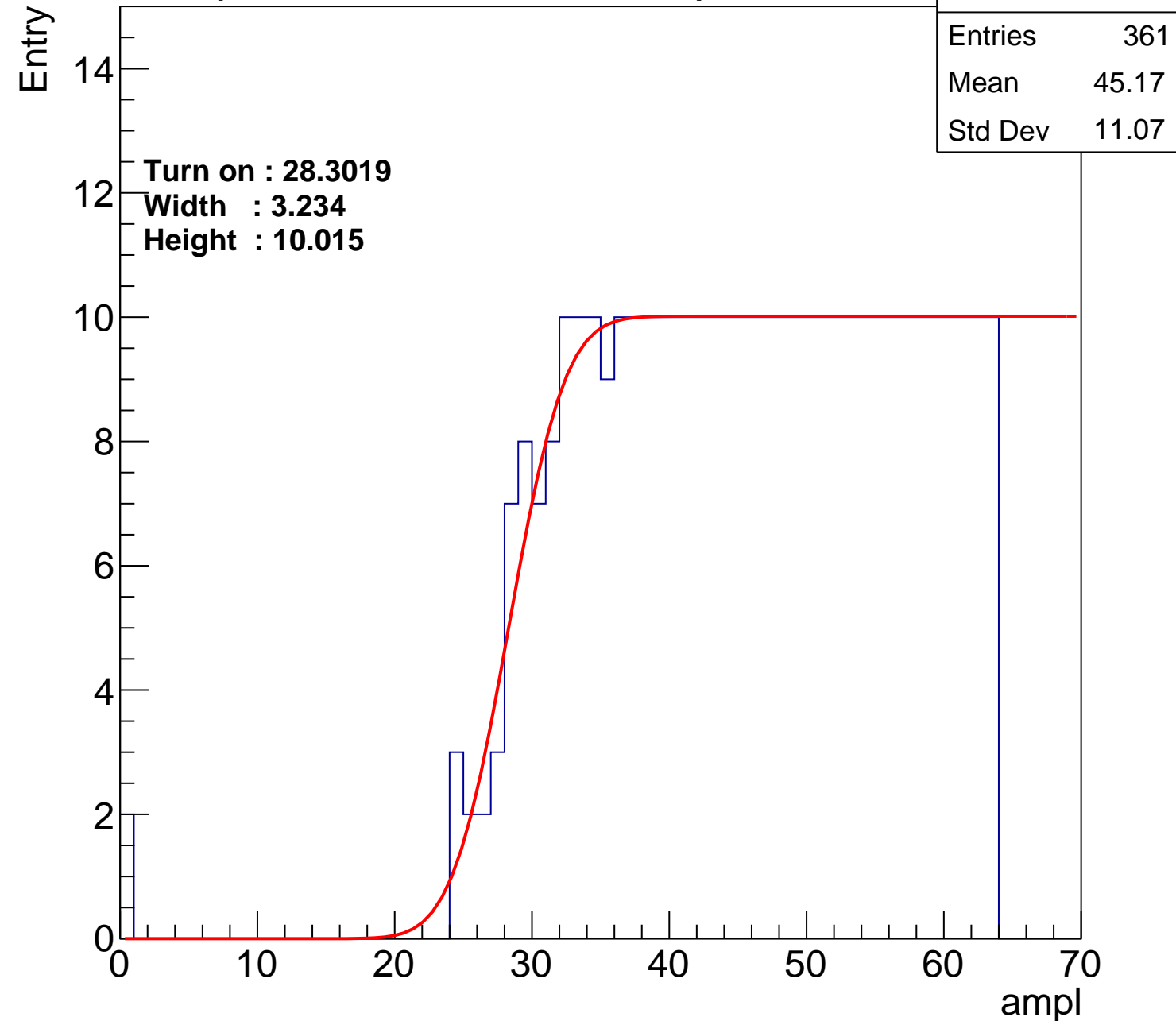
Width : 3.234

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.81
Std Dev	10.69

Turn on : 29.0725

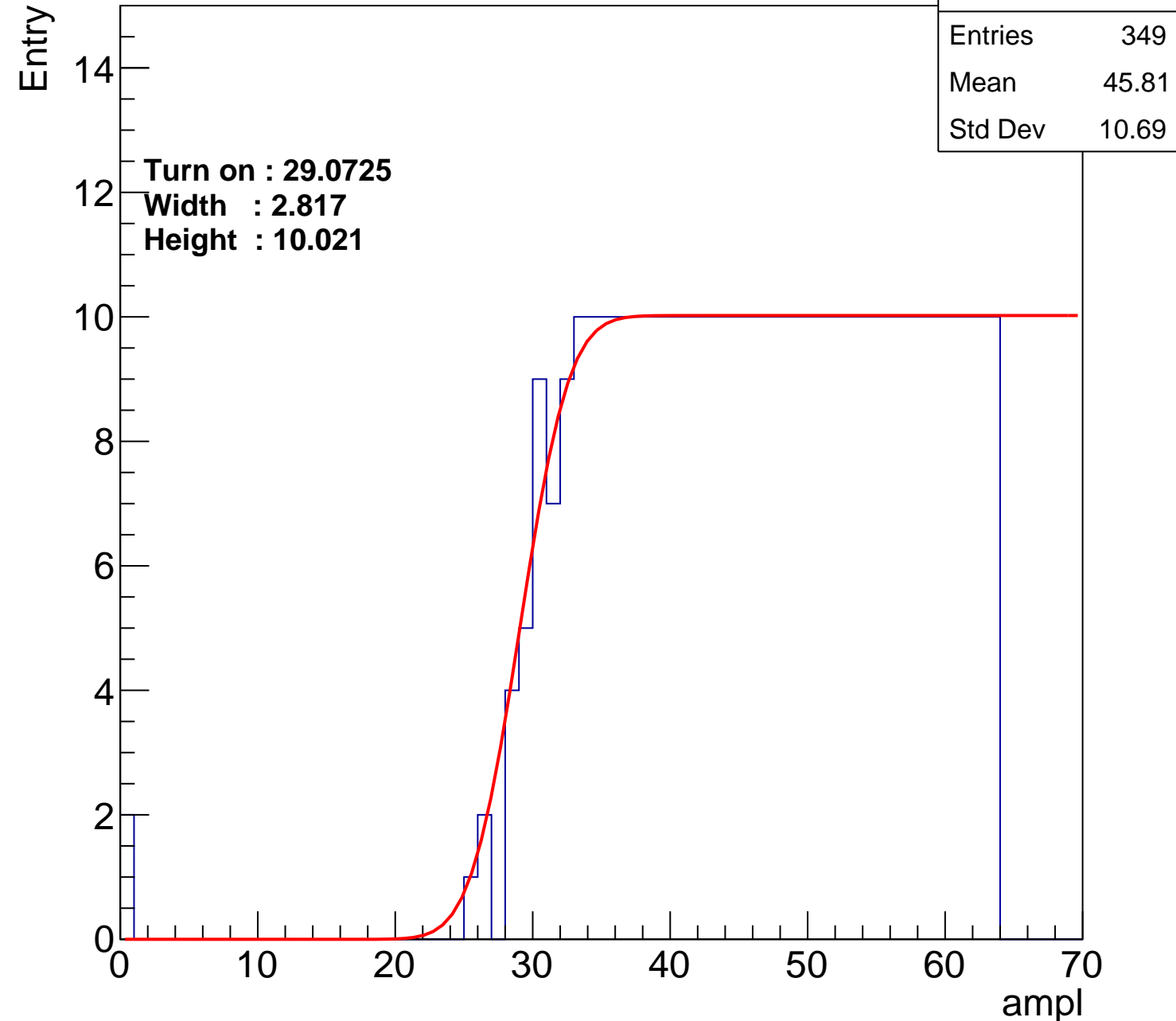
Width : 2.817

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.82
Std Dev	11.45

Turn on : 28.4909

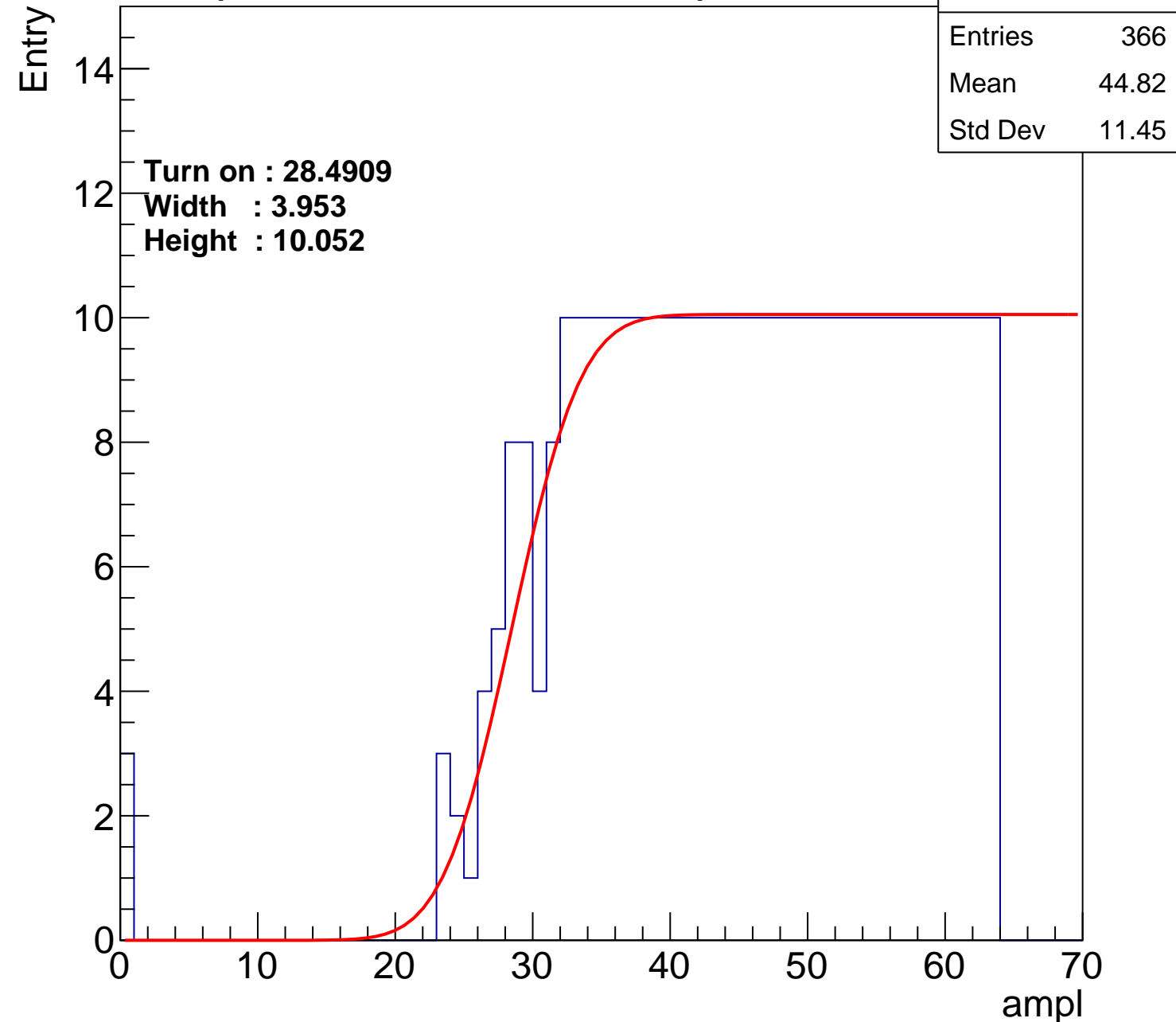
Width : 3.953

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch43

calib_packv5_042523_0143.root, FC#8, port C1

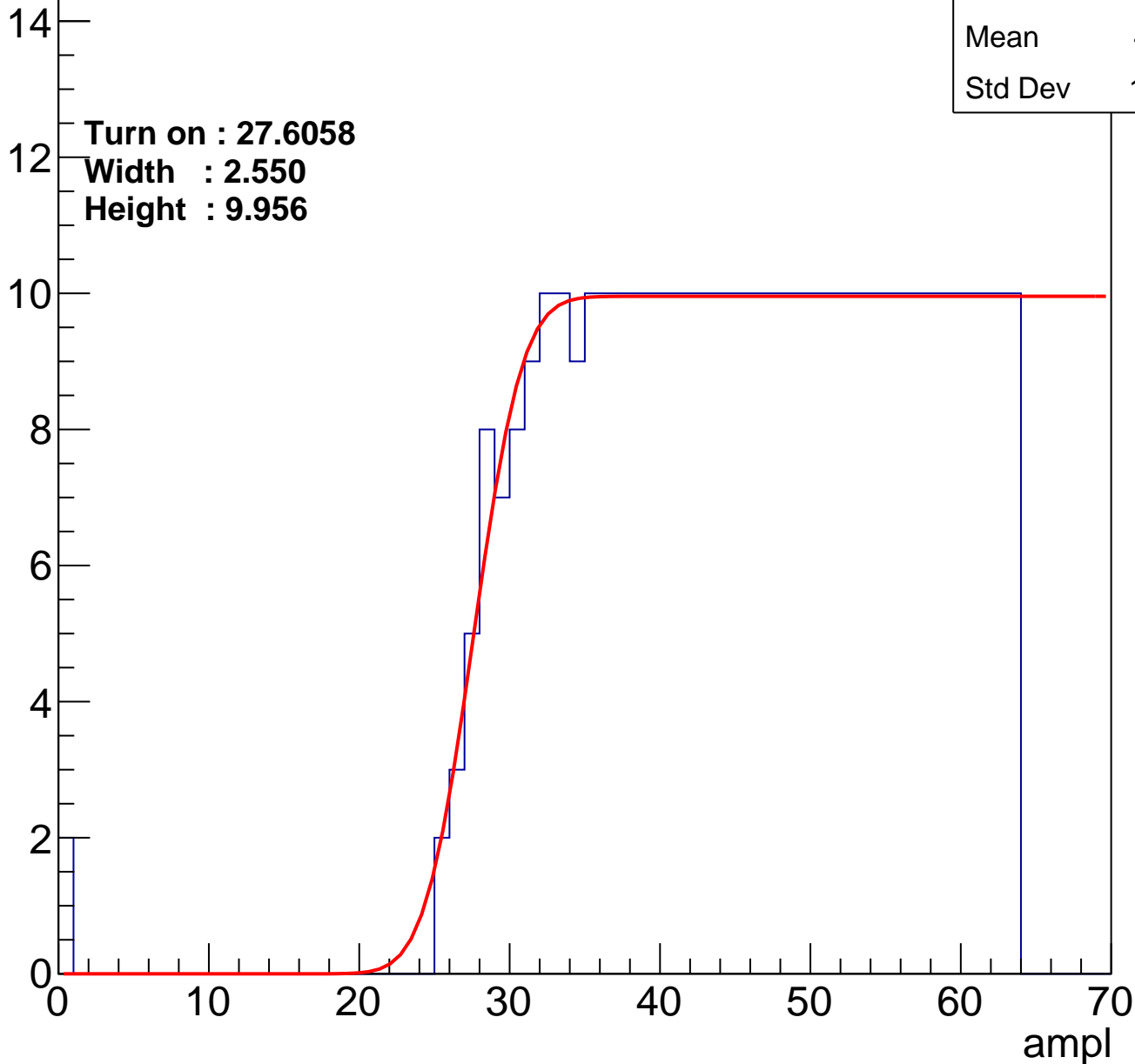
Entry

Entries	363
Mean	45.11
Std Dev	11.06

Turn on : 27.6058

Width : 2.550

Height : 9.956



B0L002S, U5-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	388
Mean	43.78
Std Dev	11.93

Turn on : 25.9866

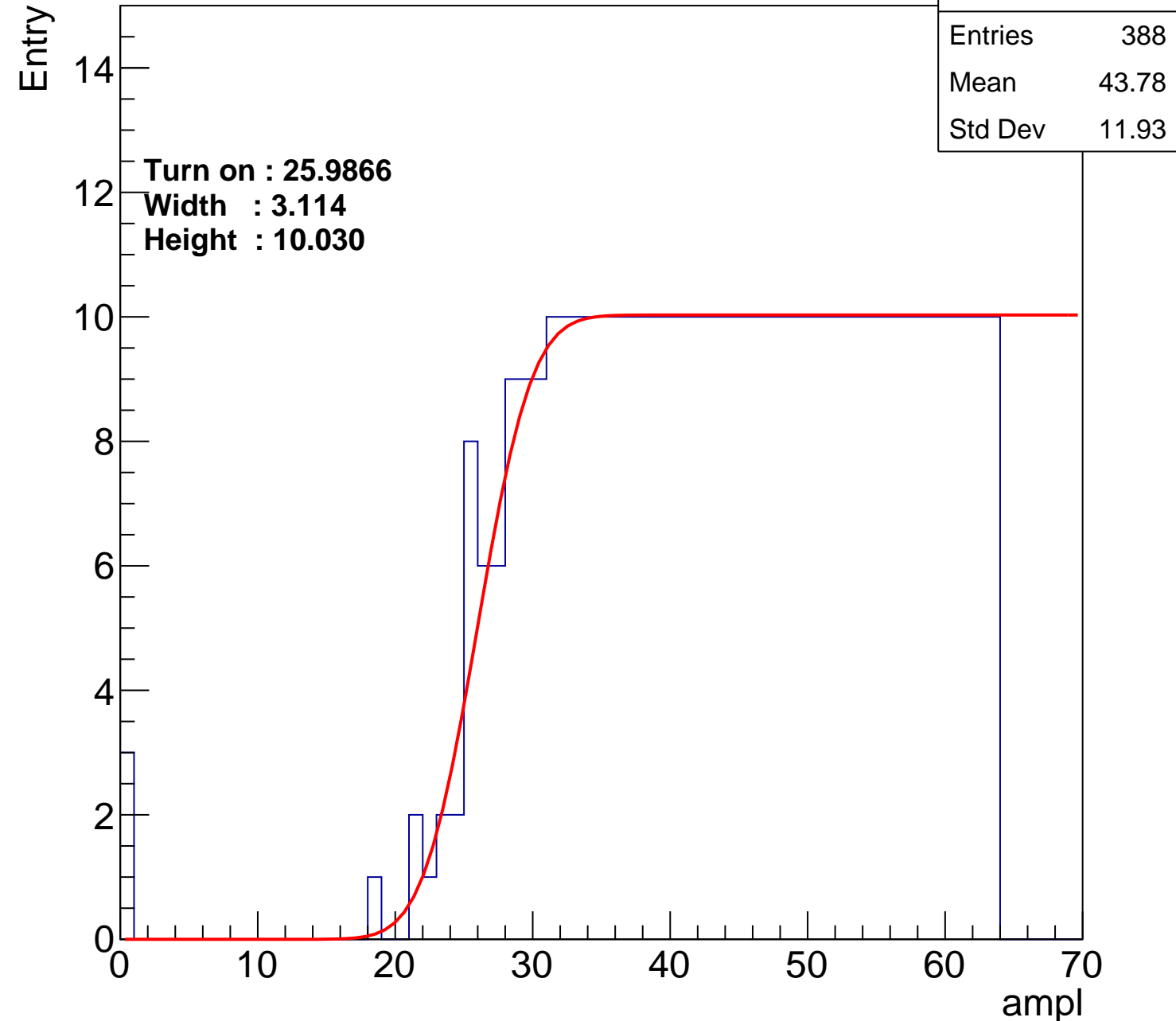
Width : 3.114

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.3860

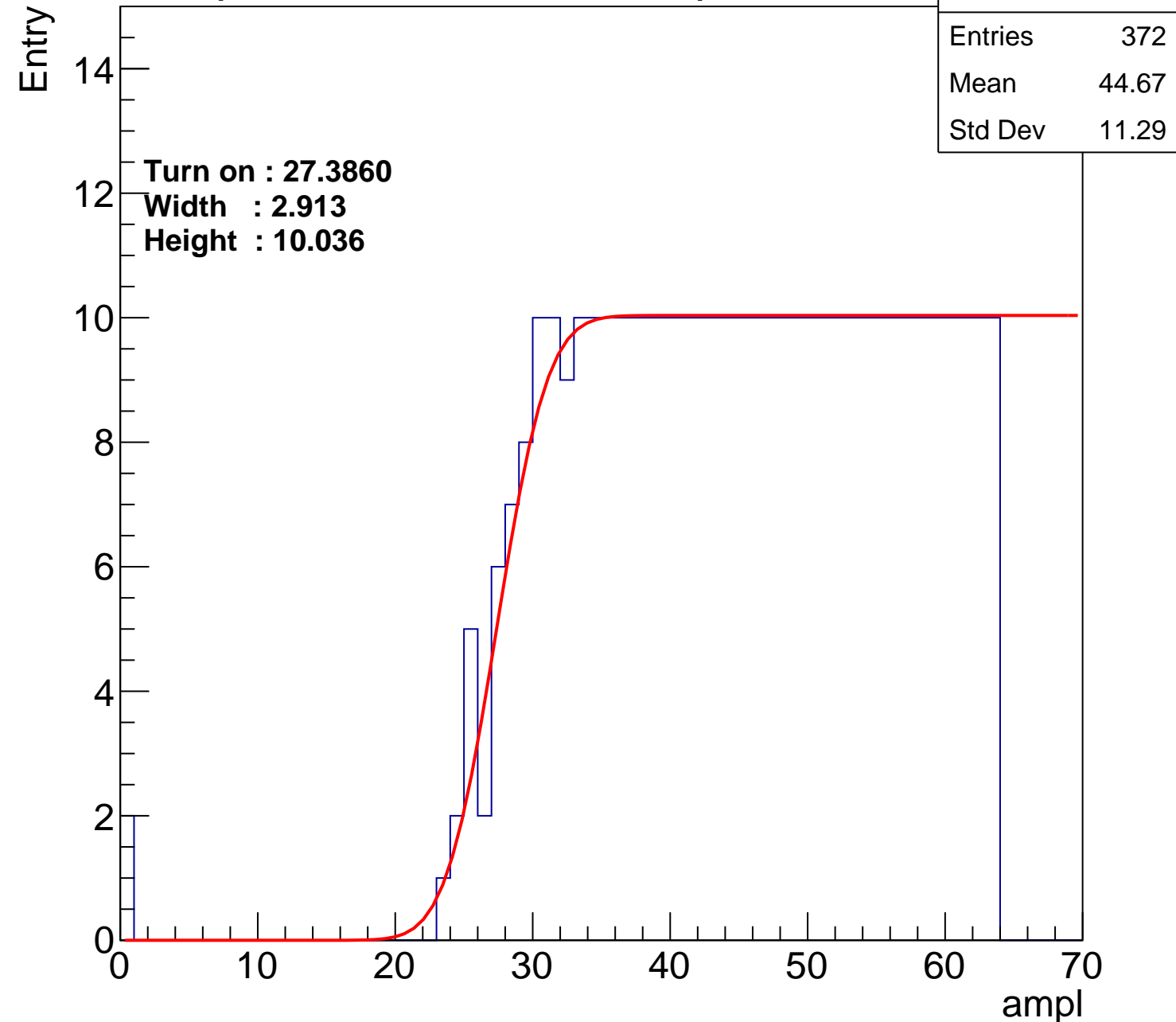
Width : 2.913

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.31
Std Dev	12.12

Turn on : 27.8174

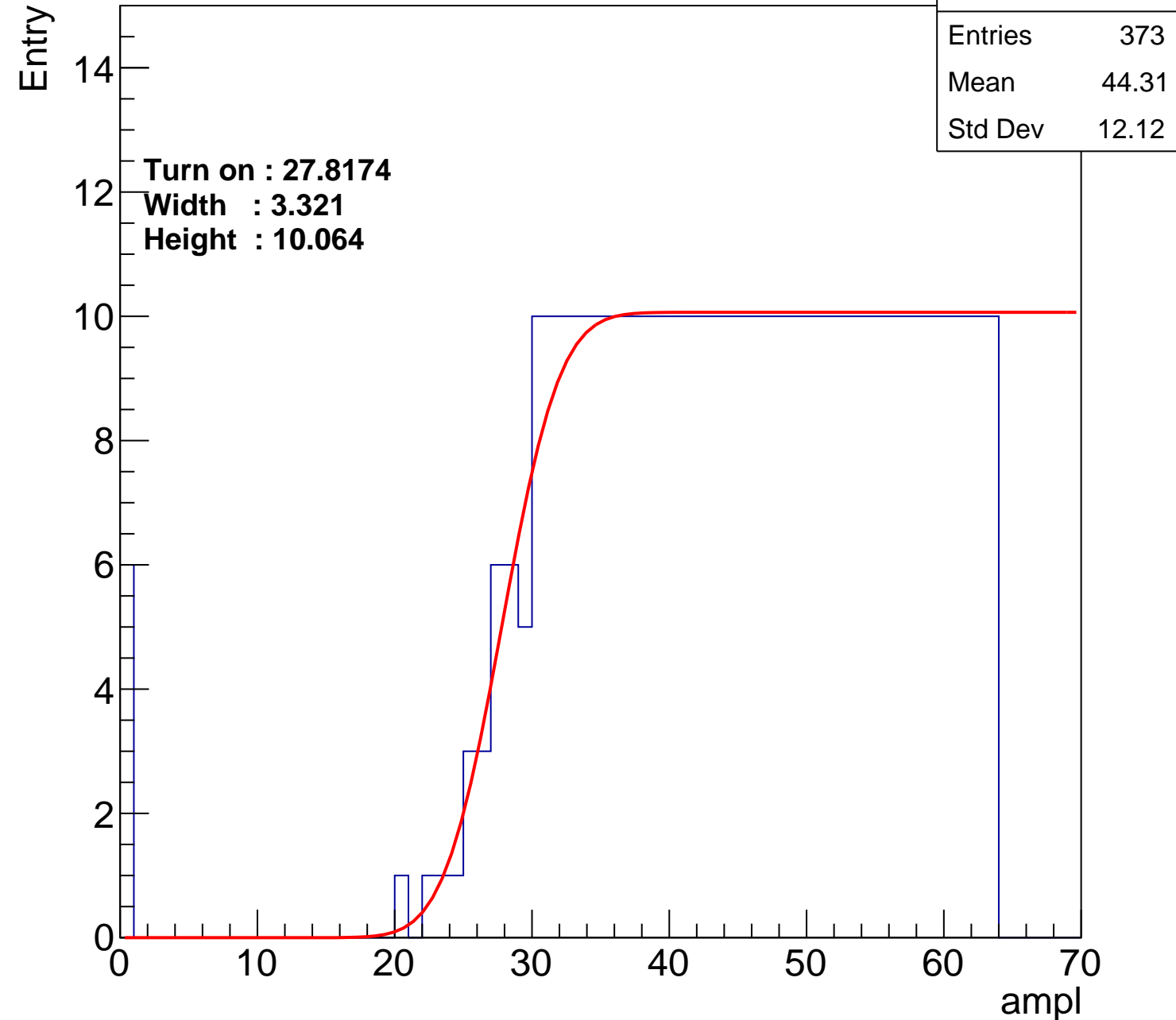
Width : 3.321

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.3449

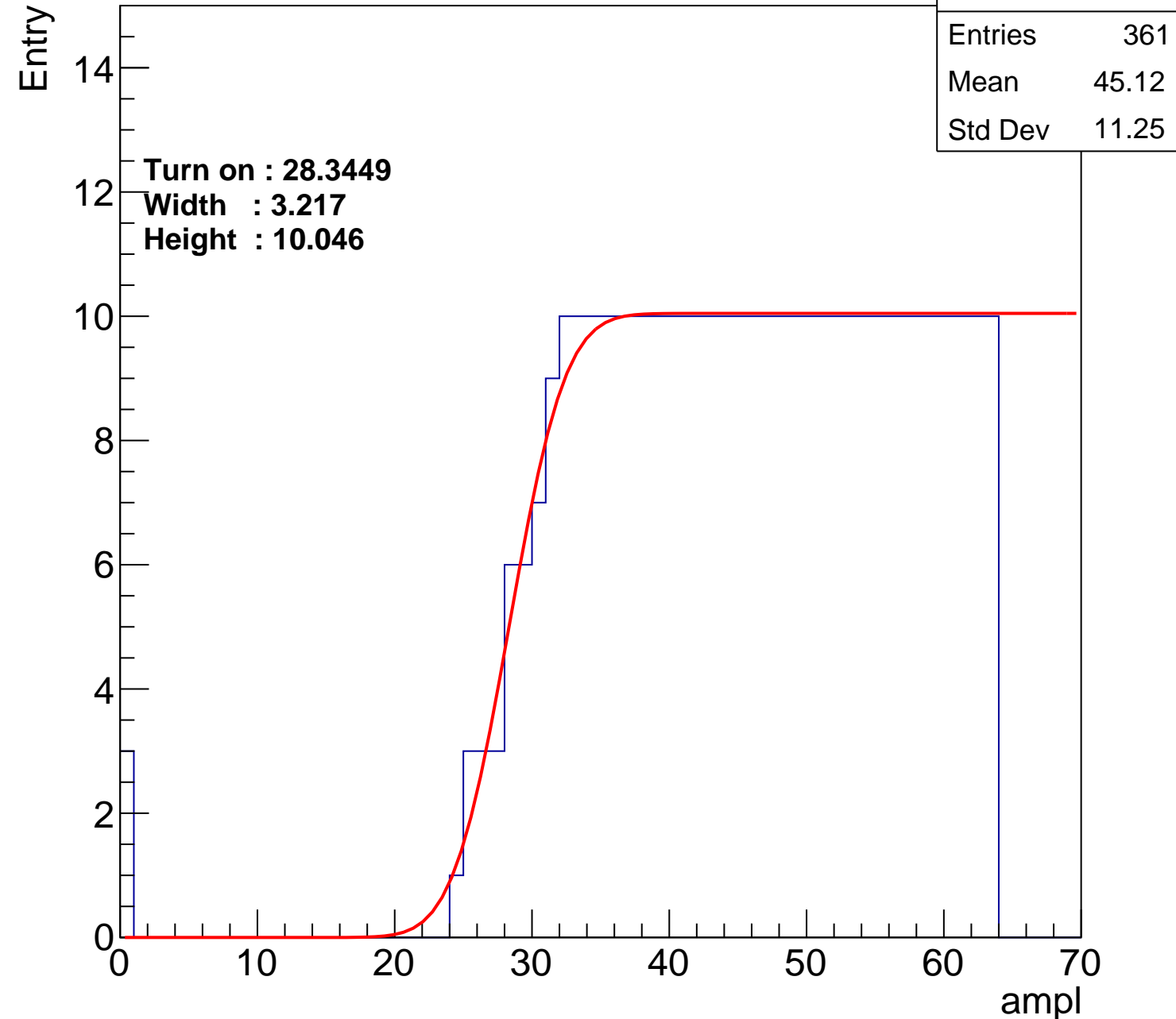
Width : 3.217

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.99
Std Dev	11.15

Turn on : 27.8487

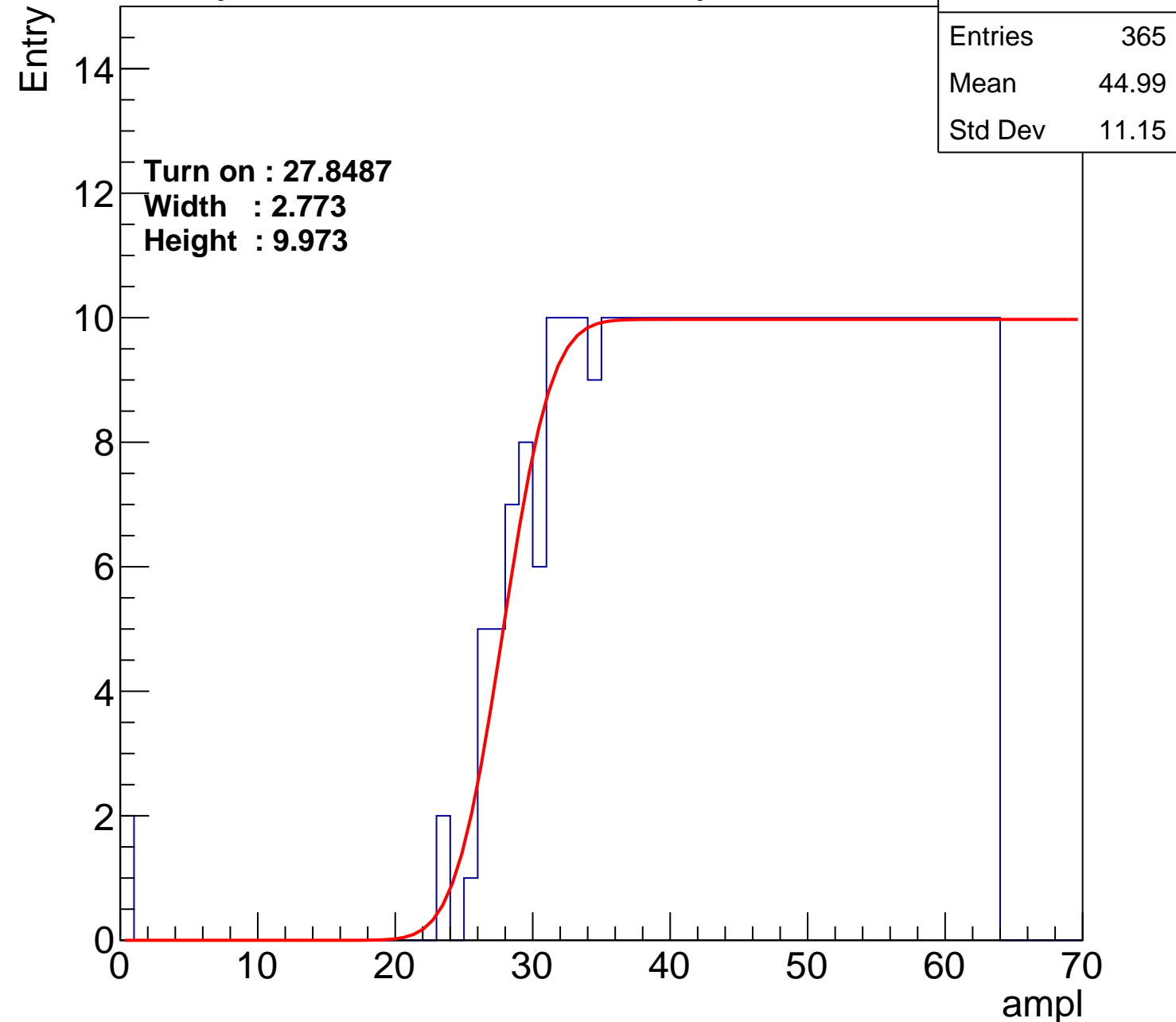
Width : 2.773

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.46
Std Dev	10.93

Turn on : 29.1587

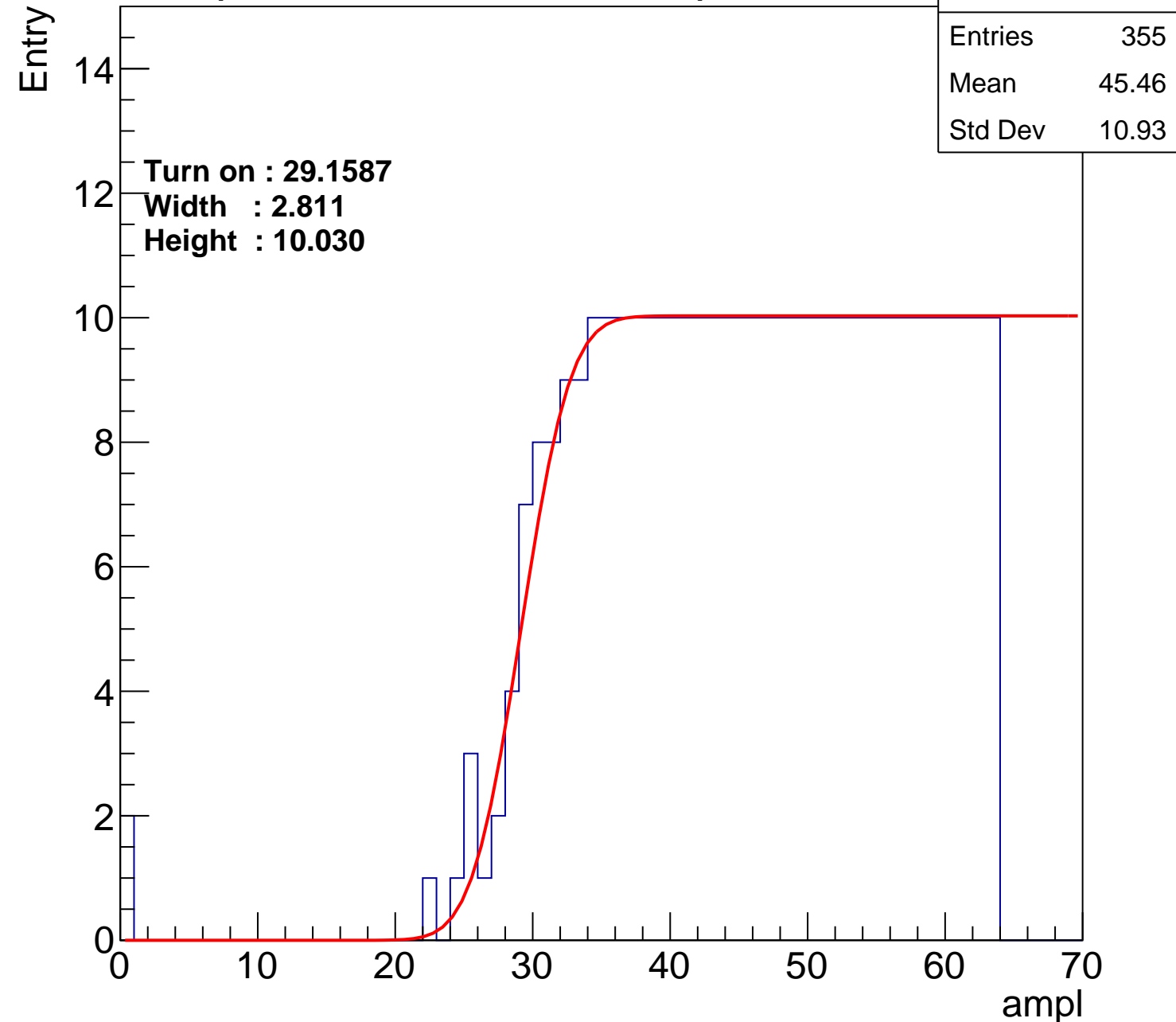
Width : 2.811

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.69
Std Dev	11.45

Turn on : 26.6193

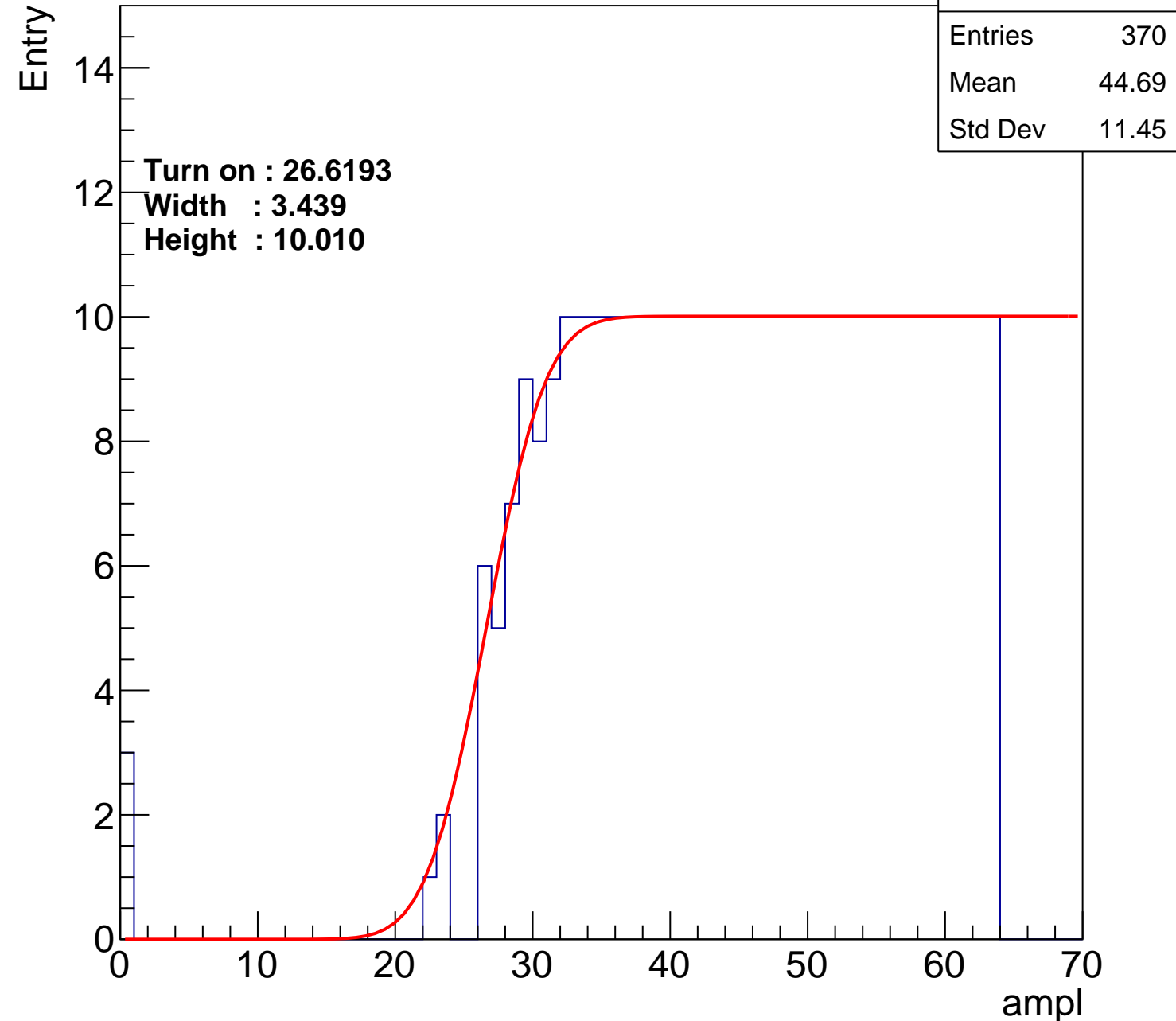
Width : 3.439

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	336
Mean	46.4
Std Dev	10.46

Turn on : 31.0547

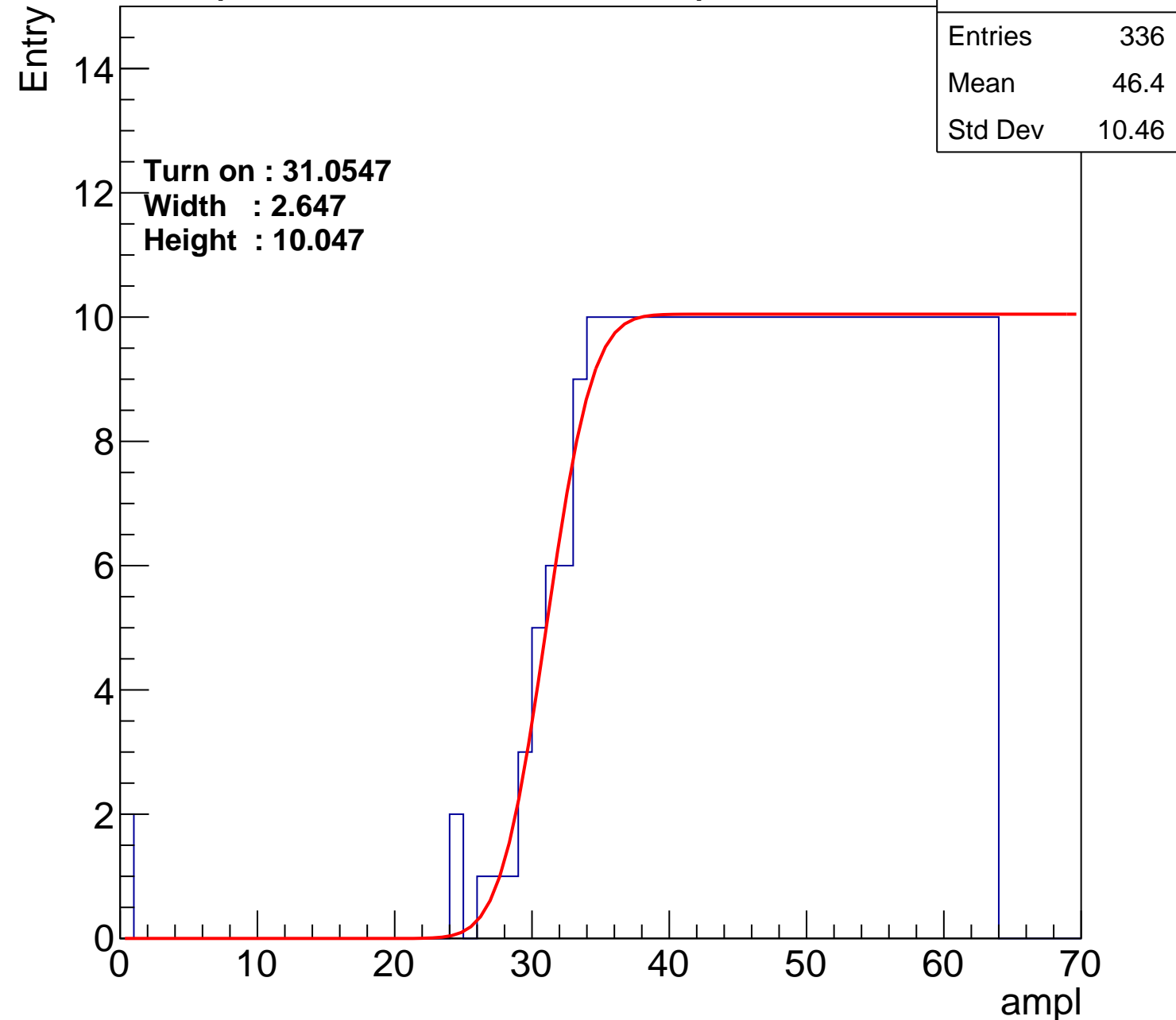
Width : 2.647

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.4671

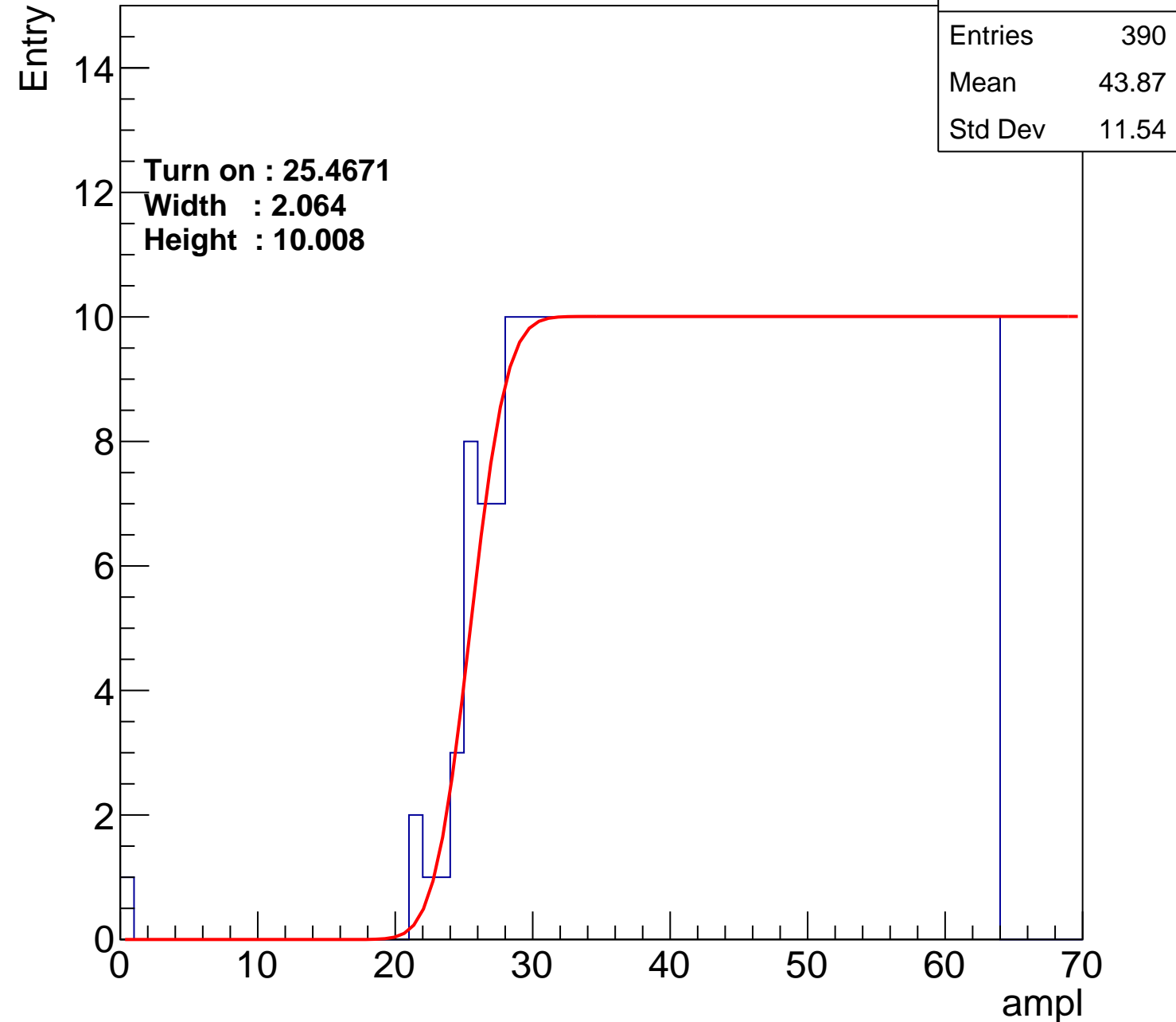
Width : 2.064

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch53

calib_packv5_042523_0143.root, FC#8, port C1

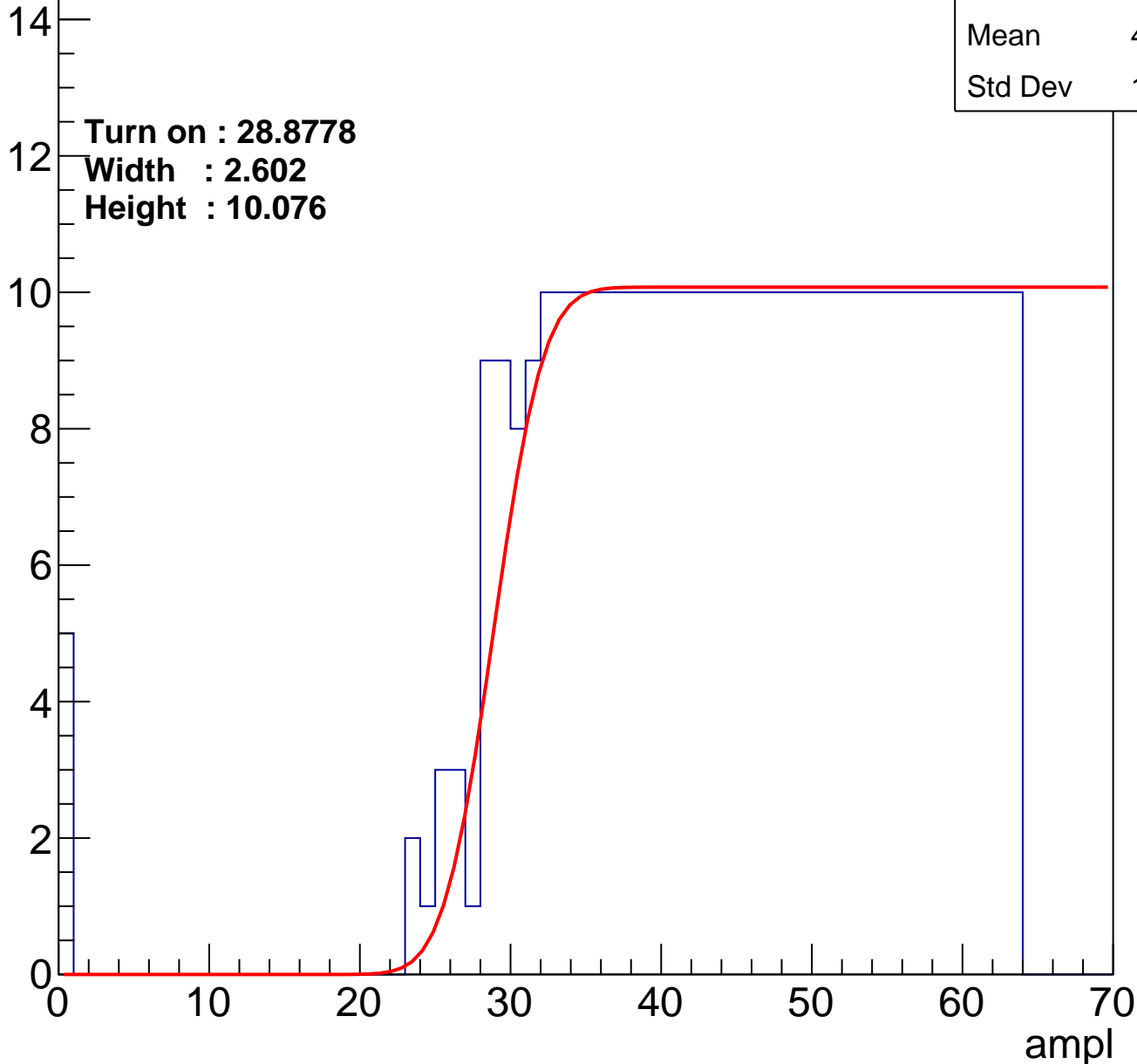
Entry

Entries	370
Mean	44.55
Std Dev	11.83

Turn on : 28.8778

Width : 2.602

Height : 10.076



B0L002S, U5-ch54

calib_packv5_042523_0143.root, FC#8, port C1

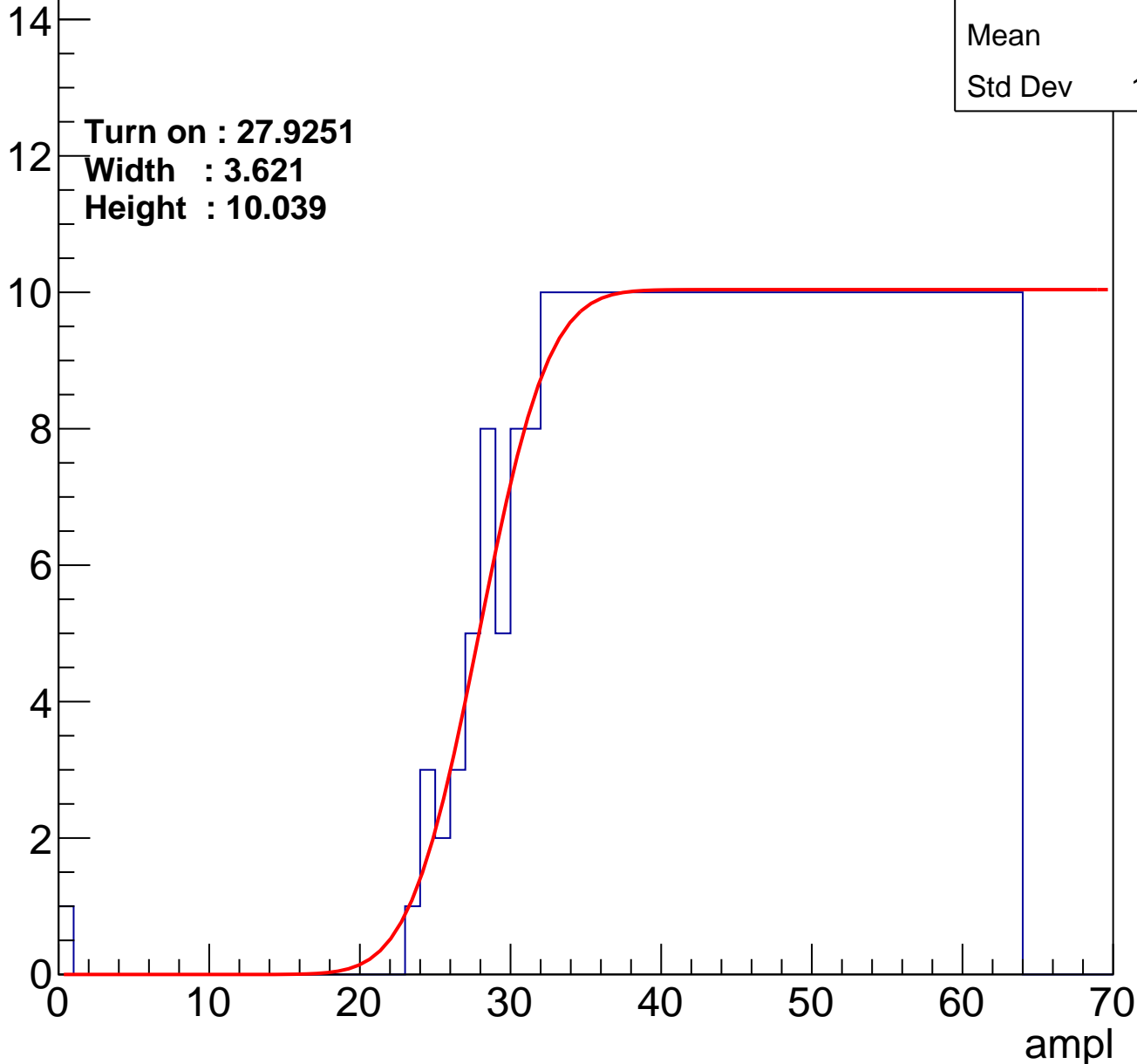
Entries	364
Mean	45.1
Std Dev	10.94

Turn on : 27.9251

Width : 3.621

Height : 10.039

Entry



B0L002S, U5-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.34
Std Dev	10.76

Turn on : 28.3763

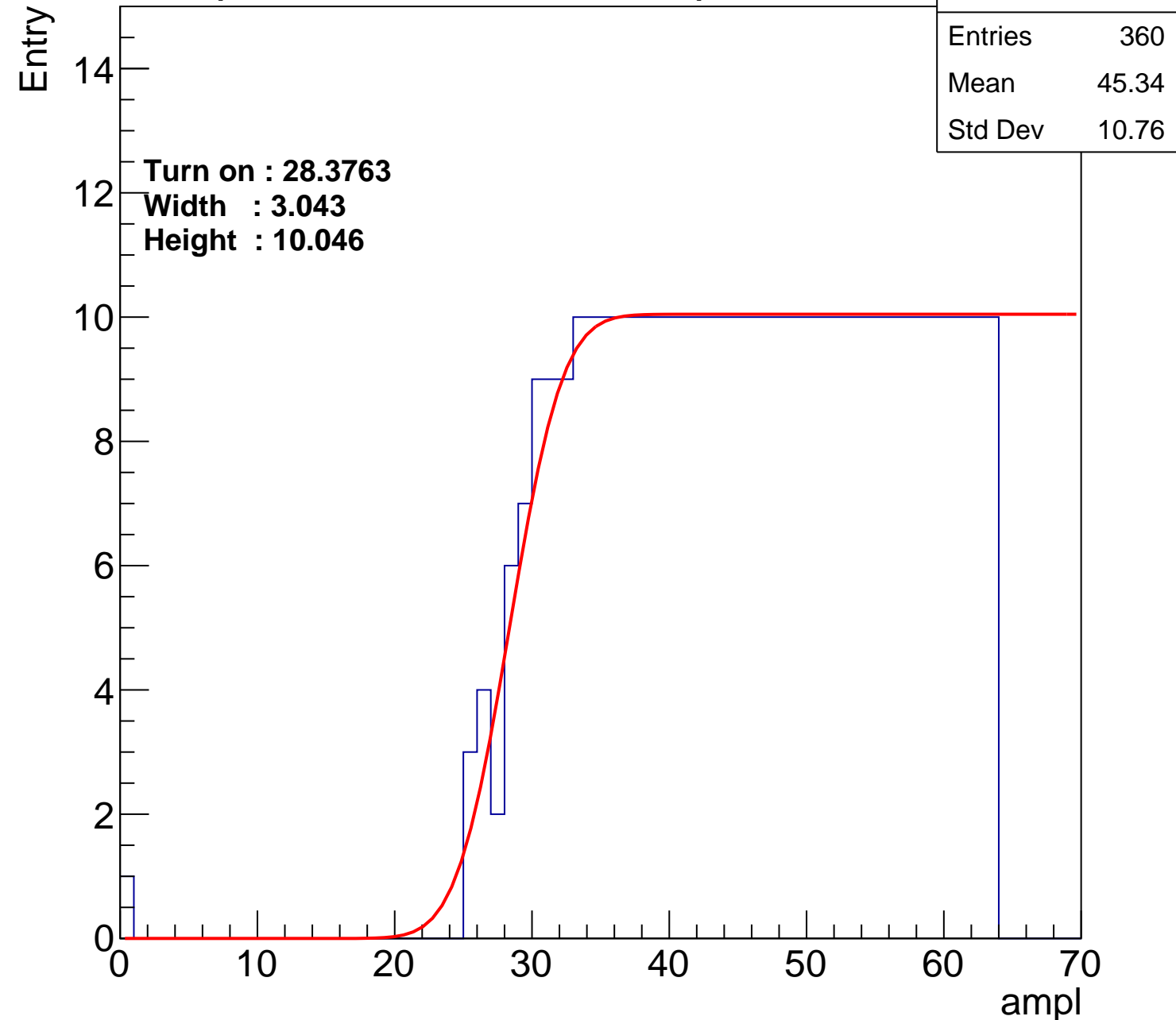
Width : 3.043

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.12
Std Dev	11.25

Turn on : 28.5668

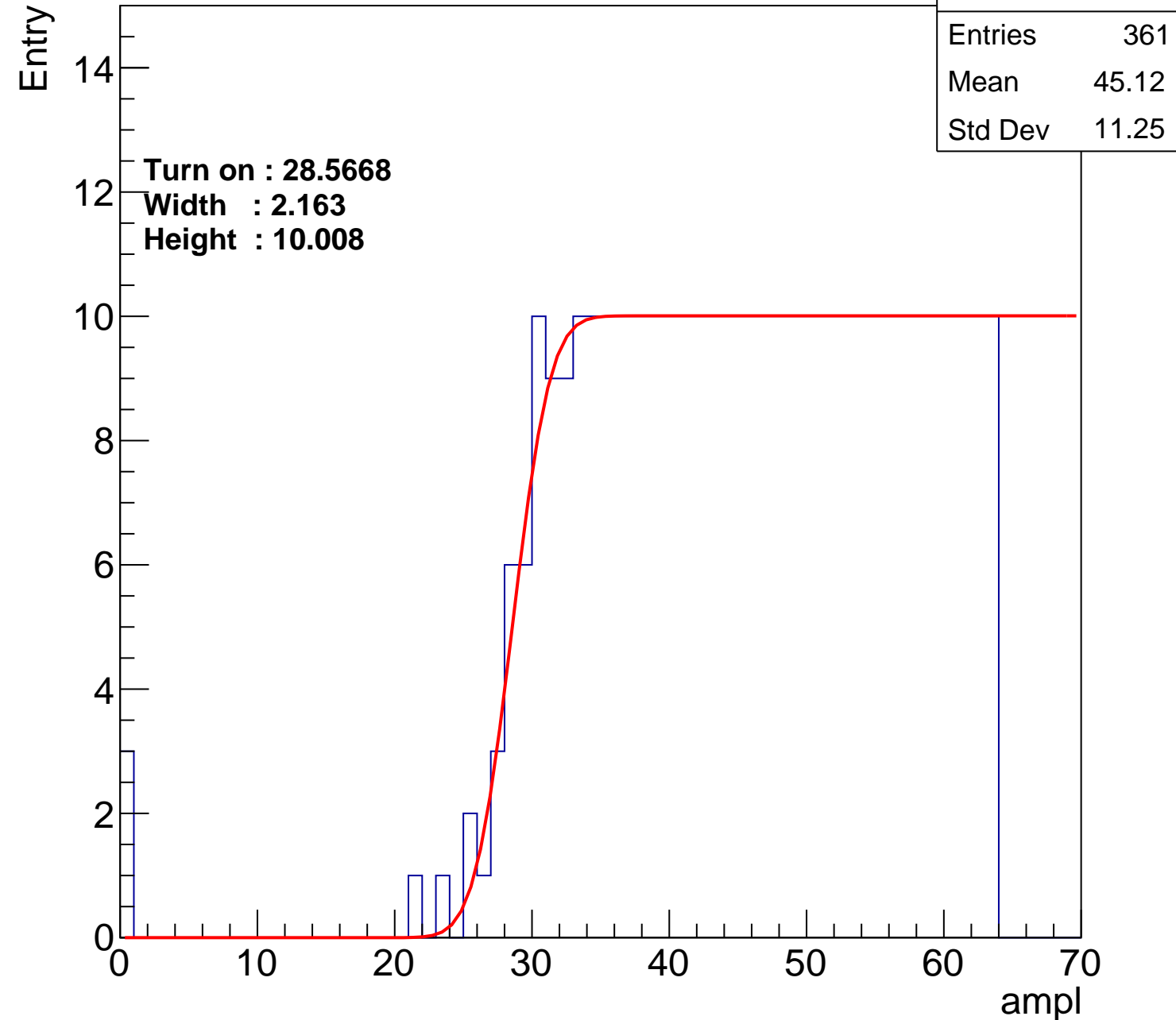
Width : 2.163

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.7764

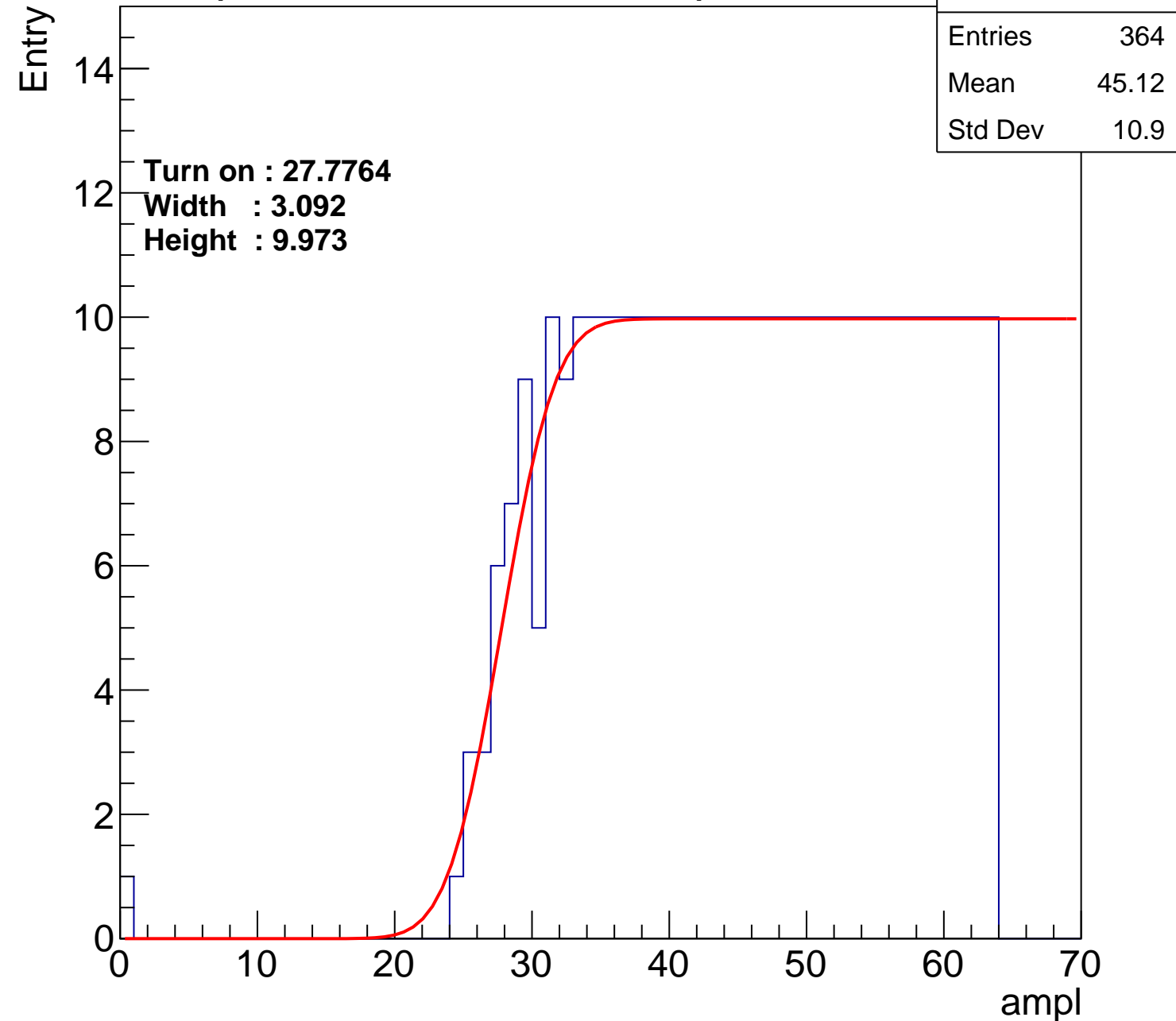
Width : 3.092

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.72
Std Dev	10.58

Turn on : 29.4672

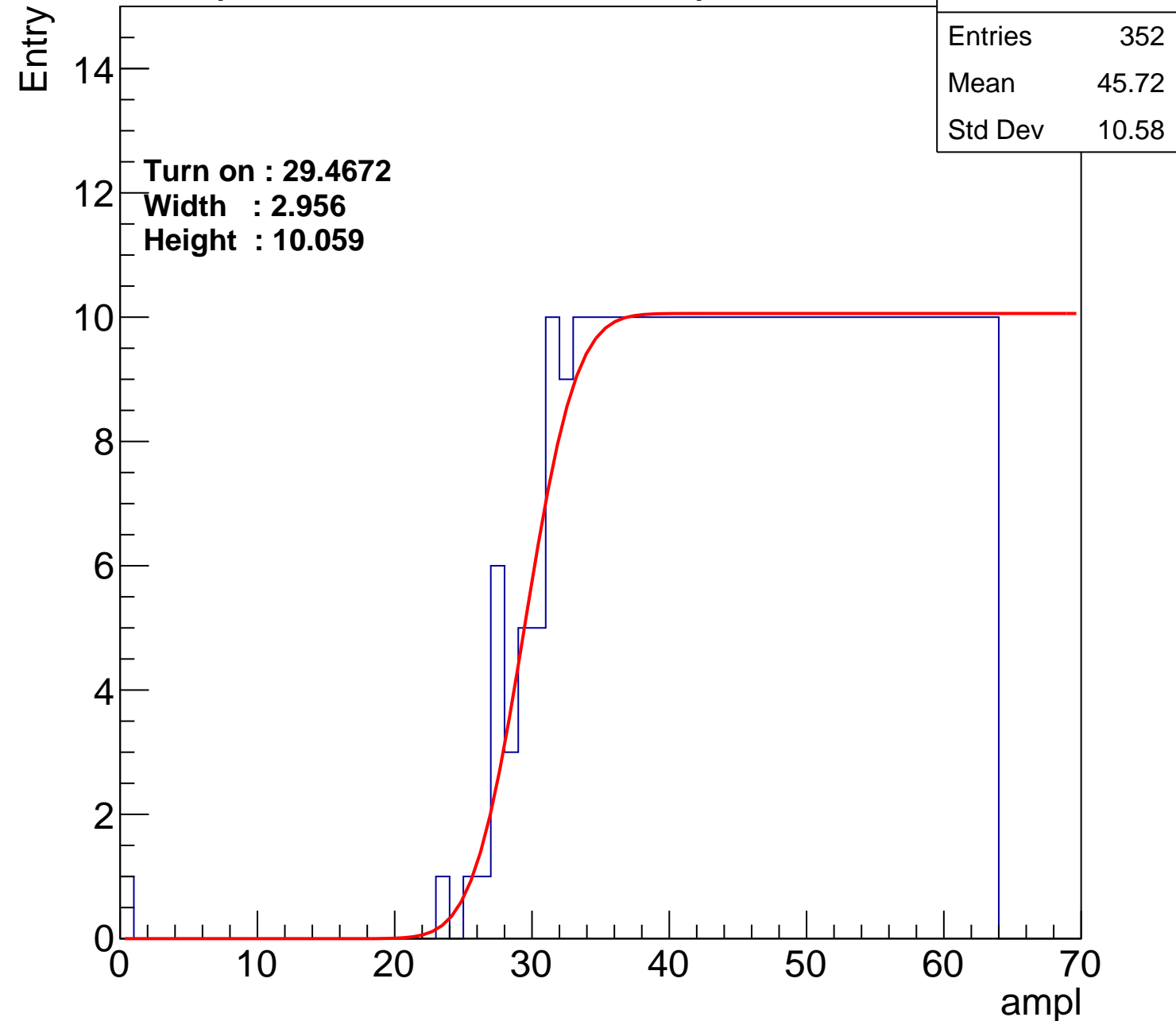
Width : 2.956

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	44.88
Std Dev	11.55

Turn on : 28.3479

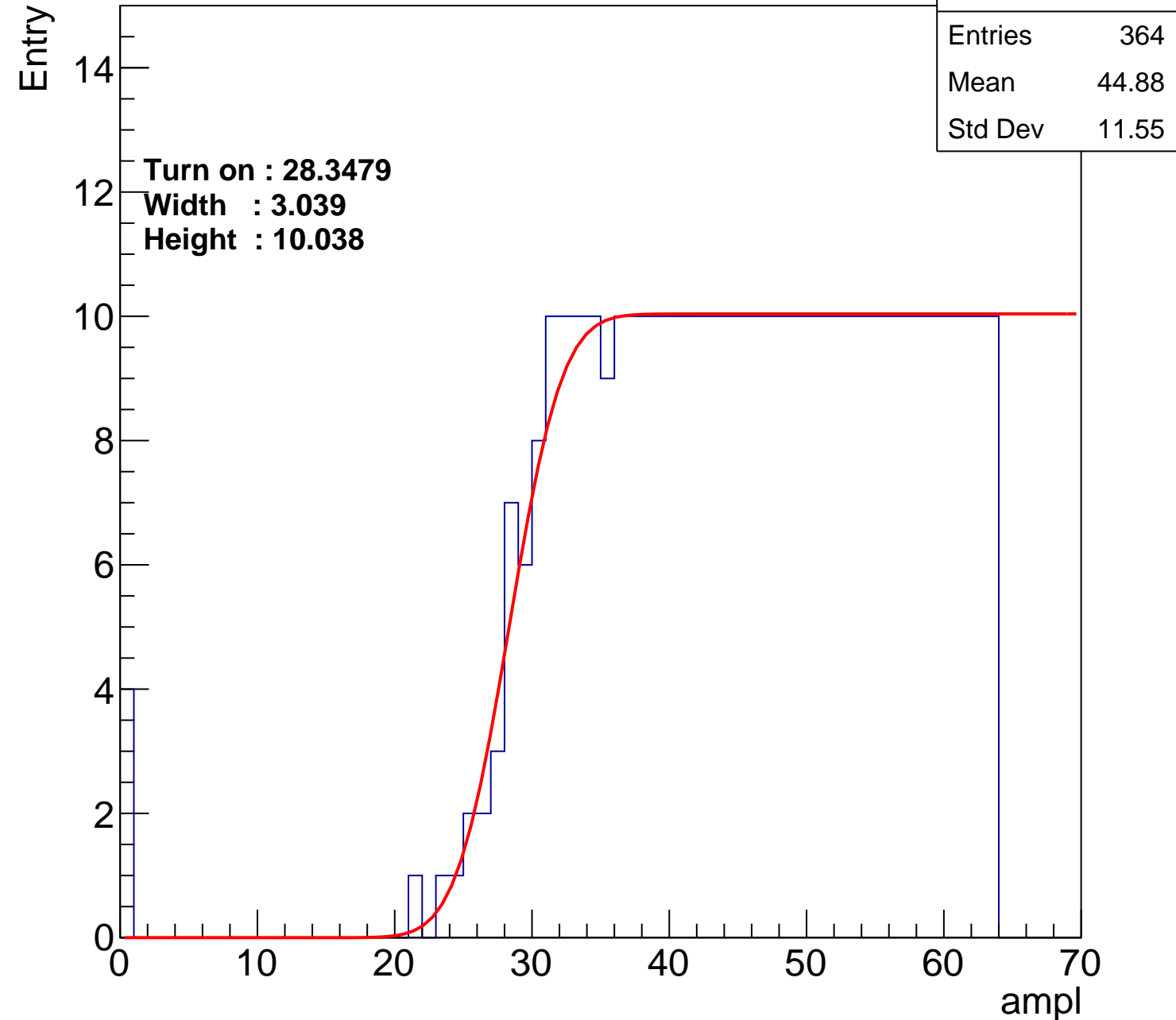
Width : 3.039

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch60

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.48
Std Dev	11.88

Turn on : 27.3594

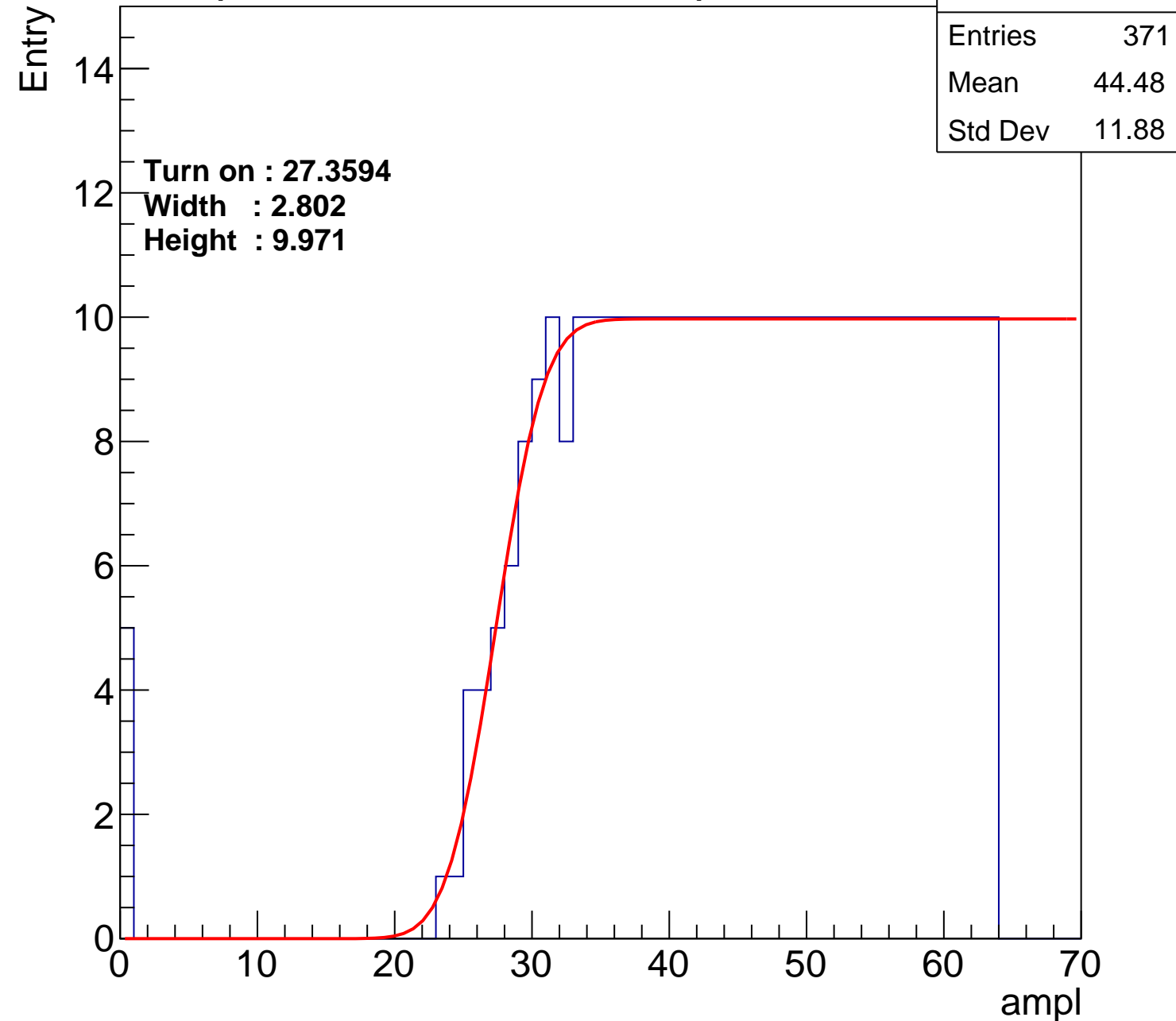
Width : 2.802

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch61

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.6929

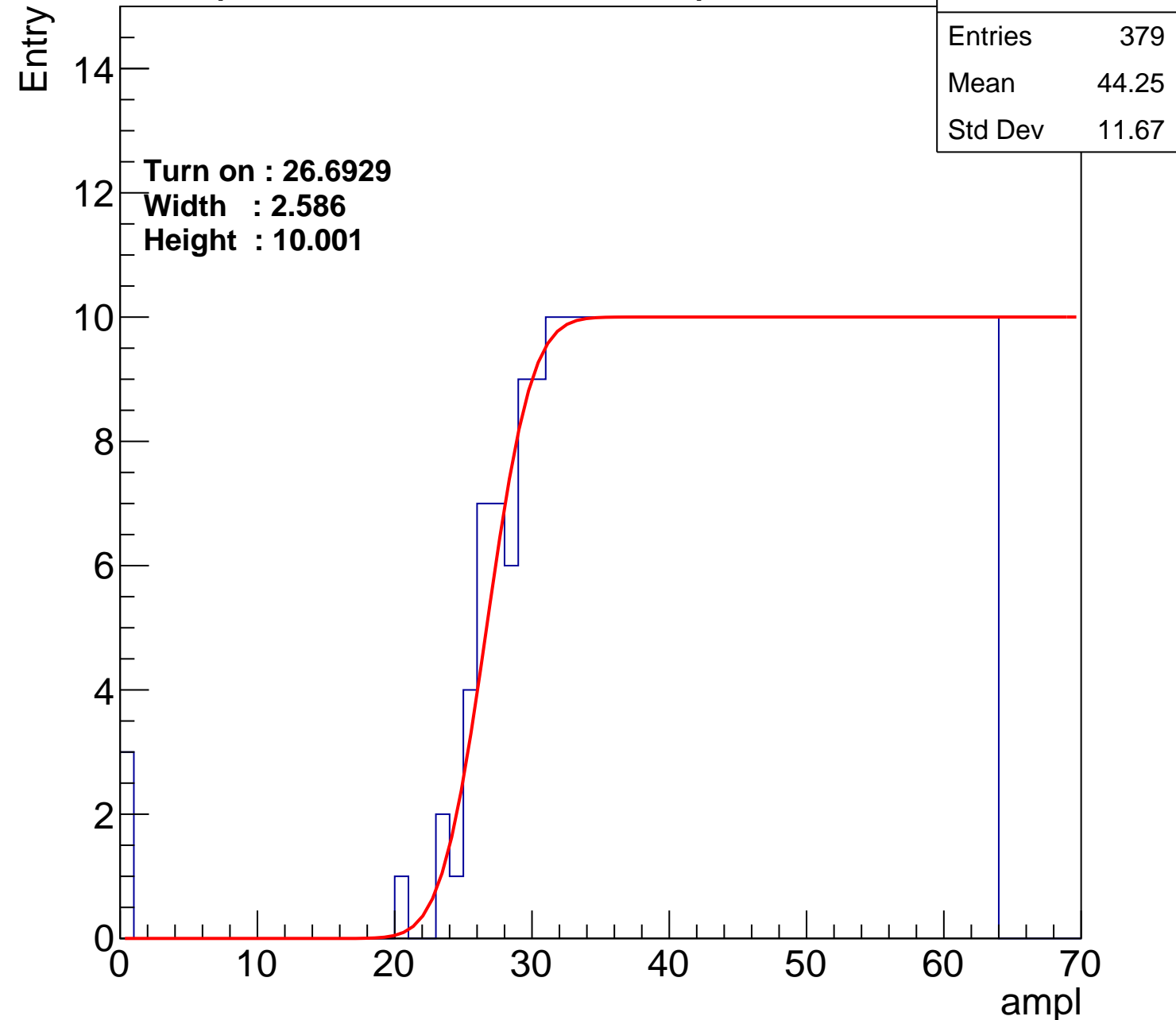
Width : 2.586

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.5067

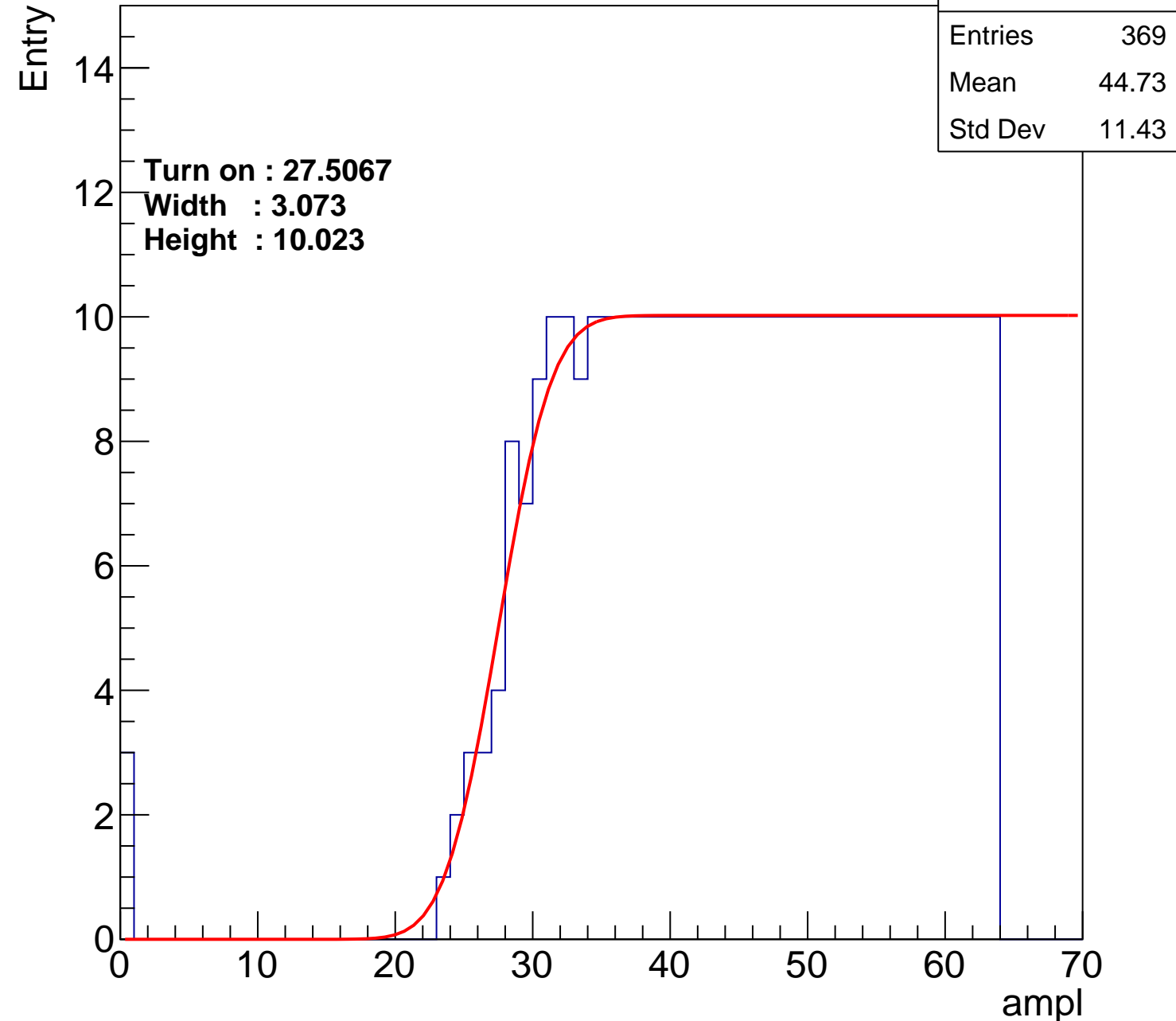
Width : 3.073

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.61
Std Dev	11.53

Turn on : 28.1761

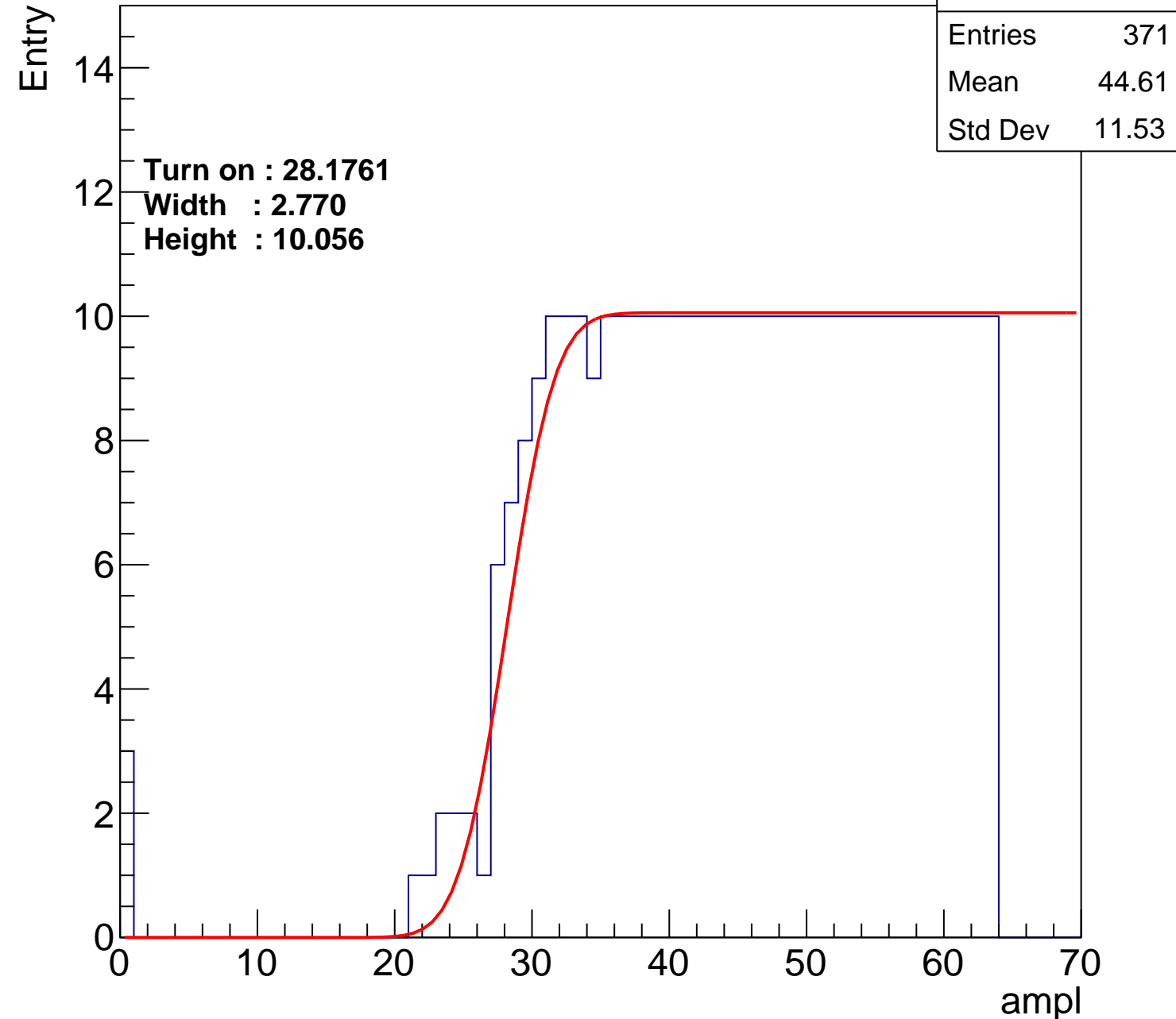
Width : 2.770

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.83

Turn on : 27.8065

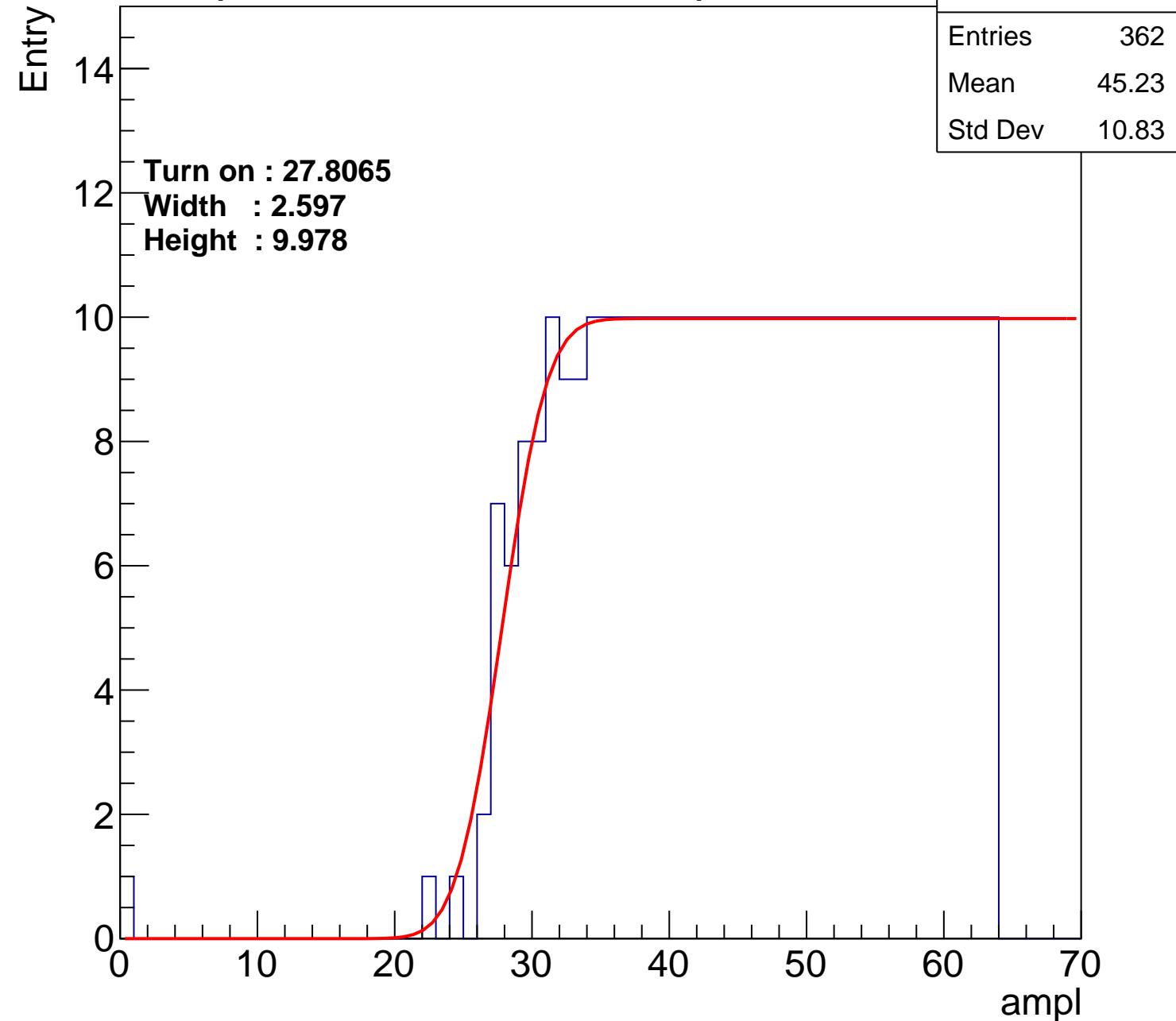
Width : 2.597

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch65

calib_packv5_042523_0143.root, FC#8, port C1

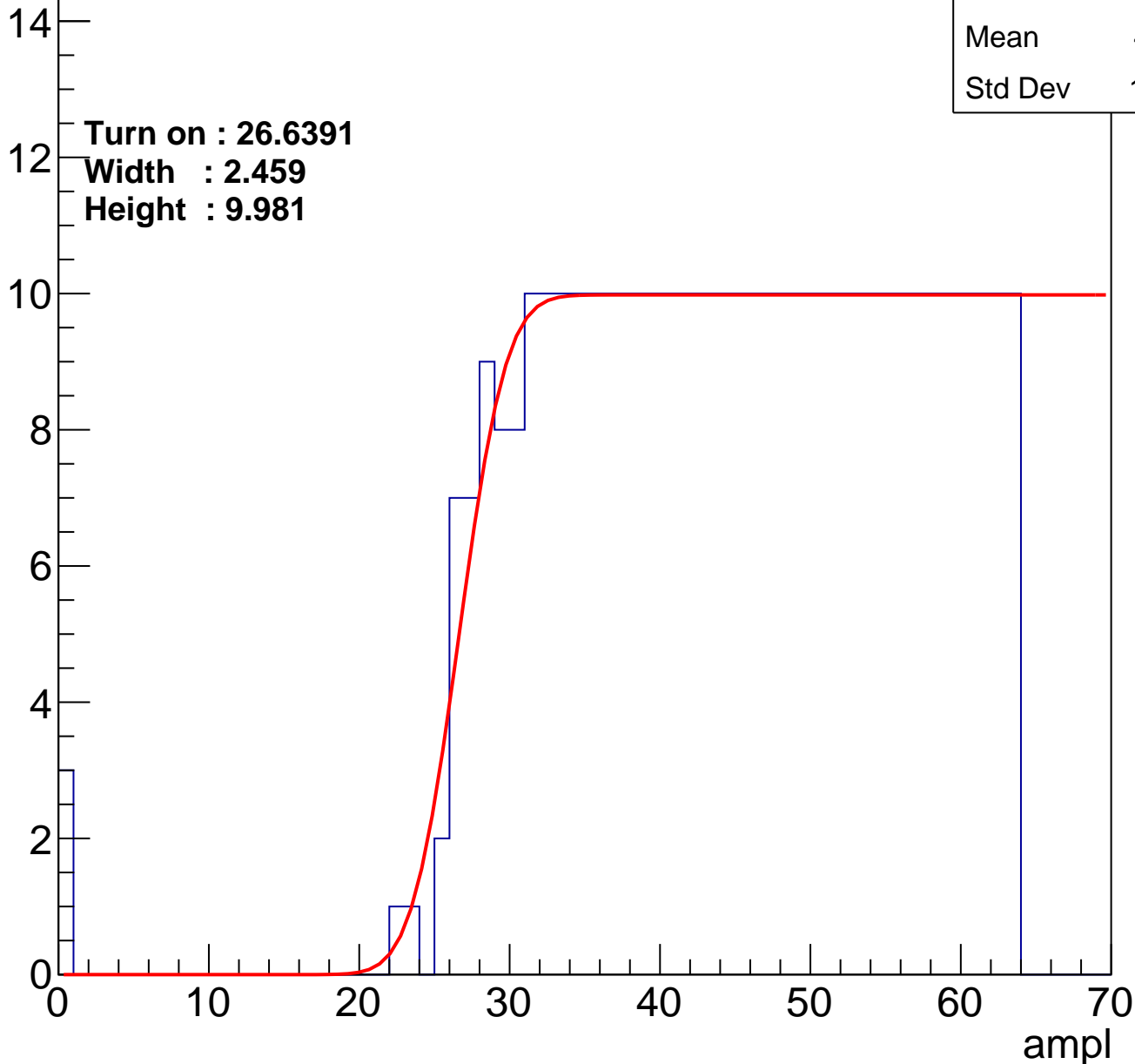
Entry

Entries	376
Mean	44.41
Std Dev	11.56

Turn on : 26.6391

Width : 2.459

Height : 9.981



B0L002S, U5-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	43.79
Std Dev	12.2

Turn on : 26.3719

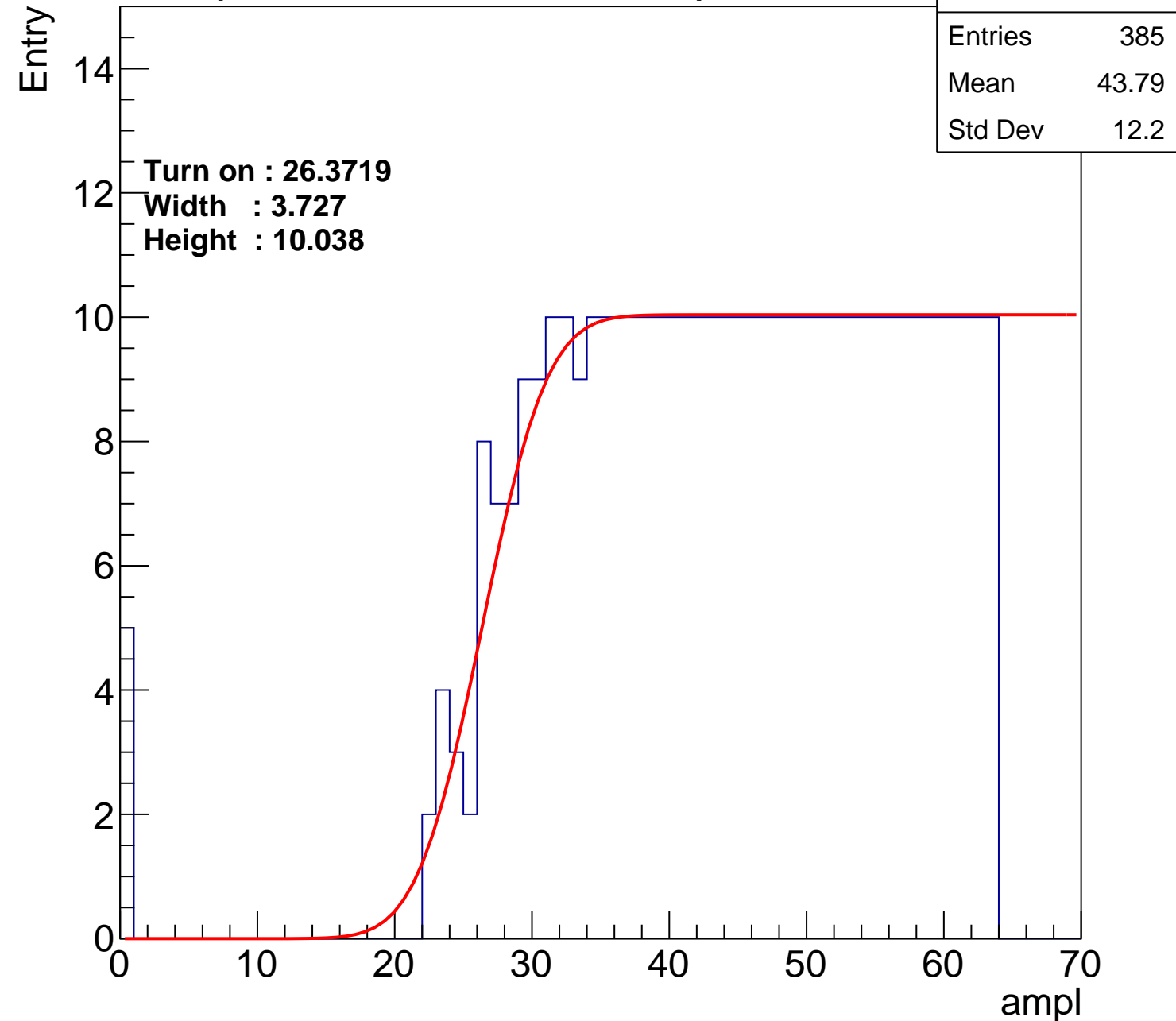
Width : 3.727

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 28.0837

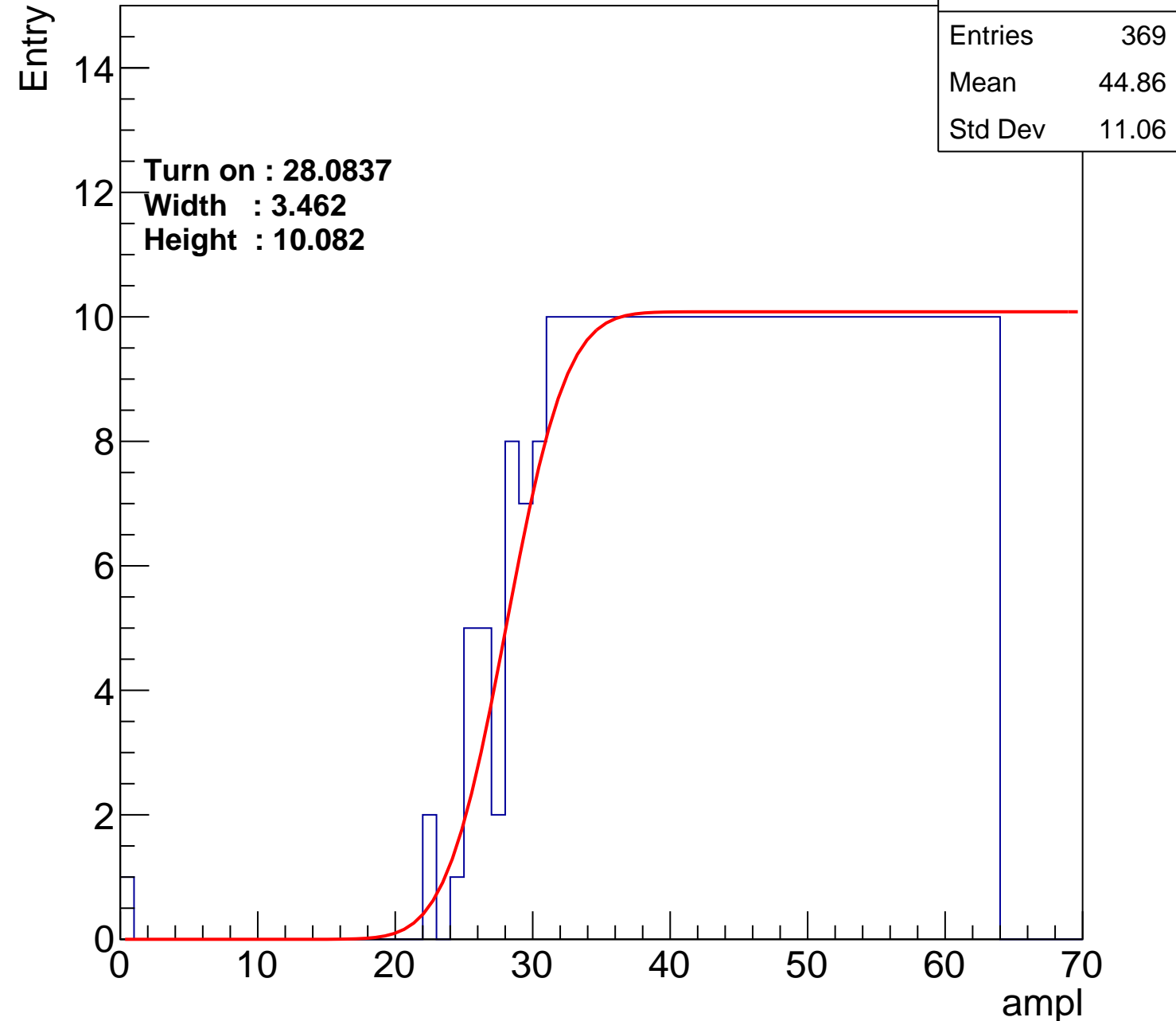
Width : 3.462

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch68

calib_packv5_042523_0143.root, FC#8, port C1

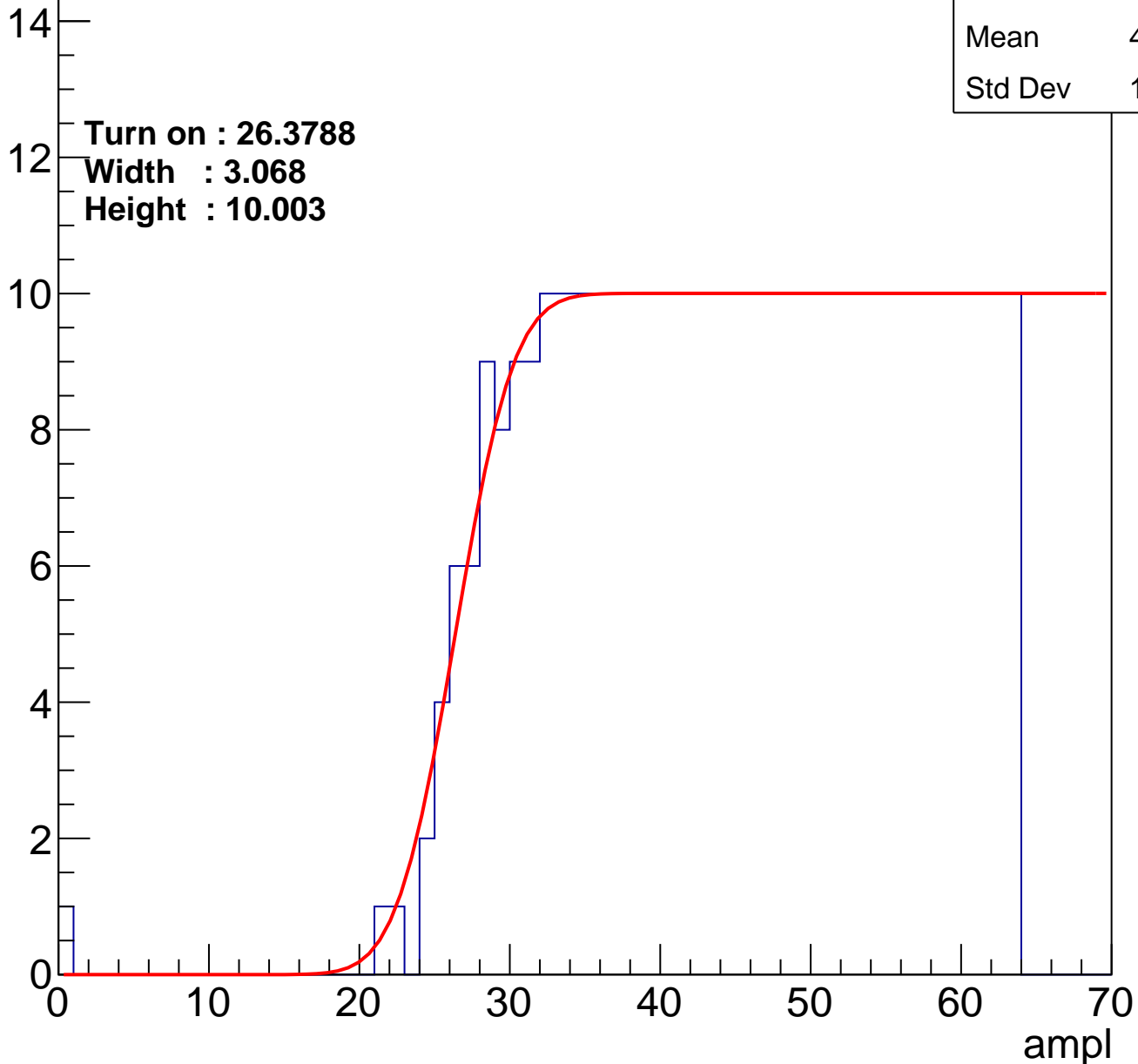
Entries	376
Mean	44.53
Std Dev	11.22

Turn on : 26.3788

Width : 3.068

Height : 10.003

Entry



B0L002S, U5-ch69

calib_packv5_042523_0143.root, FC#8, port C1

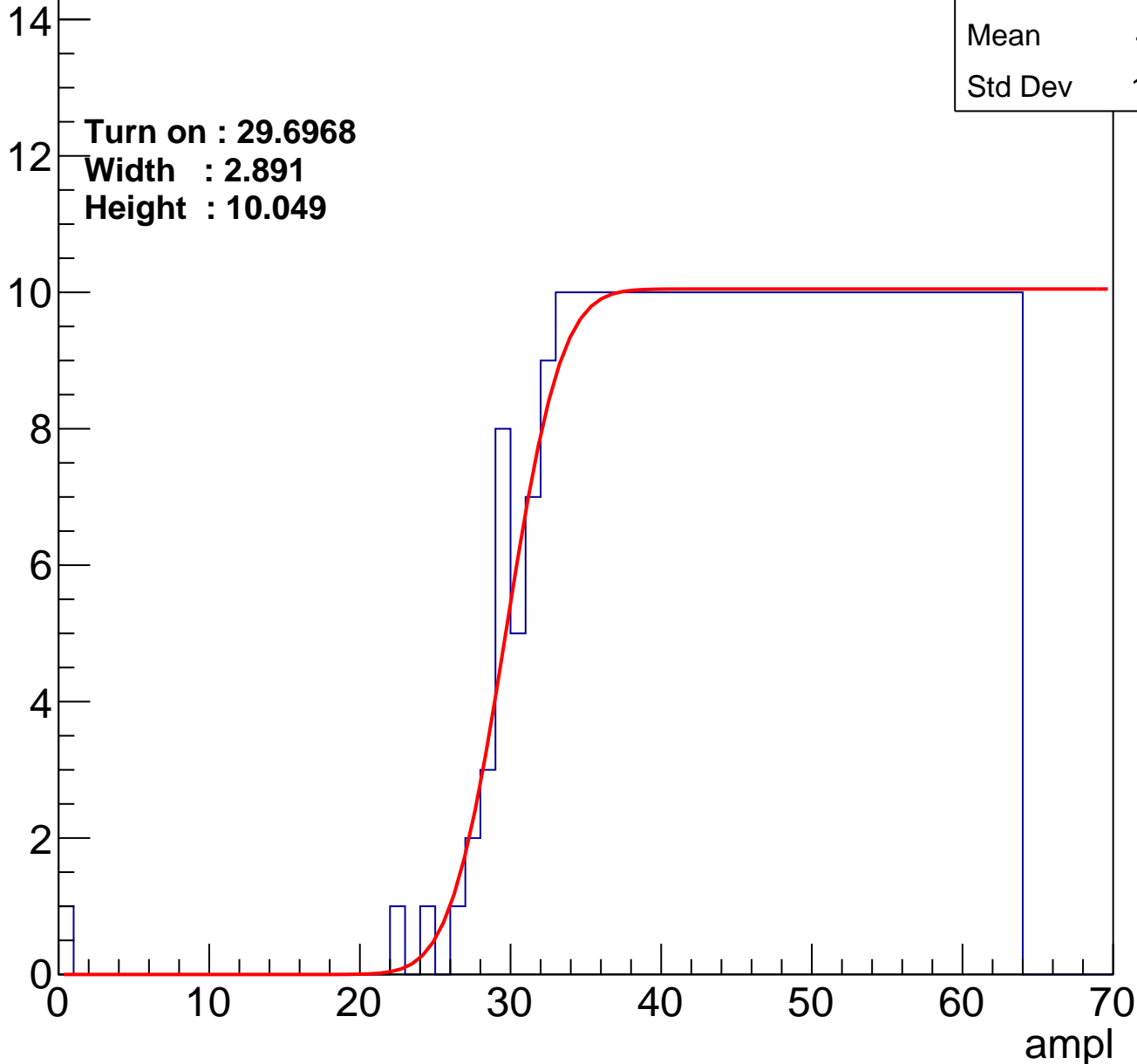
Entry

Entries	348
Mean	45.91
Std Dev	10.48

Turn on : 29.6968

Width : 2.891

Height : 10.049



B0L002S, U5-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.86
Std Dev	10.49

Turn on : 28.9912

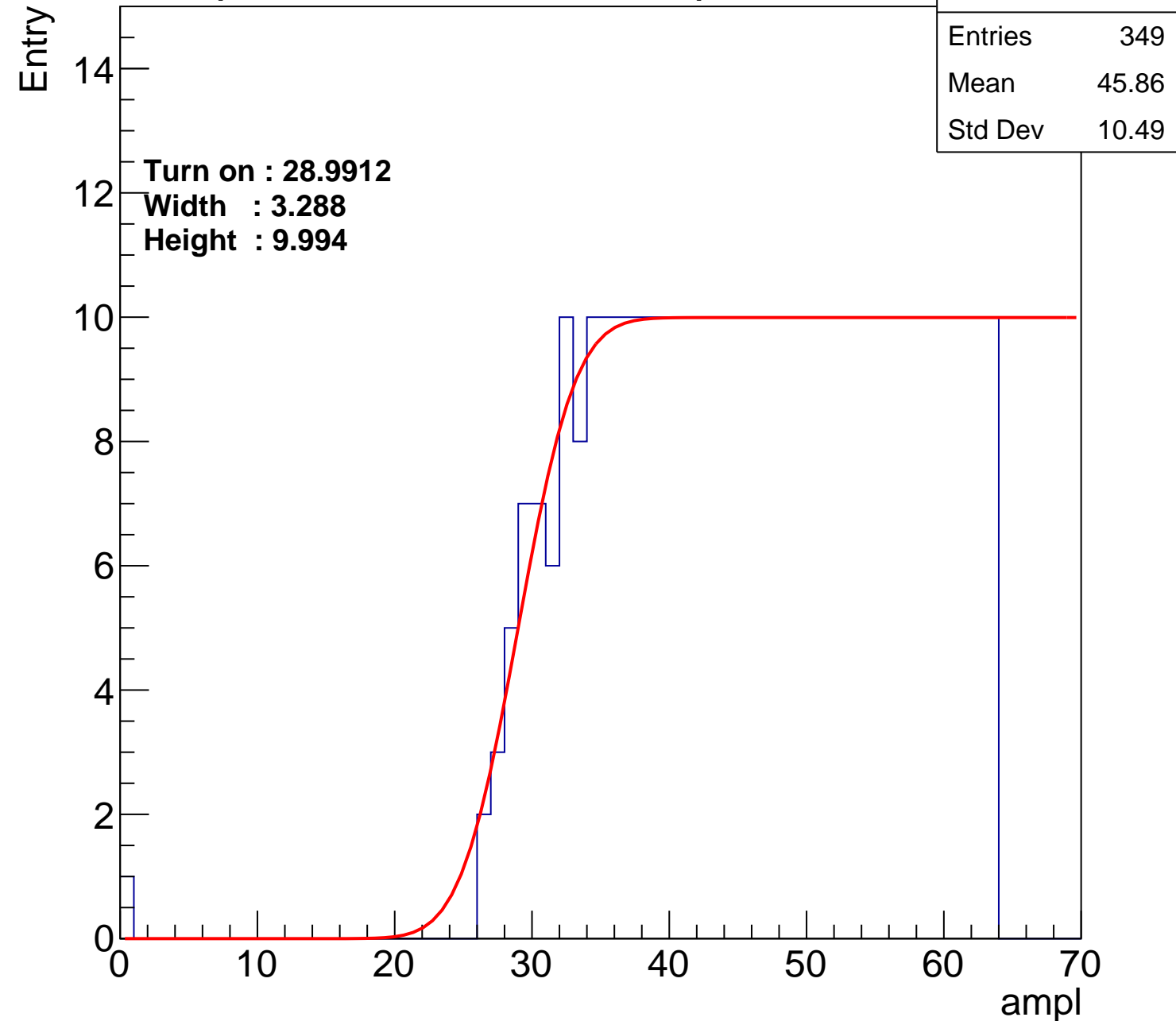
Width : 3.288

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch71

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 27.9426

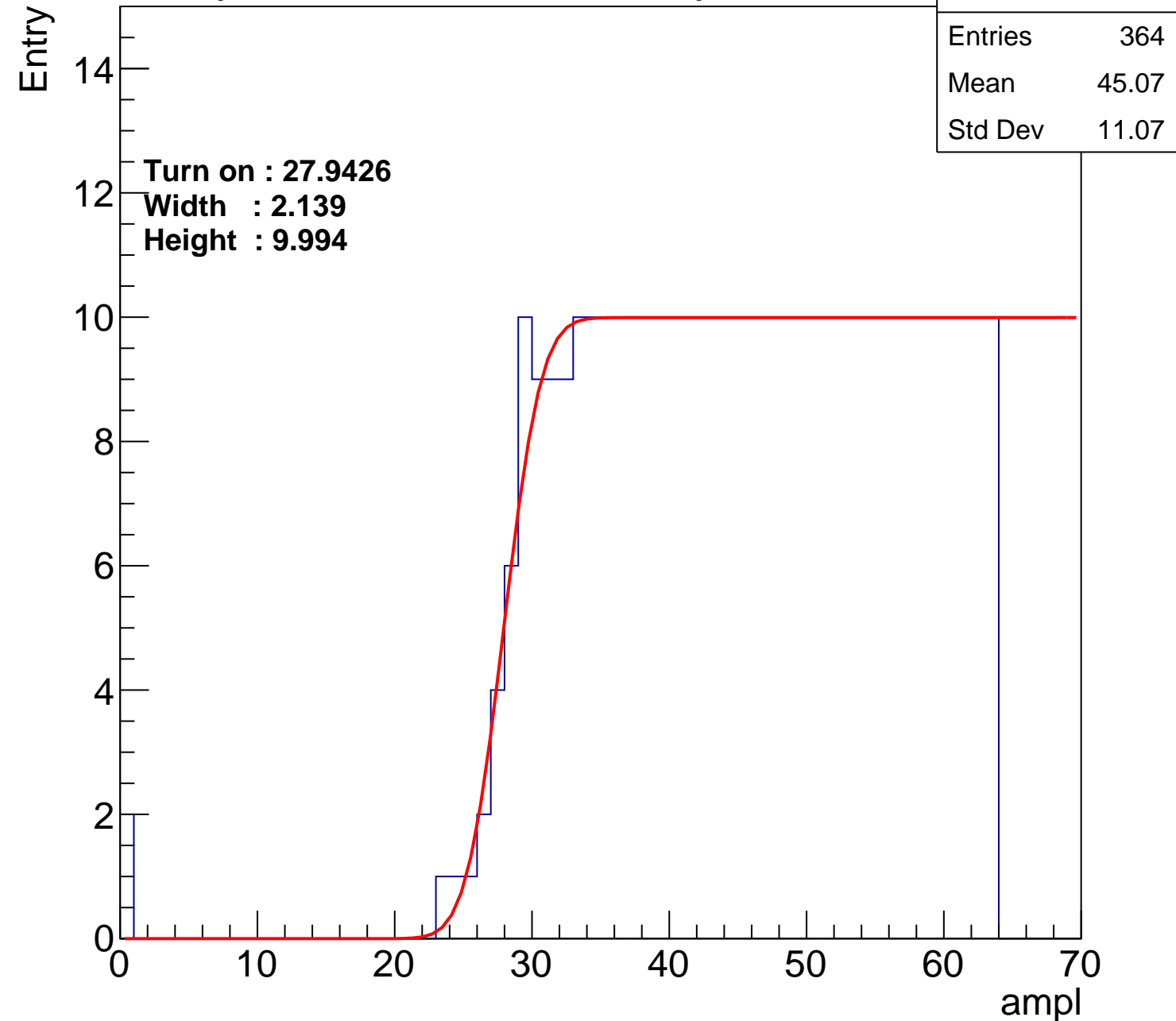
Width : 2.139

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.74
Std Dev	11.95

Turn on : 25.6832

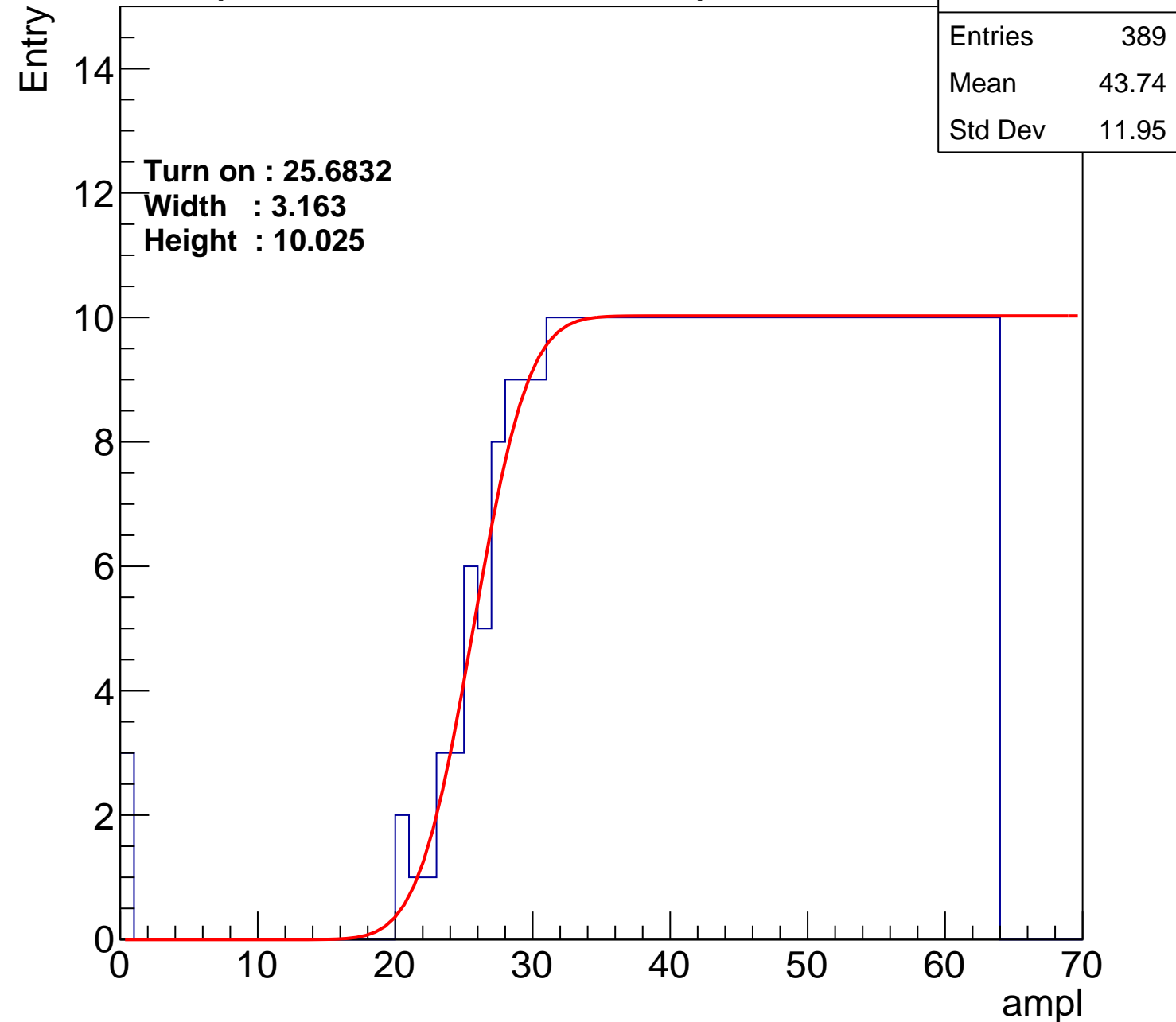
Width : 3.163

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch73

calib_packv5_042523_0143.root, FC#8, port C1

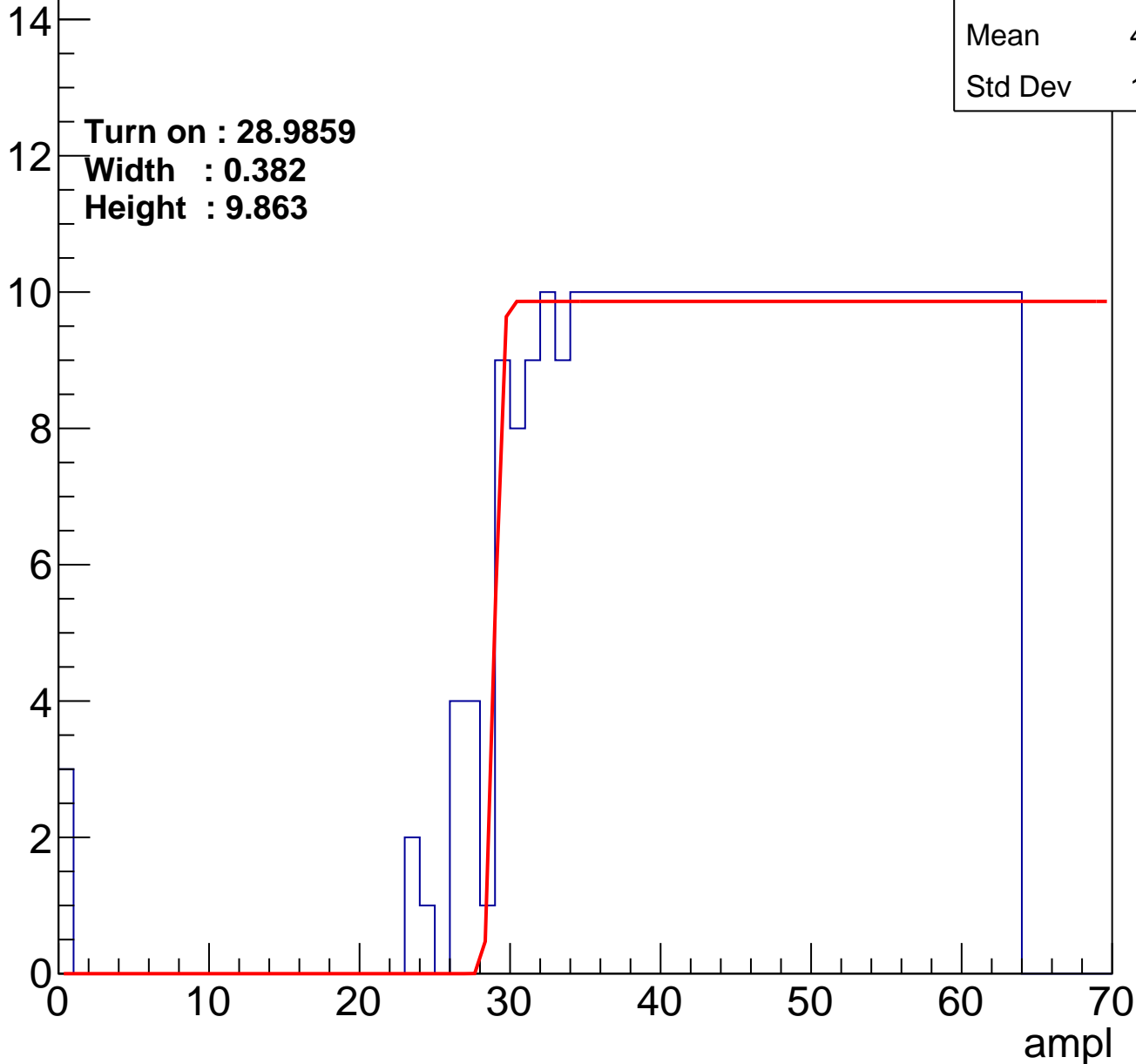
Entry

Entries	360
Mean	45.16
Std Dev	11.24

Turn on : 28.9859

Width : 0.382

Height : 9.863



B0L002S, U5-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	393
Mean	43.72
Std Dev	11.64

Turn on : 24.9603

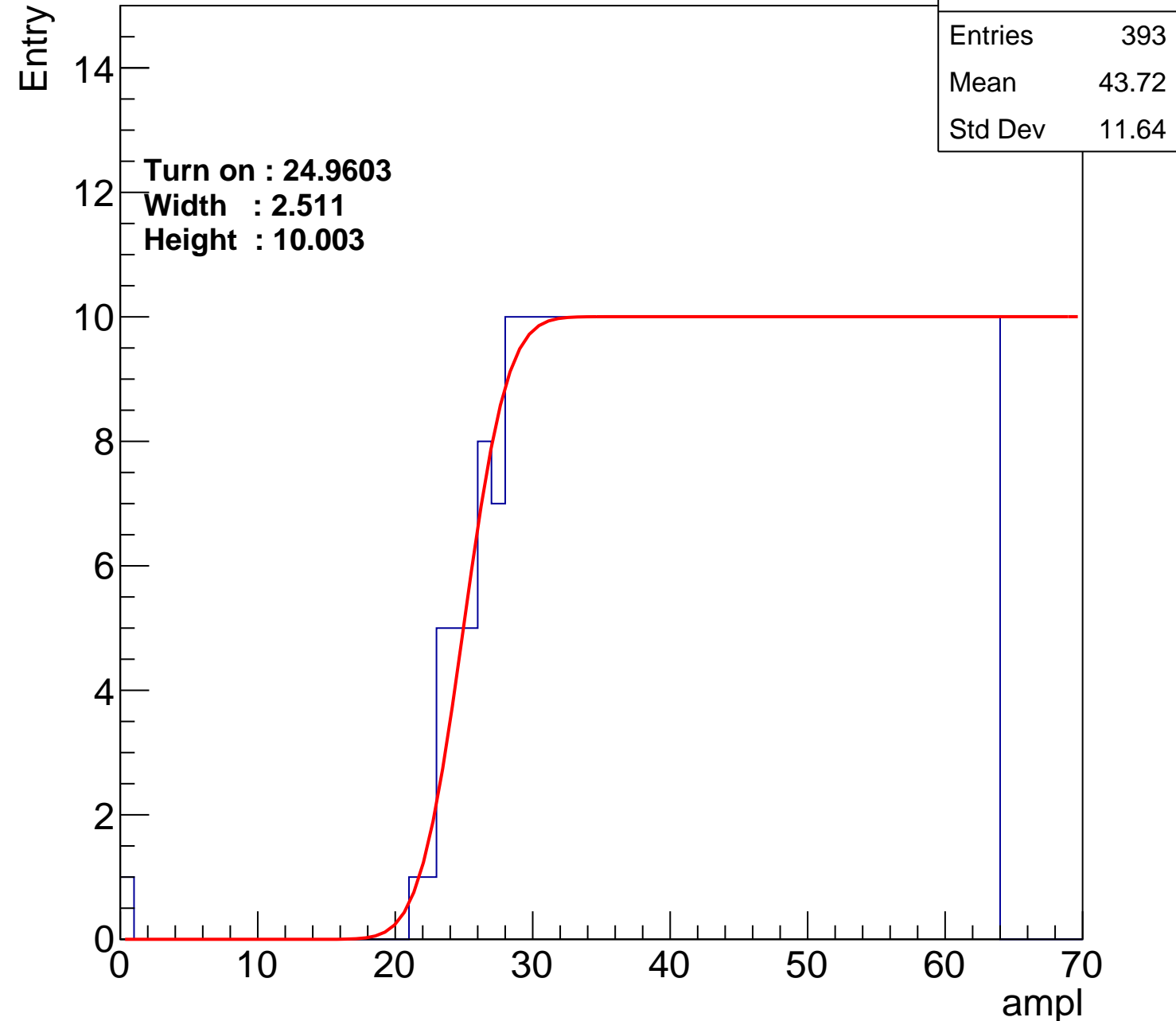
Width : 2.511

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.27
Std Dev	11.17

Turn on : 28.7679

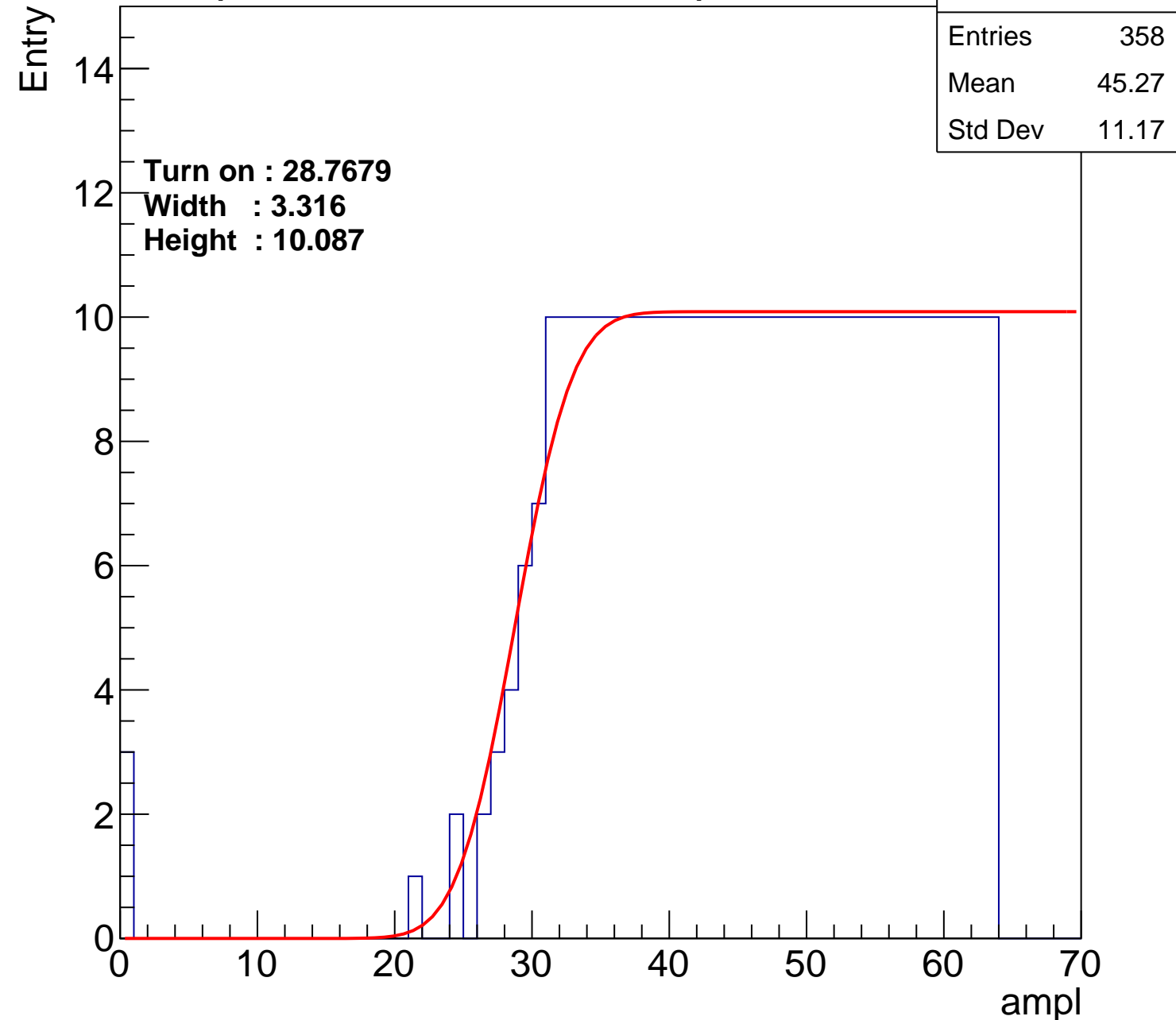
Width : 3.316

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.53
Std Dev	11.67

Turn on : 27.6264

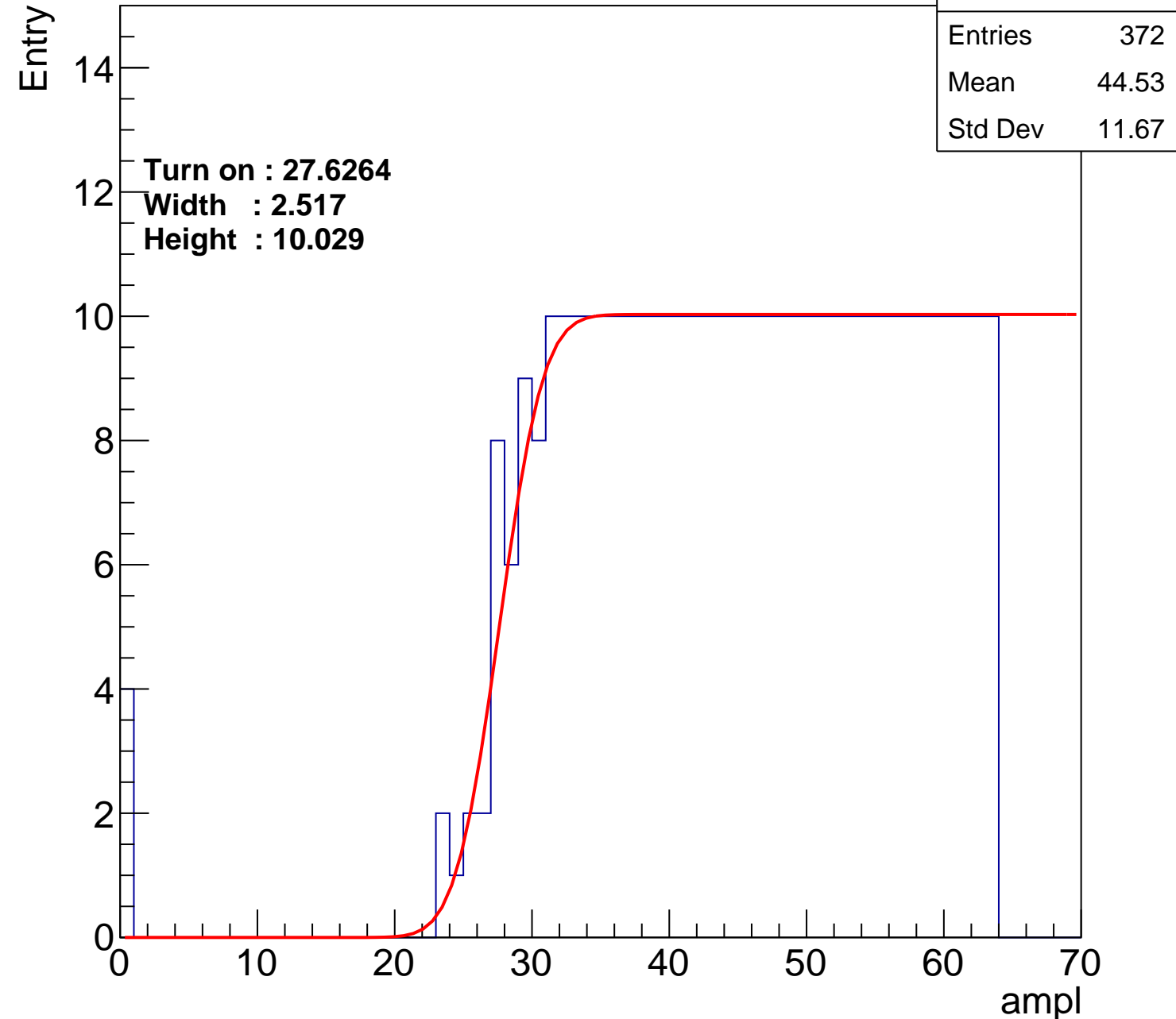
Width : 2.517

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.01
Std Dev	11.26

Turn on : 28.0287

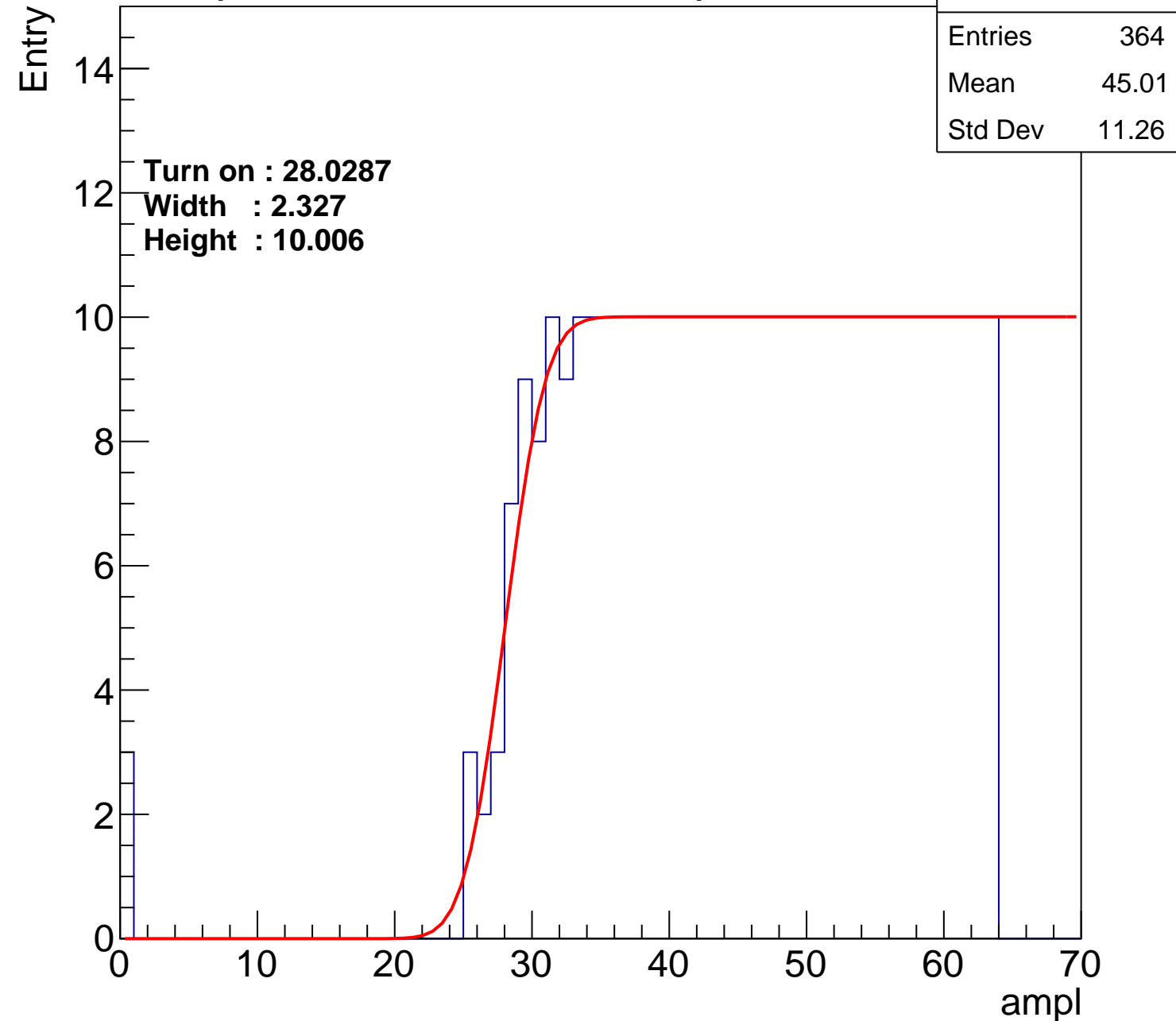
Width : 2.327

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.06
Std Dev	11.63

Turn on : 26.1215

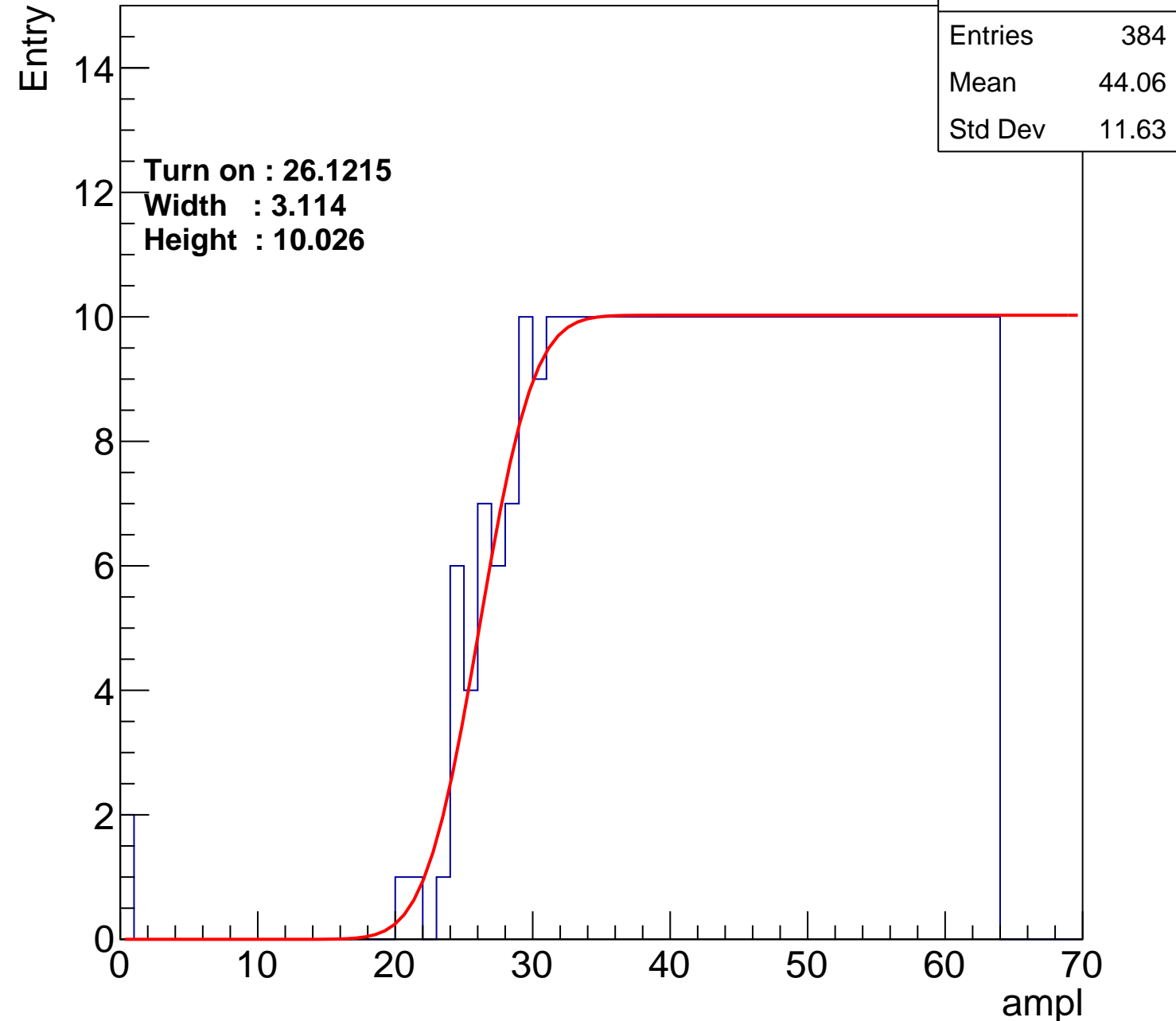
Width : 3.114

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.17
Std Dev	11.15

Turn on : 29.3425

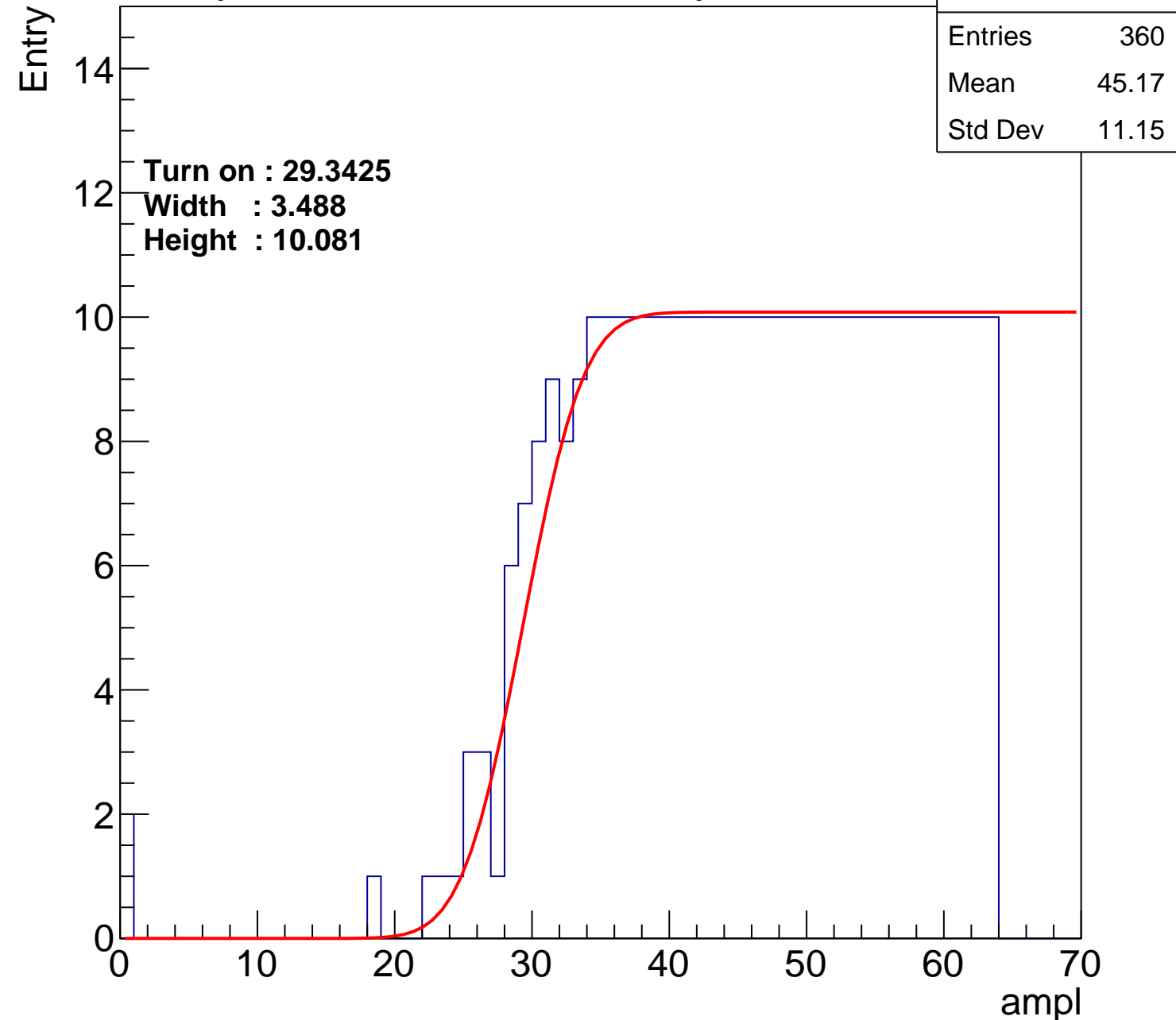
Width : 3.488

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 27.7561

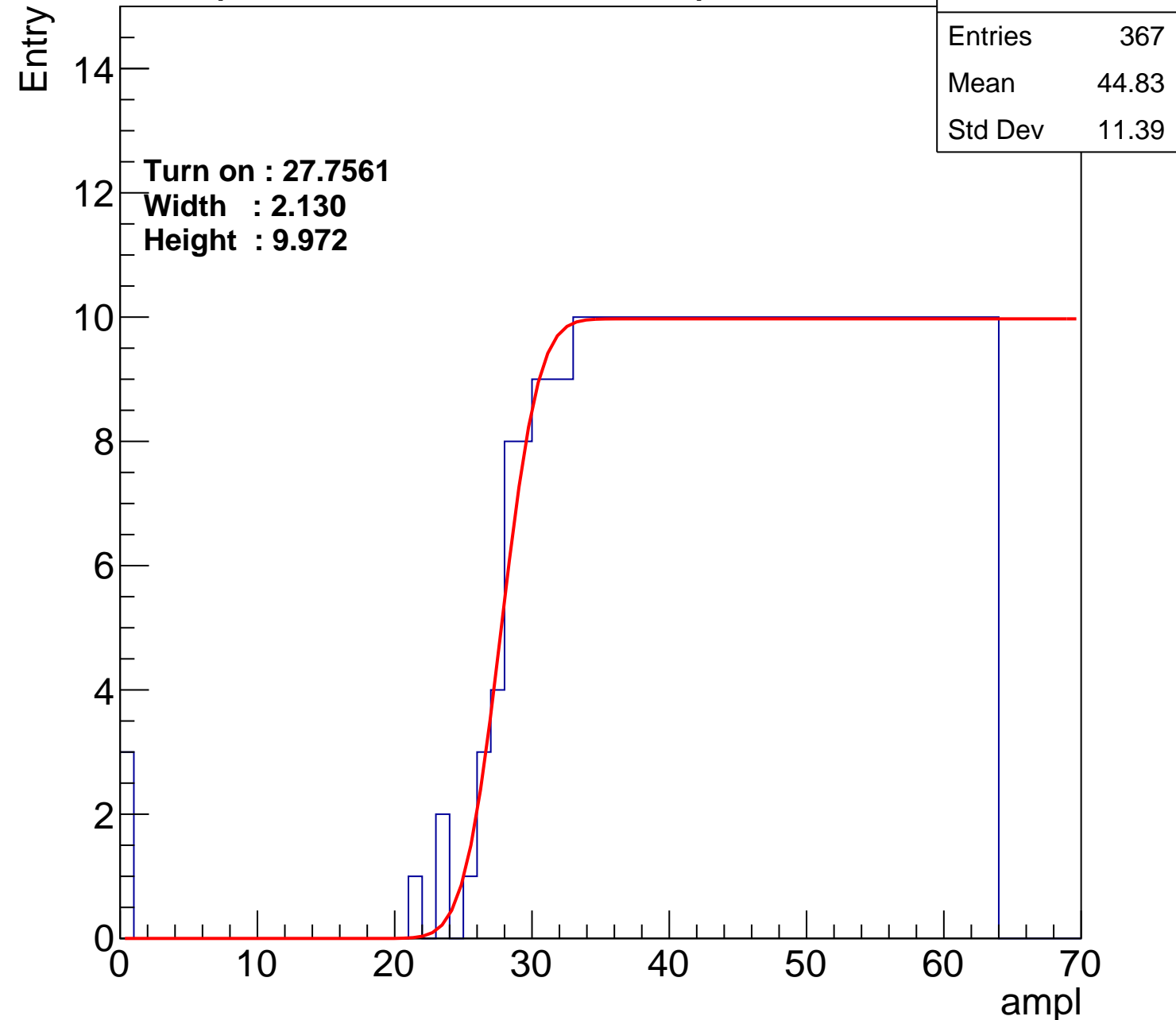
Width : 2.130

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.83
Std Dev	11.18

Turn on : 27.2147

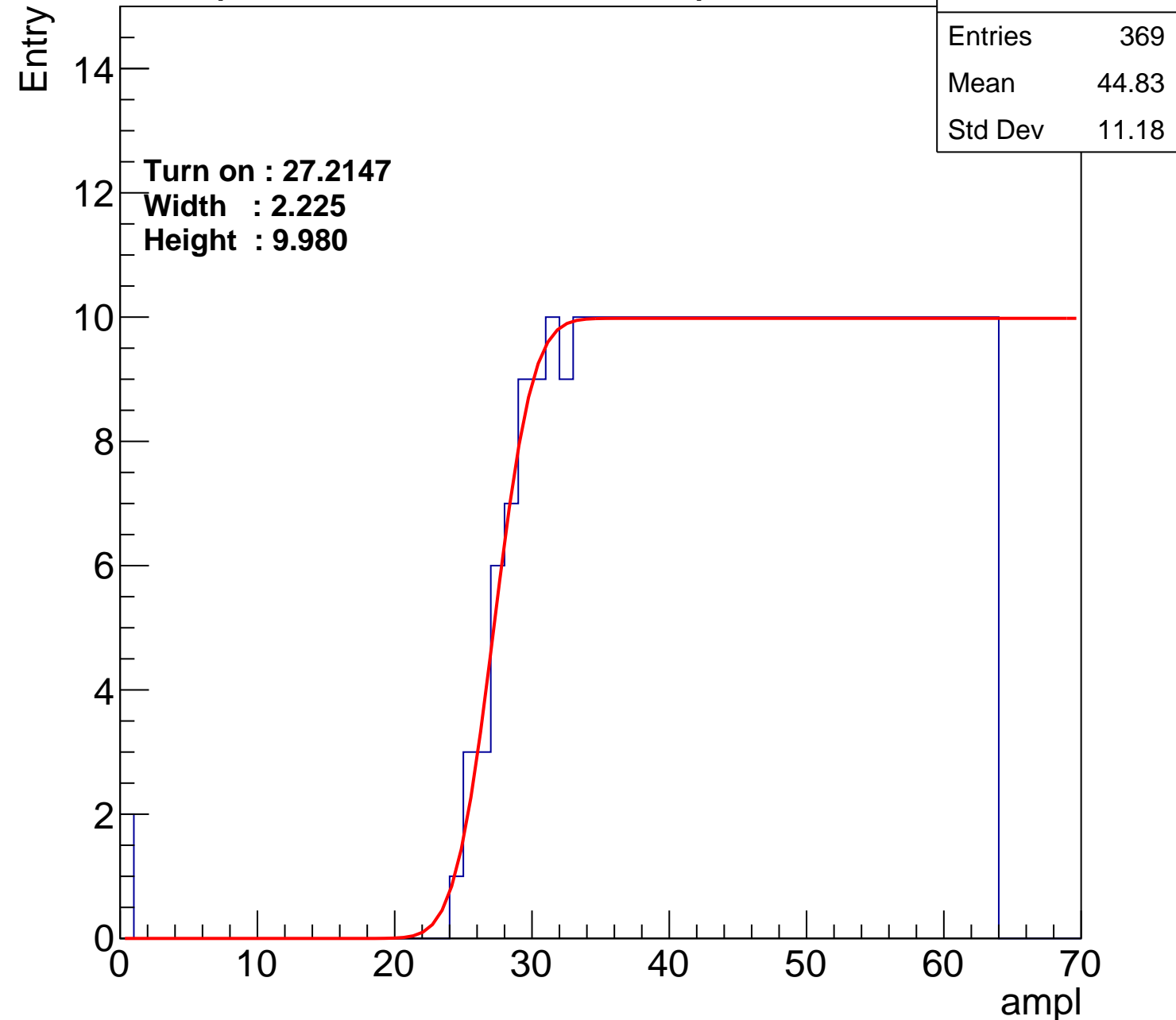
Width : 2.225

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.61
Std Dev	11.38

Turn on : 27.0470

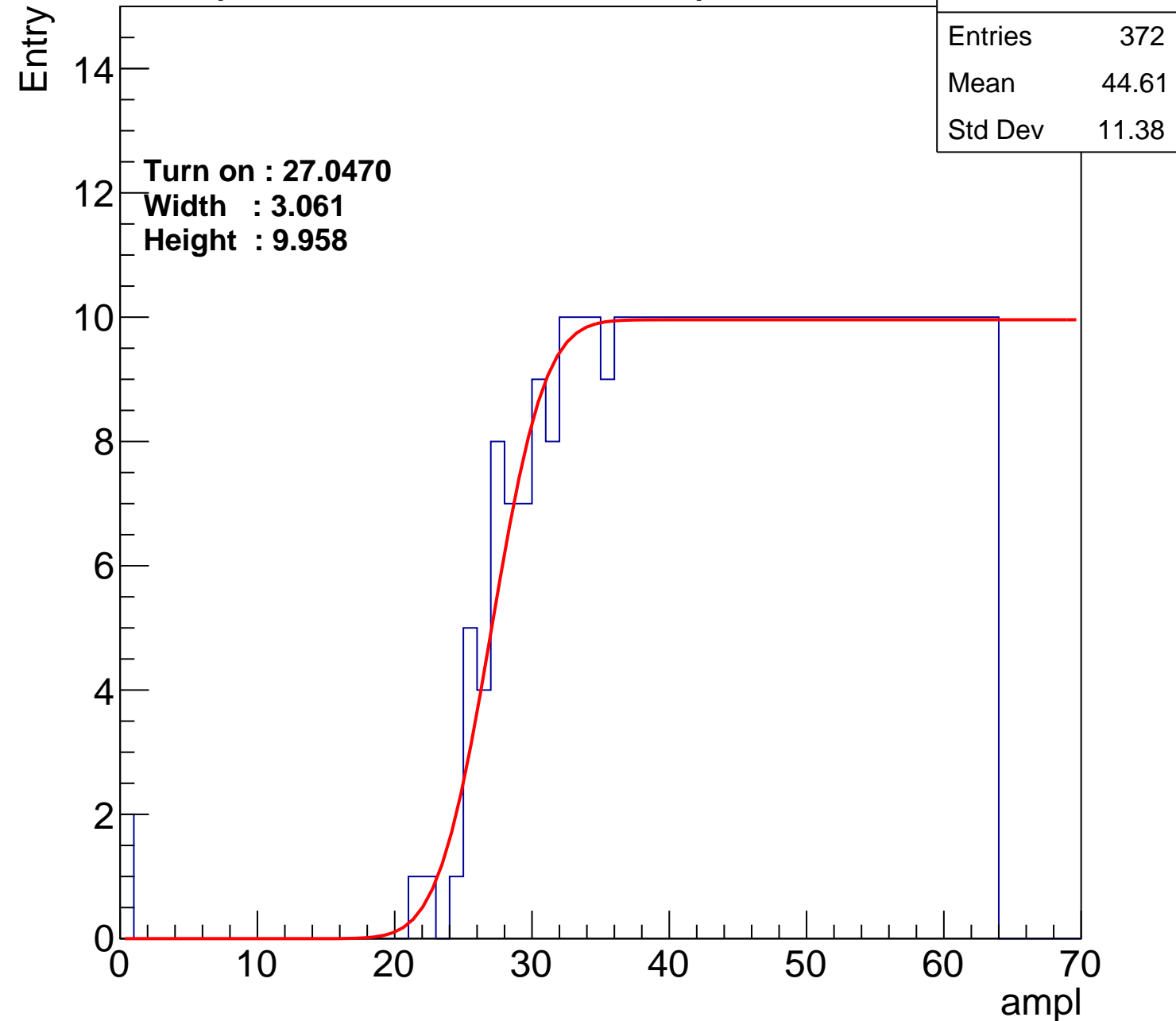
Width : 3.061

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.95
Std Dev	11.02

Turn on : 27.9798

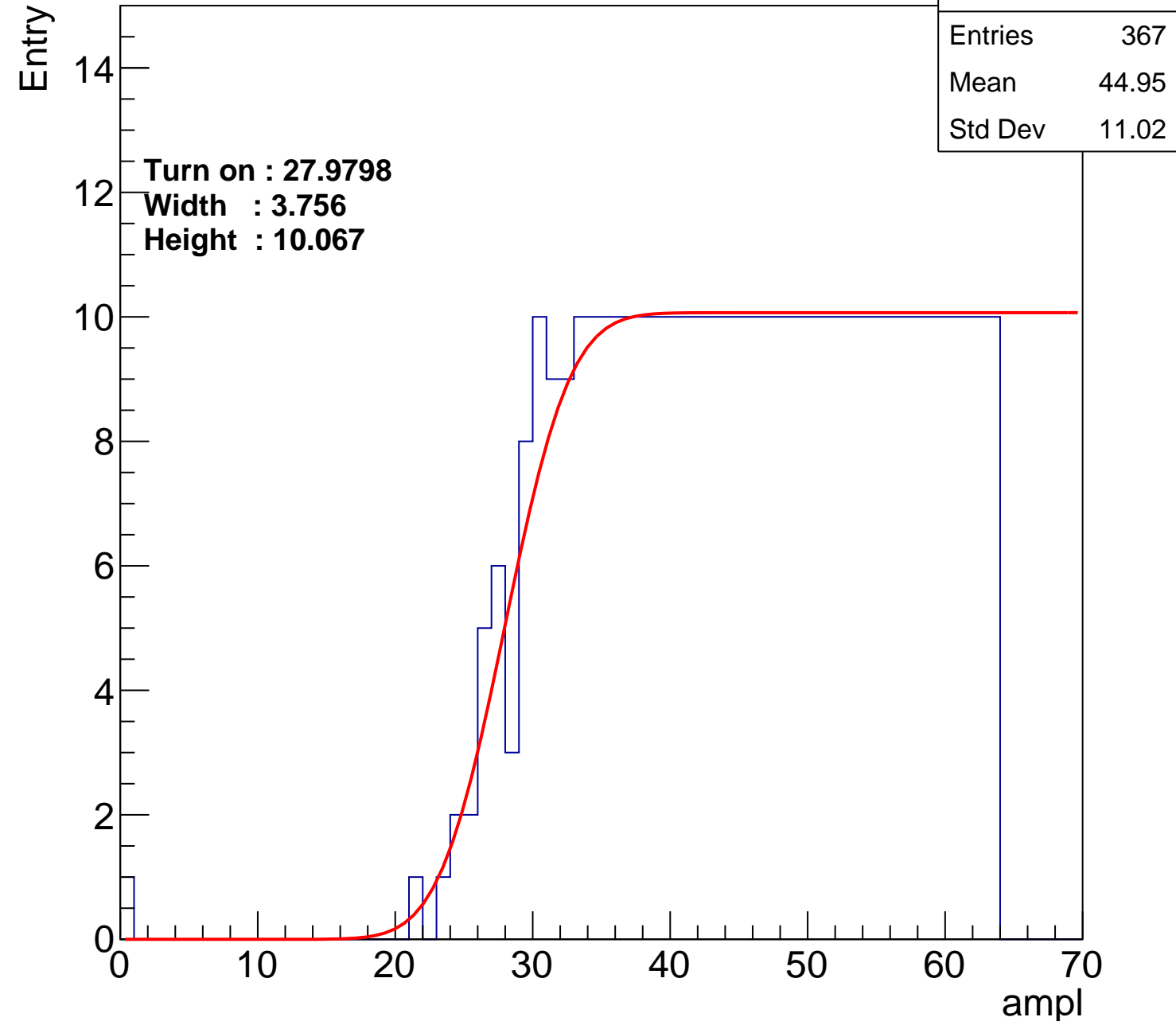
Width : 3.756

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch84

calib_packv5_042523_0143.root, FC#8, port C1

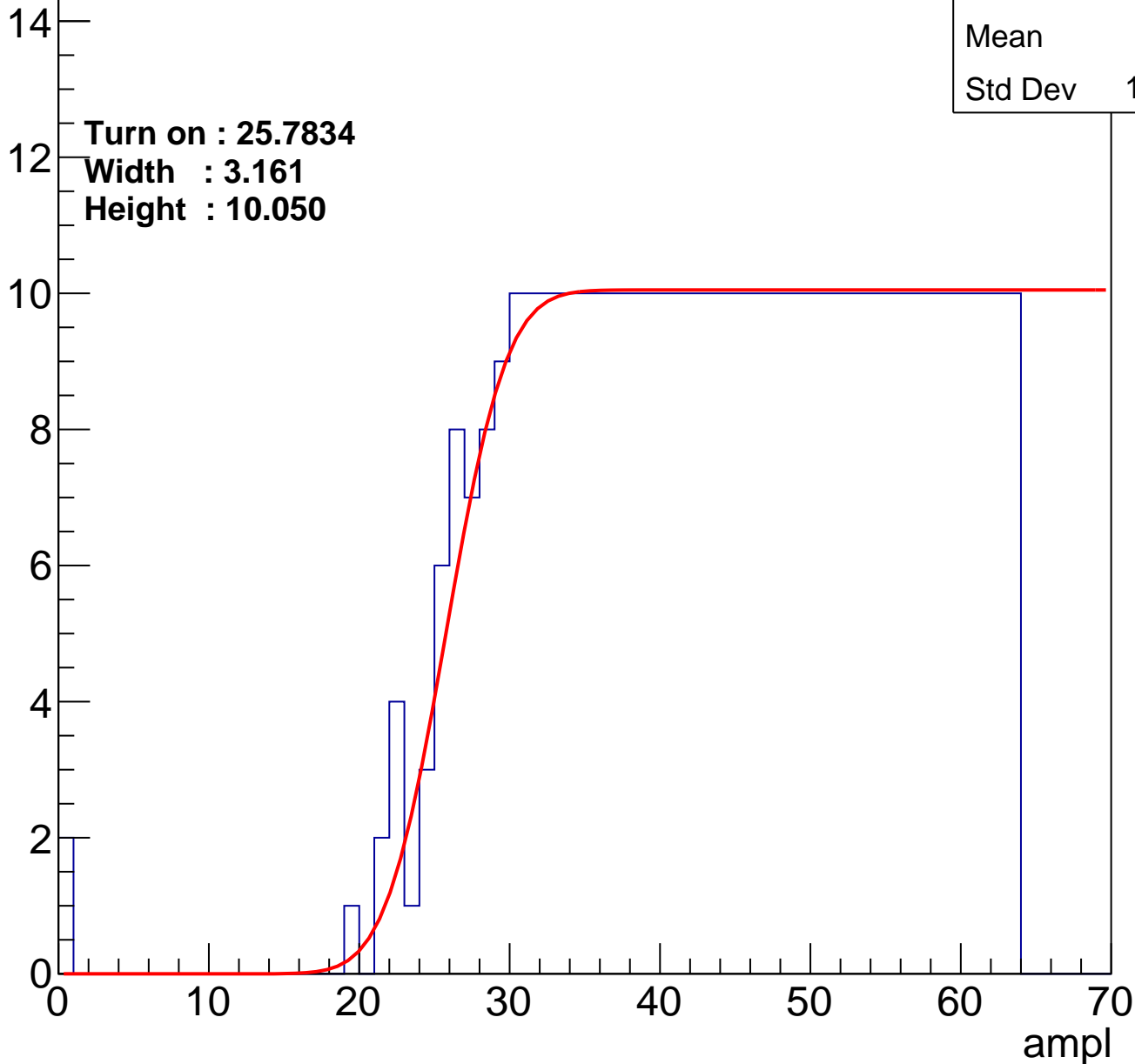
Entry

Entries	391
Mean	43.7
Std Dev	11.84

Turn on : 25.7834

Width : 3.161

Height : 10.050



B0L002S, U5-ch85

calib_packv5_042523_0143.root, FC#8, port C1

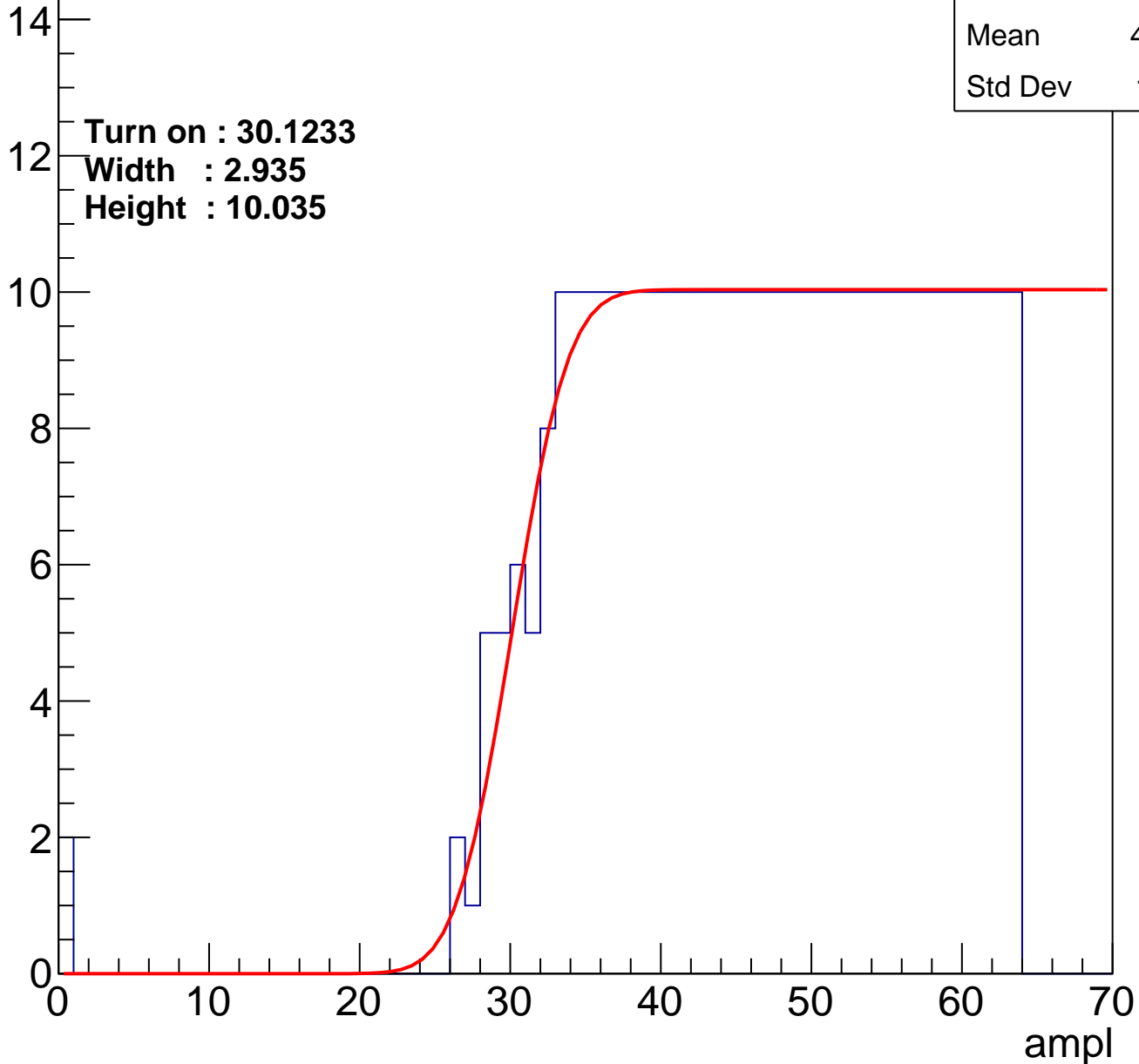
Entries	344
Mean	46.03
Std Dev	10.61

Turn on : 30.1233

Width : 2.935

Height : 10.035

Entry



B0L002S, U5-ch86

calib_packv5_042523_0143.root, FC#8, port C1

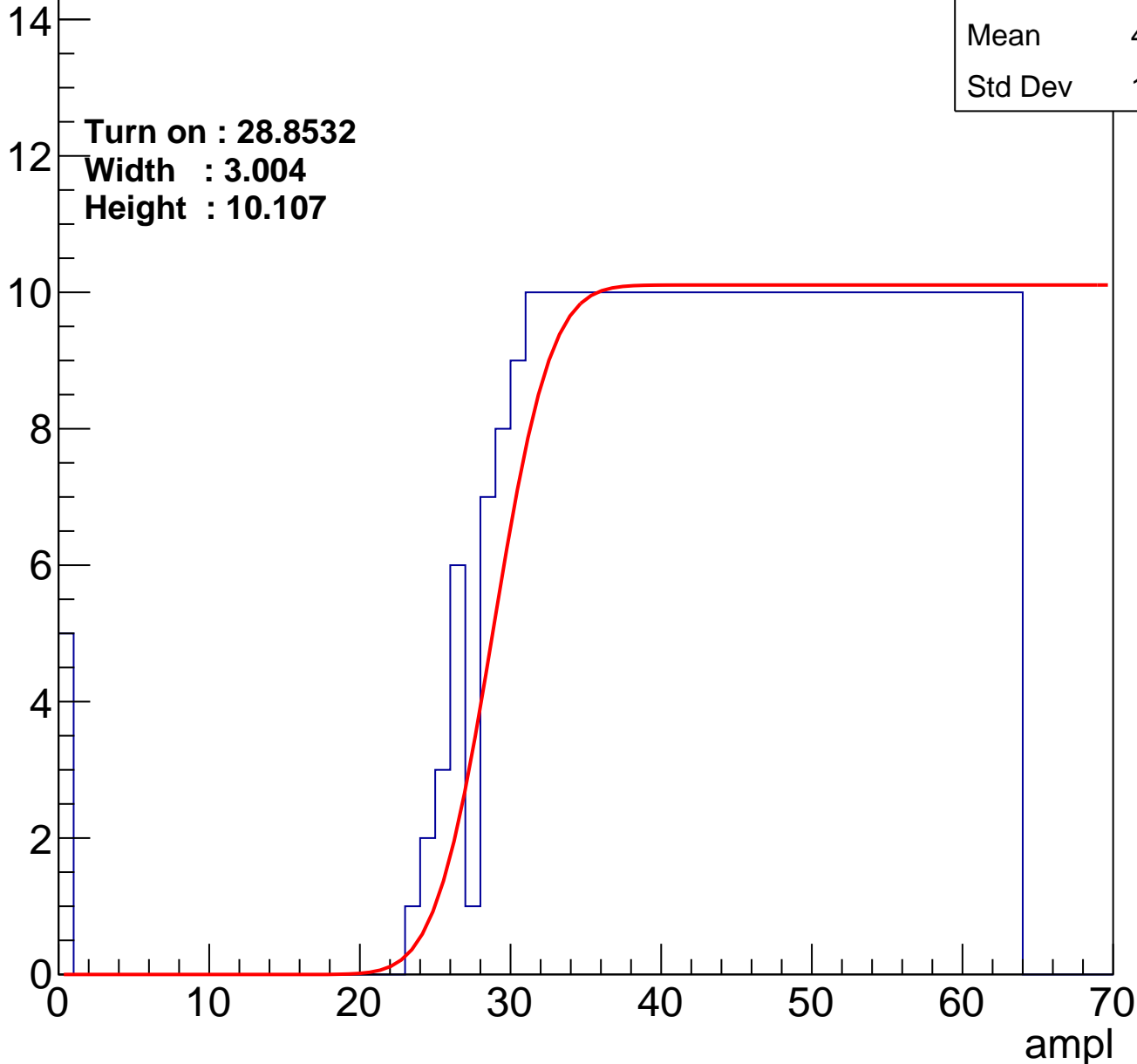
Entry

Entries	372
Mean	44.45
Std Dev	11.87

Turn on : 28.8532

Width : 3.004

Height : 10.107



B0L002S, U5-ch87

calib_packv5_042523_0143.root, FC#8, port C1

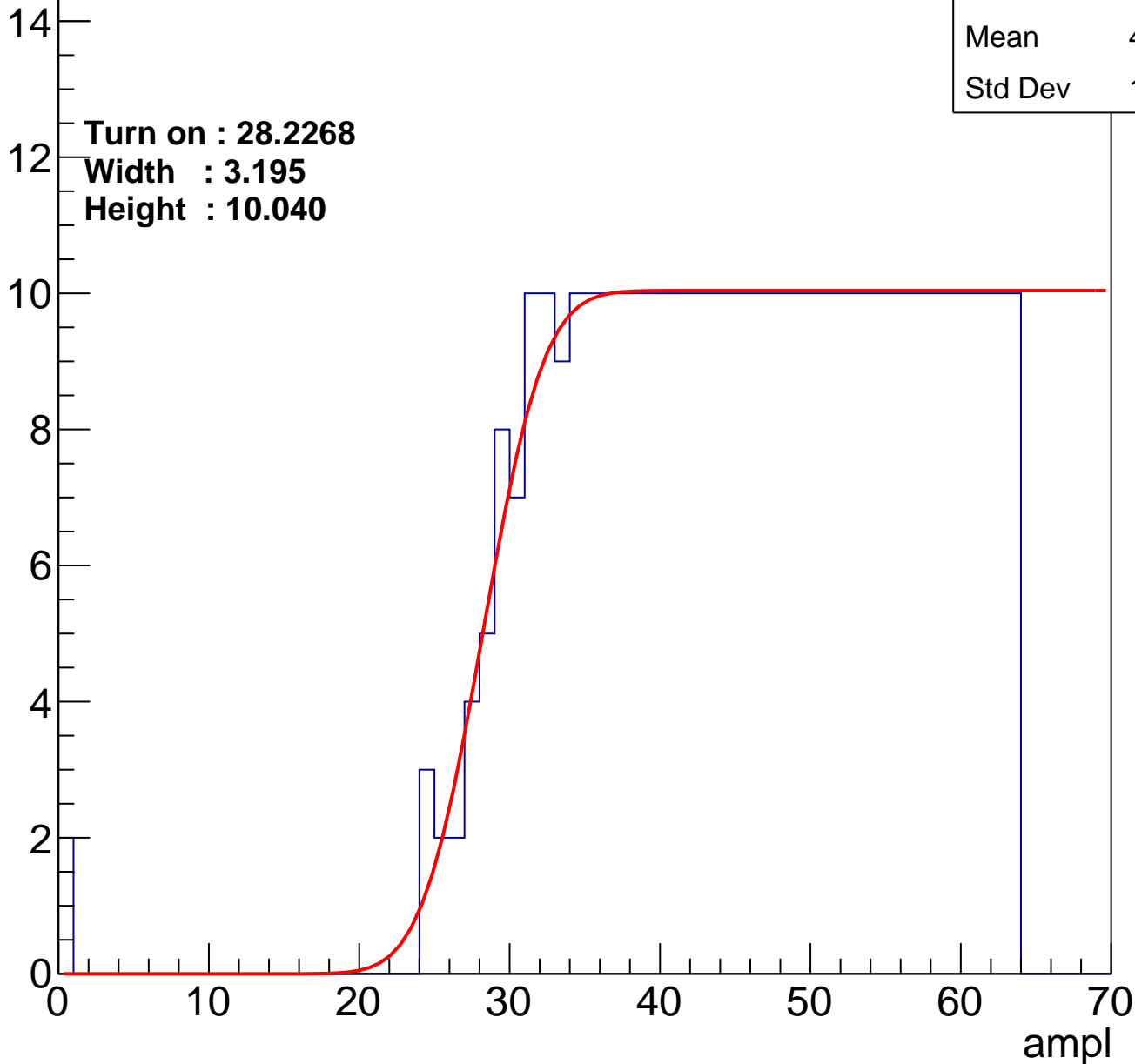
Entry

Entries	362
Mean	45.14
Std Dev	11.07

Turn on : 28.2268

Width : 3.195

Height : 10.040



B0L002S, U5-ch88

calib_packv5_042523_0143.root, FC#8, port C1

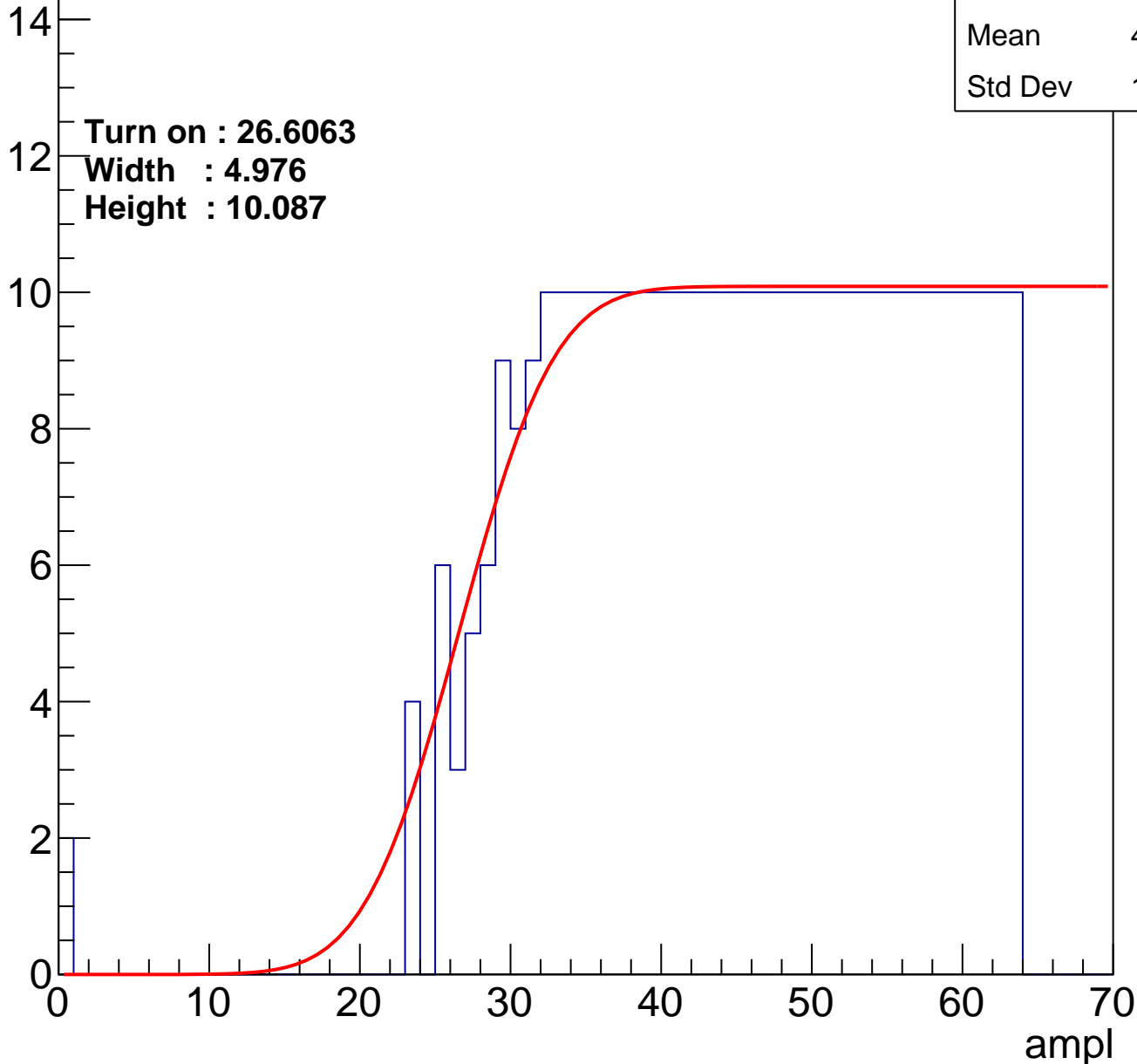
Entries	372
Mean	44.63
Std Dev	11.35

Turn on : 26.6063

Width : 4.976

Height : 10.087

Entry



B0L002S, U5-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.49
Std Dev	11.79

Turn on : 28.1535

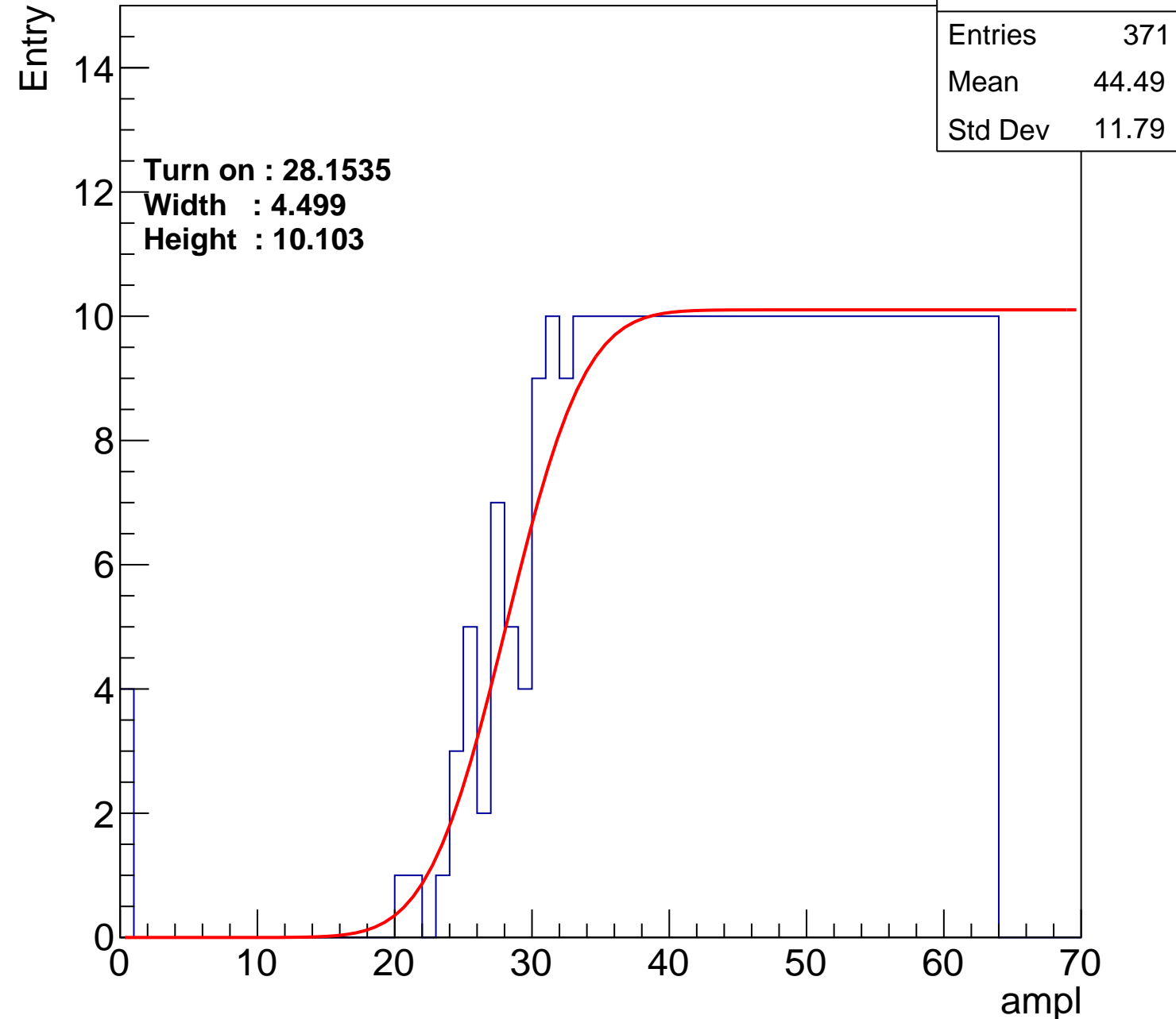
Width : 4.499

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.62
Std Dev	11.04

Turn on : 29.6395

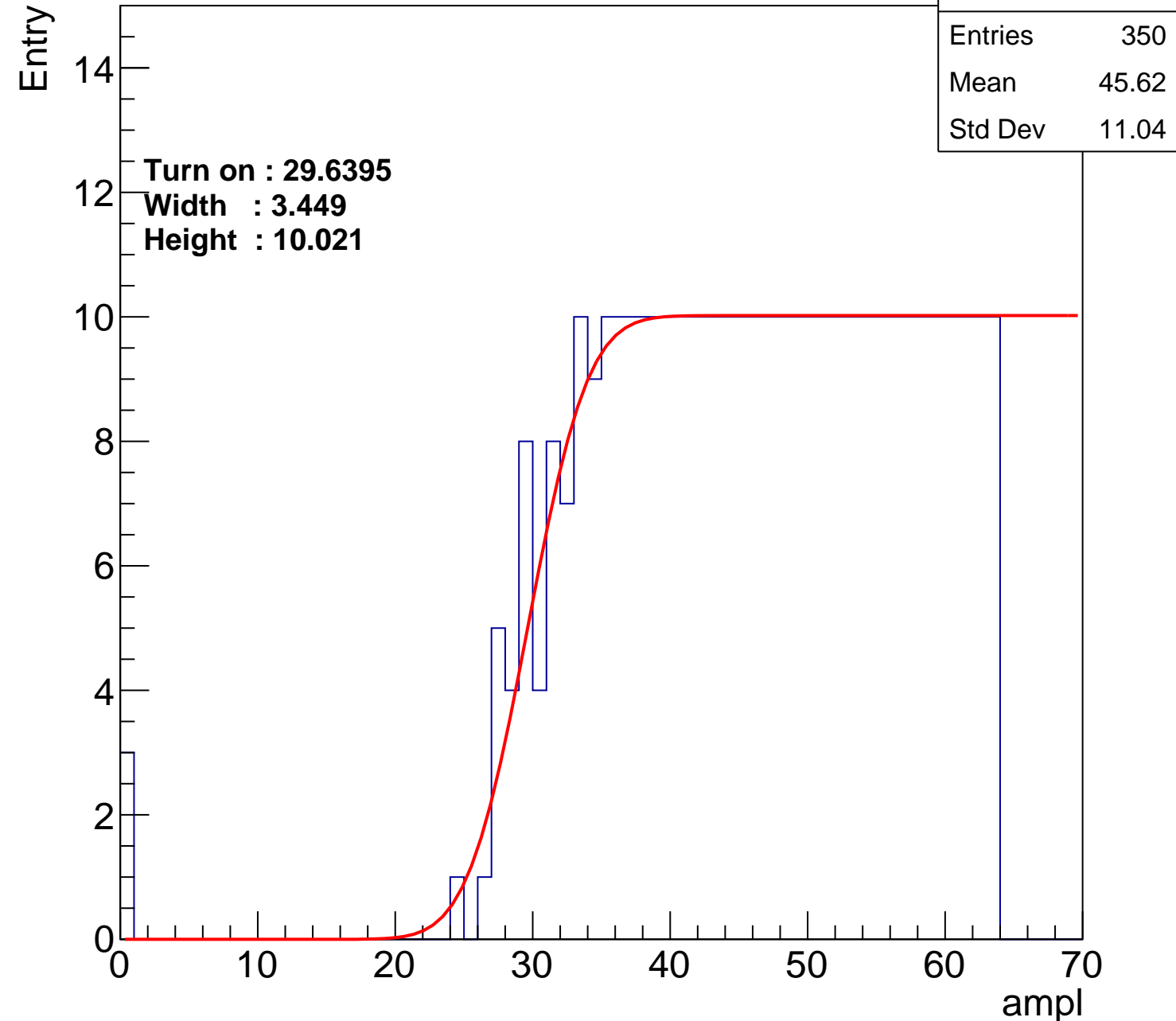
Width : 3.449

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.16
Std Dev	11.43

Turn on : 29.0444

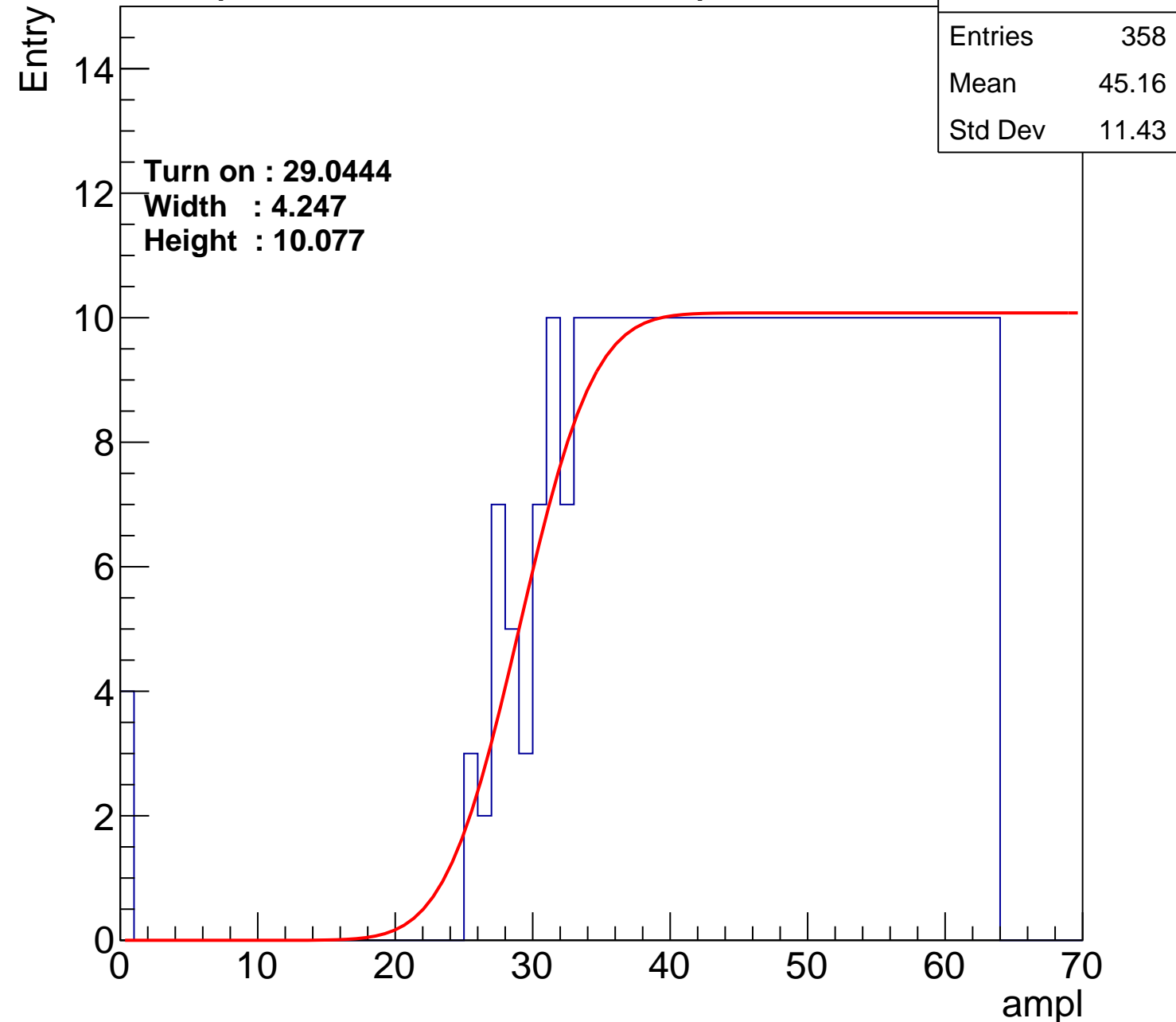
Width : 4.247

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch92

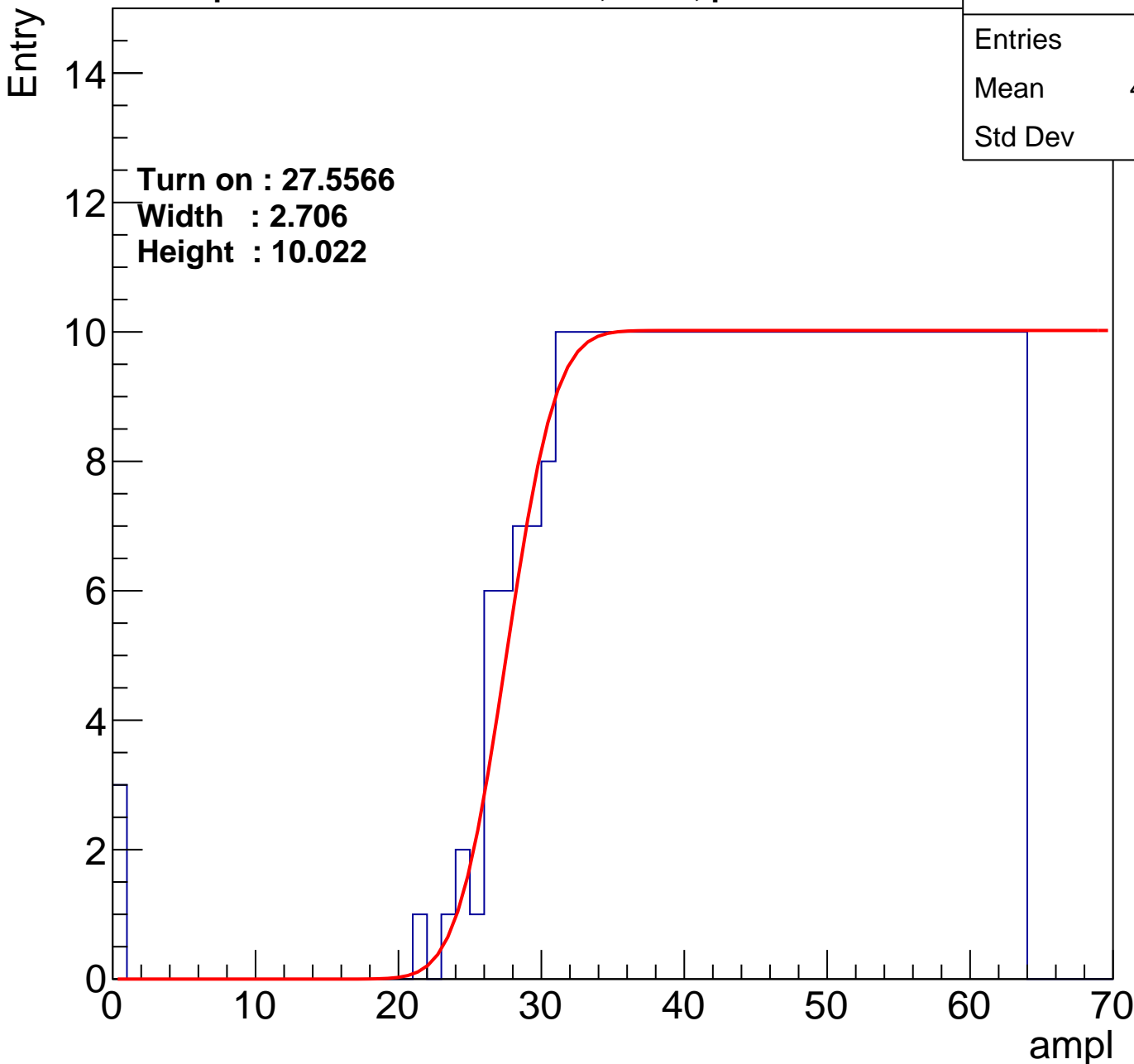
calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.5566

Width : 2.706

Height : 10.022



B0L002S, U5-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 26.8176

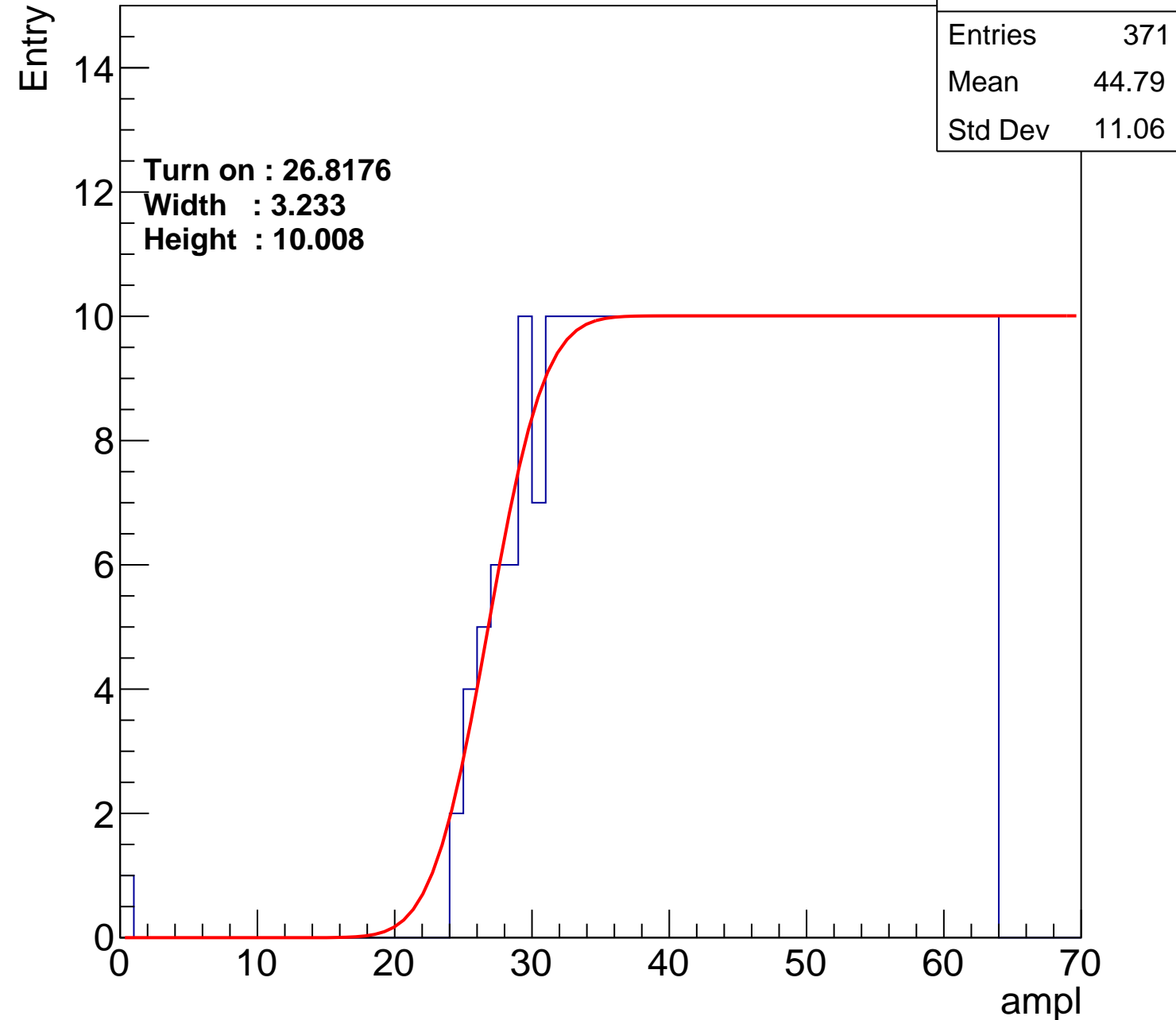
Width : 3.233

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.69
Std Dev	10.78

Turn on : 29.1577

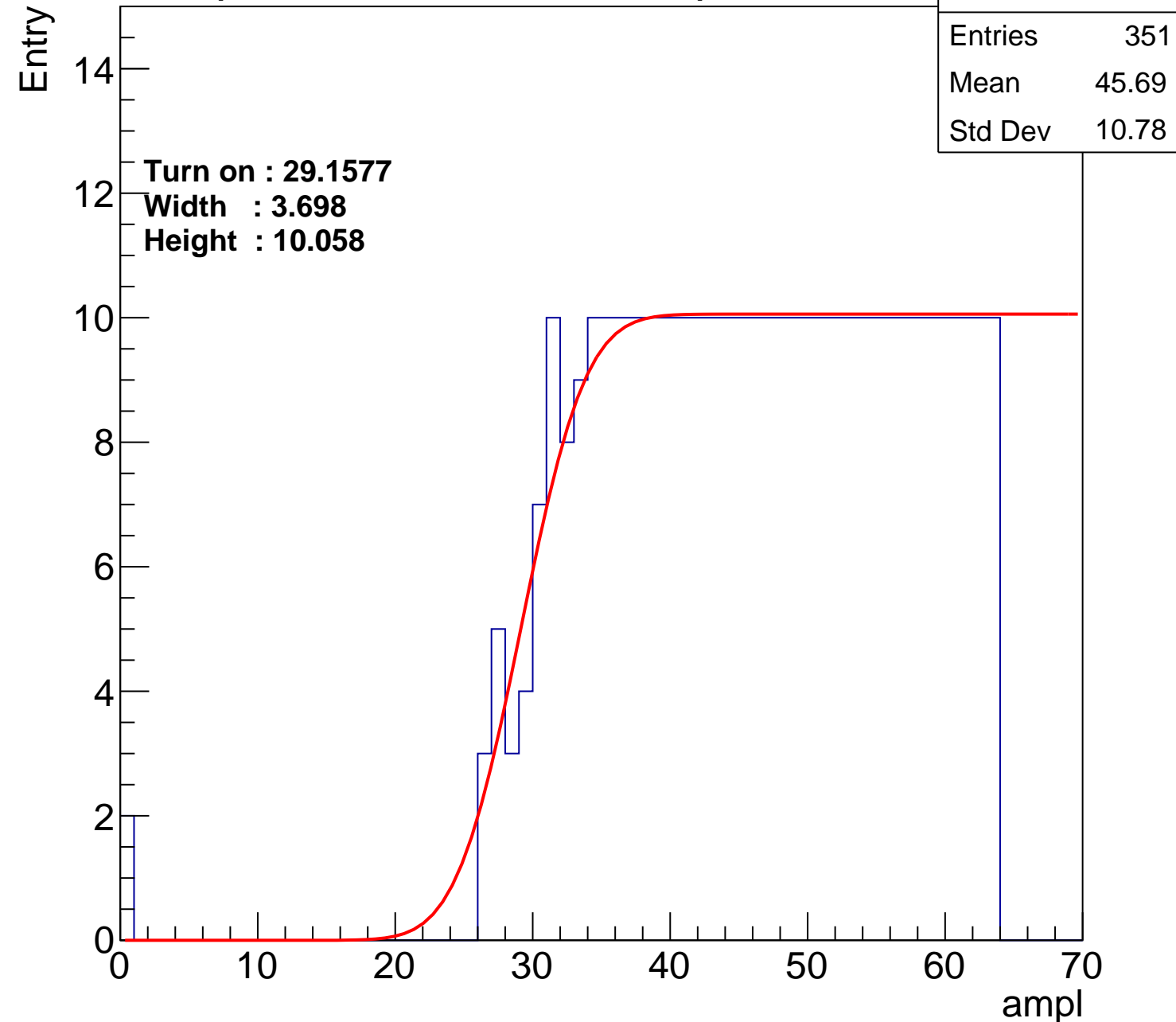
Width : 3.698

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 26.1650

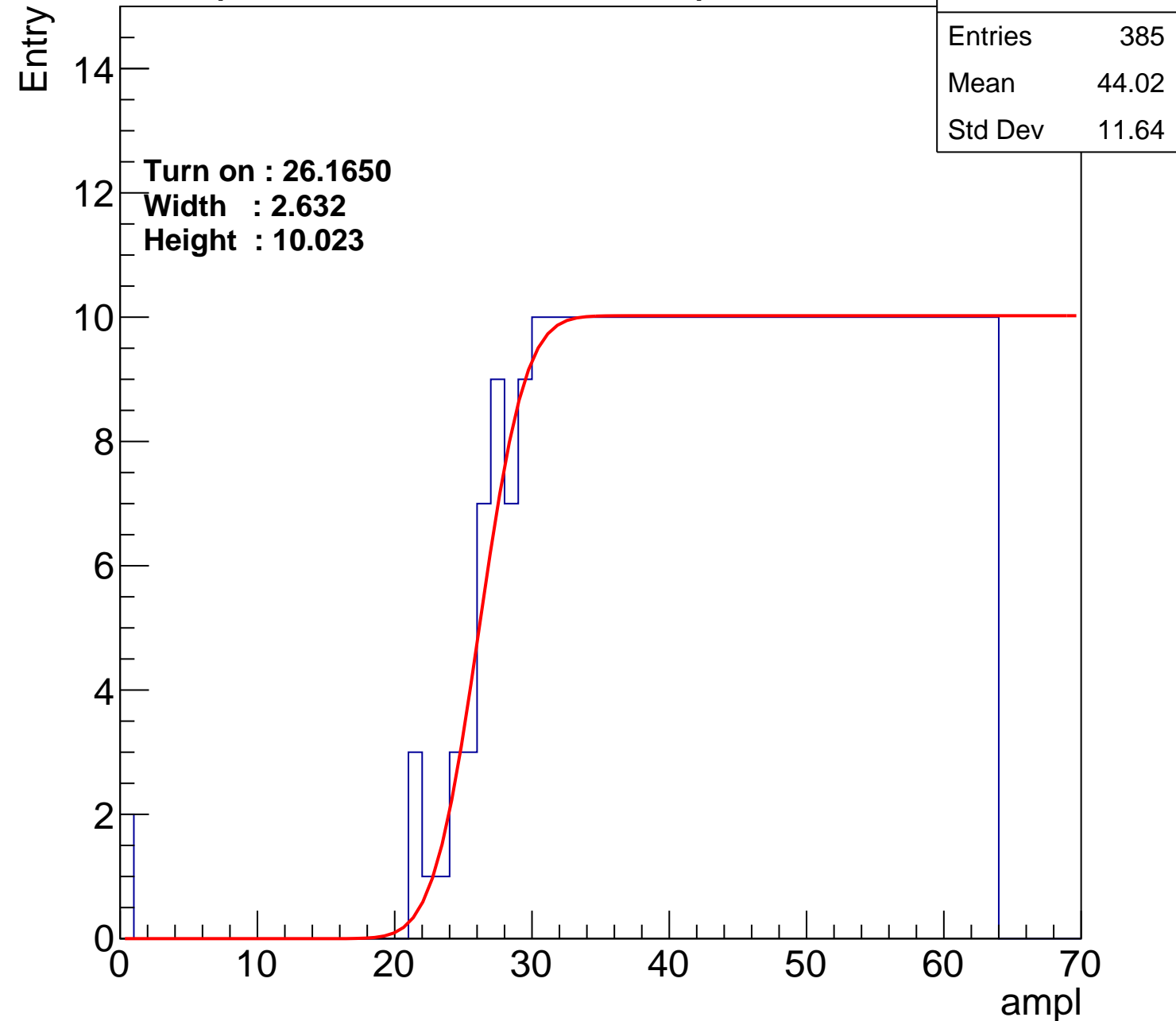
Width : 2.632

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch96

calib_packv5_042523_0143.root, FC#8, port C1

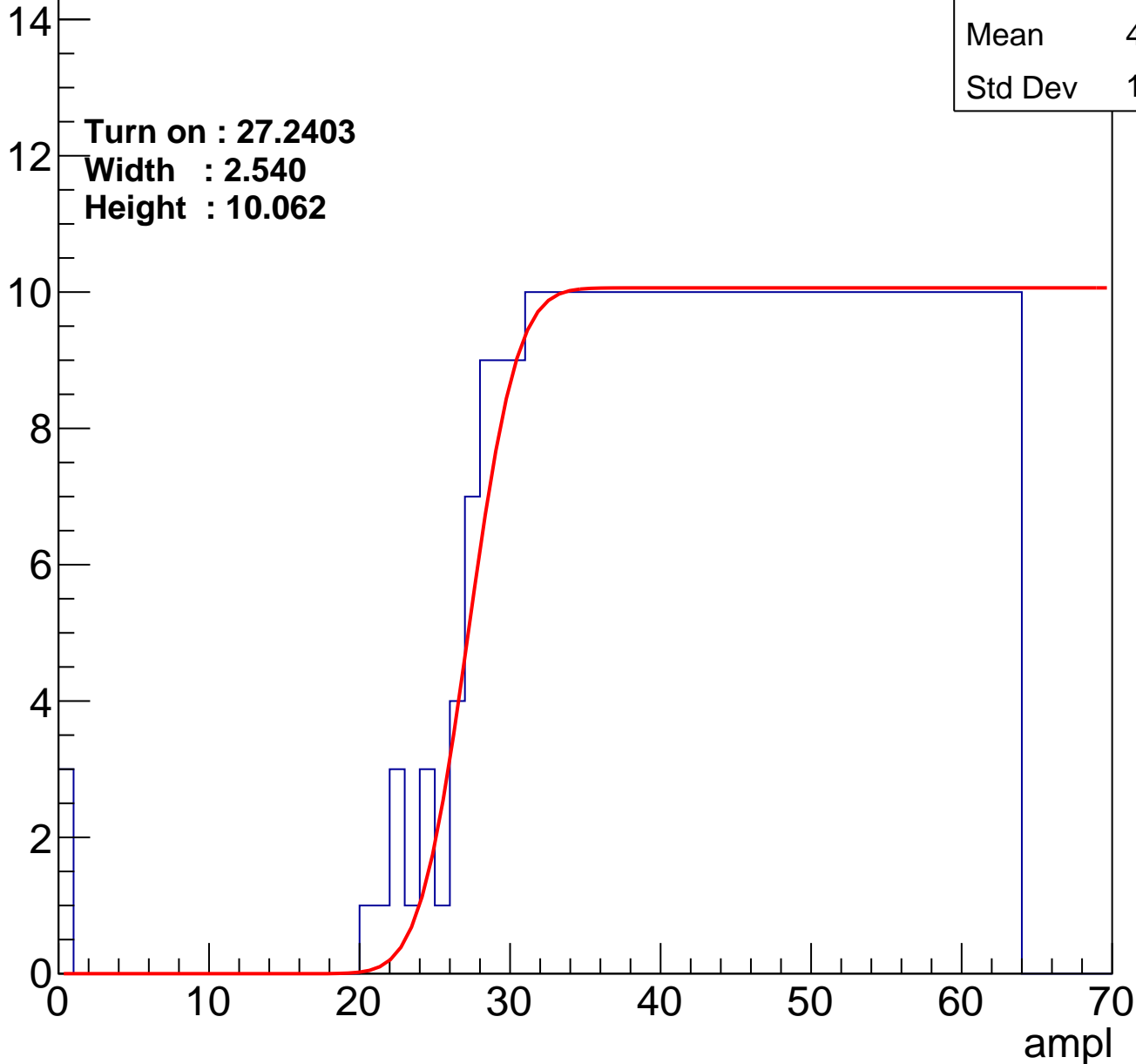
Entry

Entries	381
Mean	44.13
Std Dev	11.76

Turn on : 27.2403

Width : 2.540

Height : 10.062



B0L002S, U5-ch97

calib_packv5_042523_0143.root, FC#8, port C1

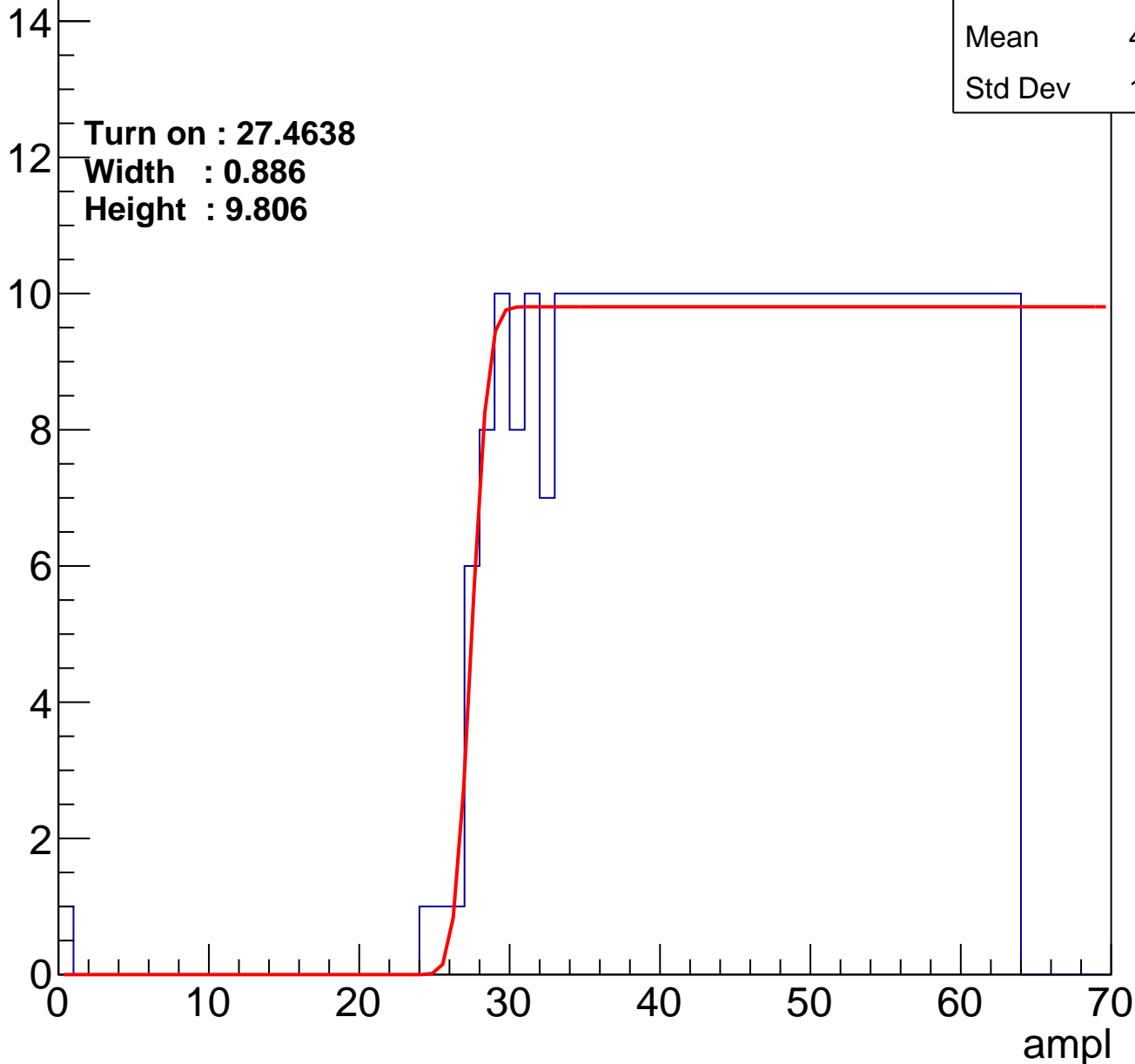
Entry

Entries	363
Mean	45.19
Std Dev	10.83

Turn on : 27.4638

Width : 0.886

Height : 9.806



B0L002S, U5-ch98

calib_packv5_042523_0143.root, FC#8, port C1

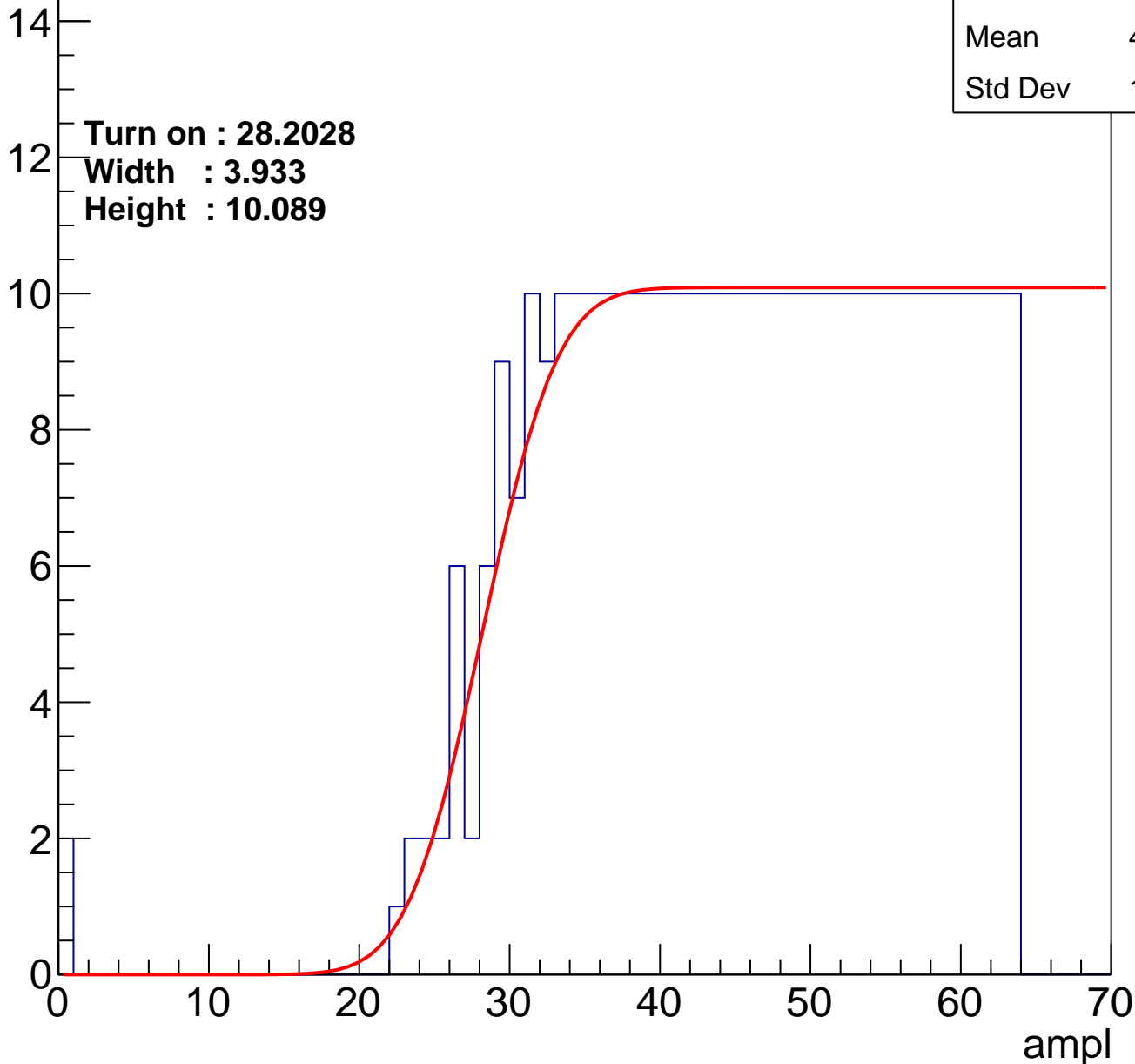
Entry

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 28.2028

Width : 3.933

Height : 10.089



B0L002S, U5-ch99

calib_packv5_042523_0143.root, FC#8, port C1

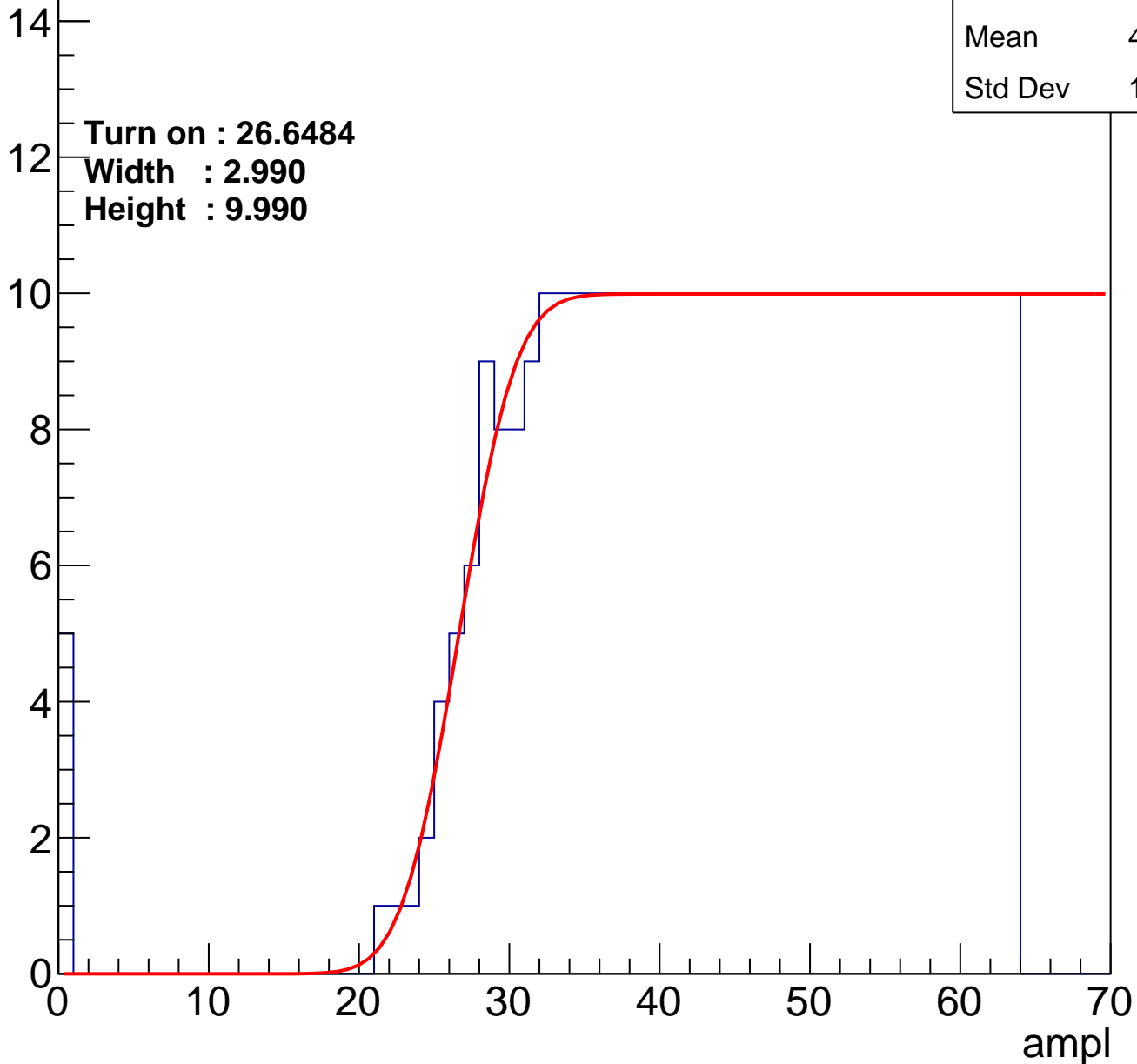
Entry

Entries	379
Mean	44.09
Std Dev	12.06

Turn on : 26.6484

Width : 2.990

Height : 9.990



B0L002S, U5-ch100

calib_packv5_042523_0143.root, FC#8, port C1

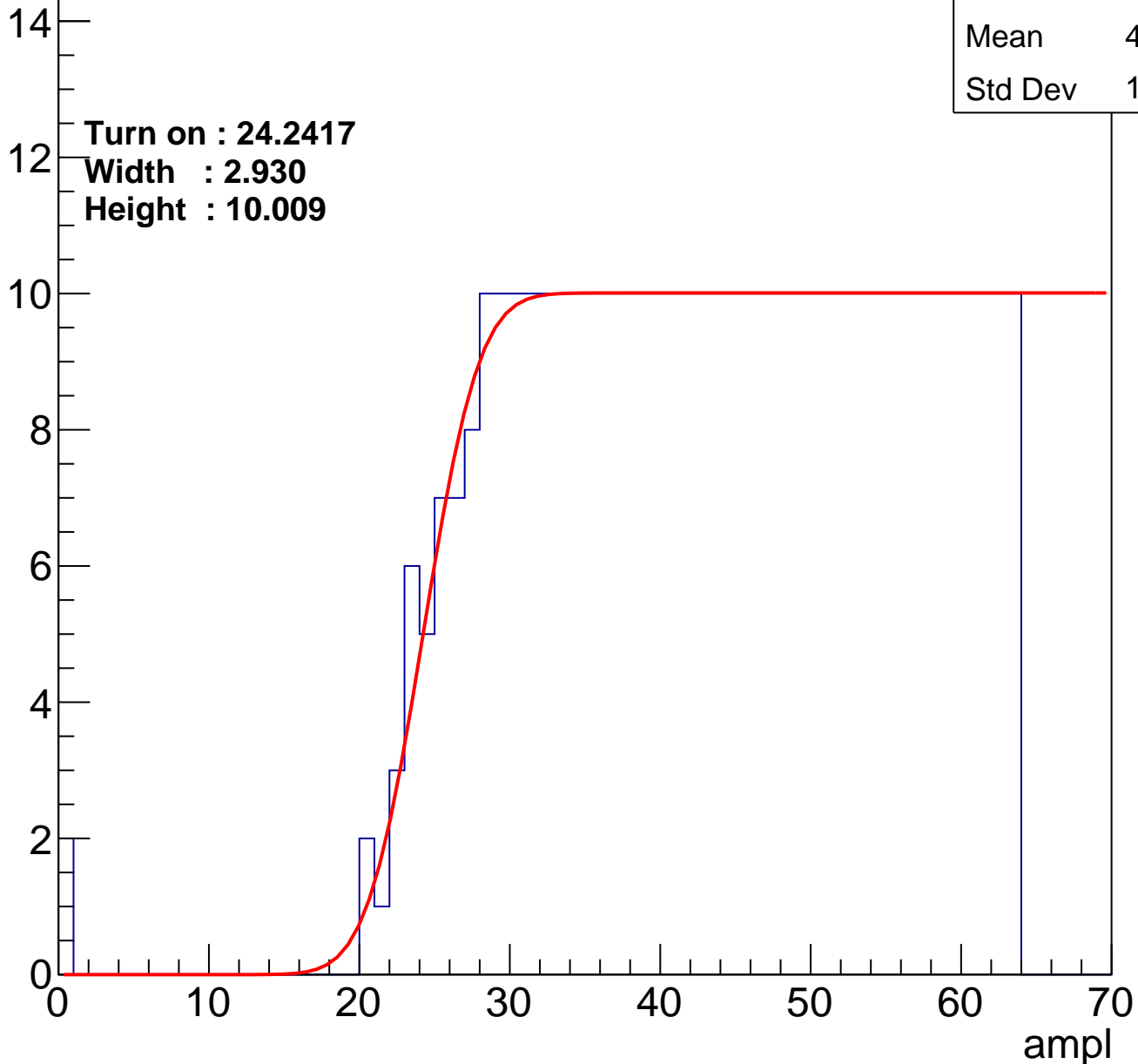
Entries	401
Mean	43.24
Std Dev	12.05

Turn on : 24.2417

Width : 2.930

Height : 10.009

Entry



B0L002S, U5-ch101

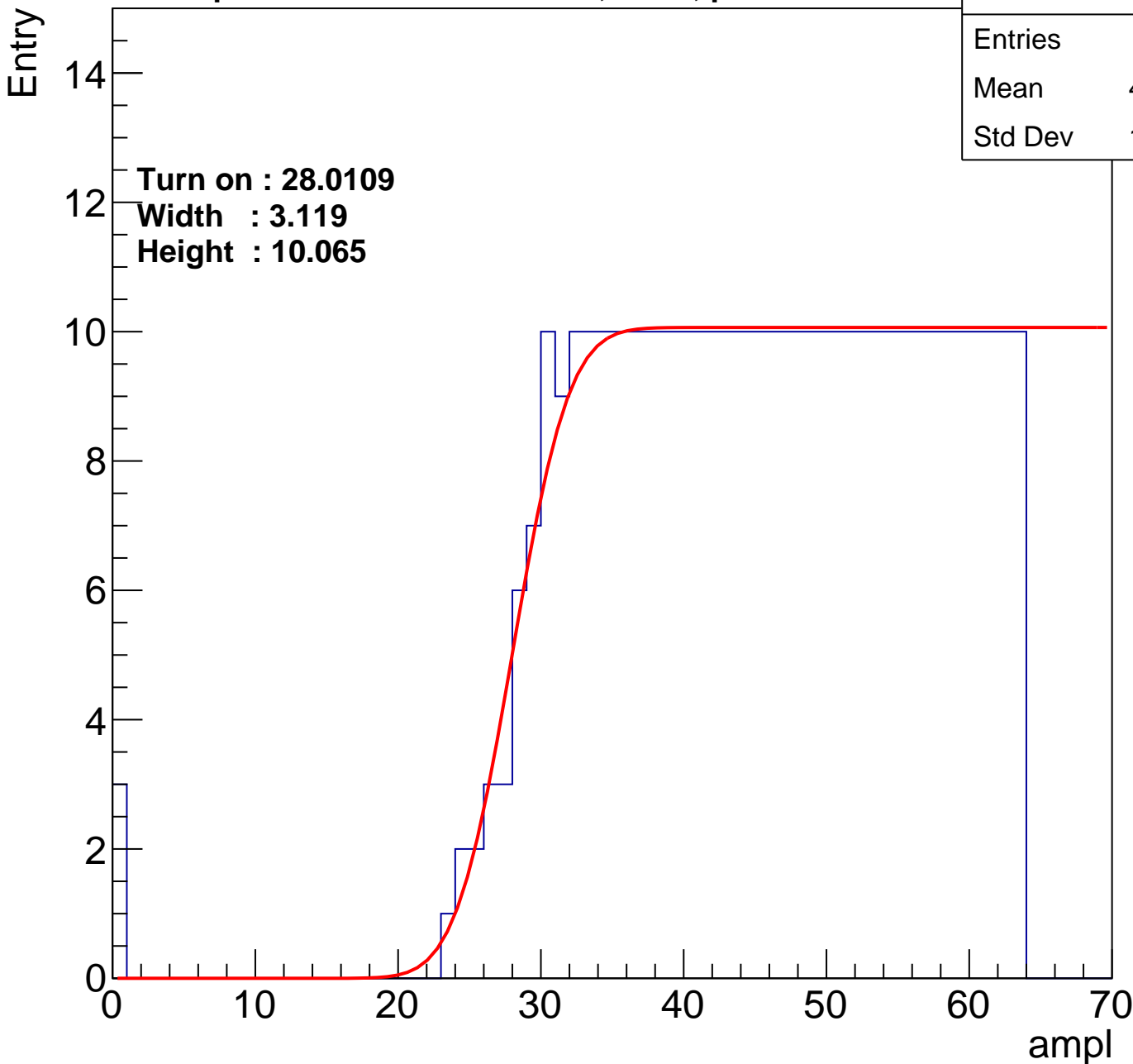
calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 28.0109

Width : 3.119

Height : 10.065



B0L002S, U5-ch102

calib_packv5_042523_0143.root, FC#8, port C1

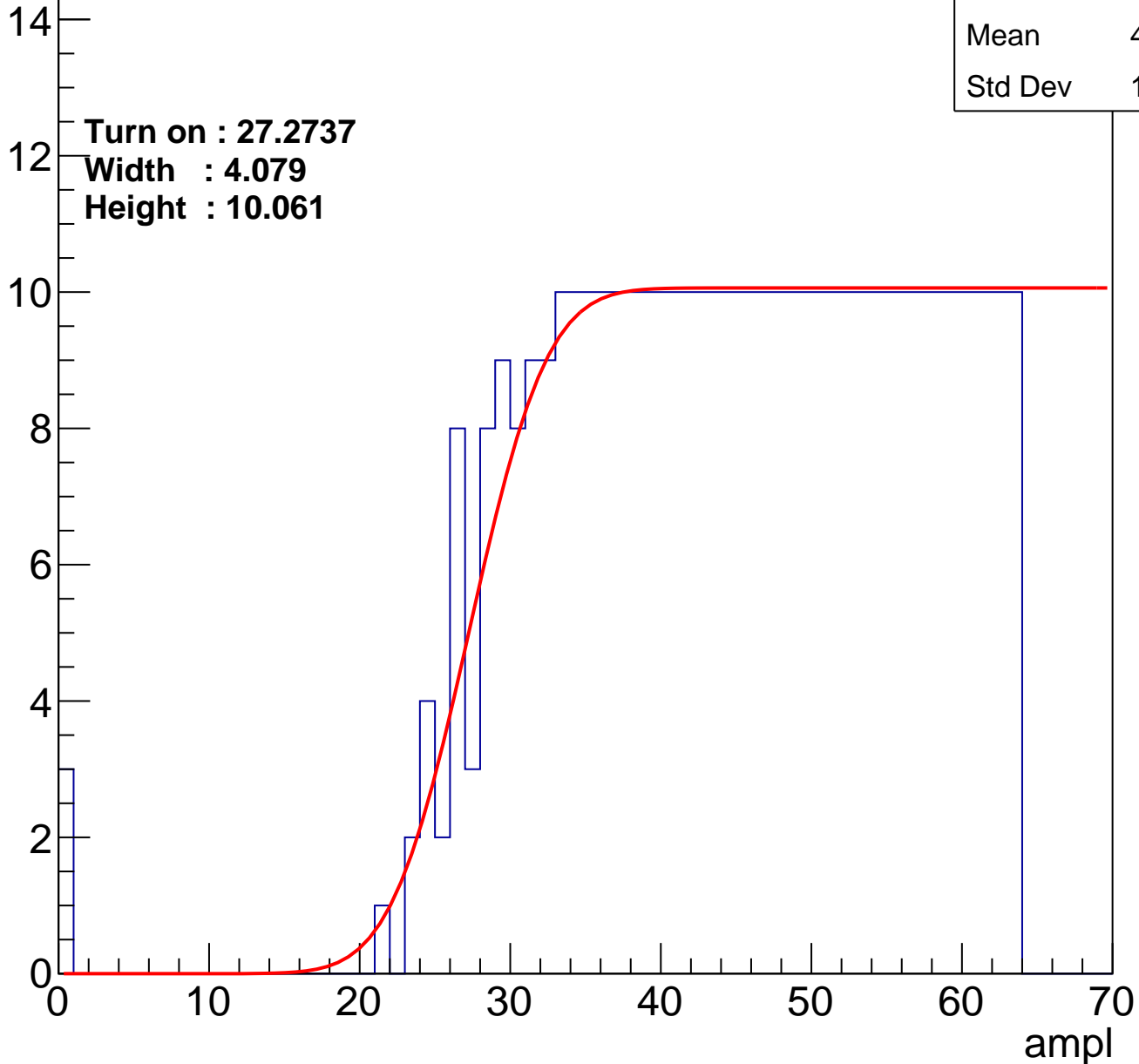
Entries	376
Mean	44.35
Std Dev	11.67

Turn on : 27.2737

Width : 4.079

Height : 10.061

Entry



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.6076
Width : 2.943
Height : 10.011



B0L002S, U5-ch104

calib_packv5_042523_0143.root, FC#8, port C1

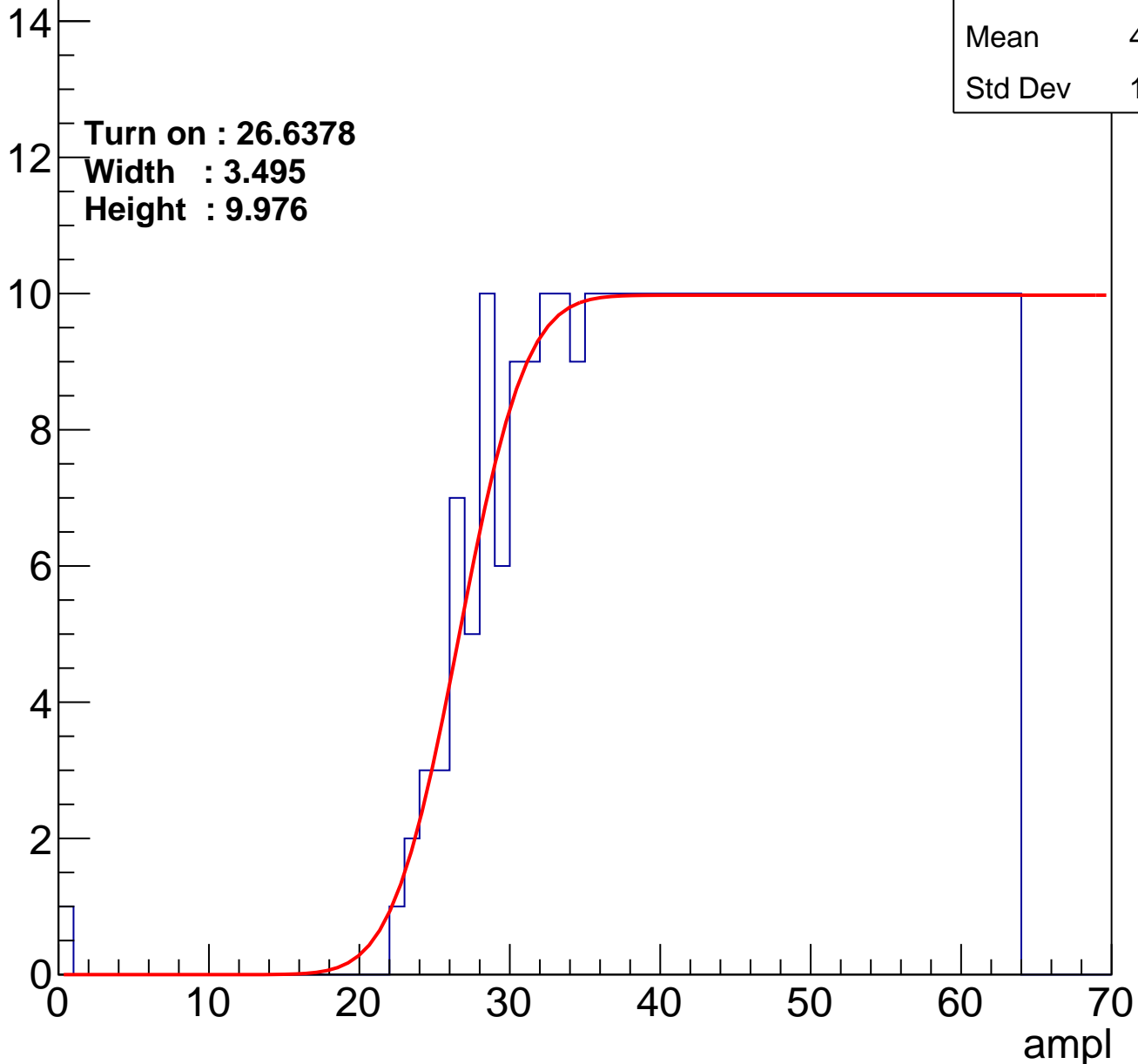
Entries	375
Mean	44.54
Std Dev	11.25

Turn on : 26.6378

Width : 3.495

Height : 9.976

Entry



B0L002S, U5-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.45
Std Dev	11.38

Turn on : 26.6166

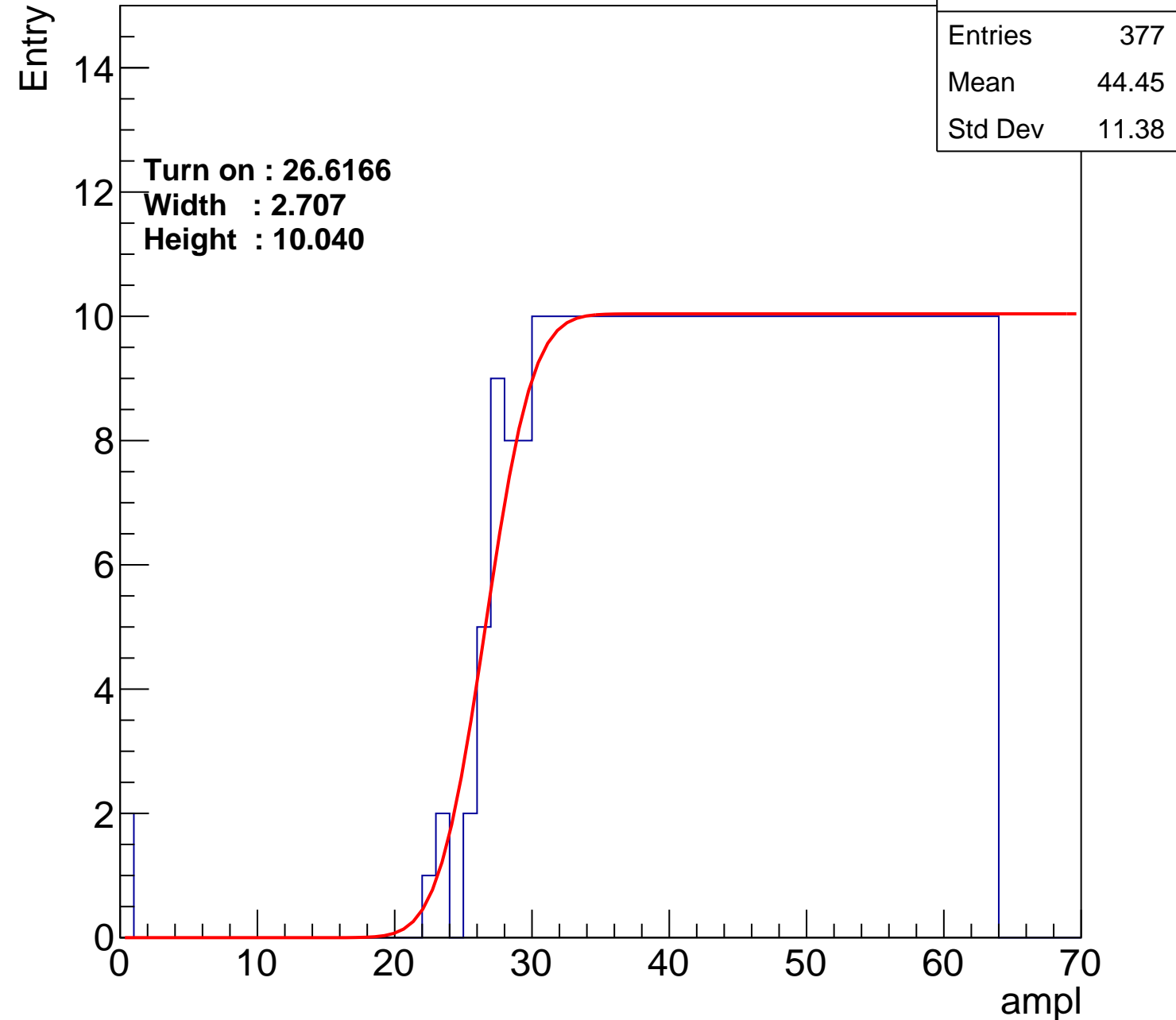
Width : 2.707

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch106

calib_packv5_042523_0143.root, FC#8, port C1

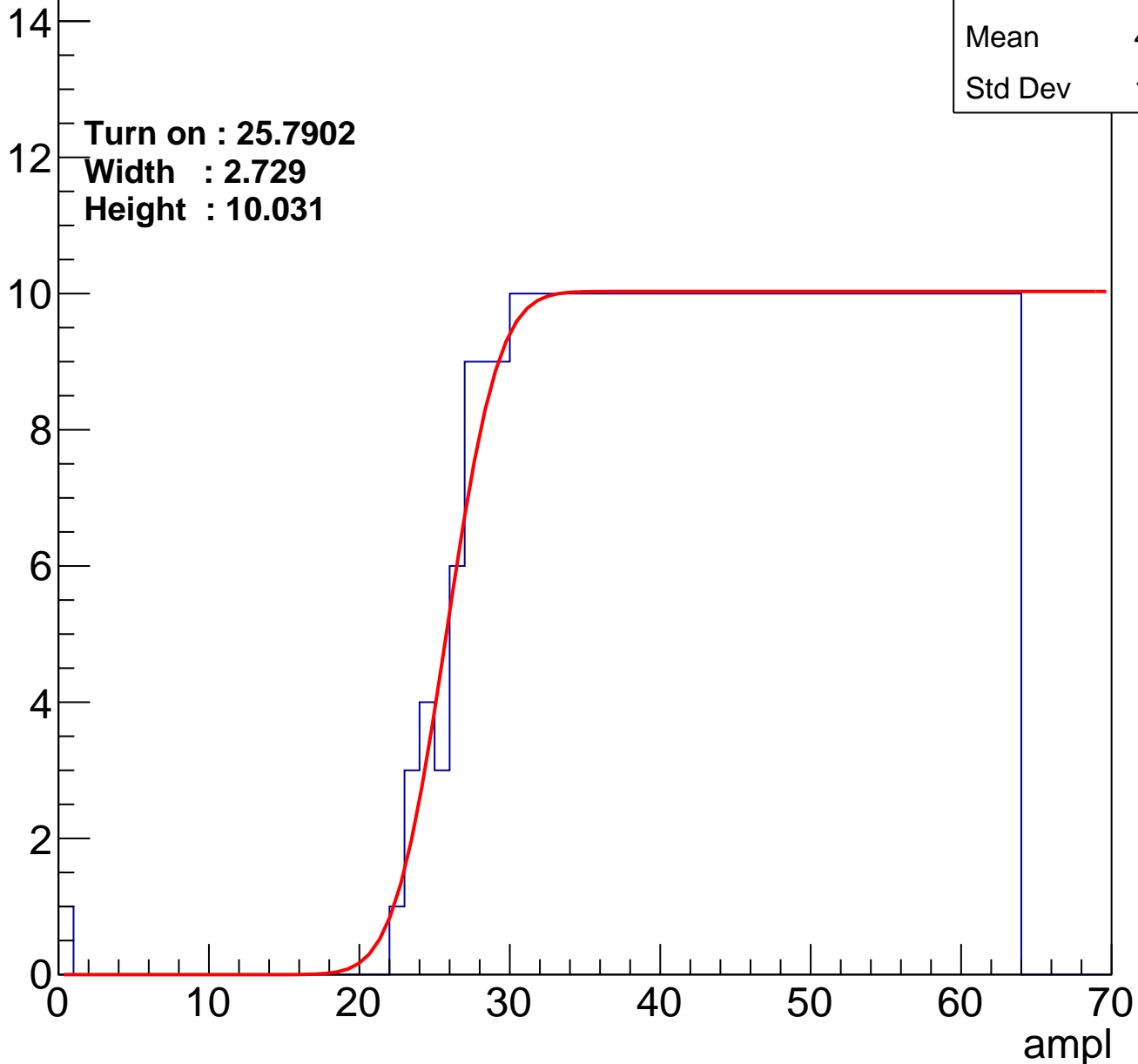
Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.7902

Width : 2.729

Height : 10.031

Entry



B0L002S, U5-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.12
Std Dev	11.24

Turn on : 28.4965

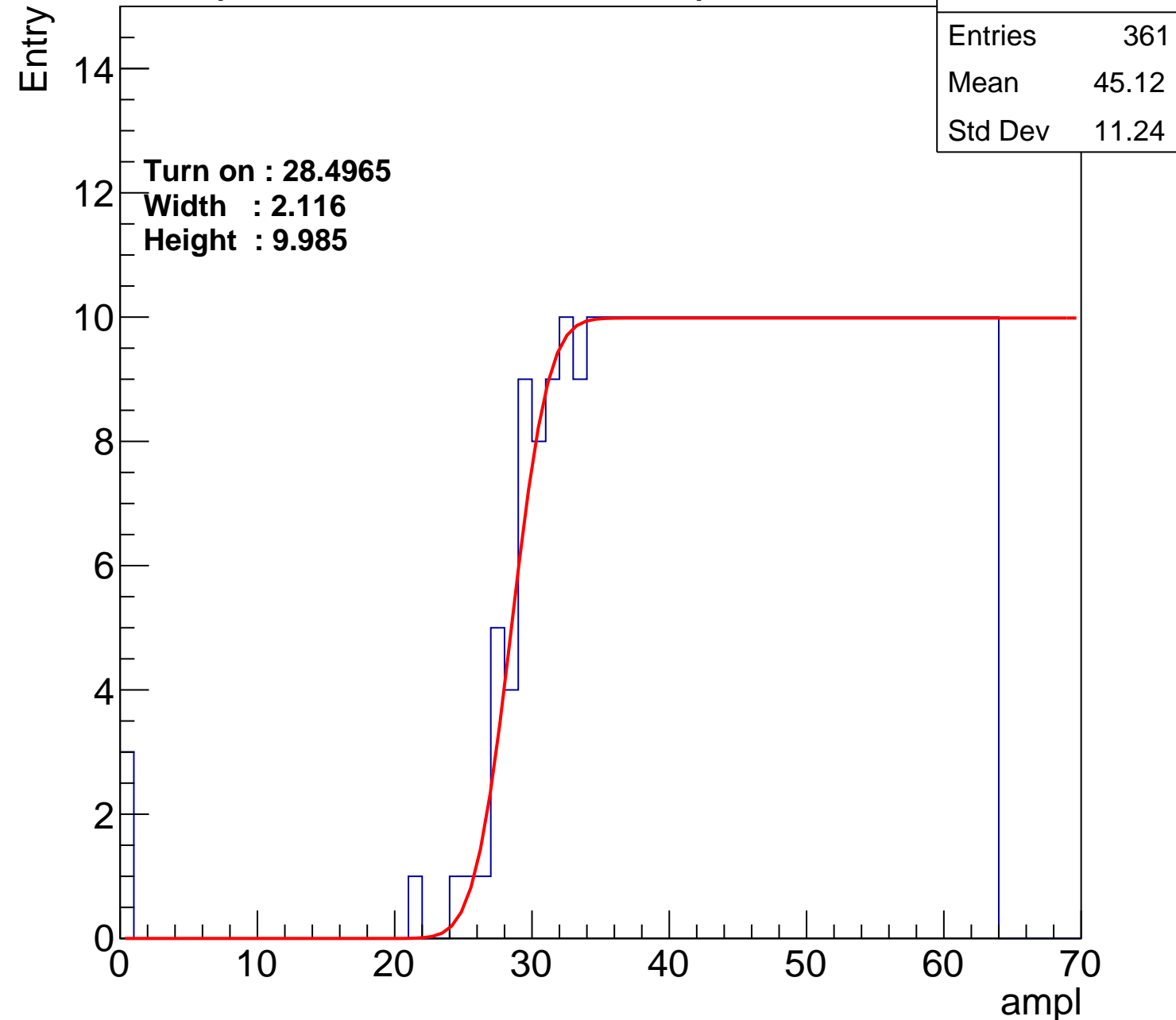
Width : 2.116

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.81
Std Dev	11.72

Turn on : 25.2781

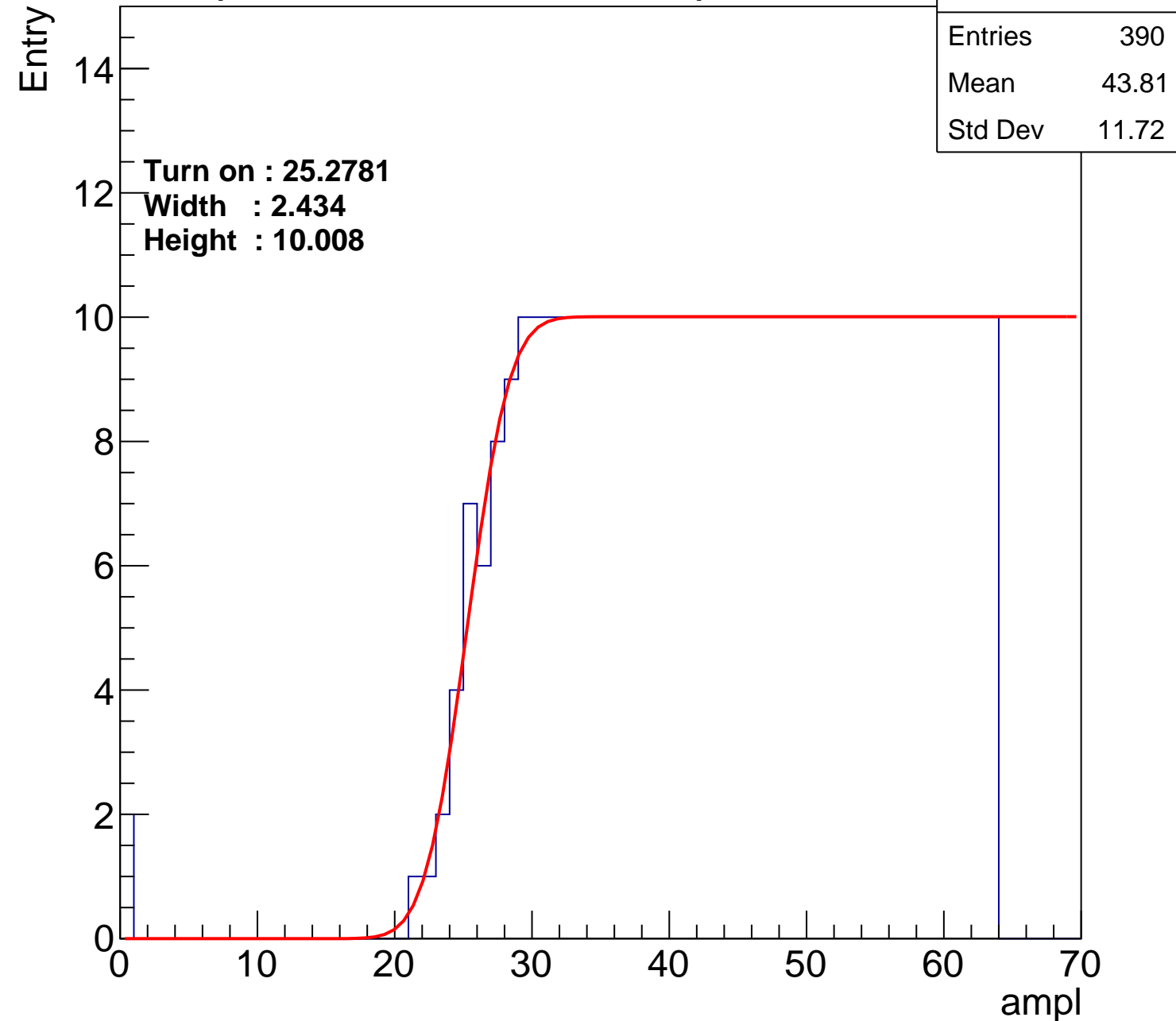
Width : 2.434

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch109

calib_packv5_042523_0143.root, FC#8, port C1

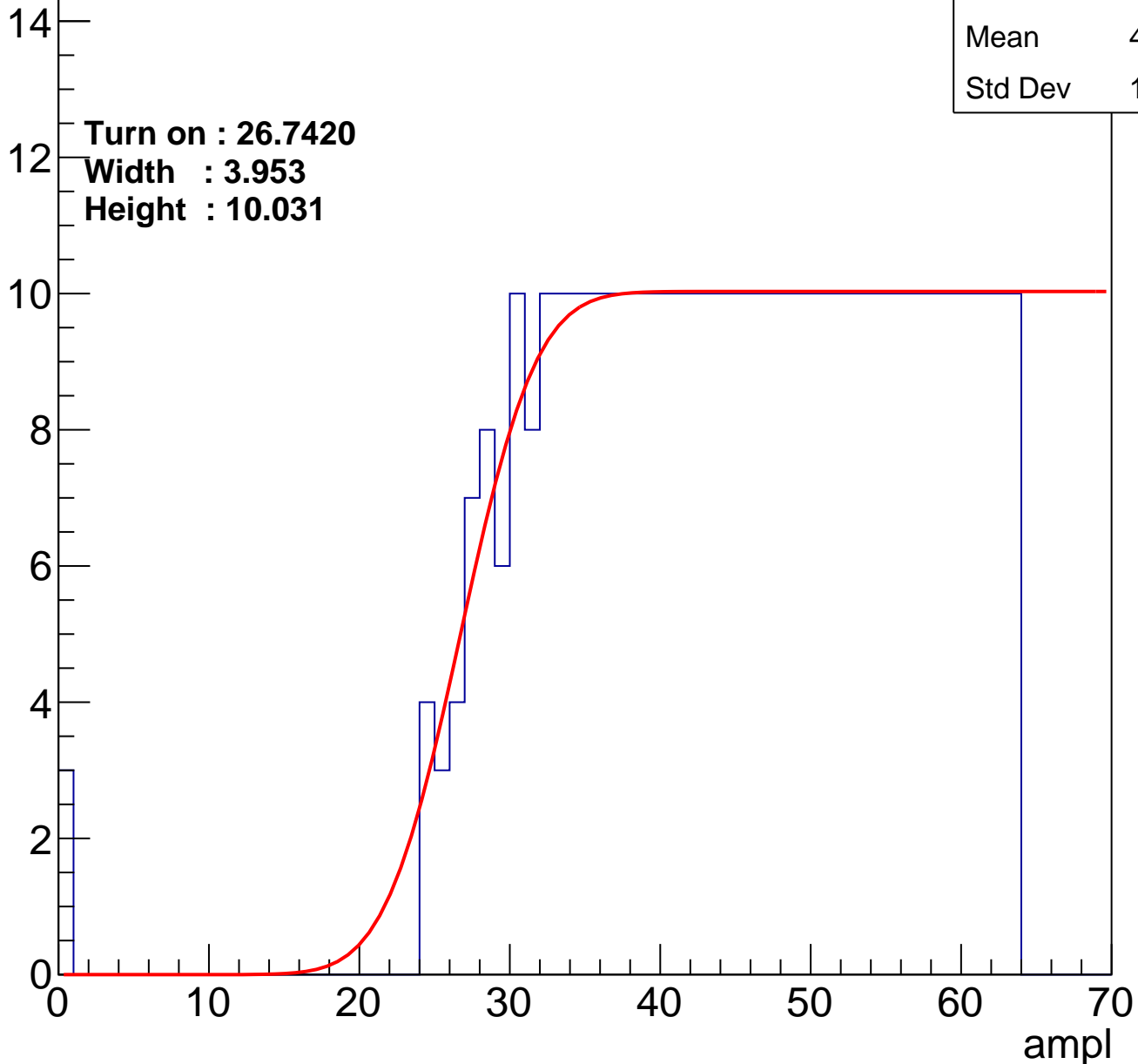
Entries	373
Mean	44.53
Std Dev	11.53

Turn on : 26.7420

Width : 3.953

Height : 10.031

Entry



B0L002S, U5-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.06
Std Dev	11.75

Turn on : 25.9876

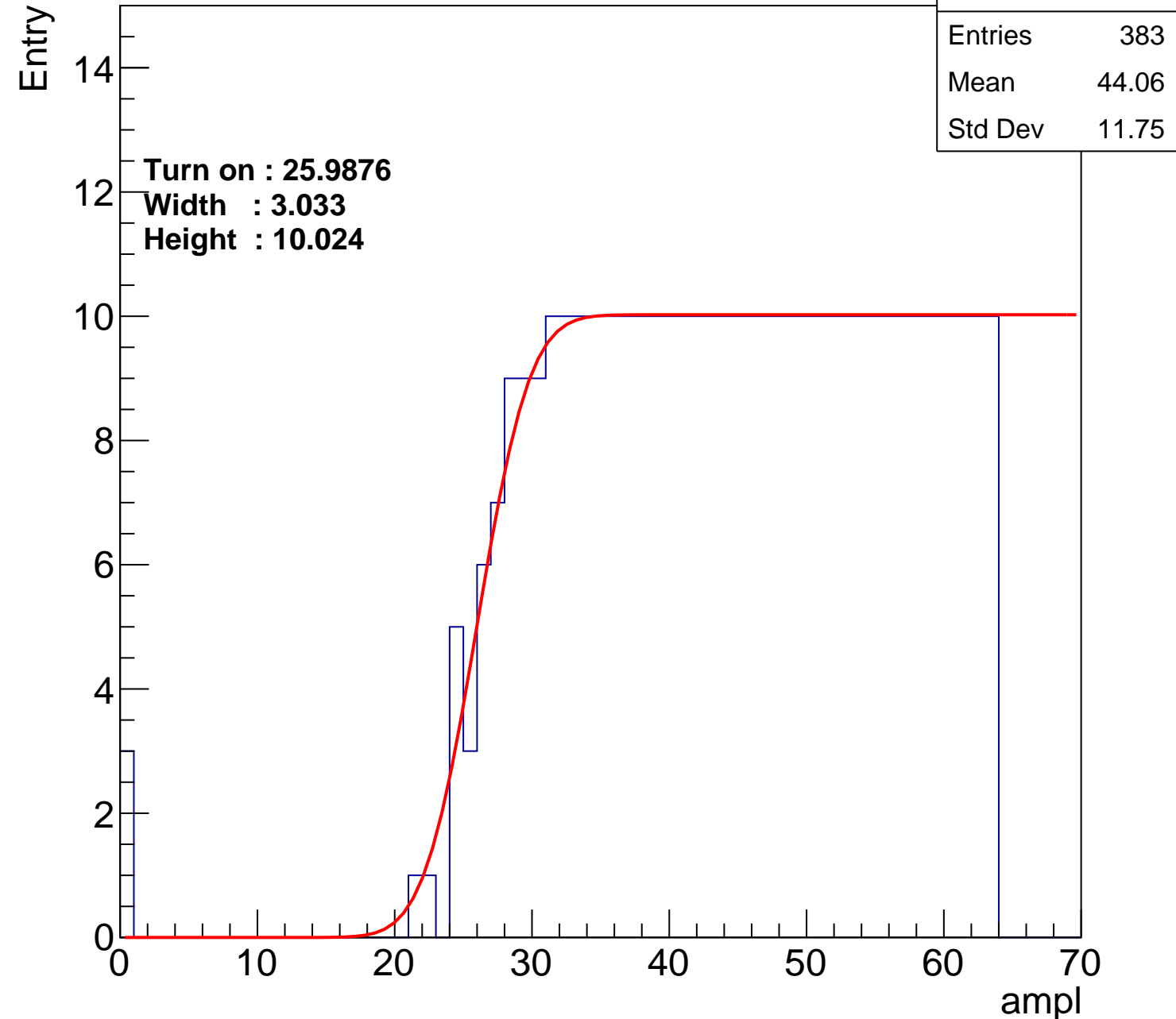
Width : 3.033

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 27.7637

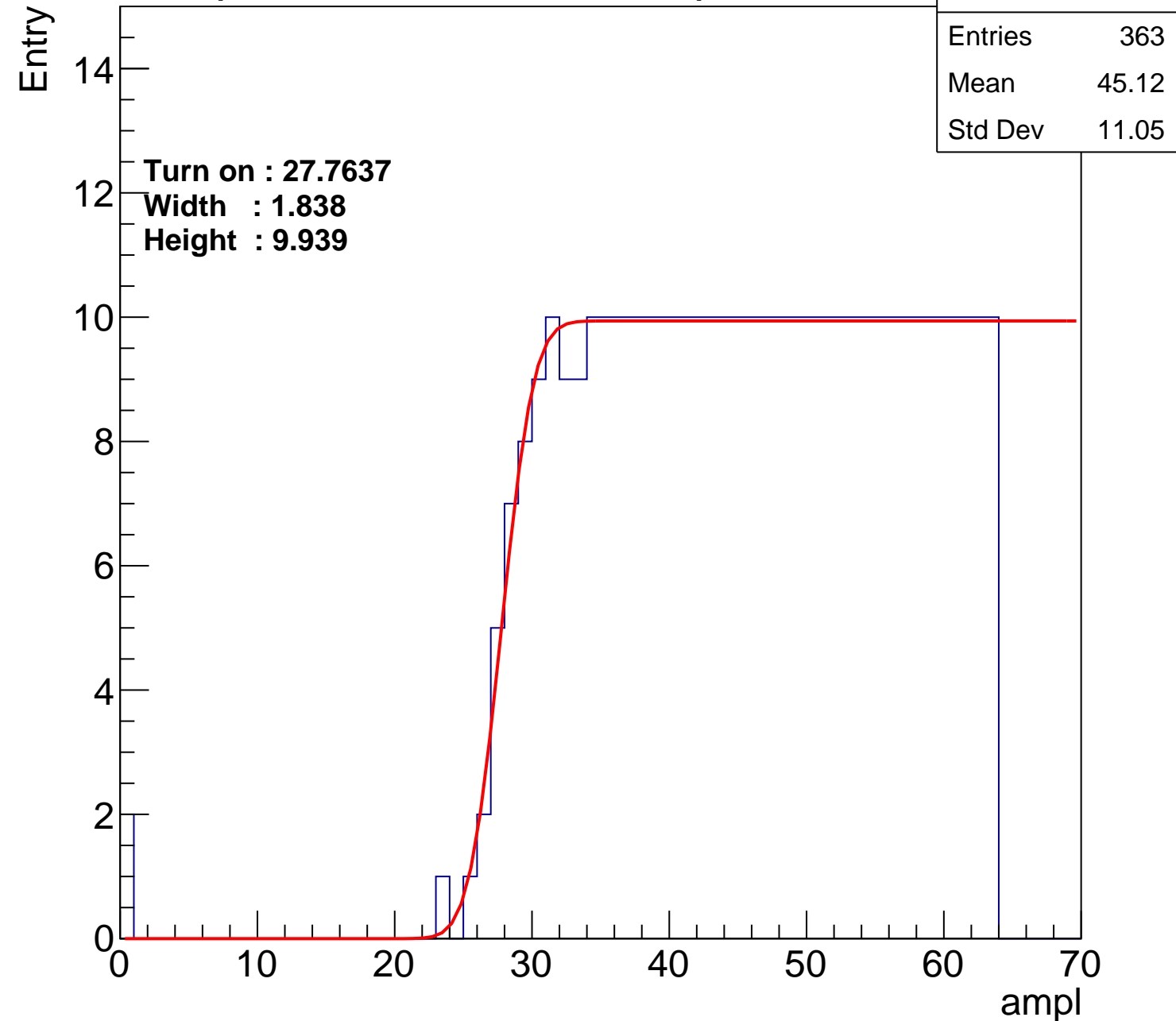
Width : 1.838

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.44
Std Dev	10.91

Turn on : 28.9136

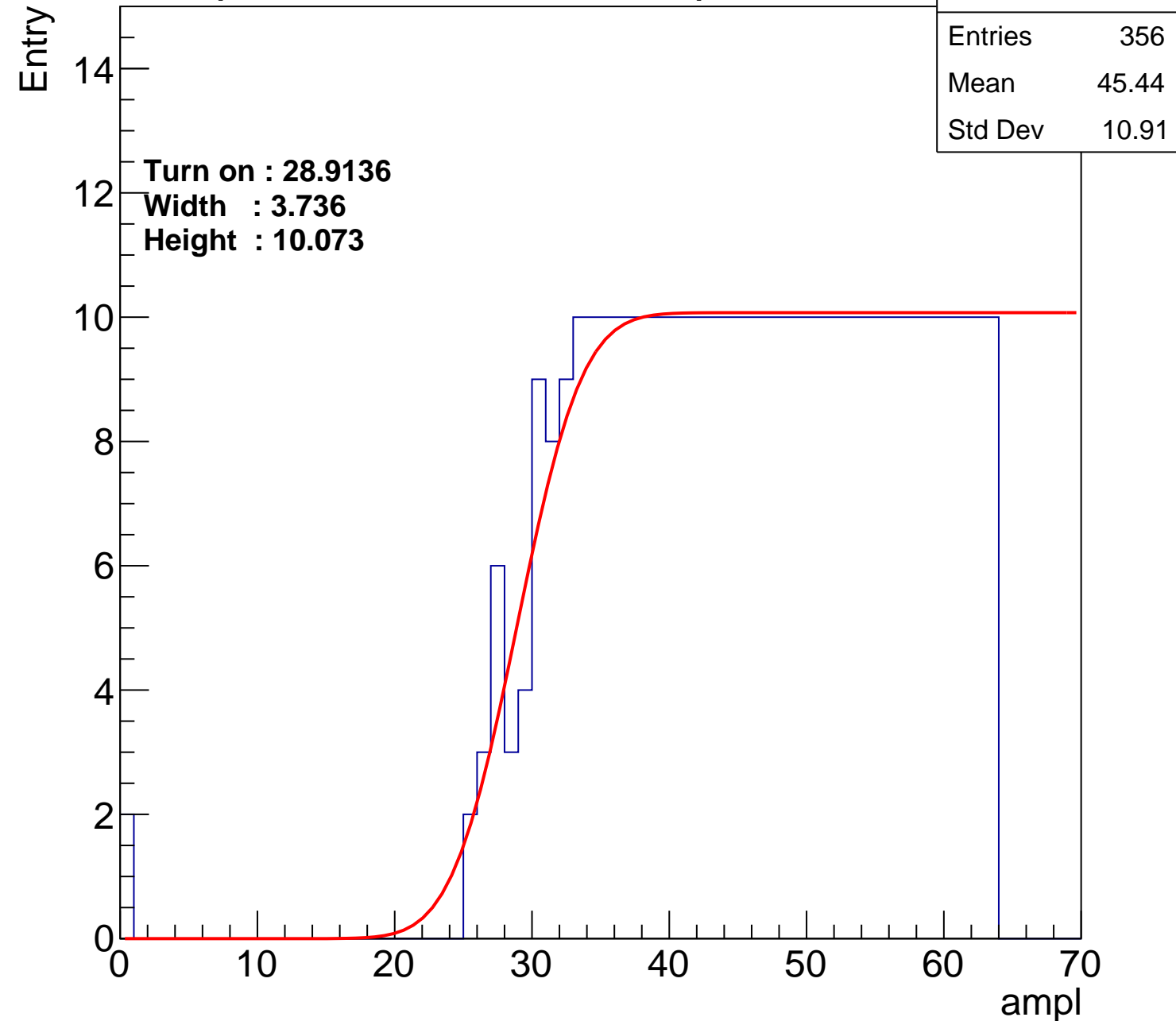
Width : 3.736

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch113

calib_packv5_042523_0143.root, FC#8, port C1

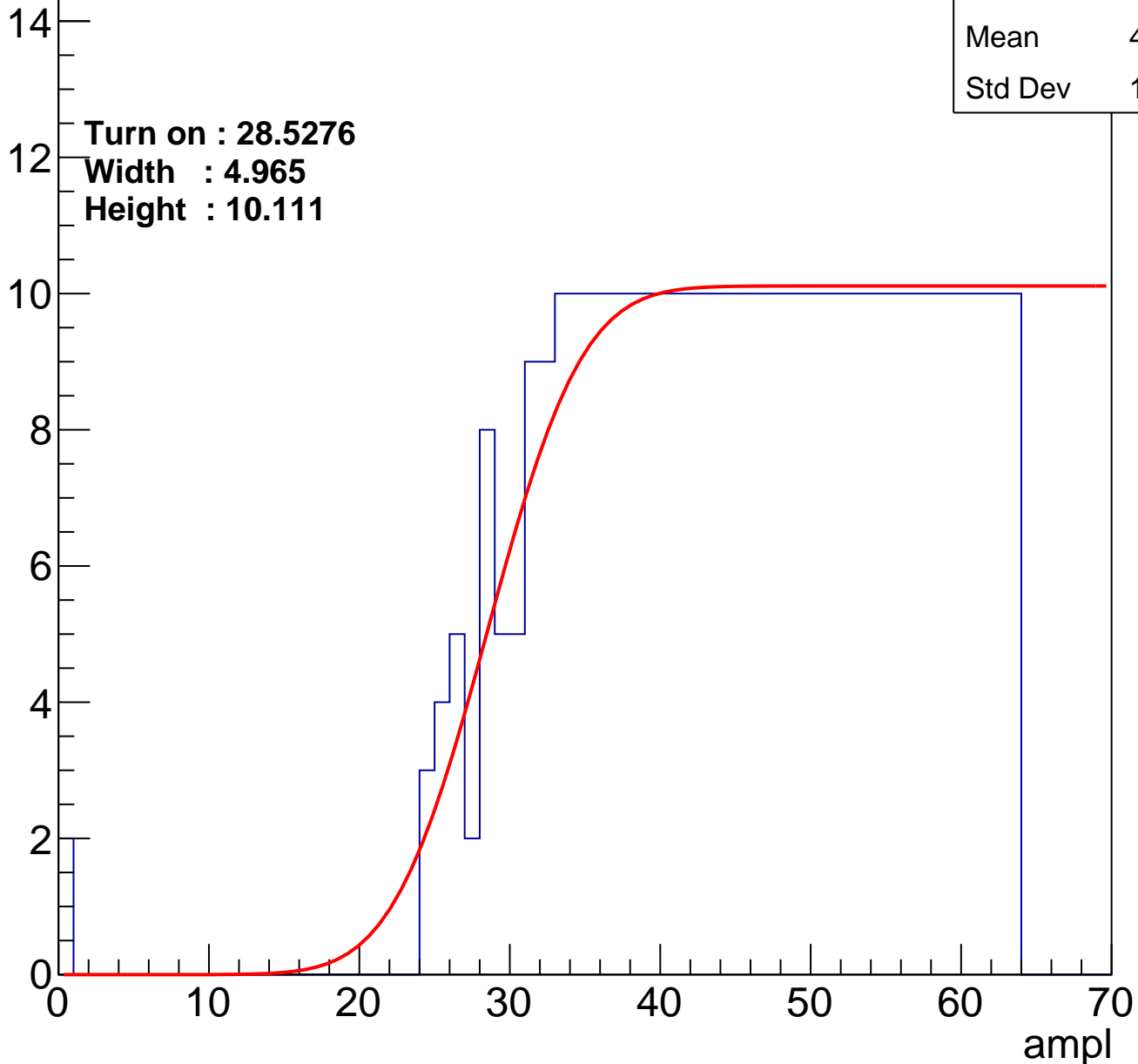
Entries	362
Mean	45.09
Std Dev	11.15

Turn on : 28.5276

Width : 4.965

Height : 10.111

Entry



B0L002S, U5-ch114

calib_packv5_042523_0143.root, FC#8, port C1

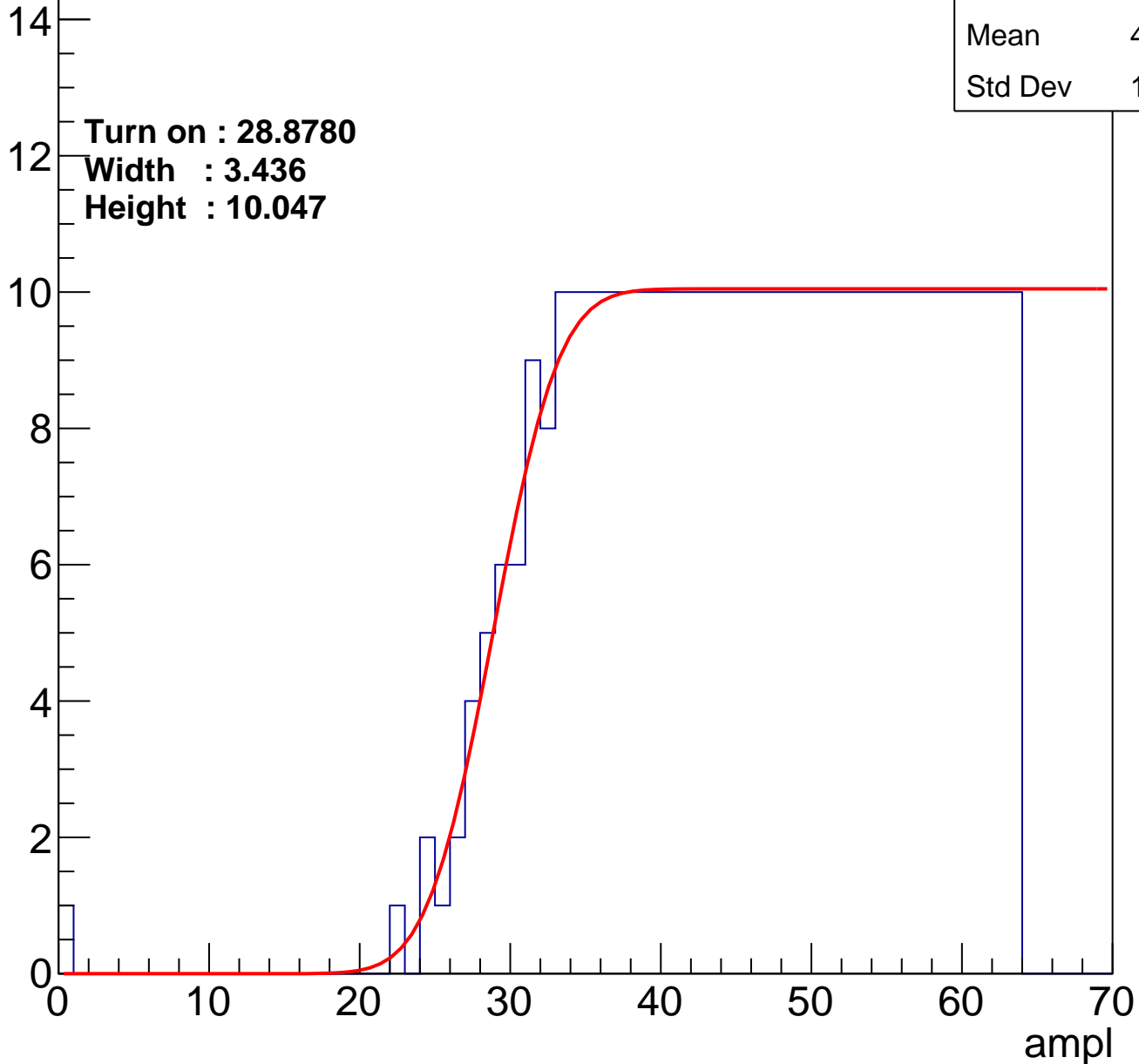
Entries	355
Mean	45.53
Std Dev	10.72

Turn on : 28.8780

Width : 3.436

Height : 10.047

Entry



B0L002S, U5-ch115

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.99
Std Dev	10.44

Turn on : 29.7488

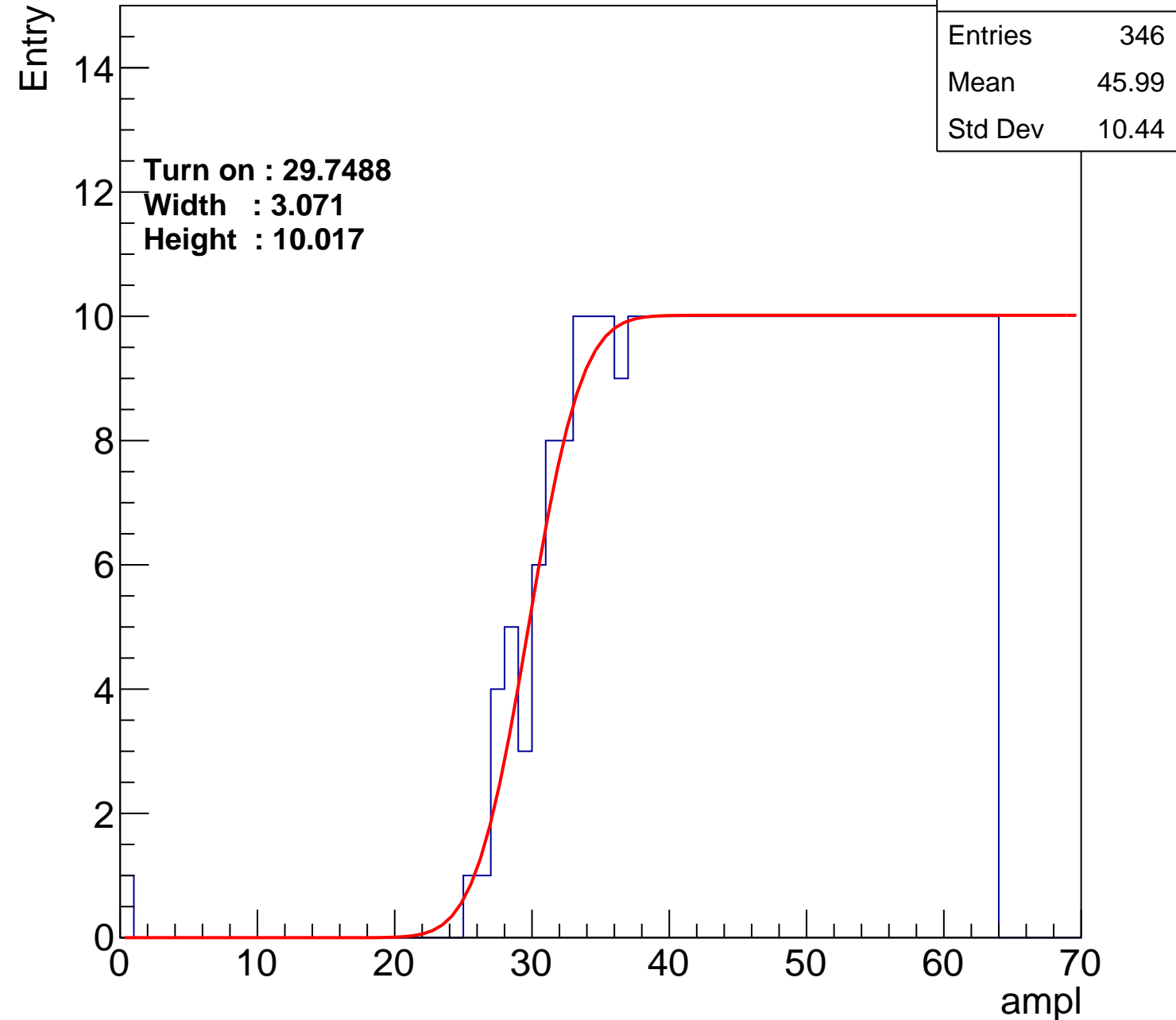
Width : 3.071

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.2
Std Dev	11.82

Turn on : 26.6800

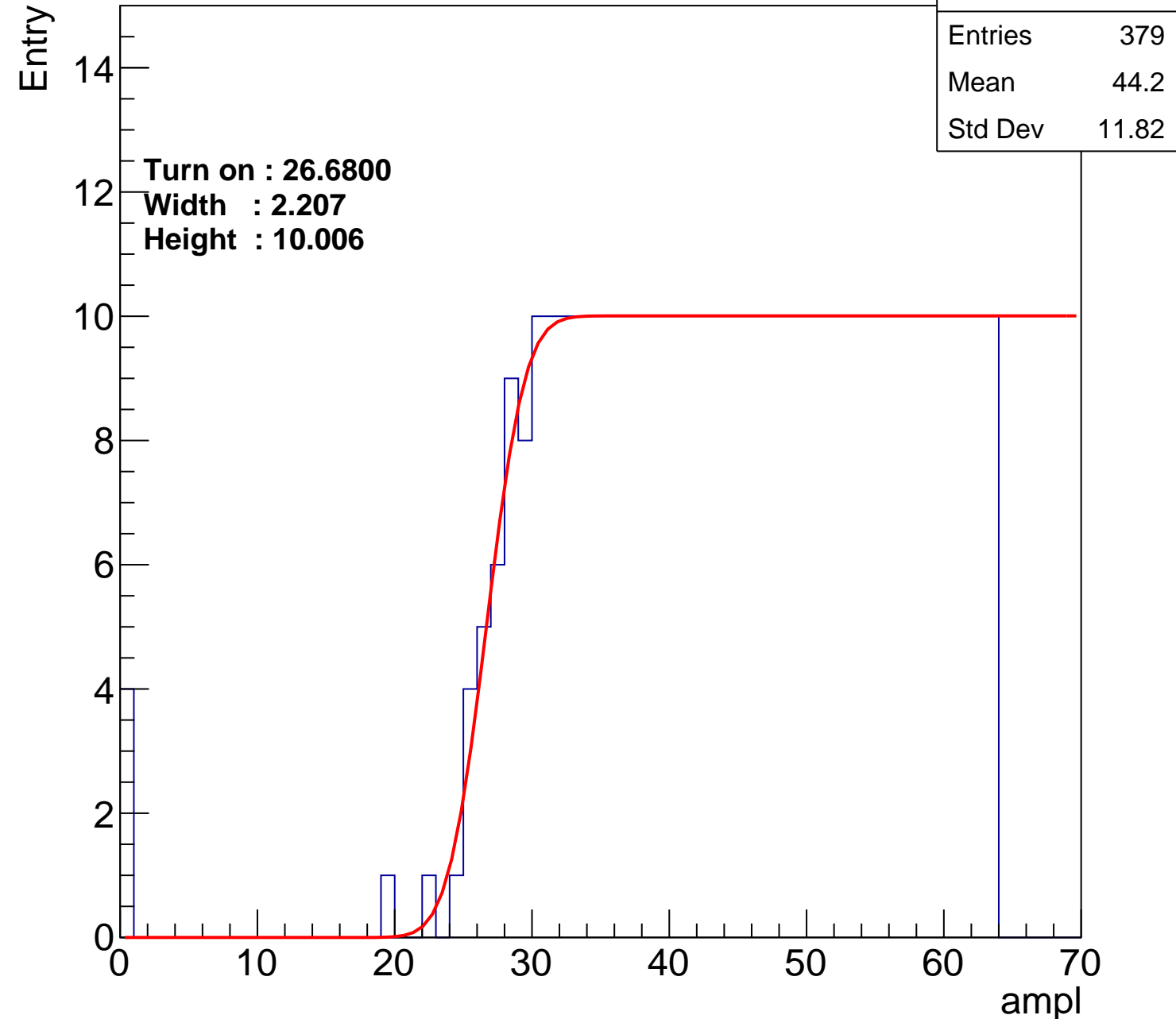
Width : 2.207

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch117

calib_packv5_042523_0143.root, FC#8, port C1

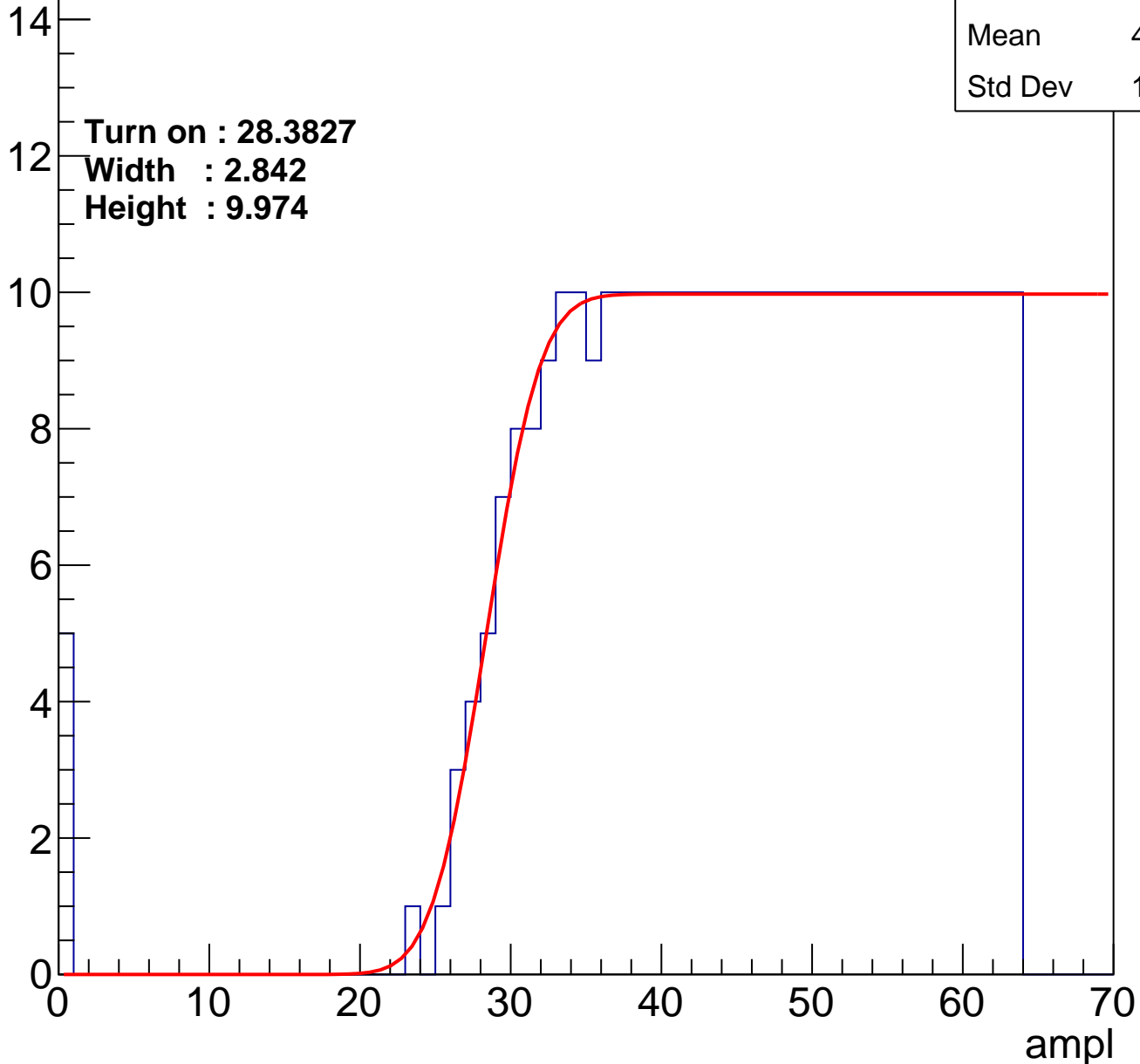
Entries	360
Mean	44.99
Std Dev	11.67

Turn on : 28.3827

Width : 2.842

Height : 9.974

Entry



B0L002S, U5-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.77
Std Dev	11.29

Turn on : 27.5879

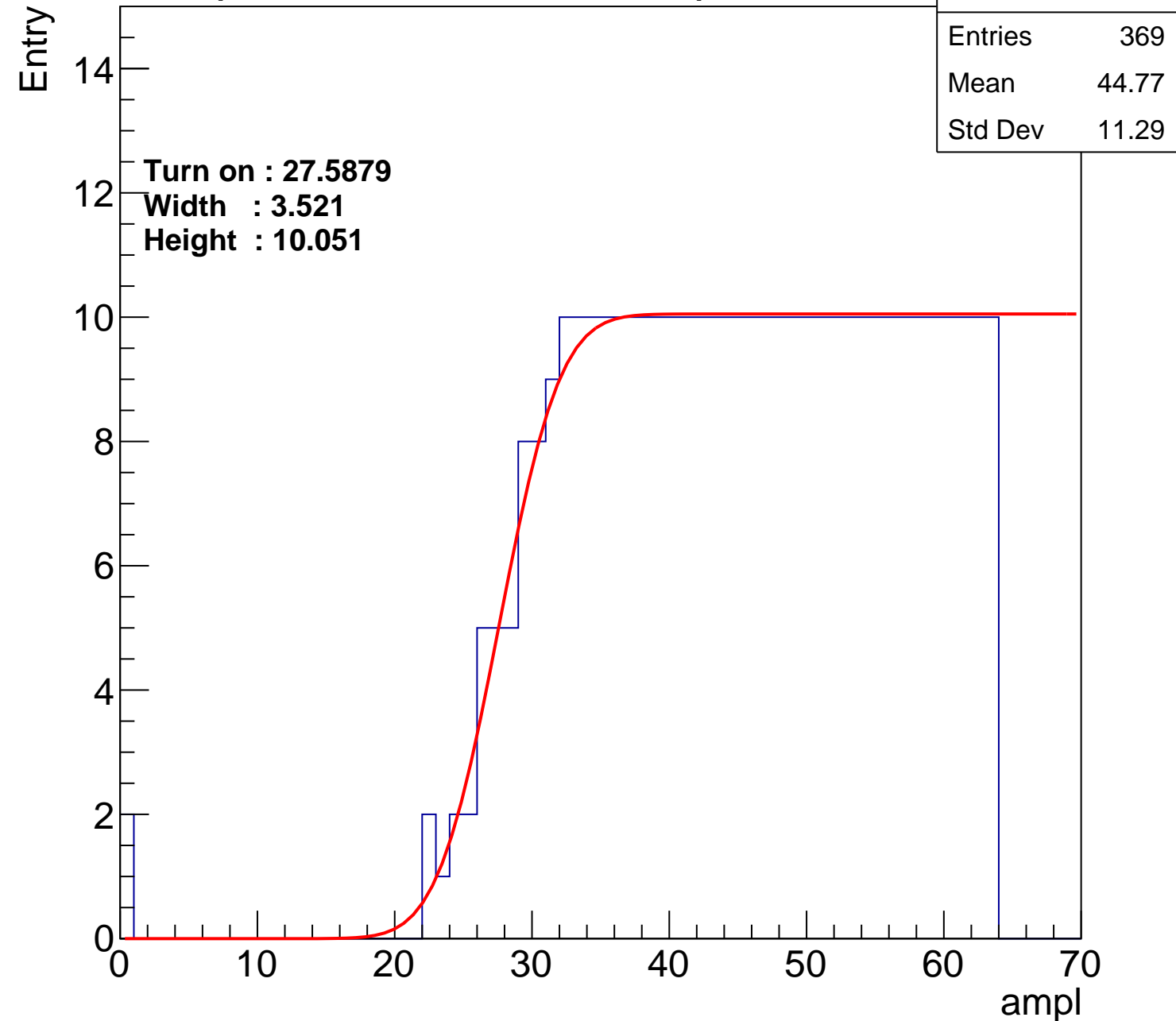
Width : 3.521

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch119

calib_packv5_042523_0143.root, FC#8, port C1

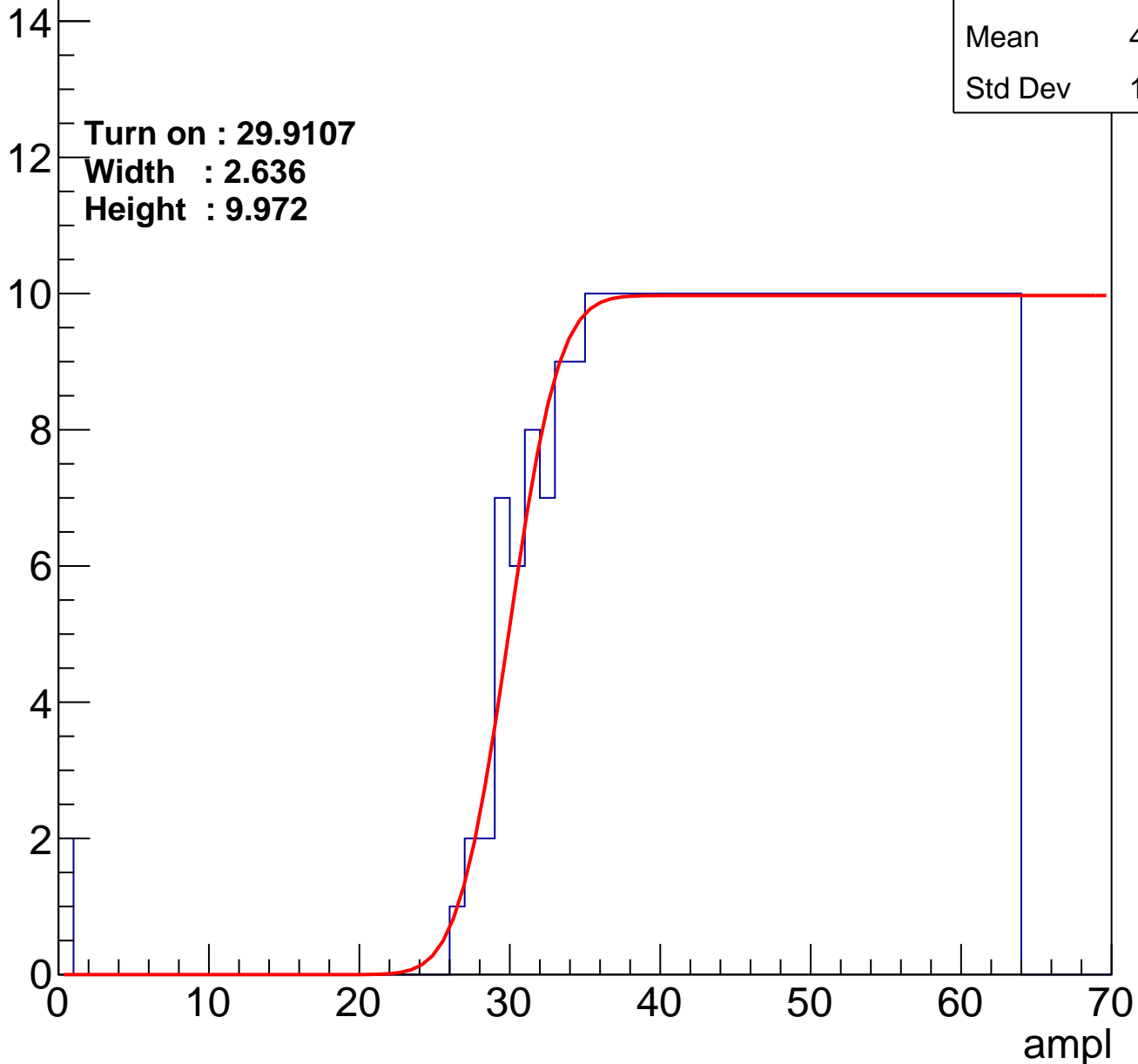
Entries	343
Mean	46.08
Std Dev	10.59

Turn on : 29.9107

Width : 2.636

Height : 9.972

Entry



B0L002S, U5-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.61
Std Dev	10.79

Turn on : 28.6957

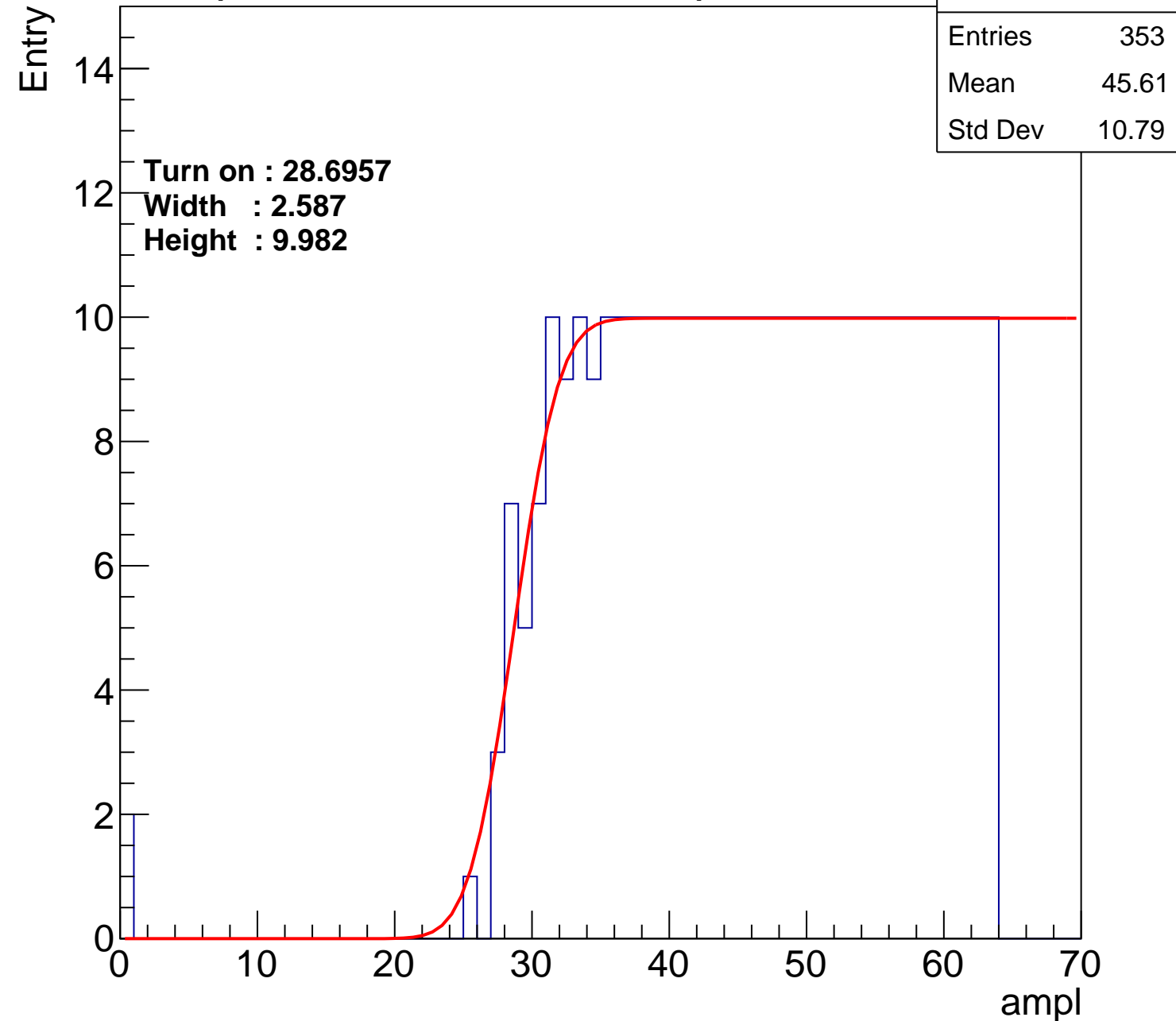
Width : 2.587

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.96
Std Dev	11.2

Turn on : 27.7385

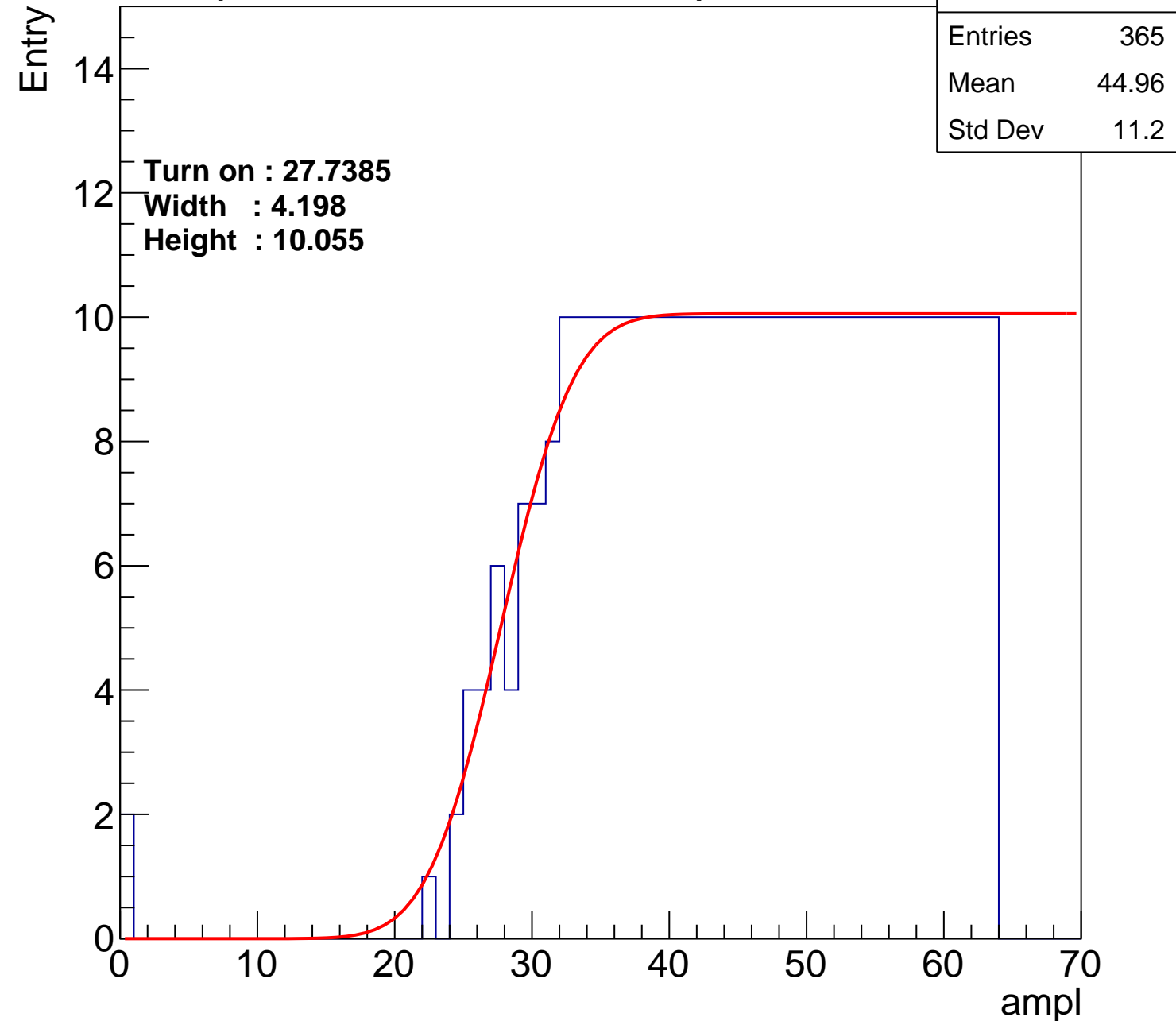
Width : 4.198

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.15
Std Dev	11.31

Turn on : 28.6975

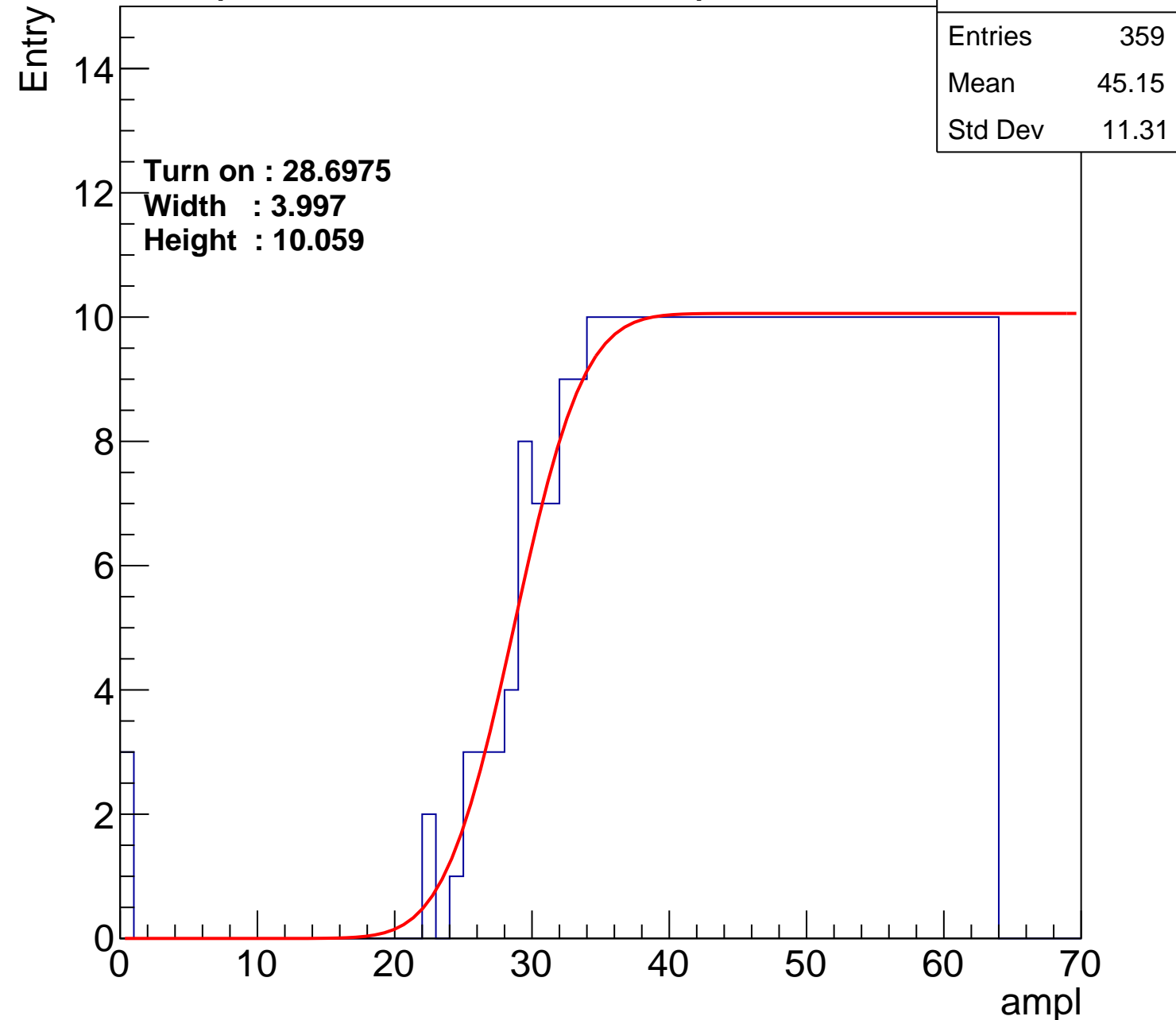
Width : 3.997

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.43
Std Dev	10.87

Turn on : 28.7877

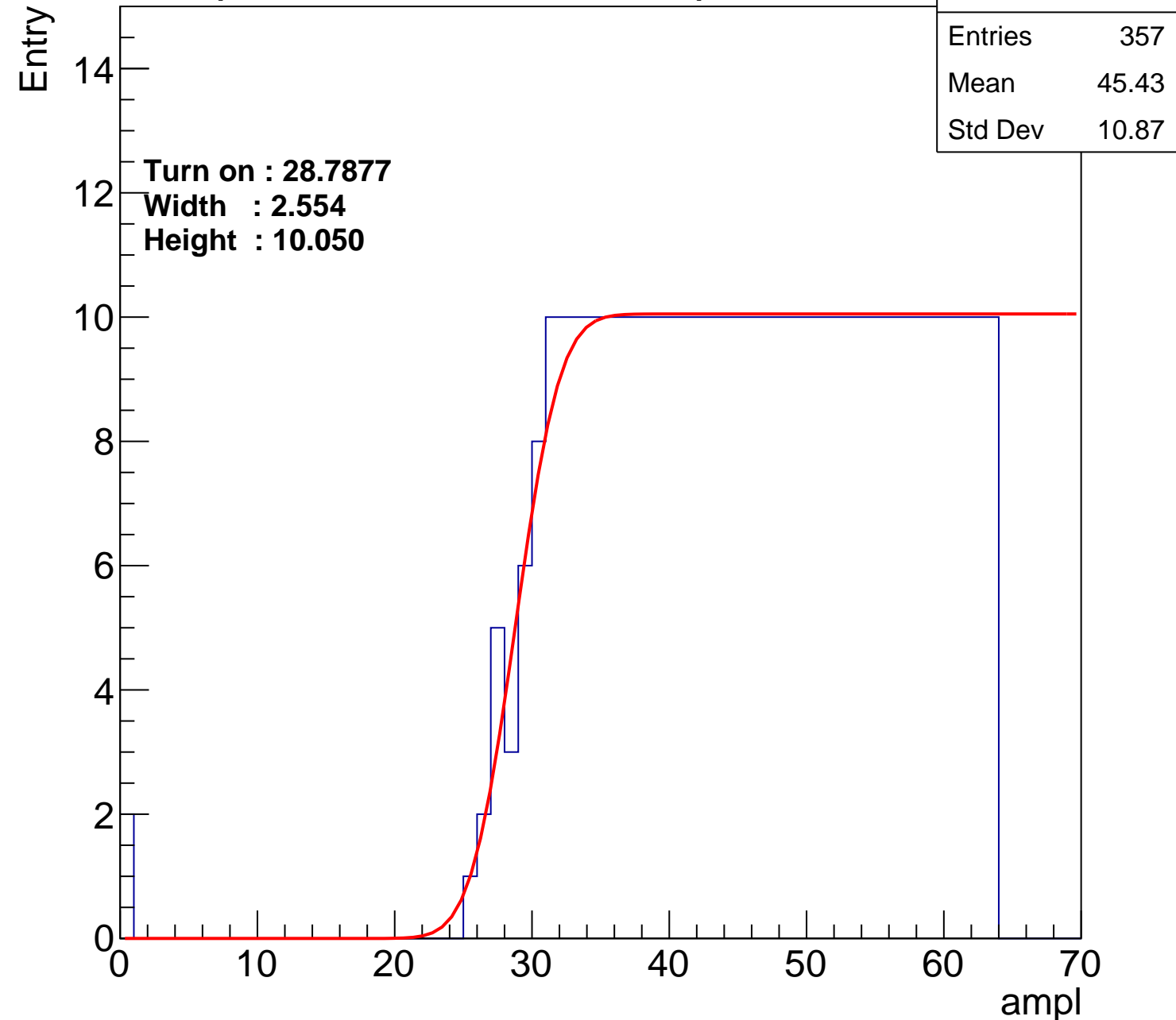
Width : 2.554

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.42
Std Dev	11.14

Turn on : 28.6298

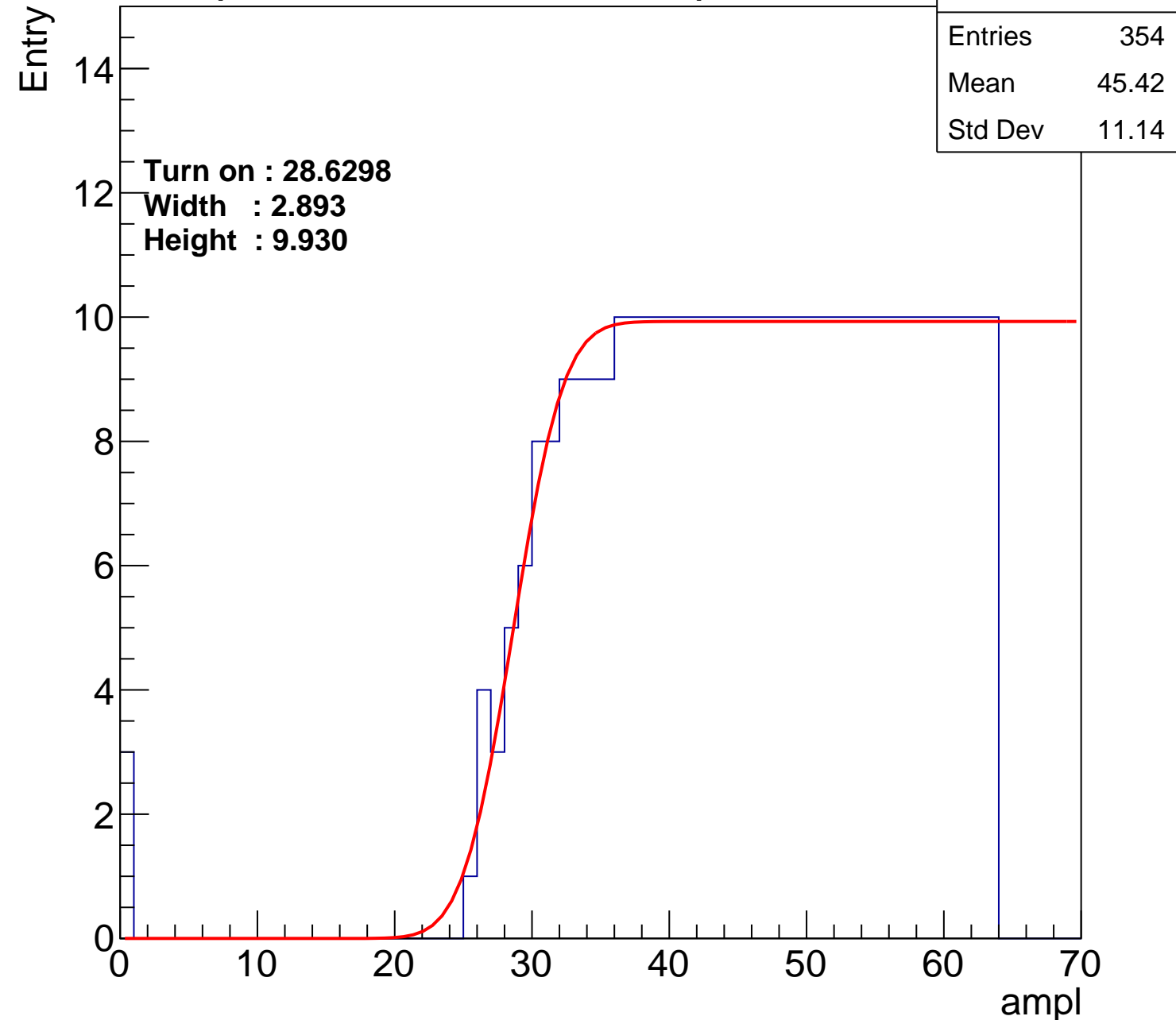
Width : 2.893

Height : 9.930

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.18
Std Dev	10.92

Turn on : 28.2601

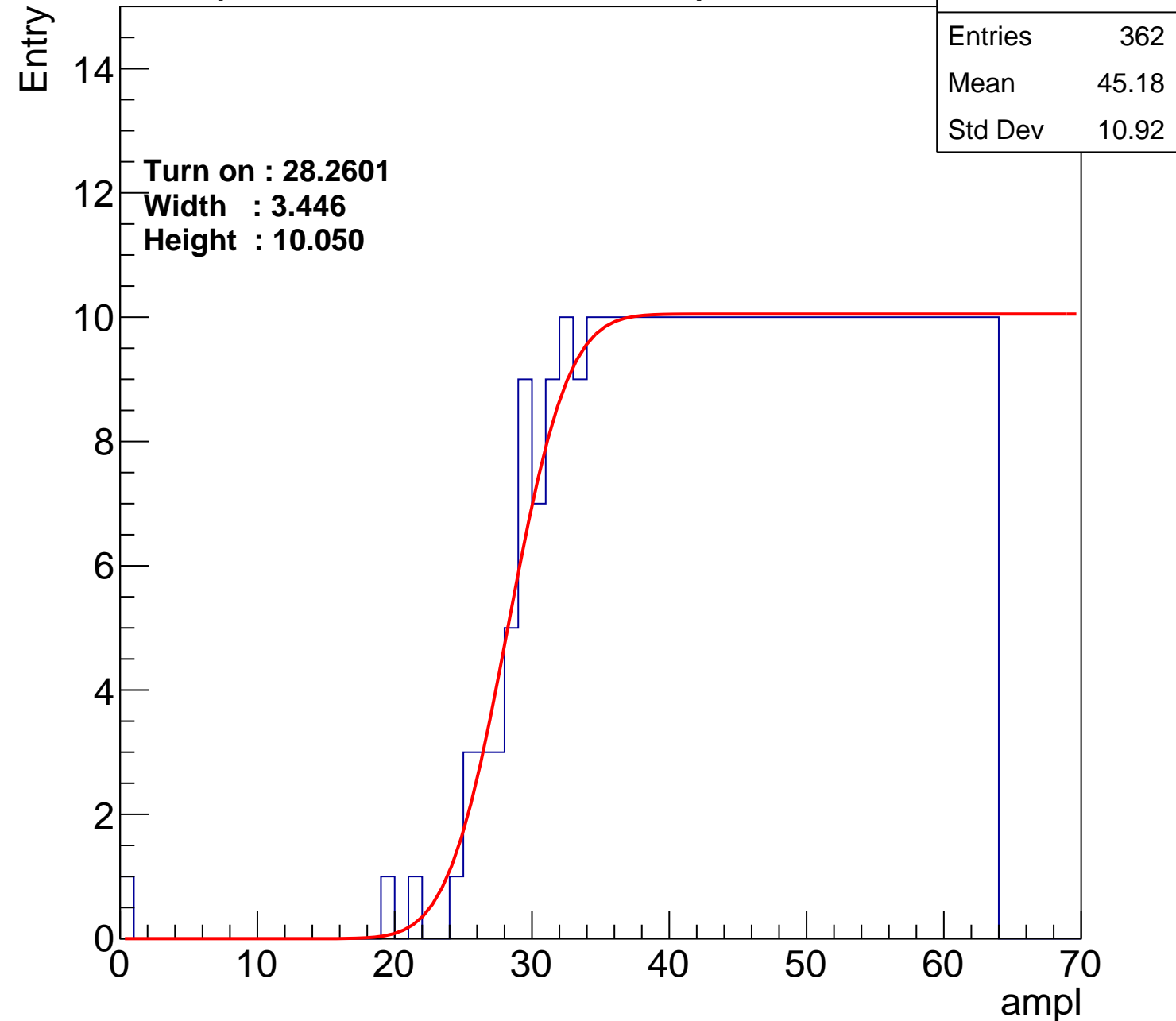
Width : 3.446

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.11
Std Dev	11.79

Turn on : 26.6133

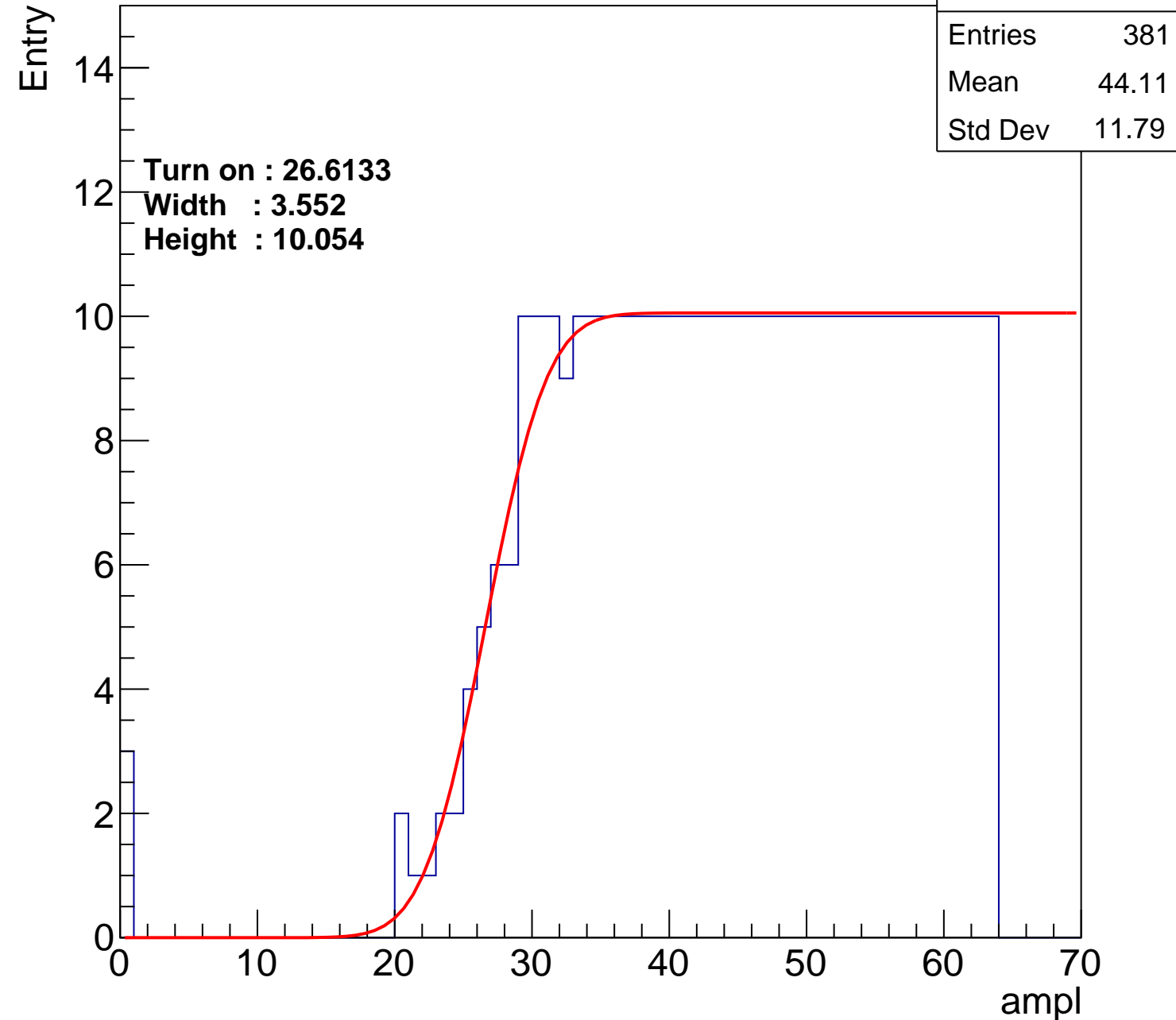
Width : 3.552

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U5-ch127

calib_packv5_042523_0143.root, FC#8, port C1

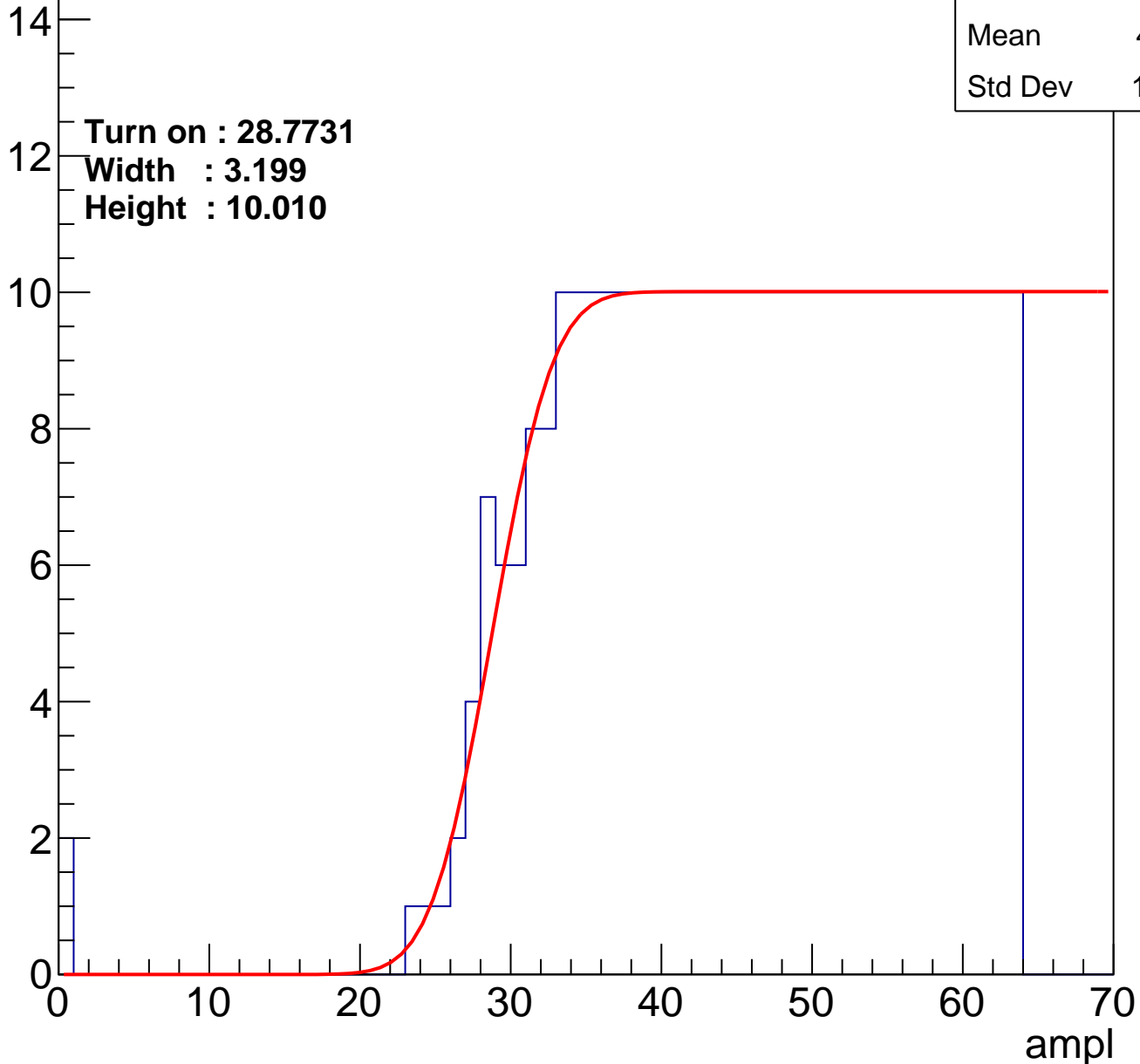
Entries	356
Mean	45.41
Std Dev	10.96

Turn on : 28.7731

Width : 3.199

Height : 10.010

Entry



B0L002S, U5-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.41
Std Dev	10.96

Turn on : 28.7731

Width : 3.199

Height : 10.010

Entry

