

B1L103S, U13-ch0

calib_packv5_042523_0143.root, FC#7, port C2

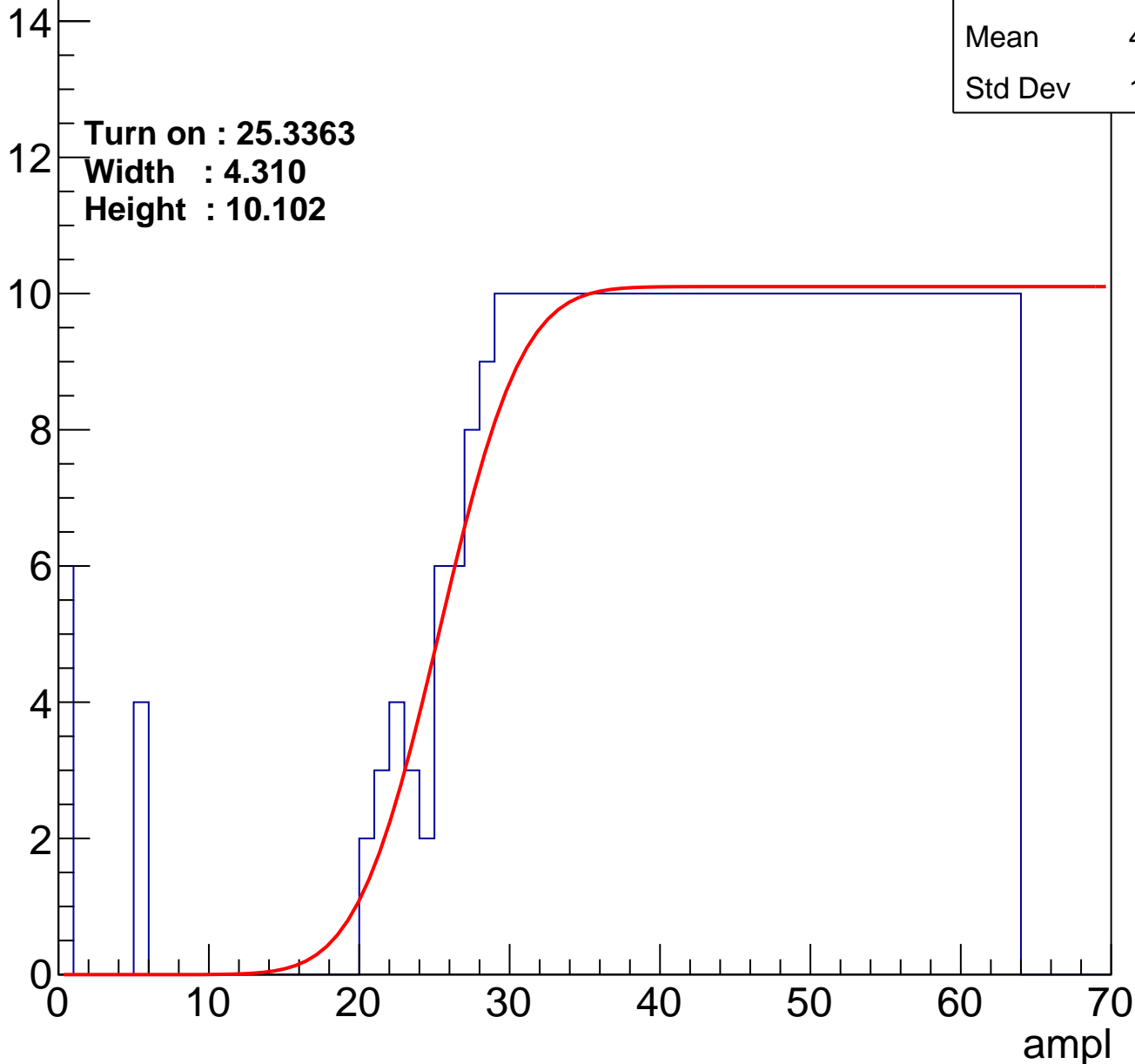
Entries	403
Mean	42.68
Std Dev	13.14

Turn on : 25.3363

Width : 4.310

Height : 10.102

Entry



B1L103S, U13-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	354
Mean	45.48
Std Dev	10.95

Turn on : 29.6170

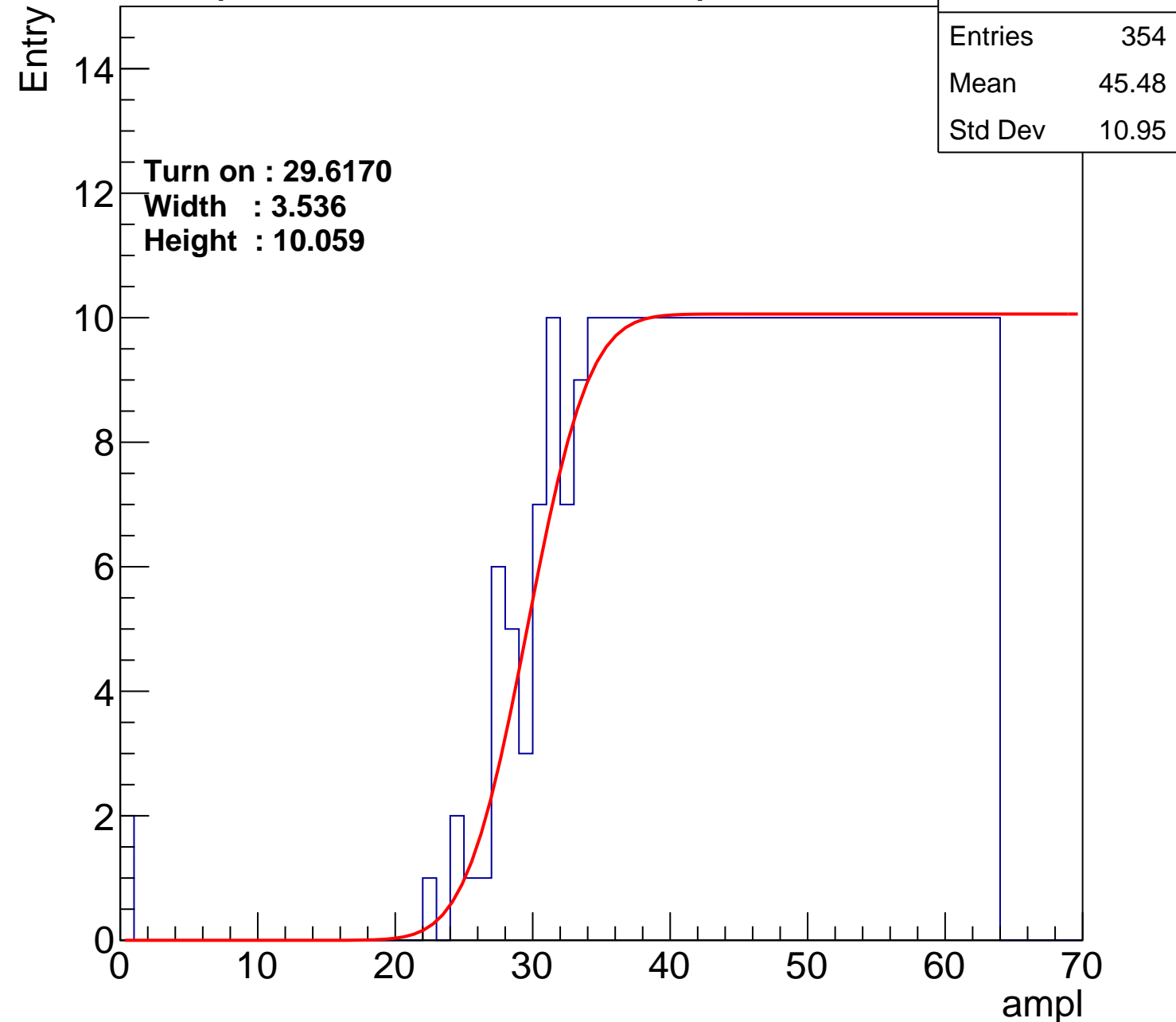
Width : 3.536

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.81
Std Dev	11.41

Turn on : 27.5391

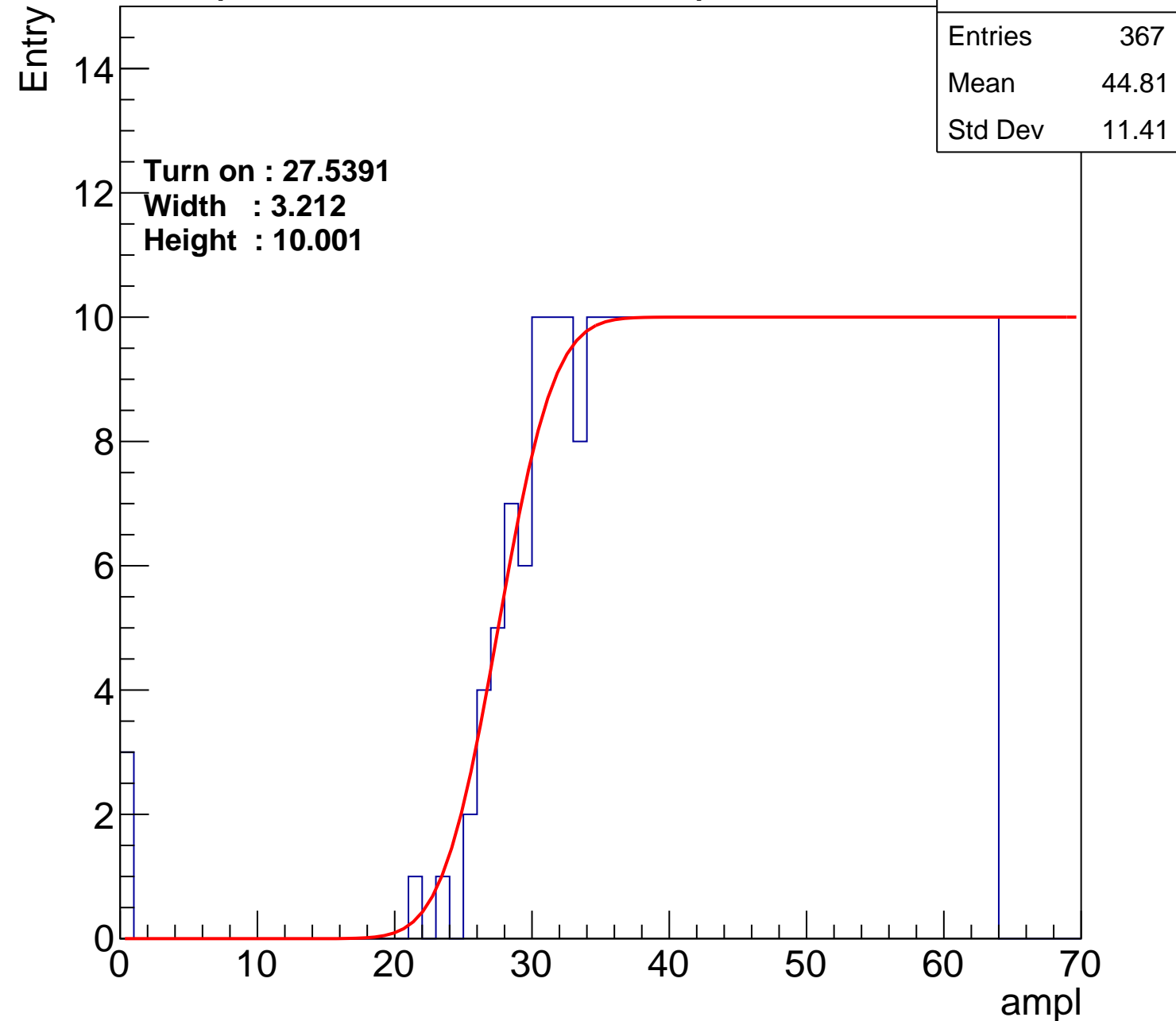
Width : 3.212

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.17
Std Dev	11.81

Turn on : 27.0319

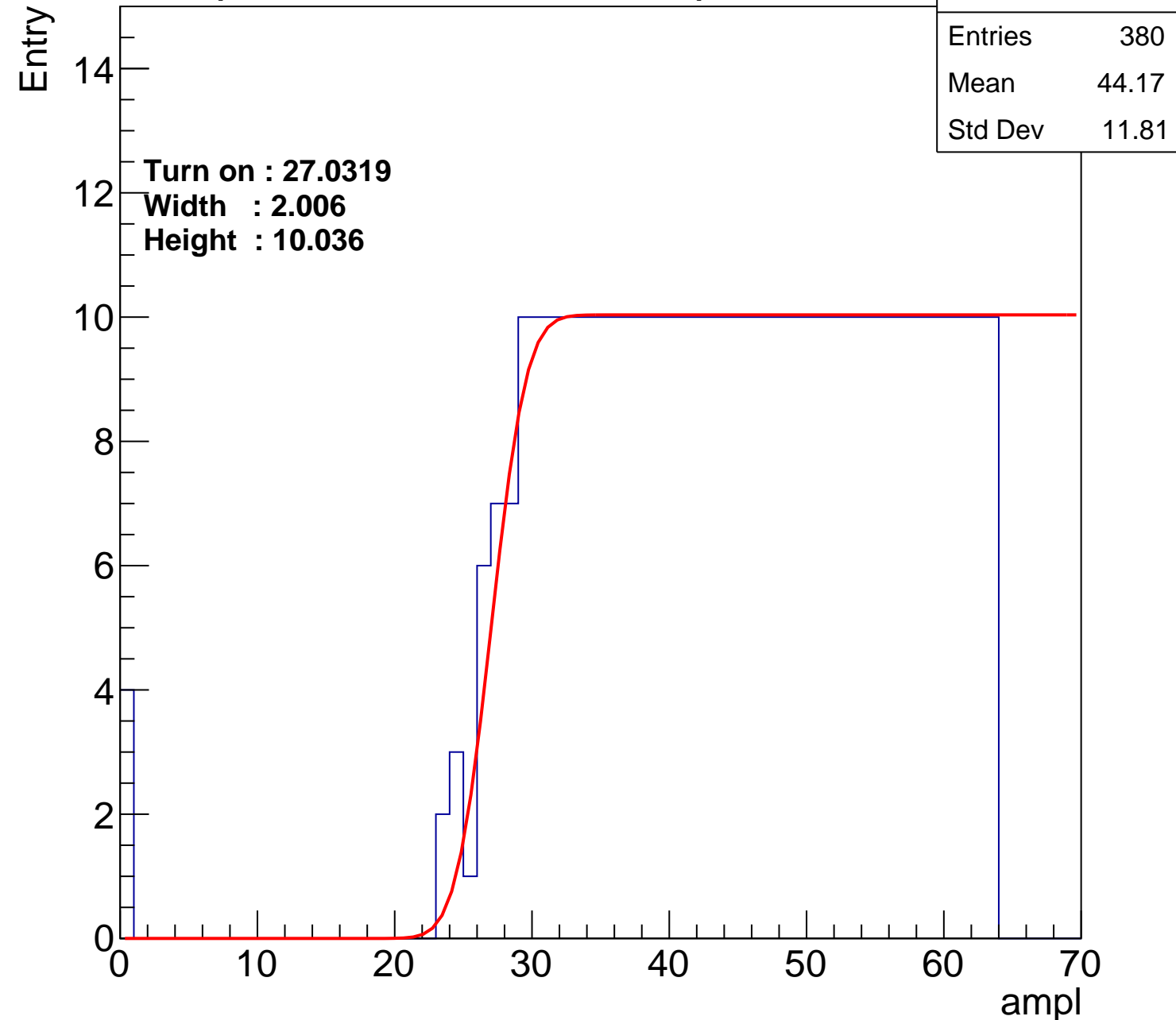
Width : 2.006

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.12
Std Dev	11.9

Turn on : 27.0513

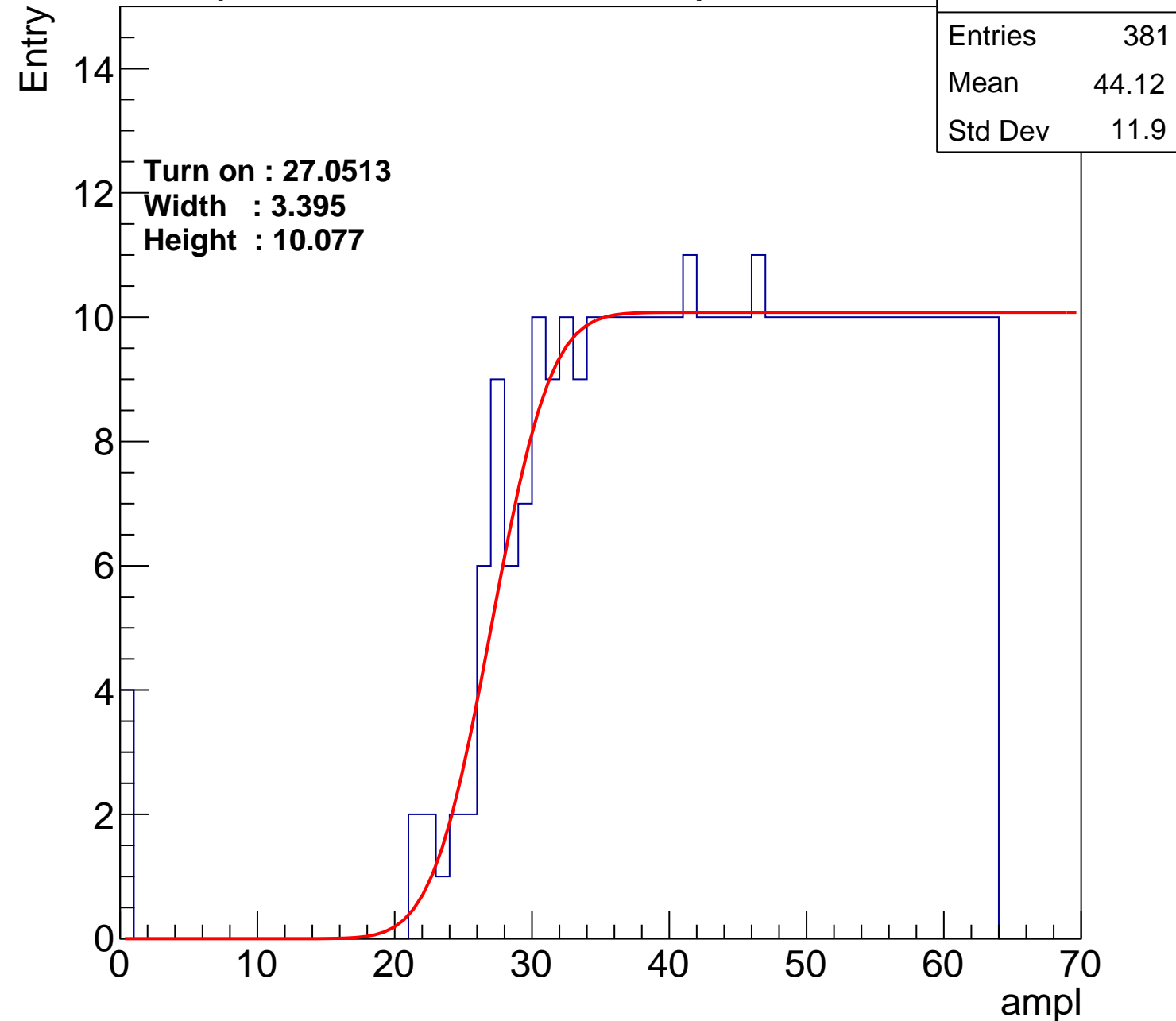
Width : 3.395

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch5

calib_packv5_042523_0143.root, FC#7, port C2

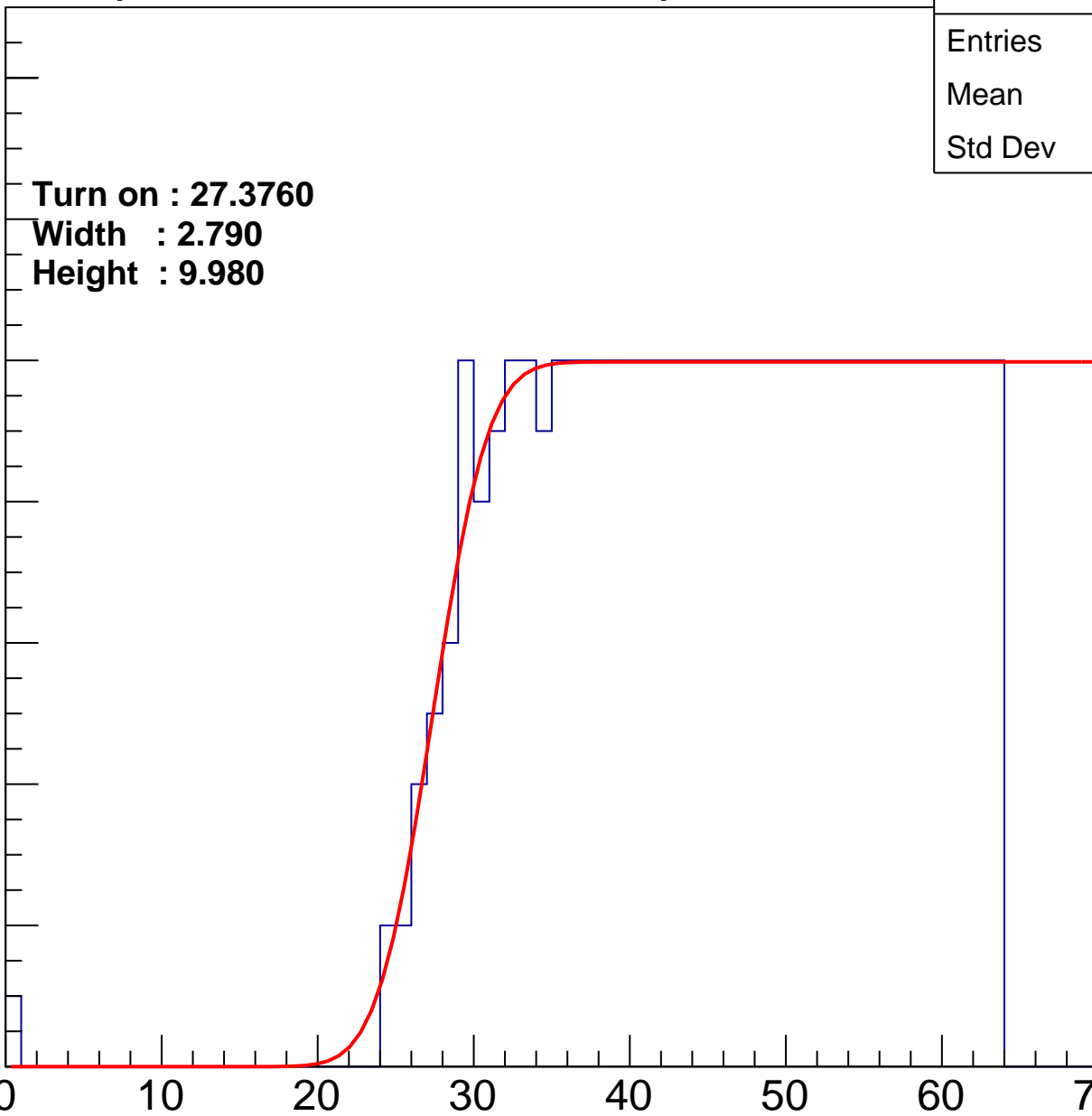
Entry

14
12
10
8
6
4
2
0

Turn on : 27.3760
Width : 2.790
Height : 9.980

Entries	366
Mean	45.03
Std Dev	10.94

ampl



B1L103S, U13-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.41
Std Dev	11.48

Turn on : 26.1317

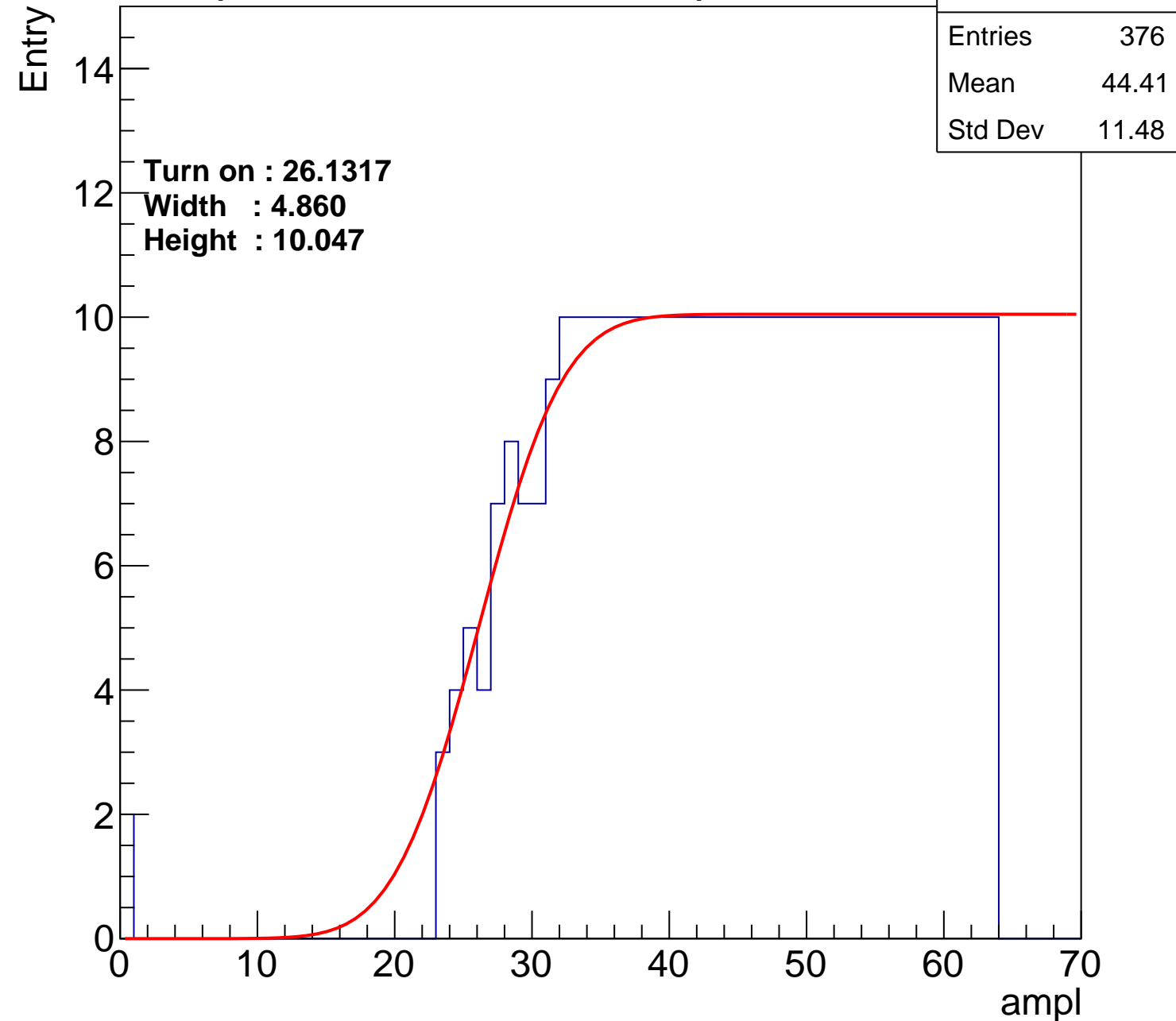
Width : 4.860

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch7

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.25
Std Dev	12.35

Turn on : 25.5190

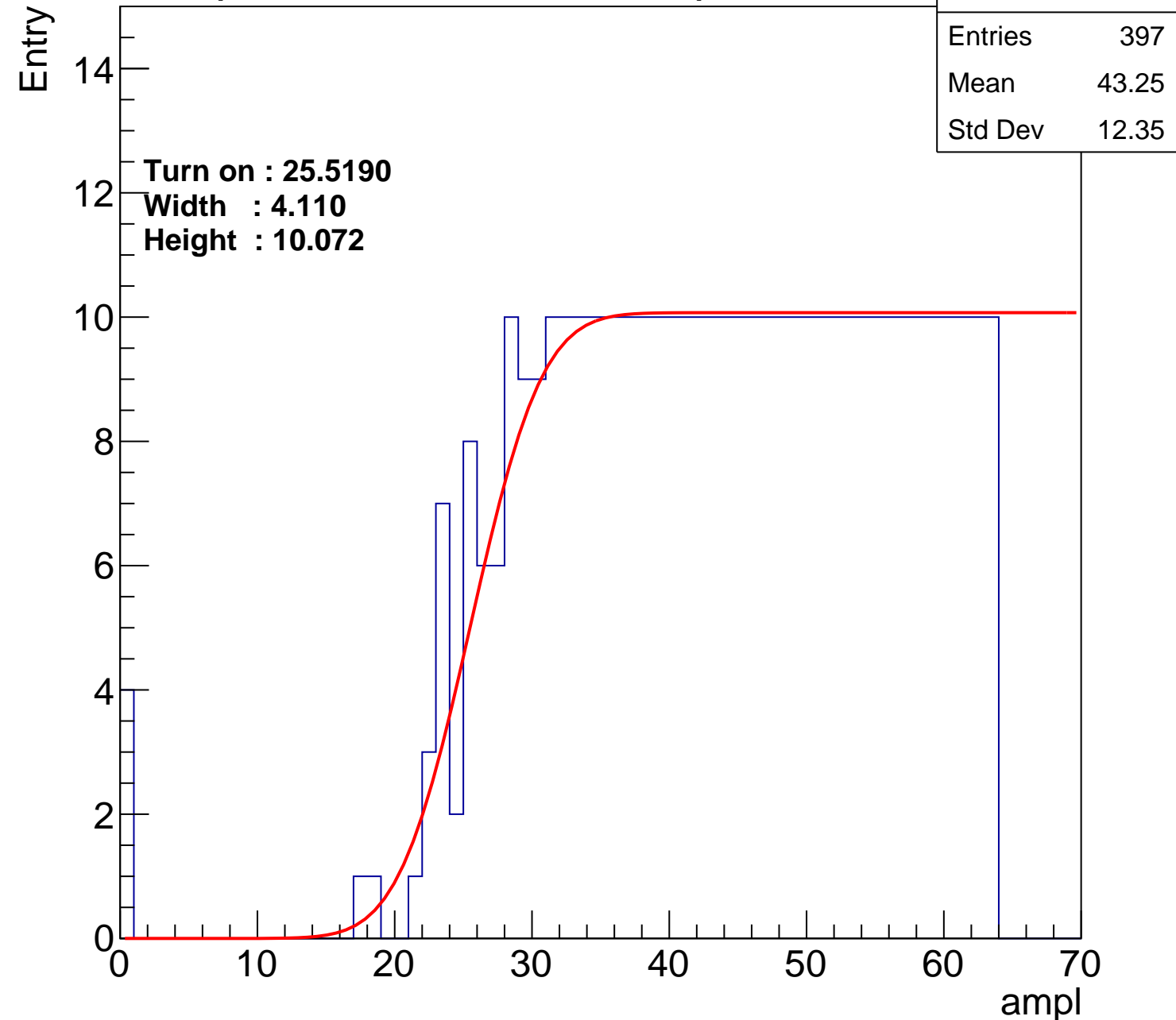
Width : 4.110

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.44
Std Dev	11.4

Turn on : 26.2514

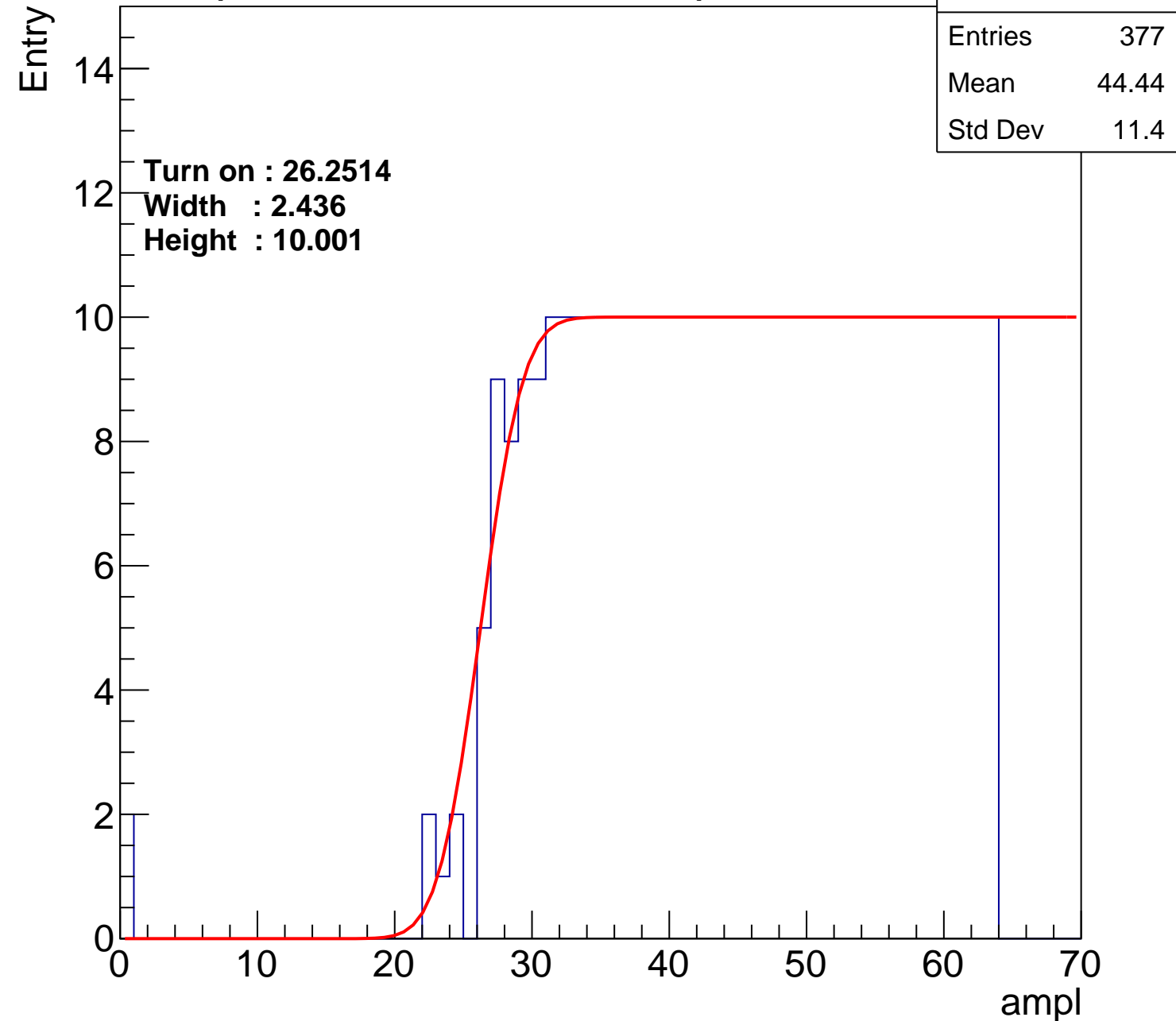
Width : 2.436

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.51
Std Dev	10.71

Turn on : 29.2235

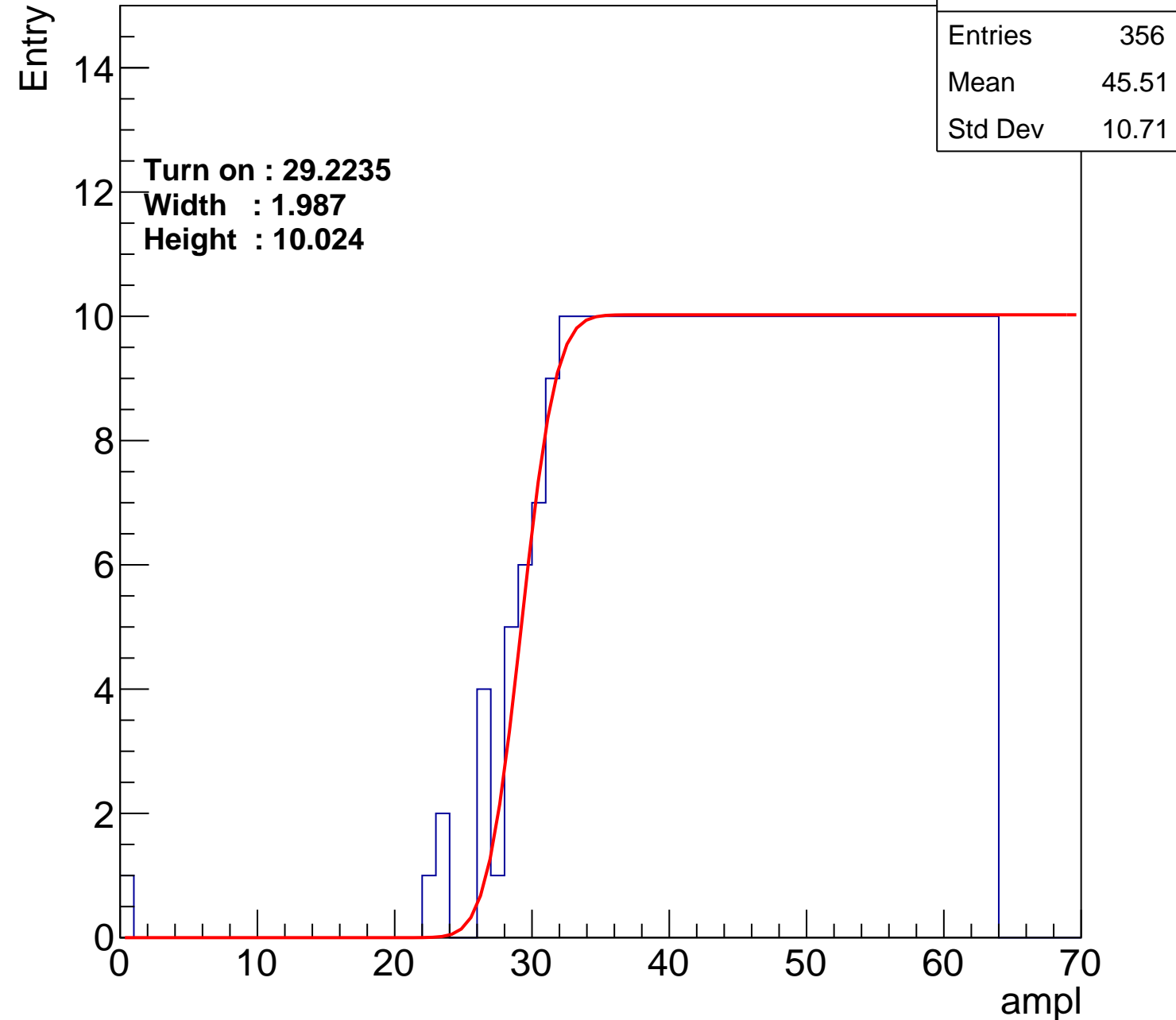
Width : 1.987

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 28.2045

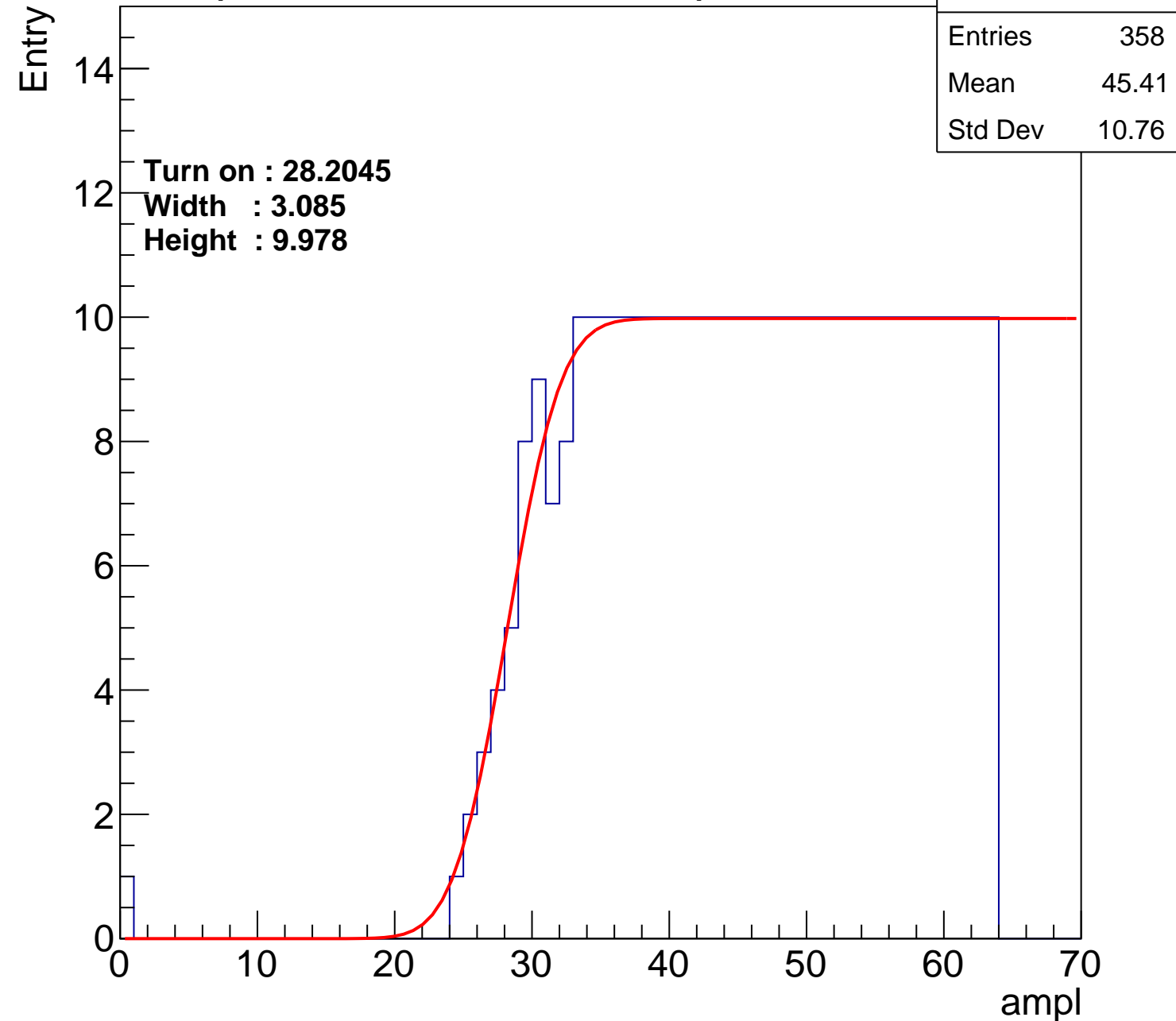
Width : 3.085

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.48
Std Dev	11.31

Turn on : 29.2564

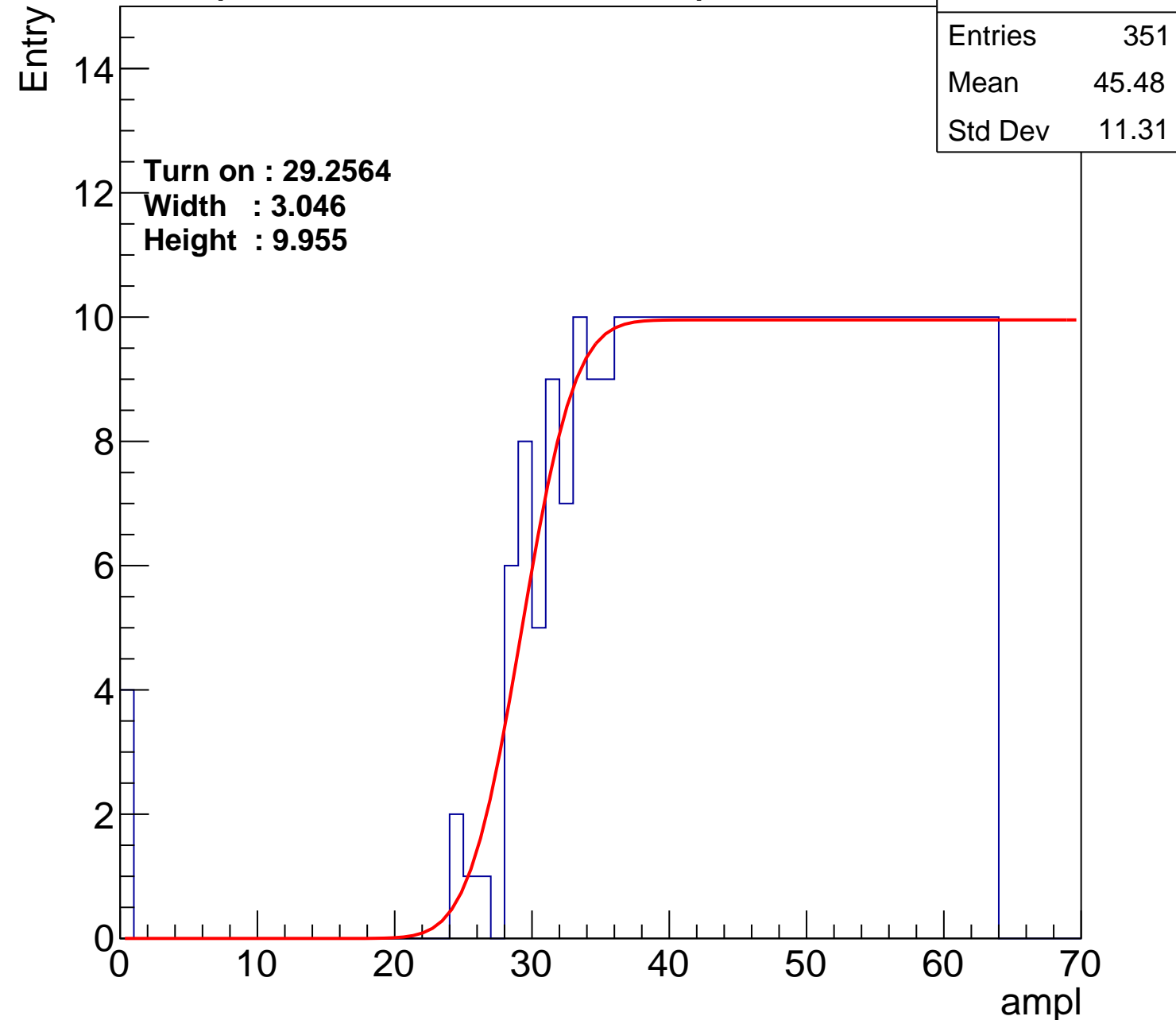
Width : 3.046

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	347
Mean	45.93
Std Dev	10.51

Turn on : 28.5636

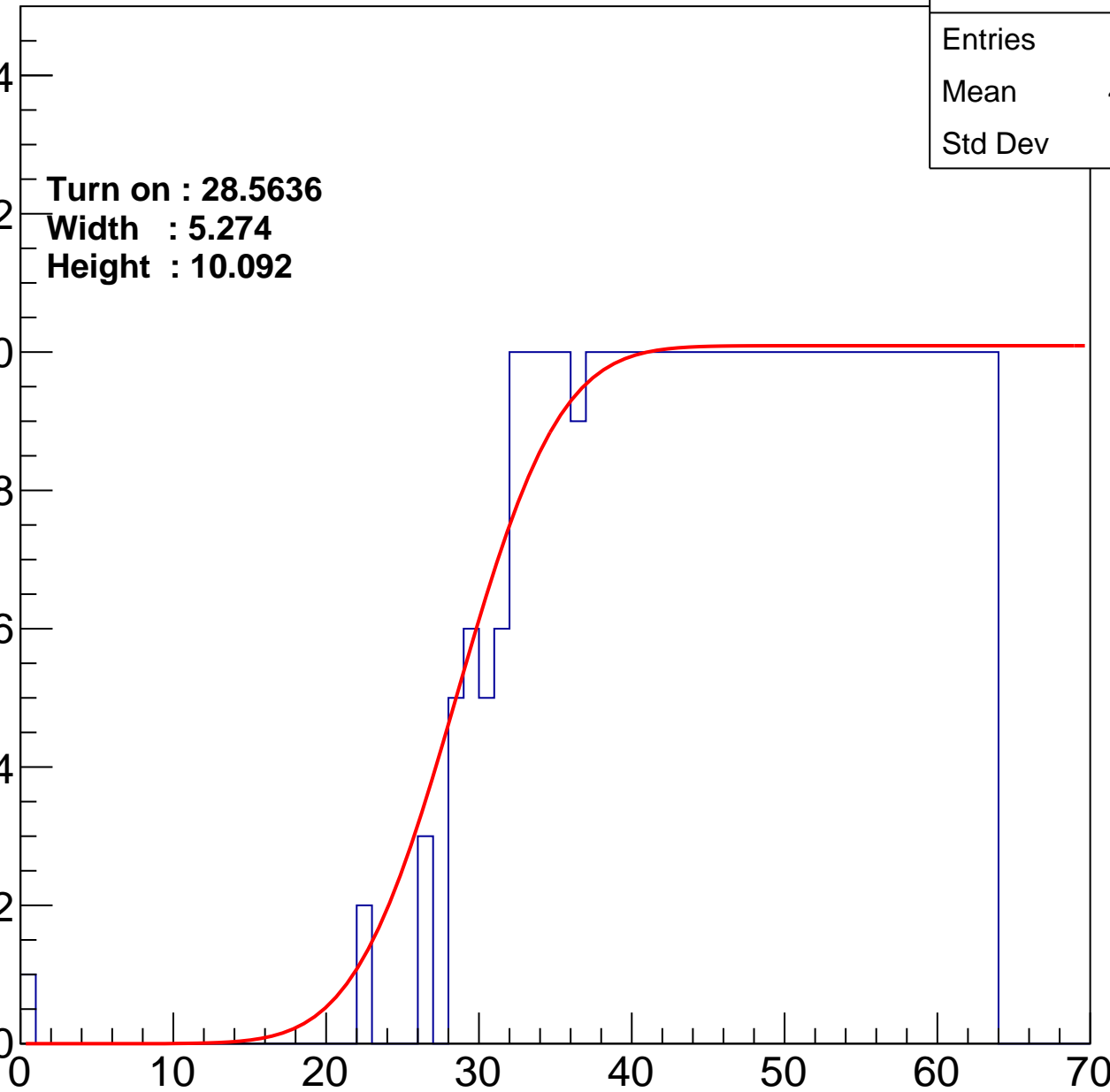
Width : 5.274

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch13

calib_packv5_042523_0143.root, FC#7, port C2

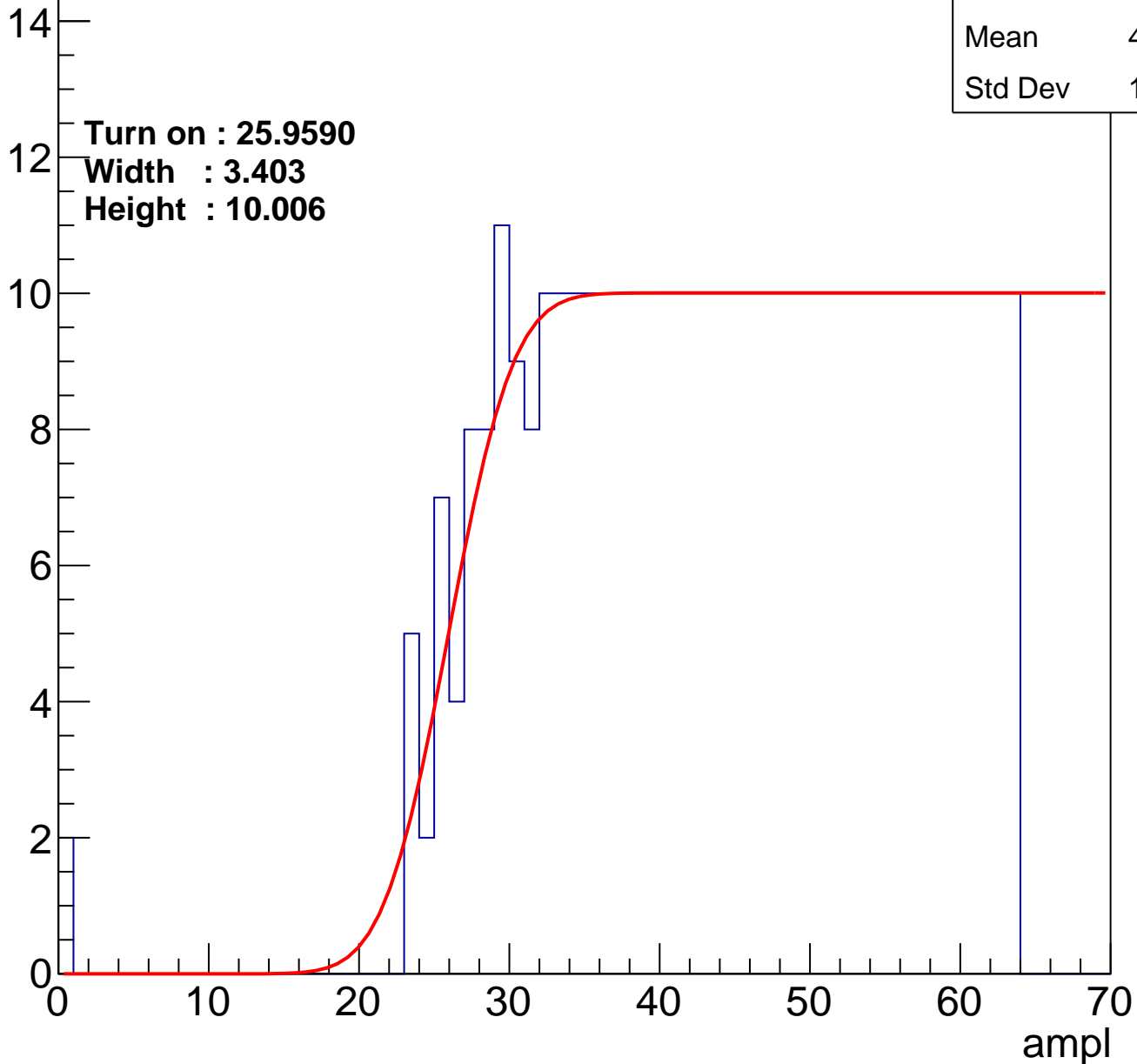
Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.9590

Width : 3.403

Height : 10.006

Entry



B1L103S, U13-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.49
Std Dev	11.29

Turn on : 27.4710

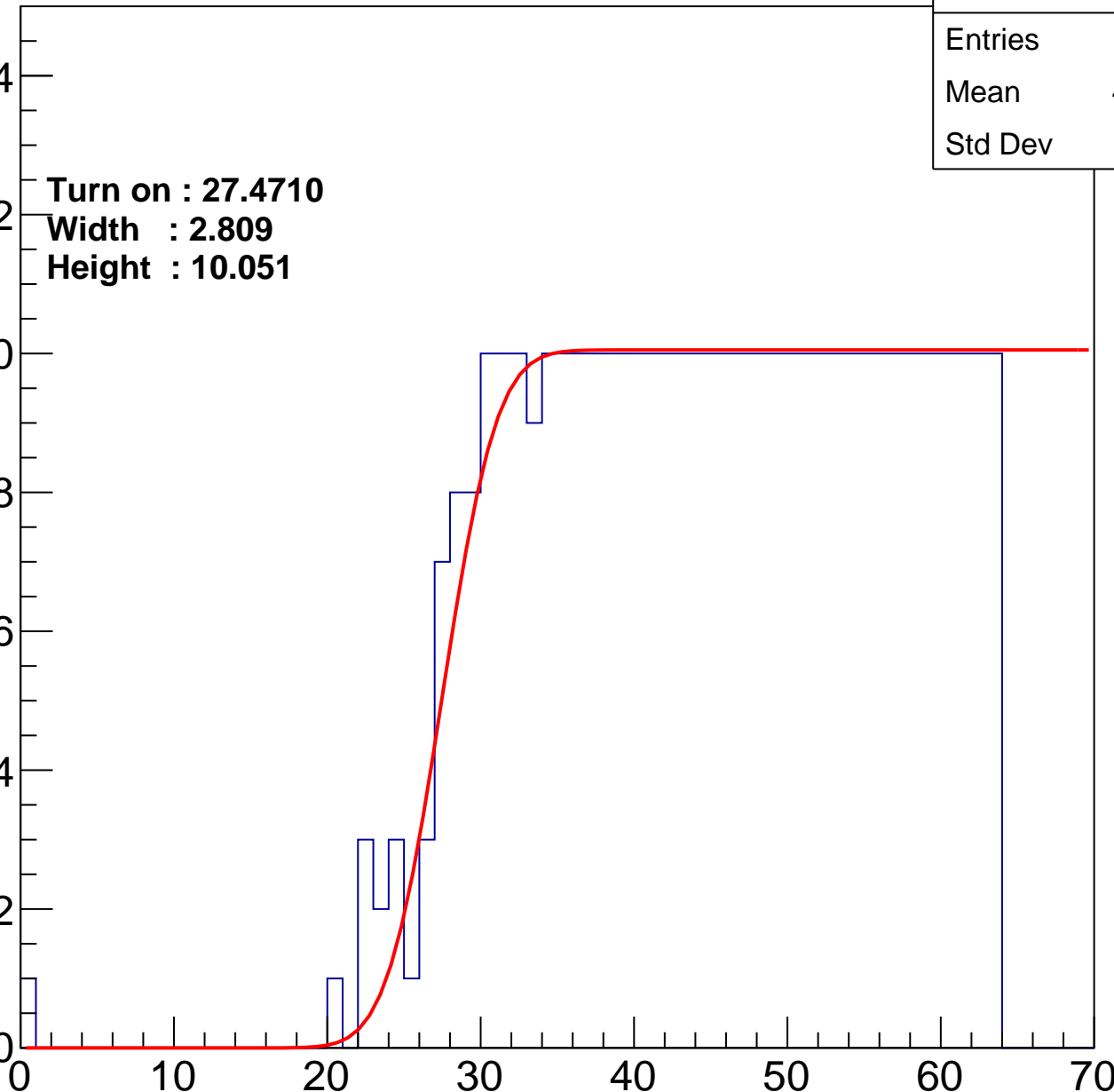
Width : 2.809

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch15

calib_packv5_042523_0143.root, FC#7, port C2

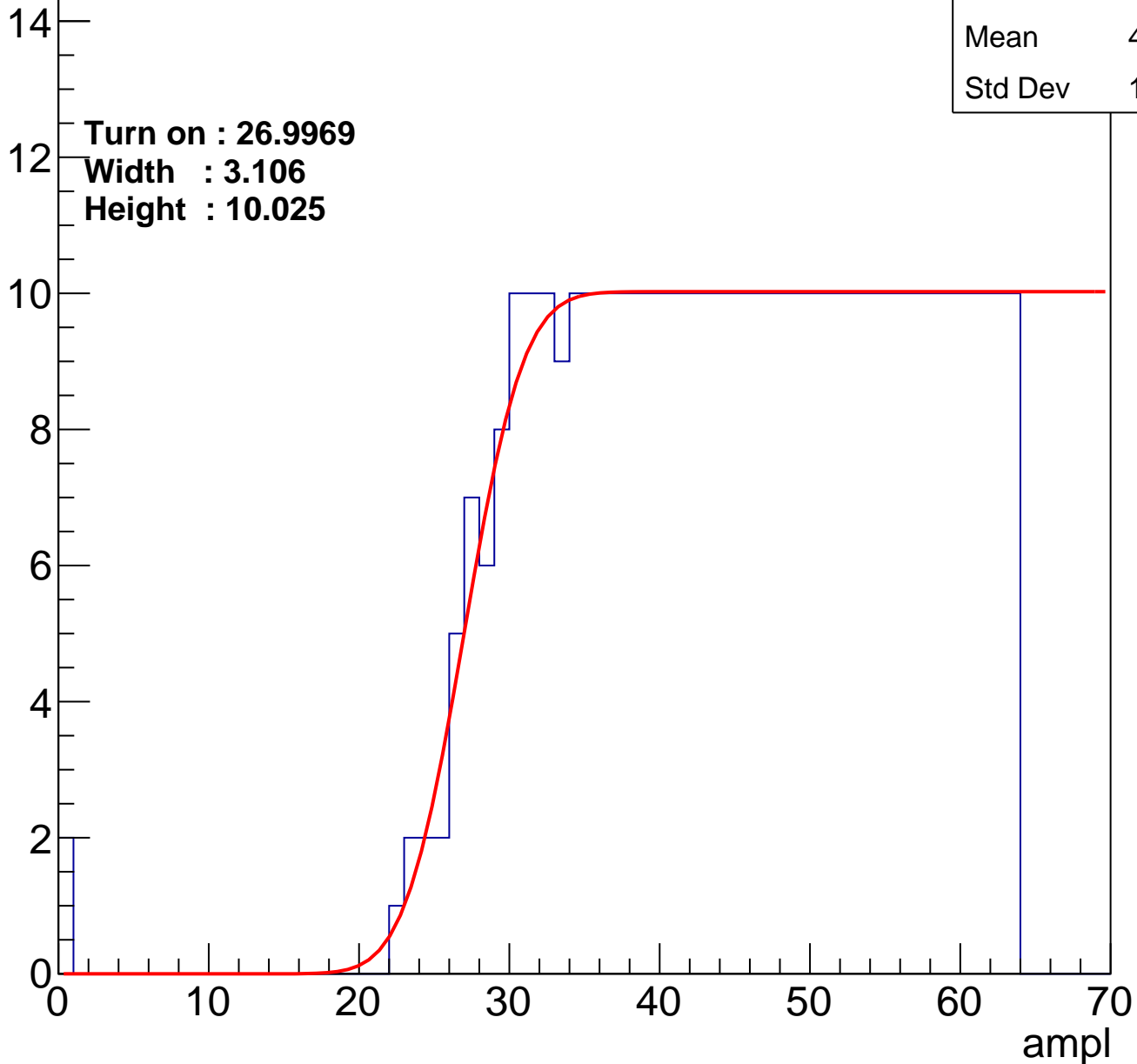
Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.9969

Width : 3.106

Height : 10.025

Entry



B1L103S, U13-ch16

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.39
Std Dev	10.93

Turn on : 28.7763

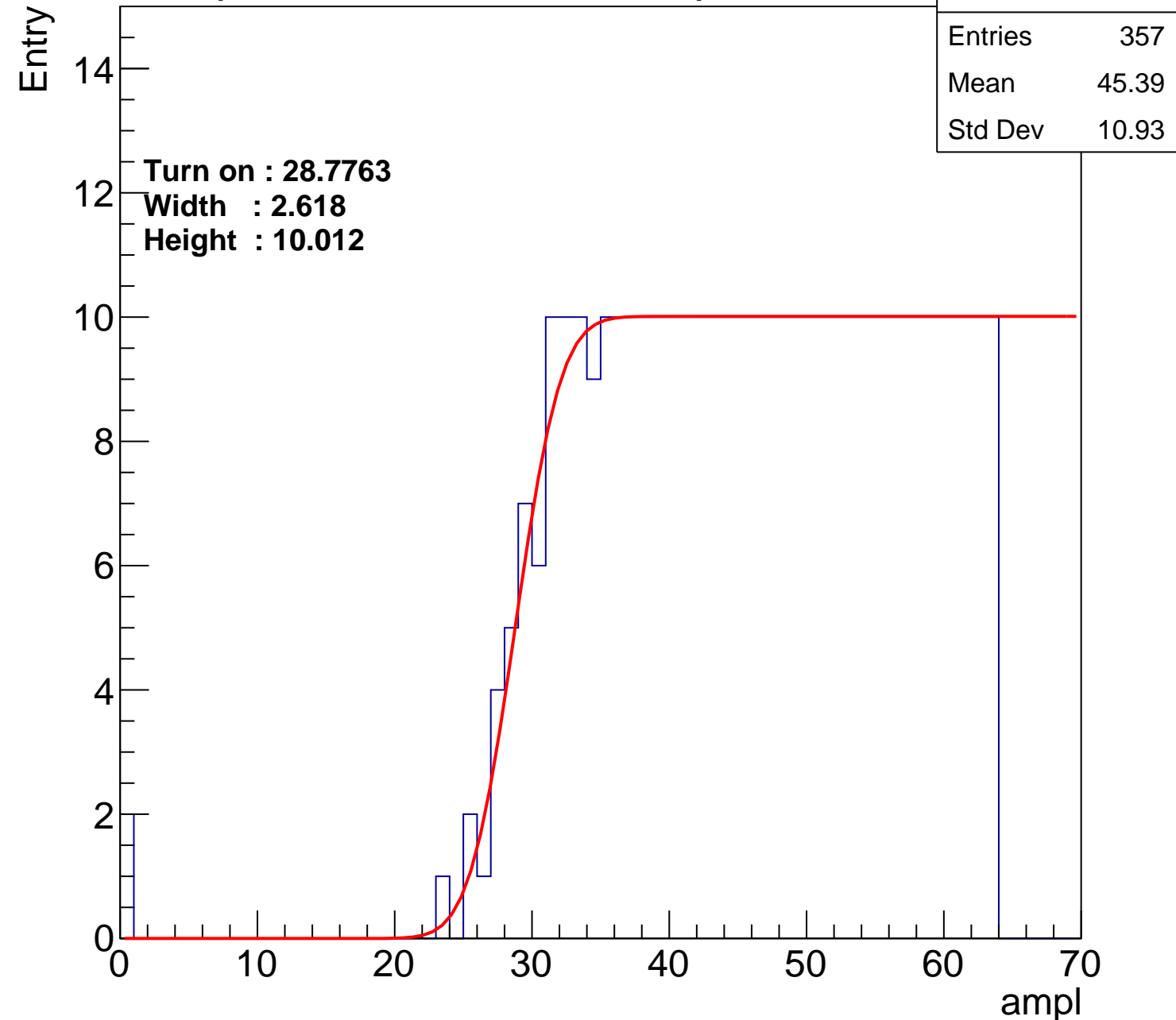
Width : 2.618

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch17

calib_packv5_042523_0143.root, FC#7, port C2

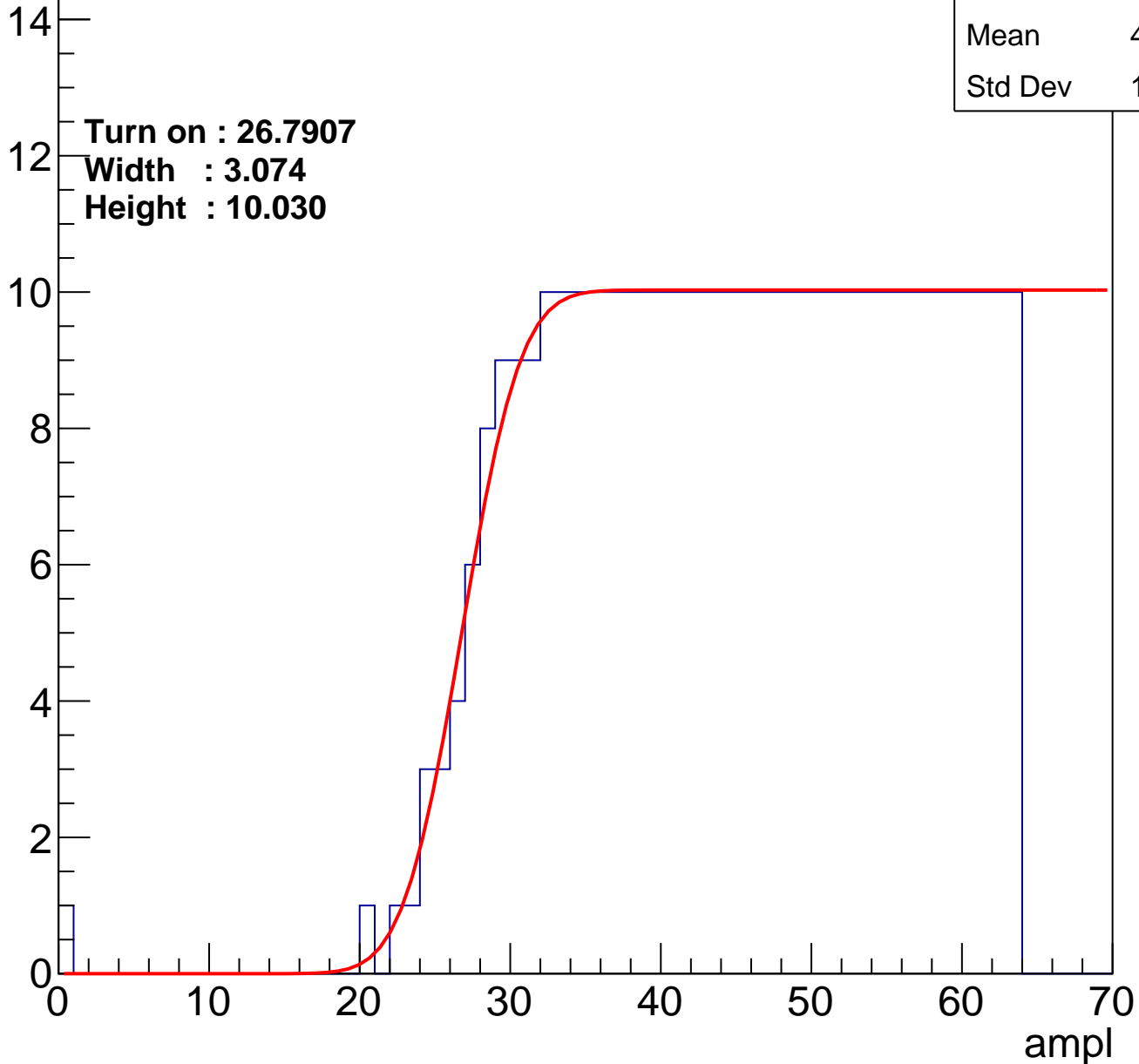
Entries	375
Mean	44.57
Std Dev	11.22

Turn on : 26.7907

Width : 3.074

Height : 10.030

Entry



B1L103S, U13-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.65
Std Dev	11.24

Turn on : 27.1174

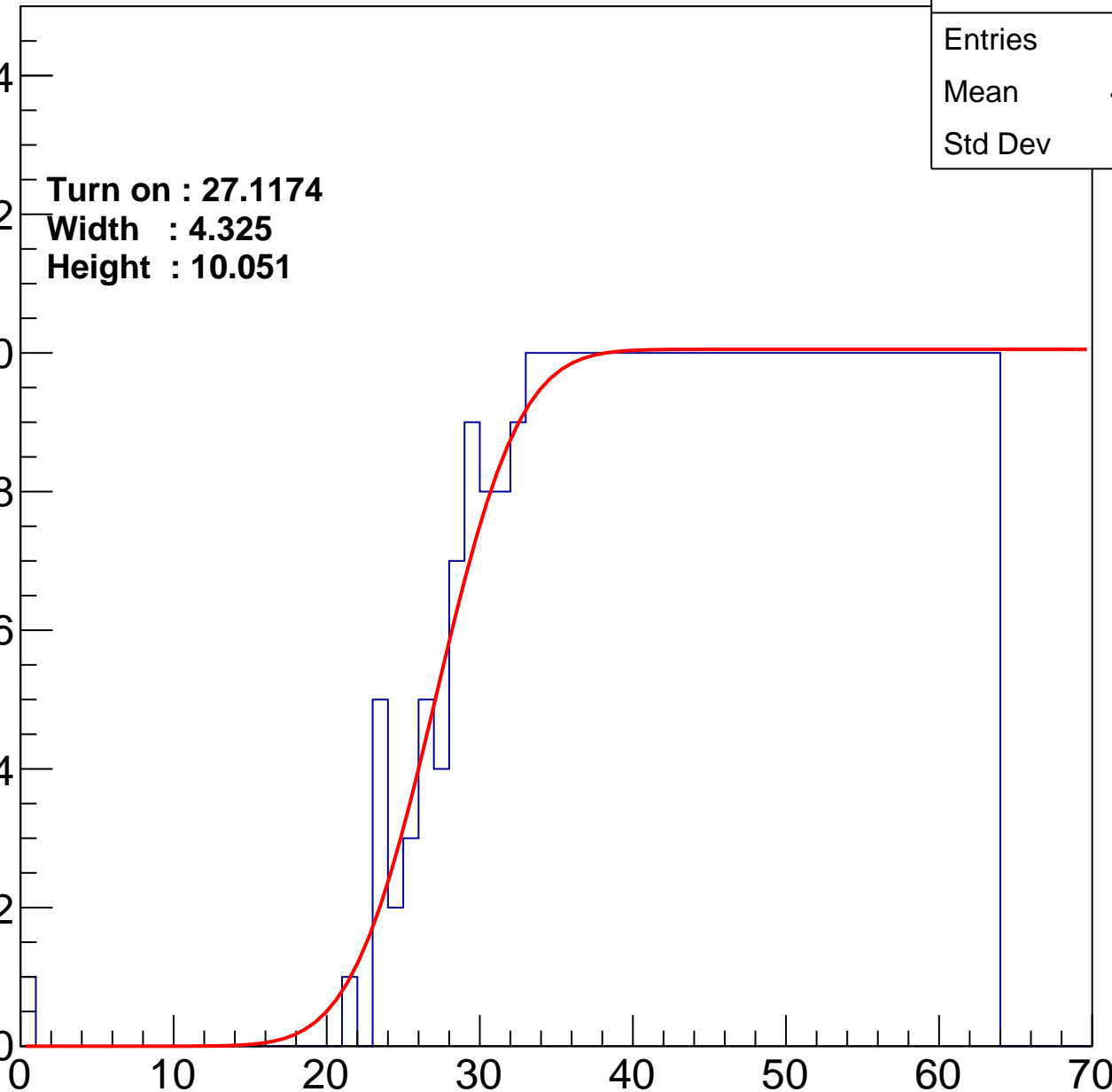
Width : 4.325

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.65
Std Dev	11.33

Turn on : 26.9799

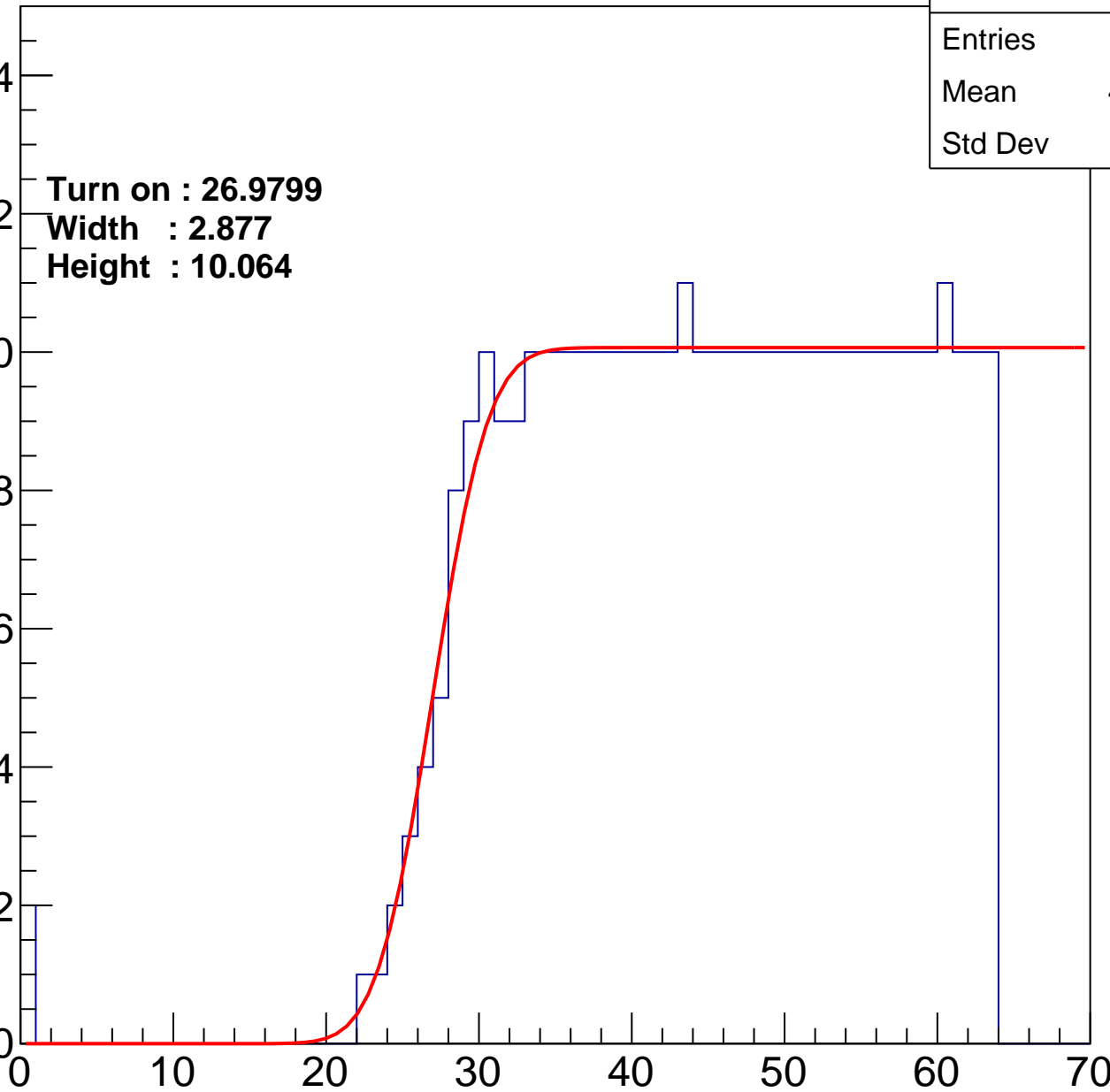
Width : 2.877

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch20

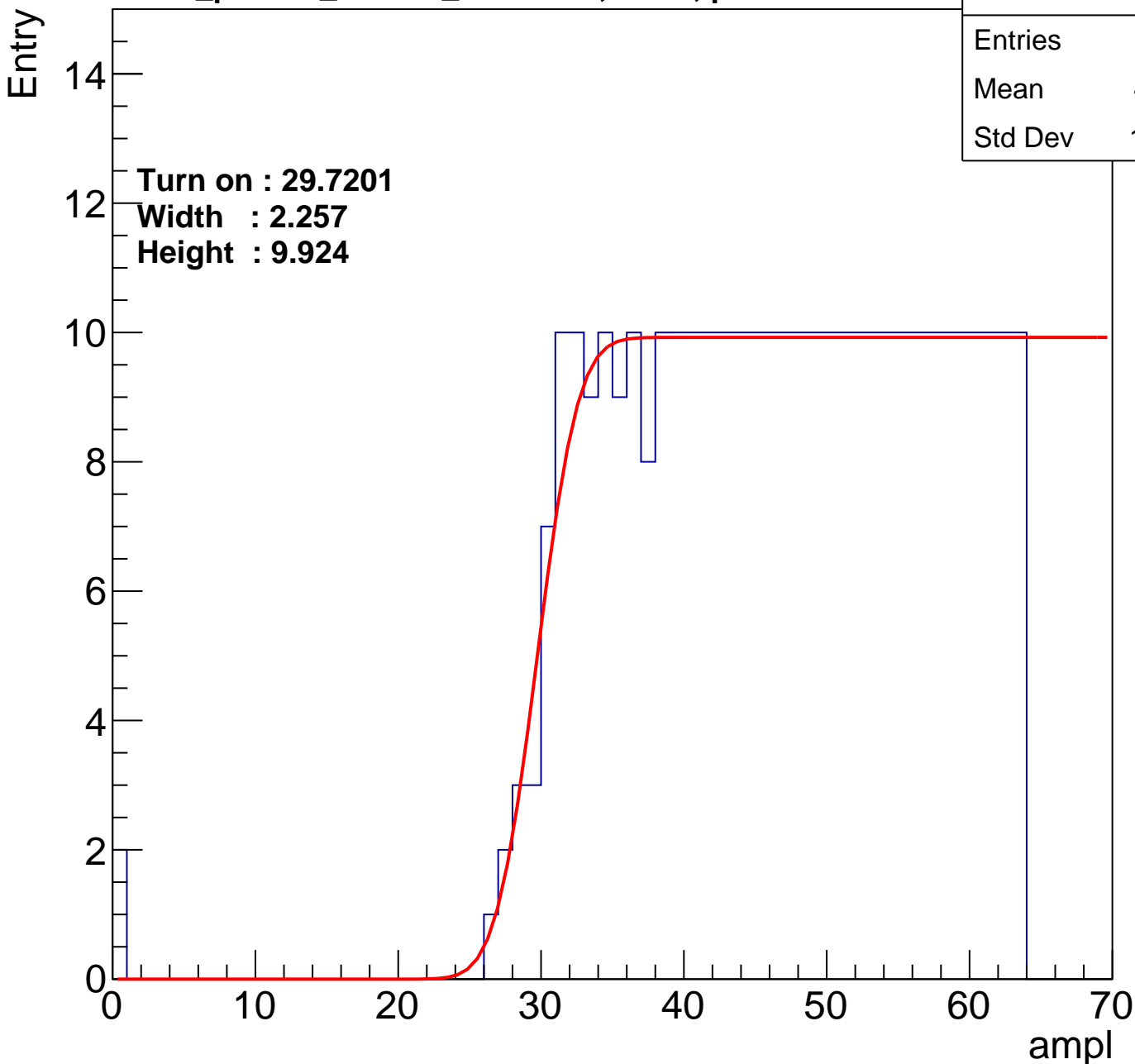
calib_packv5_042523_0143.root, FC#7, port C2

Turn on : 29.7201

Width : 2.257

Height : 9.924

Entries	344
Mean	46.01
Std Dev	10.62



B1L103S, U13-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.42
Std Dev	10.69

Turn on : 28.5369

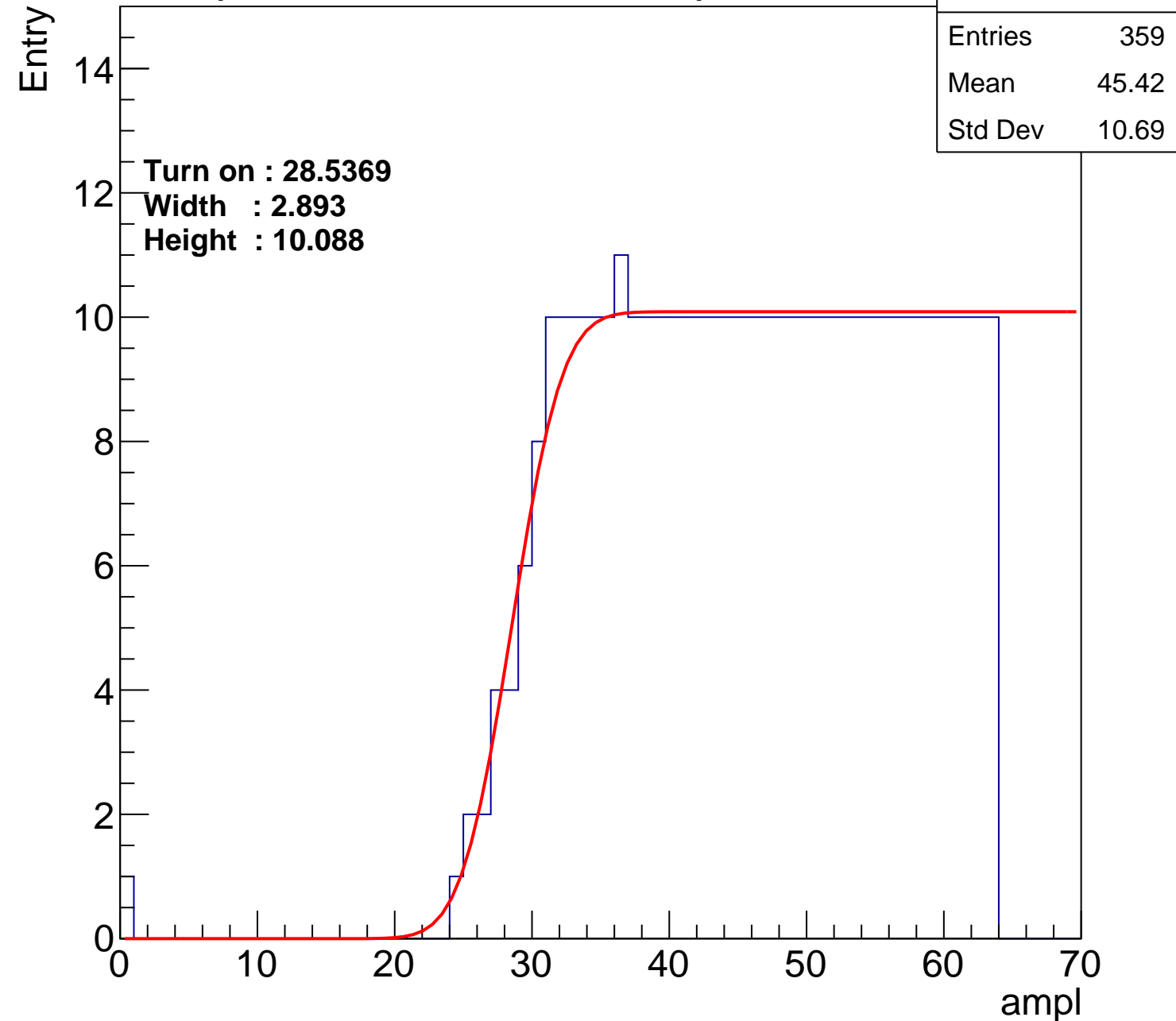
Width : 2.893

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.9
Std Dev	11.8

Turn on : 25.8118

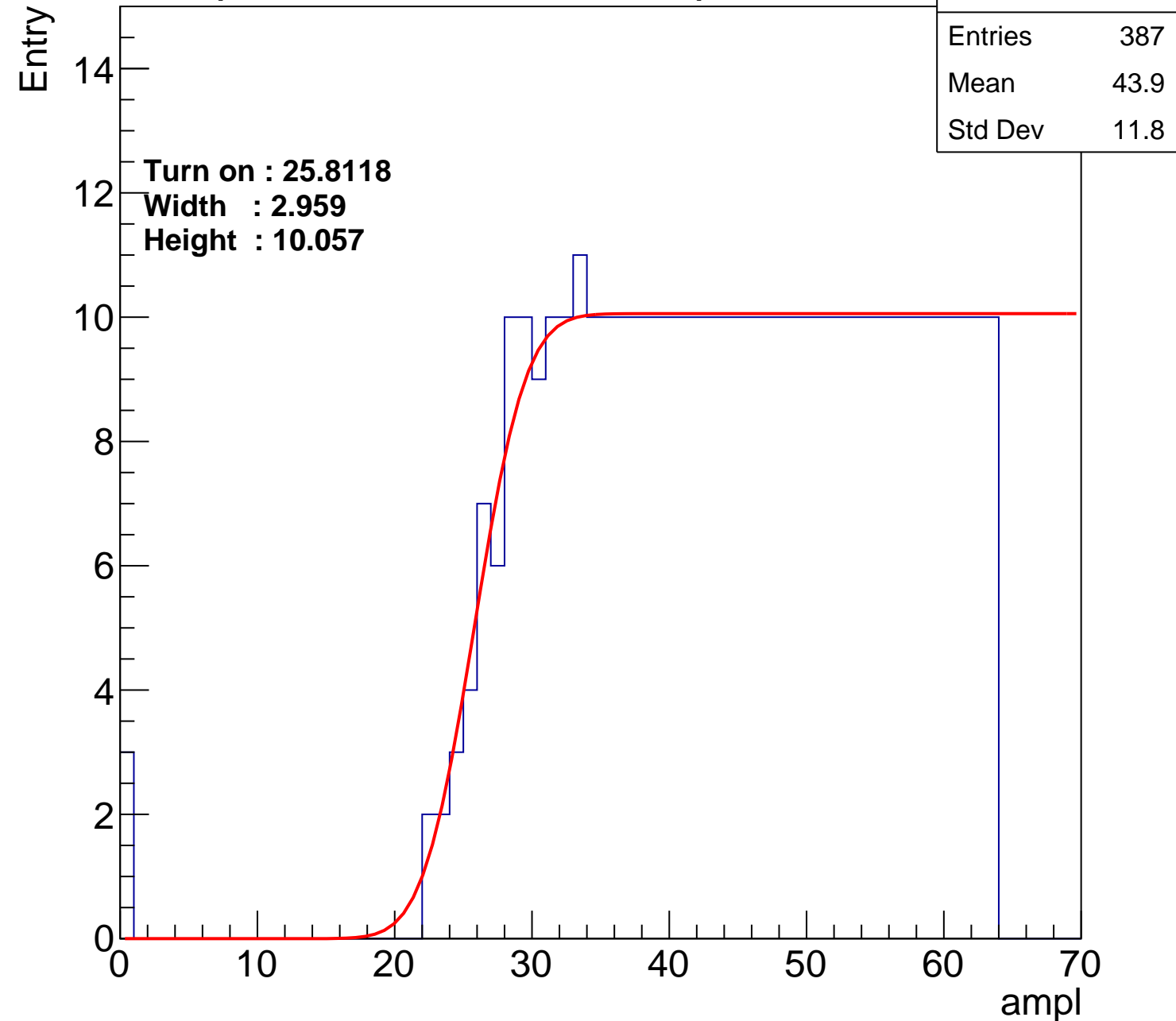
Width : 2.959

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.82
Std Dev	11.22

Turn on : 27.8693

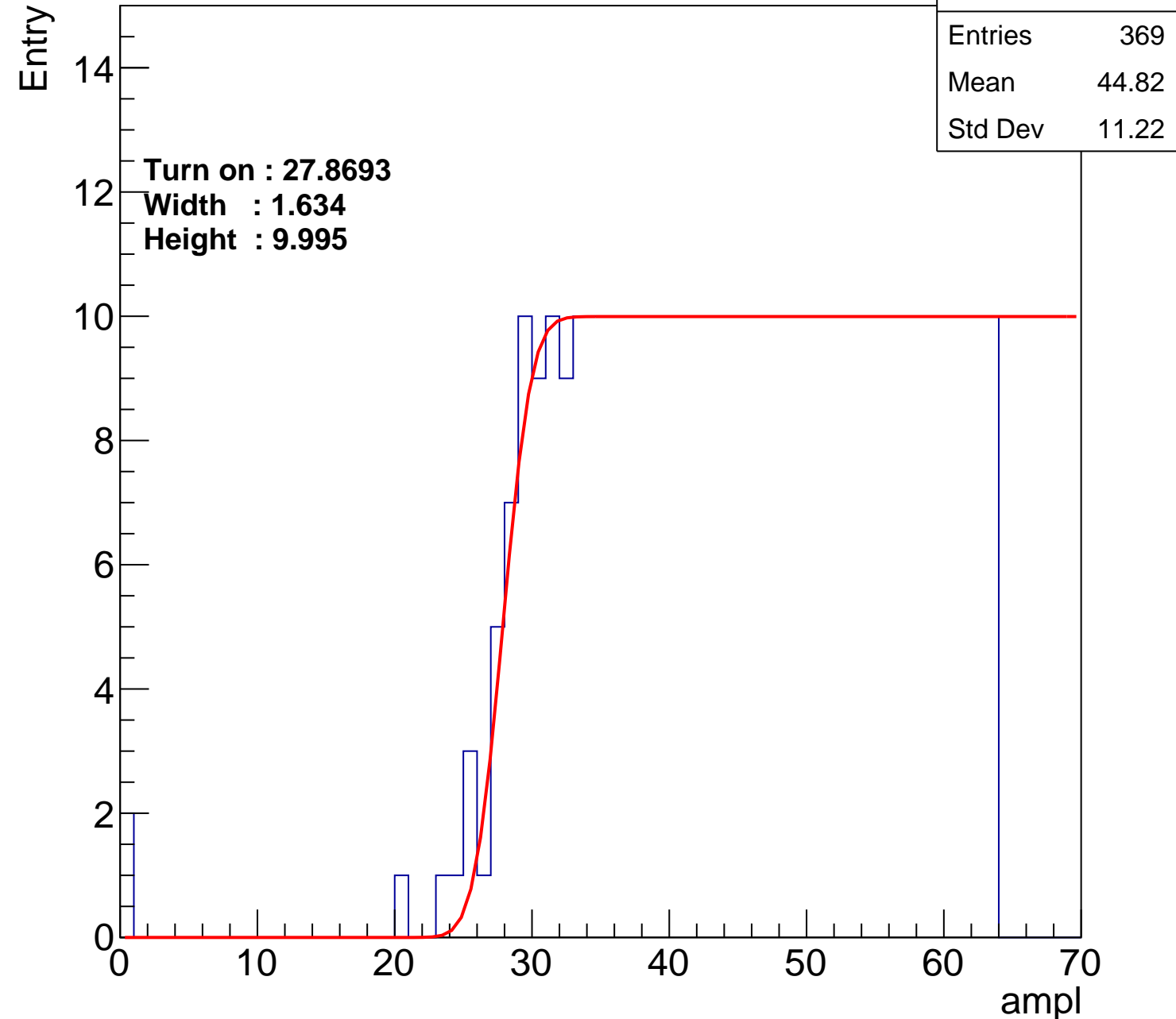
Width : 1.634

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch24

calib_packv5_042523_0143.root, FC#7, port C2

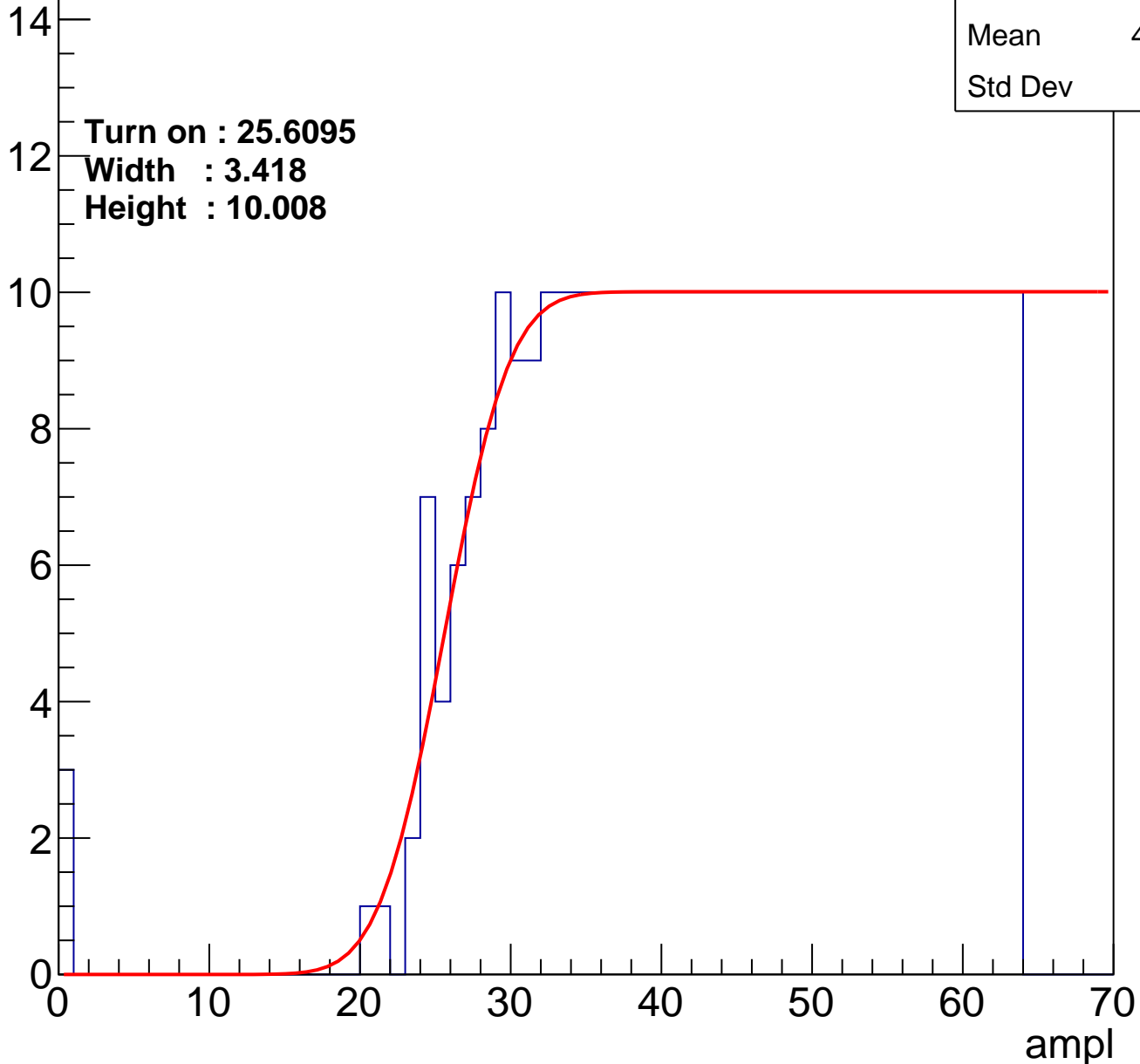
Entries	387
Mean	43.83
Std Dev	11.9

Turn on : 25.6095

Width : 3.418

Height : 10.008

Entry



B1L103S, U13-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	350
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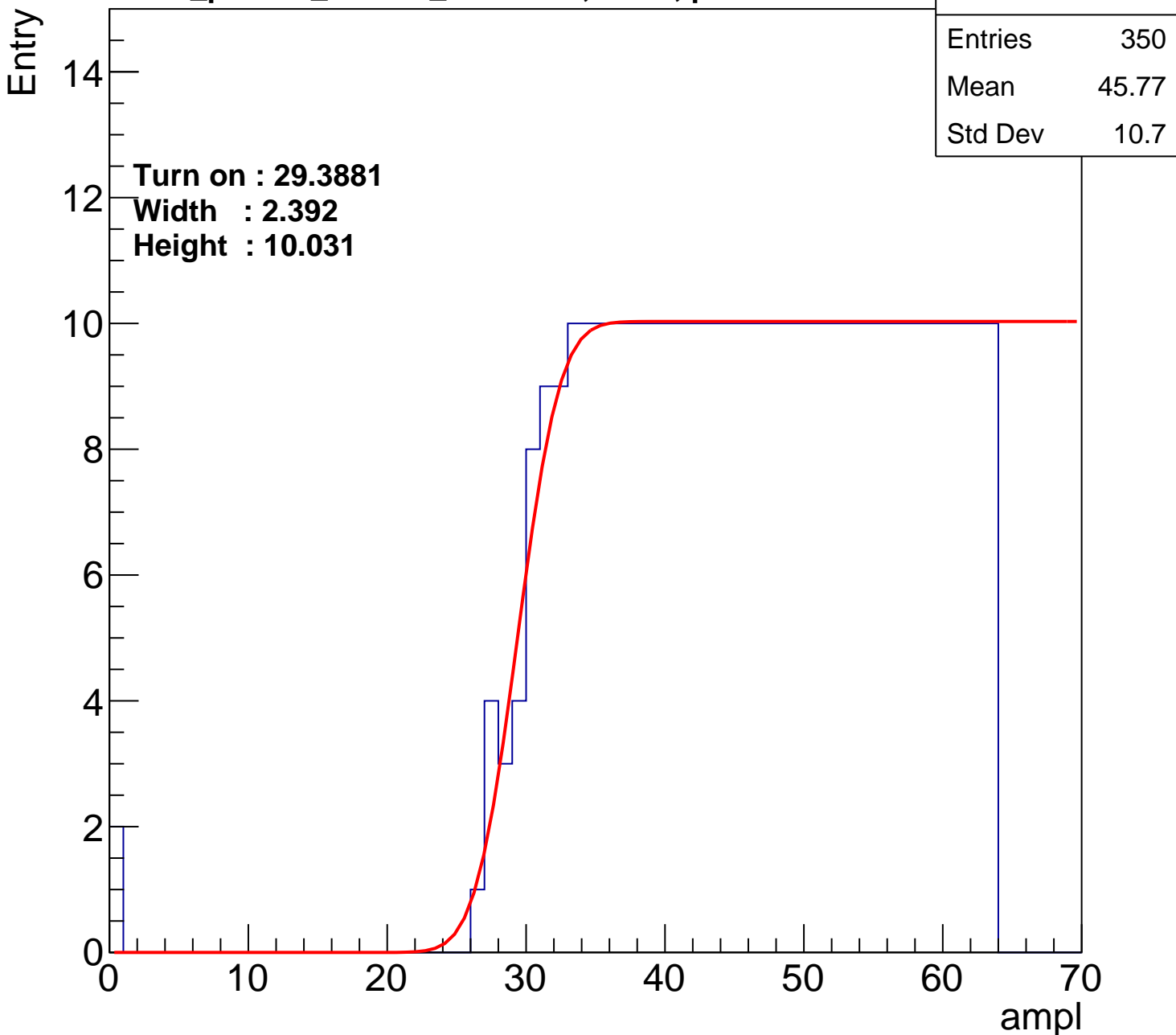
Mean	45.77
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Std Dev	10.7
---------	------

Turn on : 29.3881

Width : 2.392

Height : 10.031



B1L103S, U13-ch26

calib_packv5_042523_0143.root, FC#7, port C2

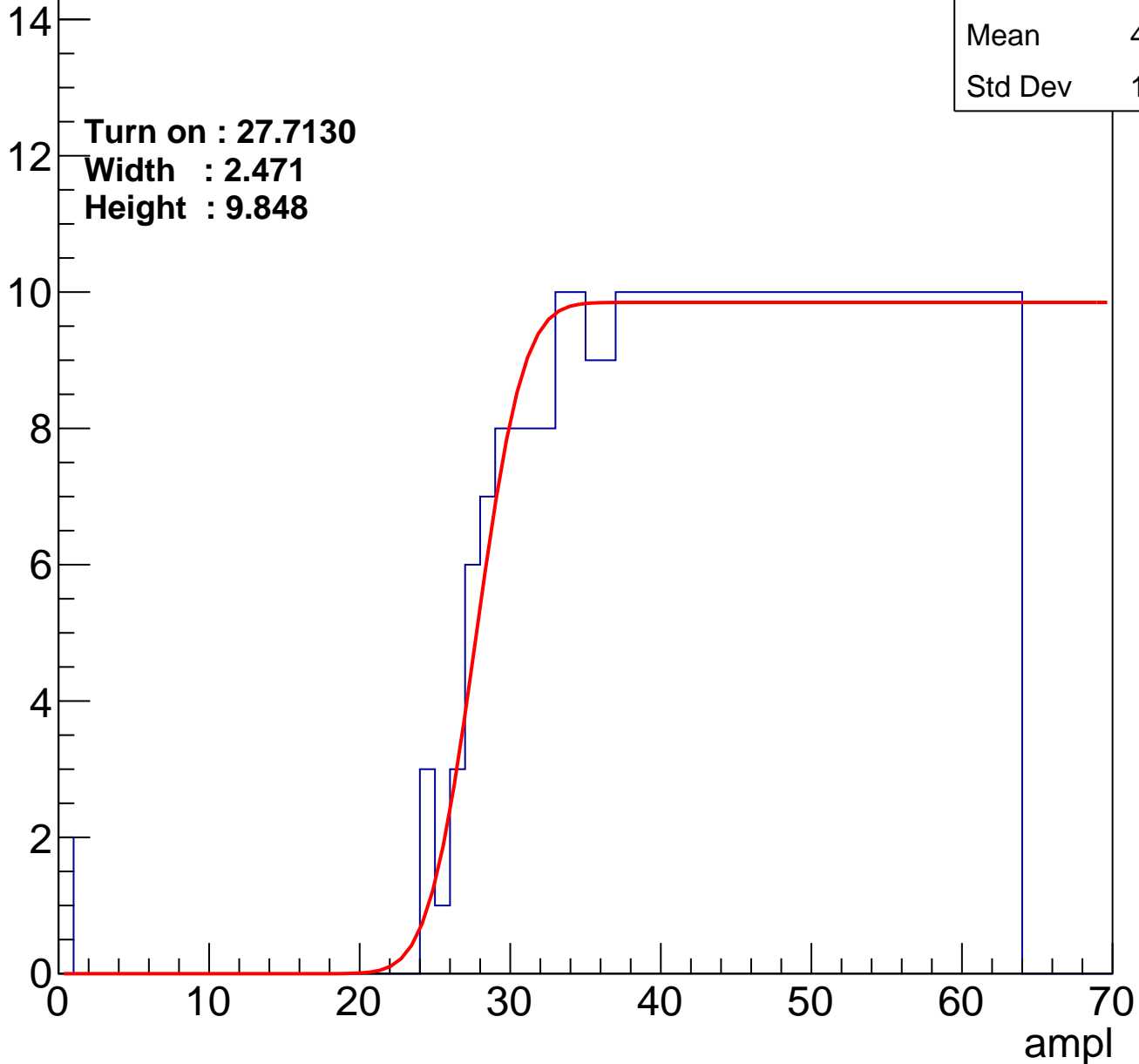
Entries	362
Mean	45.08
Std Dev	11.15

Turn on : 27.7130

Width : 2.471

Height : 9.848

Entry



B1L103S, U13-ch27

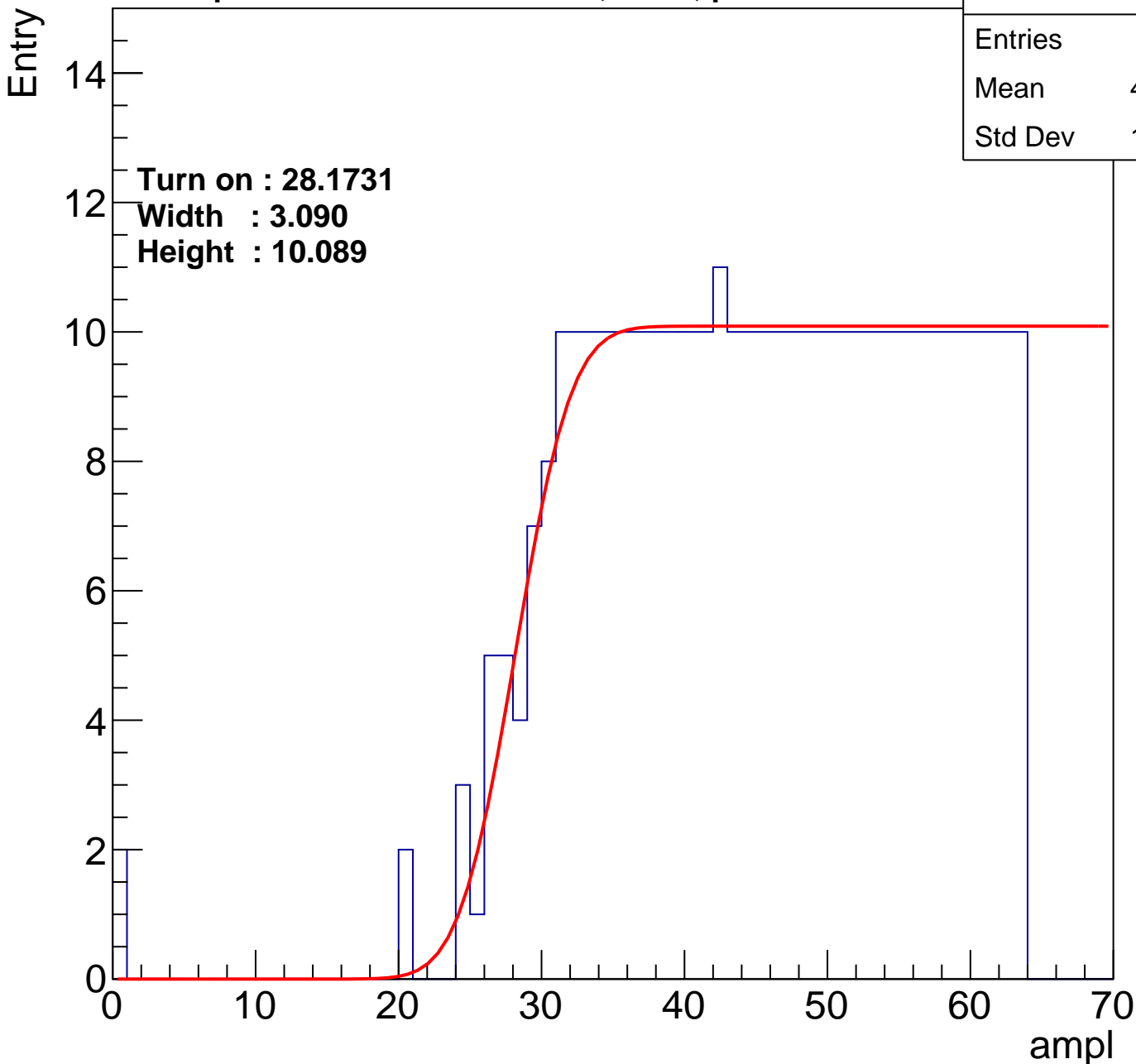
calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.86
Std Dev	11.23

Turn on : 28.1731

Width : 3.090

Height : 10.089



B1L103S, U13-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.5321

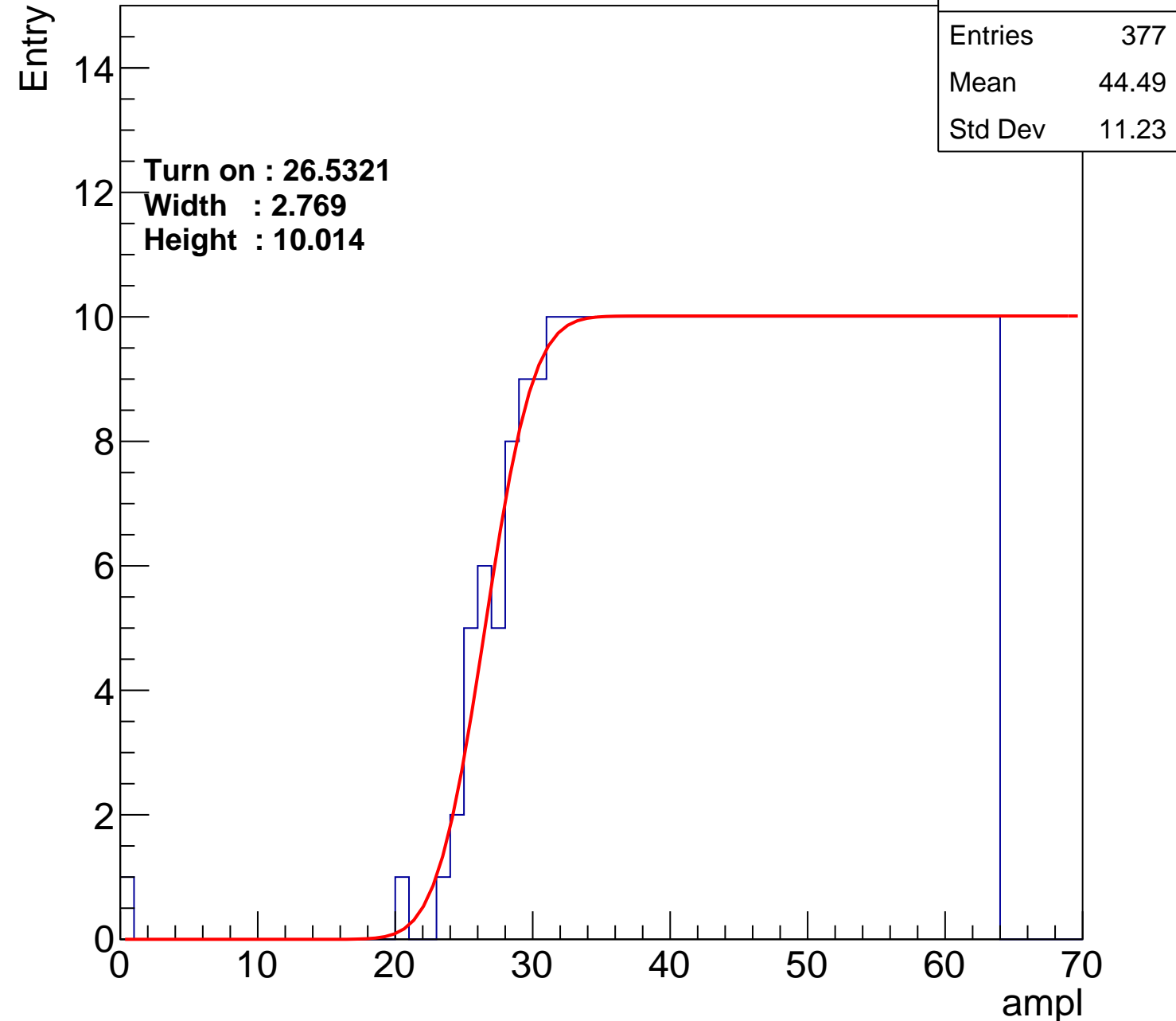
Width : 2.769

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.81
Std Dev	10.71

Turn on : 30.0890

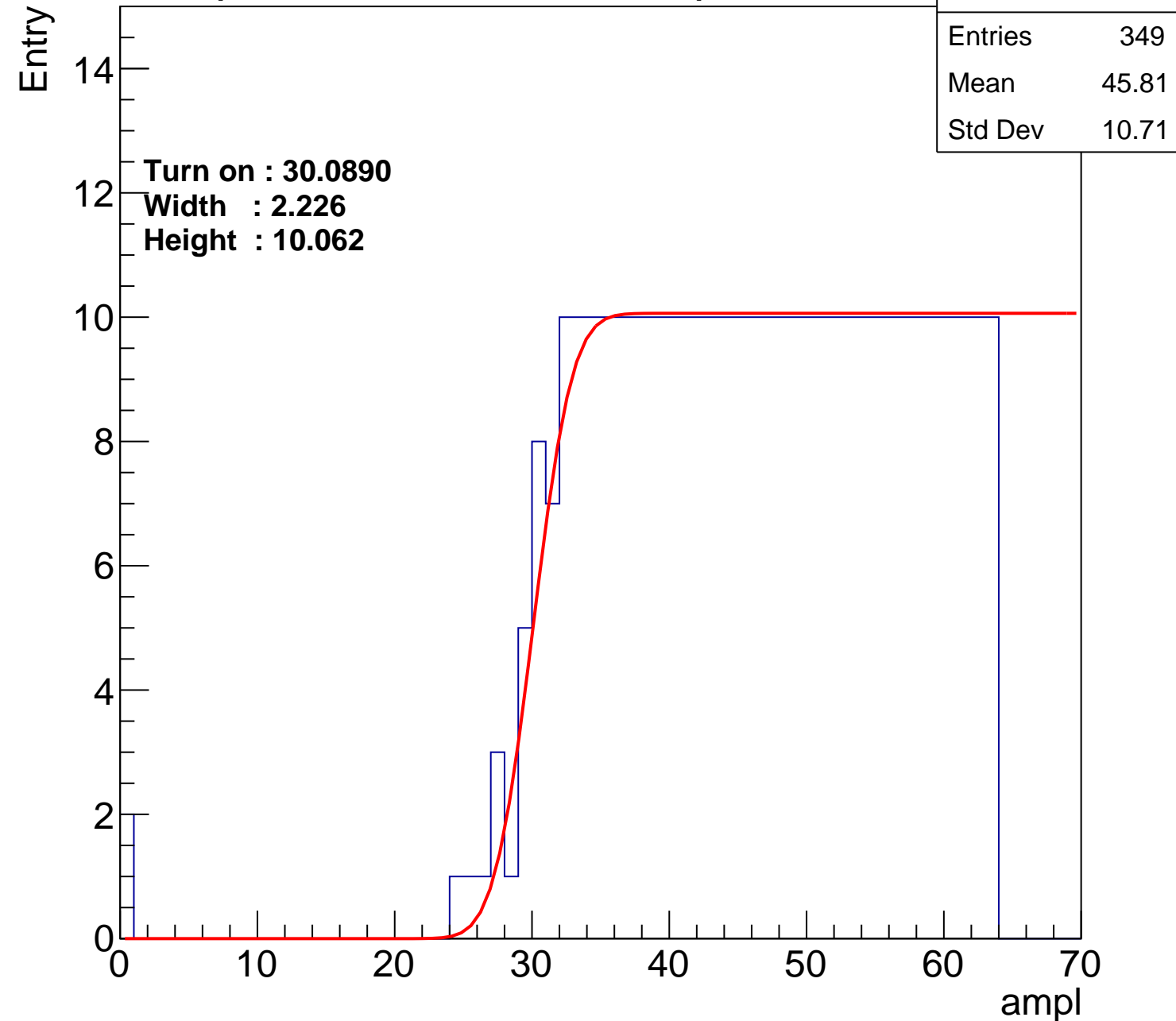
Width : 2.226

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch30

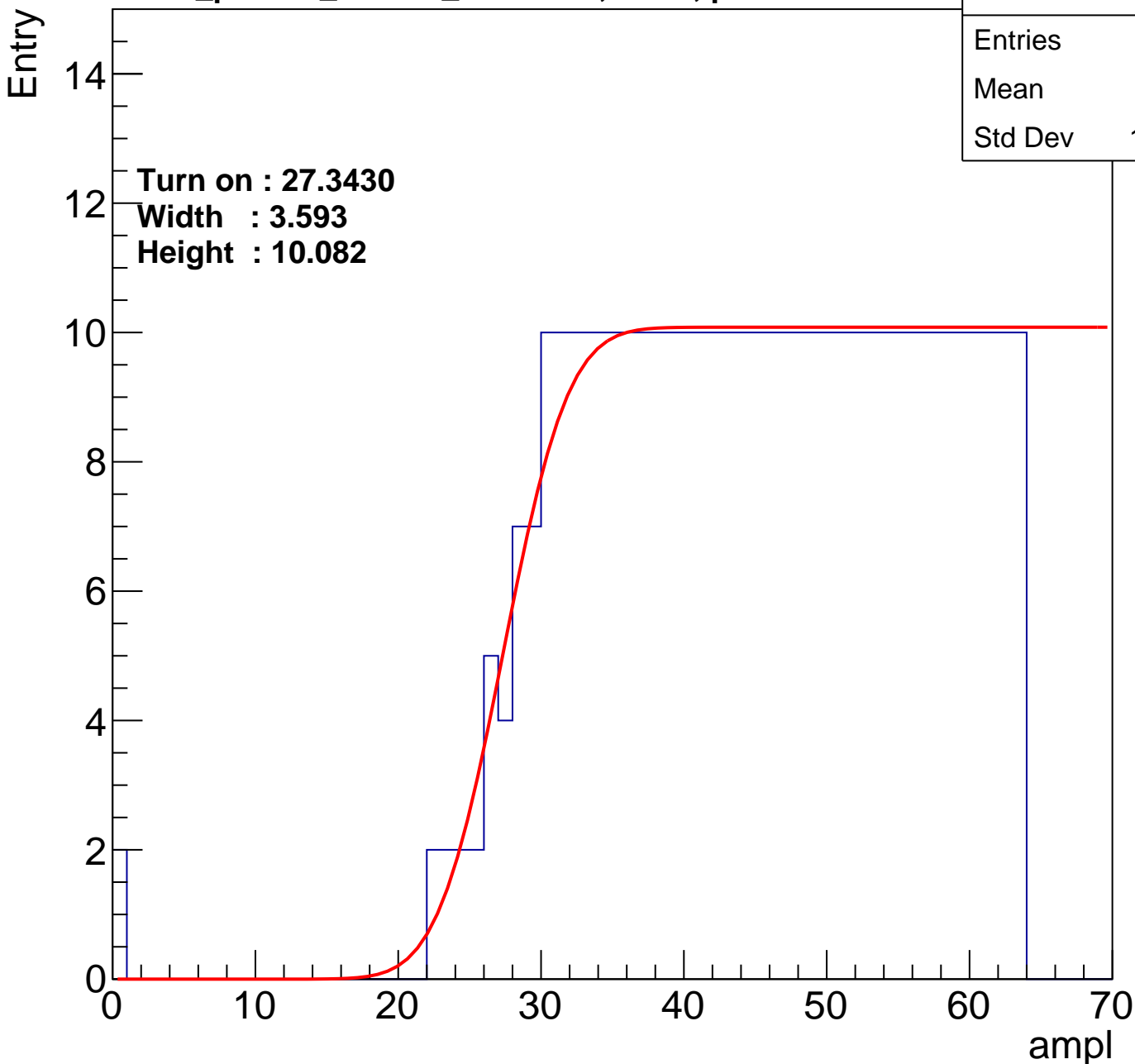
calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.6
Std Dev	11.36

Turn on : 27.3430

Width : 3.593

Height : 10.082



B1L103S, U13-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.67
Std Dev	11.34

Turn on : 27.0752

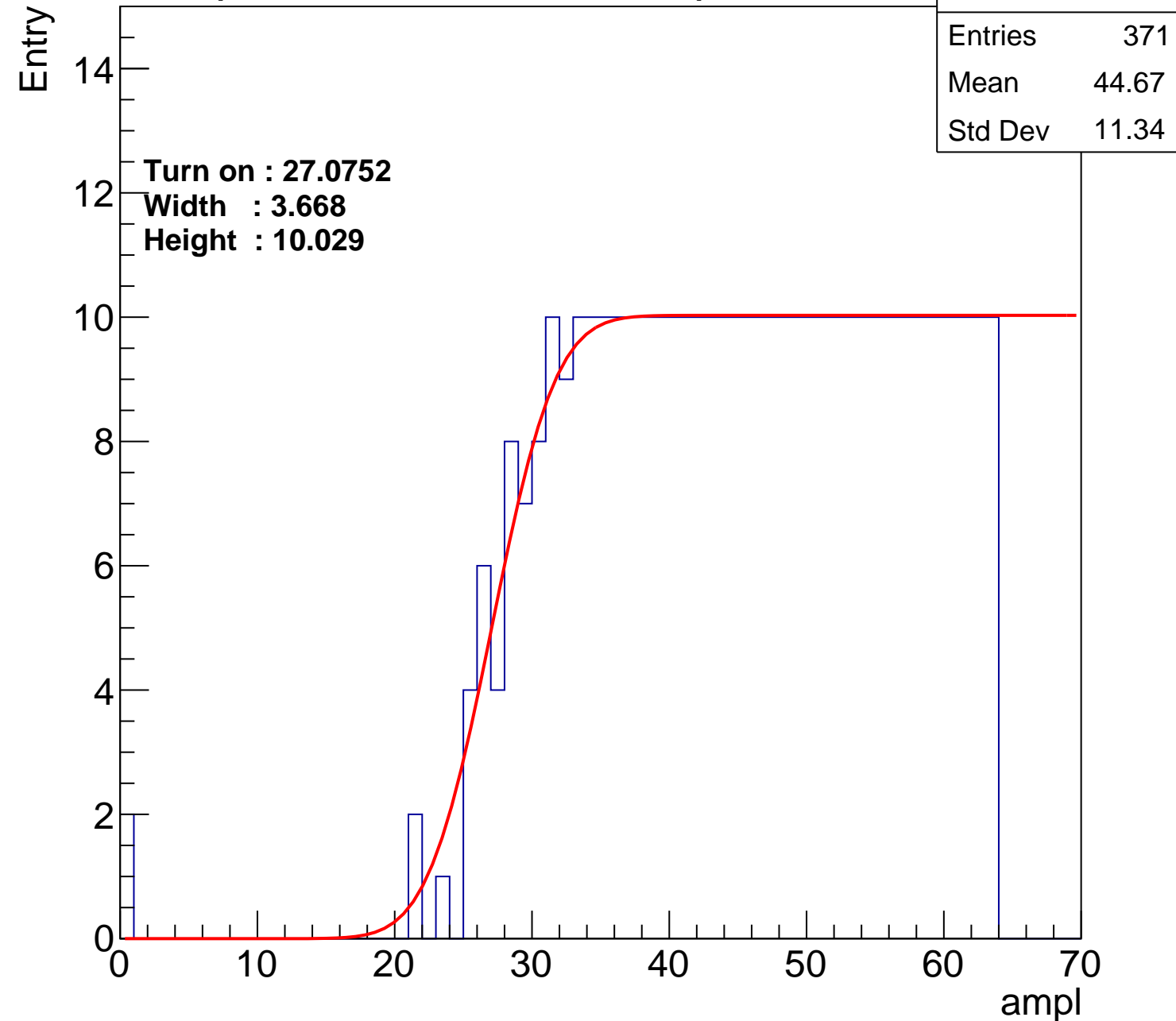
Width : 3.668

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.9
Std Dev	11.38

Turn on : 28.1476

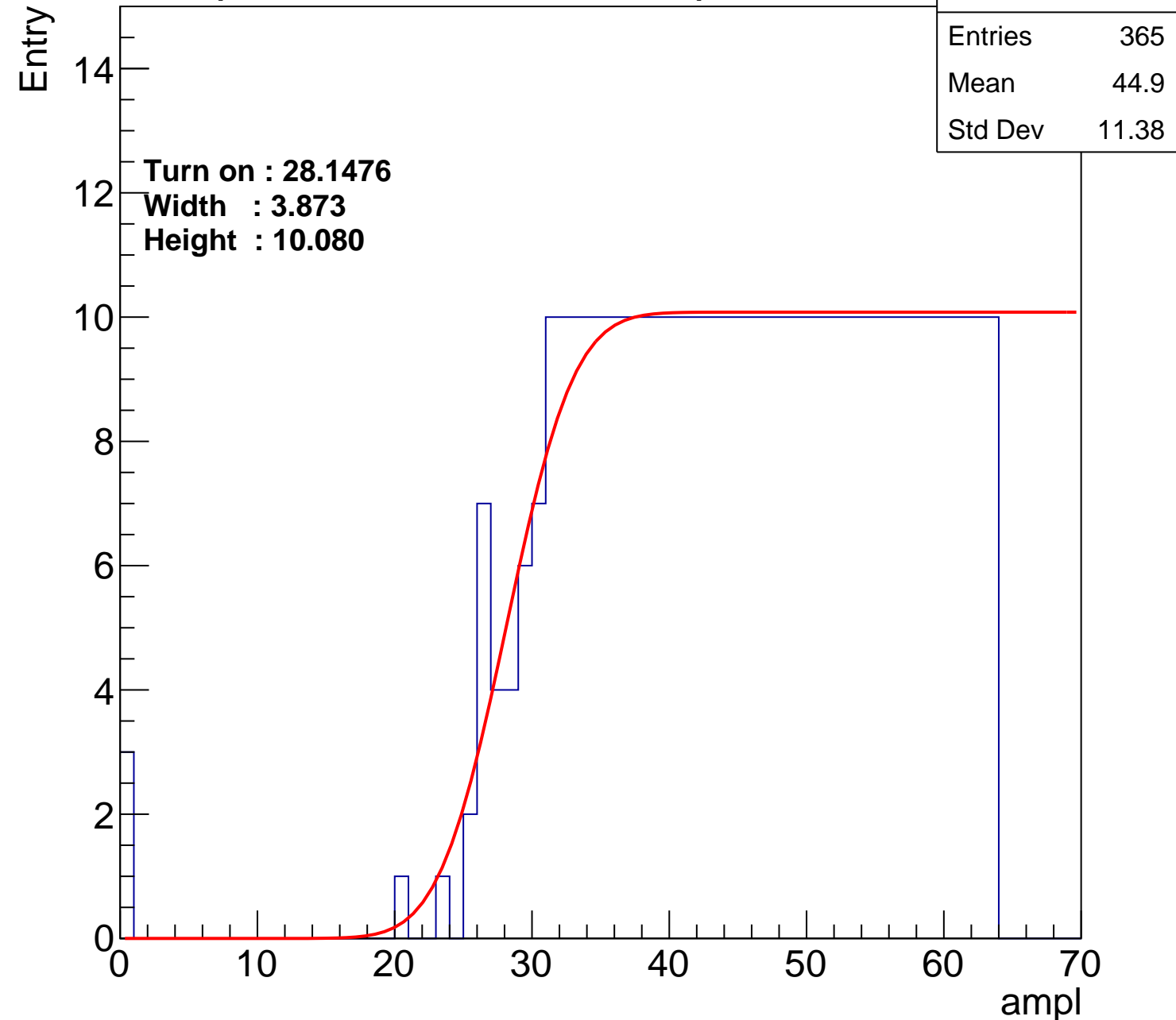
Width : 3.873

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.1
Std Dev	11.27

Height : 10.041



B1L103S, U13-ch34

calib_packv5_042523_0143.root, FC#7, port C2

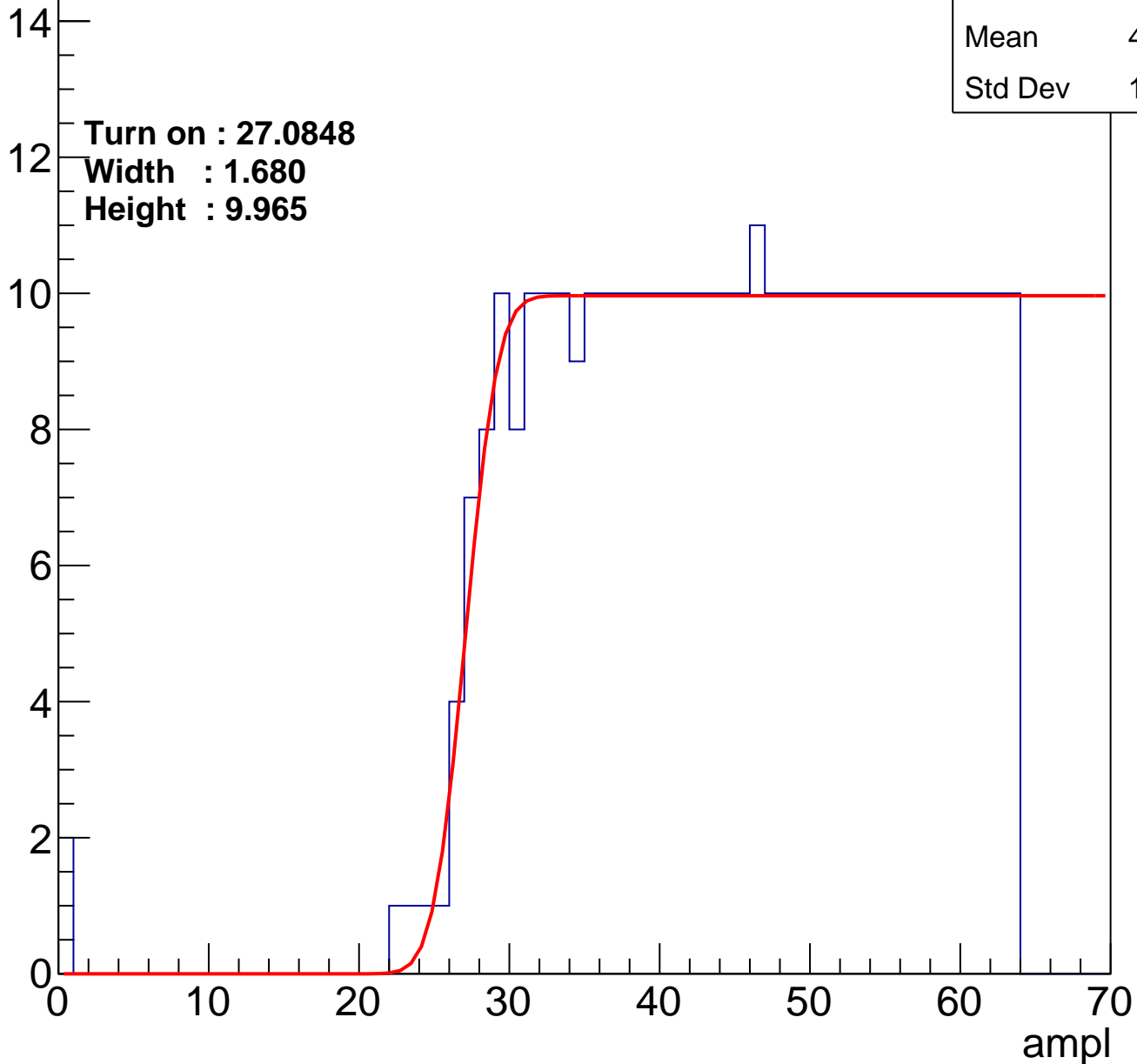
Entries	373
Mean	44.67
Std Dev	11.27

Turn on : 27.0848

Width : 1.680

Height : 9.965

Entry



B1L103S, U13-ch35

calib_packv5_042523_0143.root, FC#7, port C2

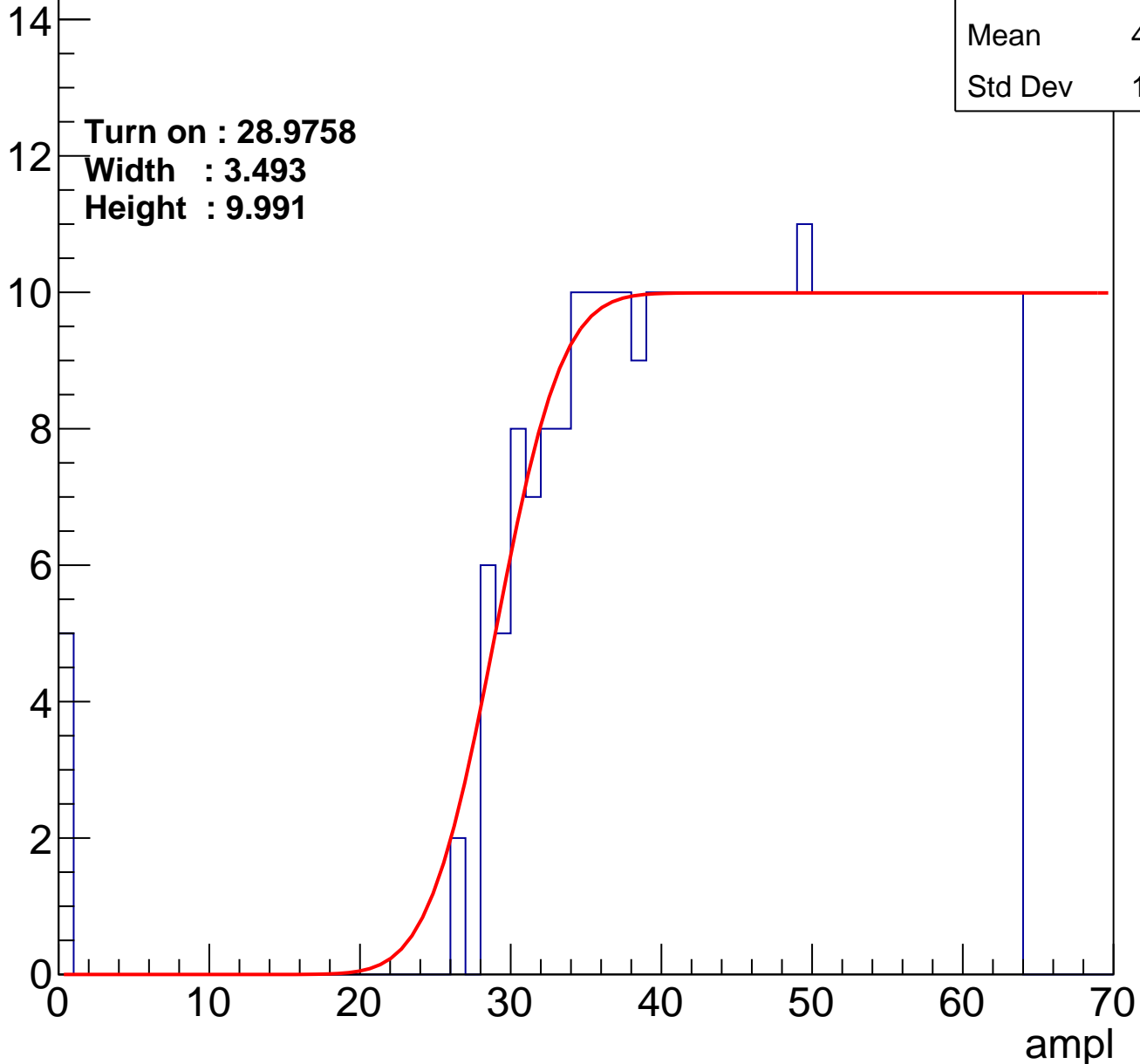
Entries	349
Mean	45.57
Std Dev	11.42

Turn on : 28.9758

Width : 3.493

Height : 9.991

Entry



B1L103S, U13-ch36

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.4
Std Dev	10.92

Turn on : 28.0929

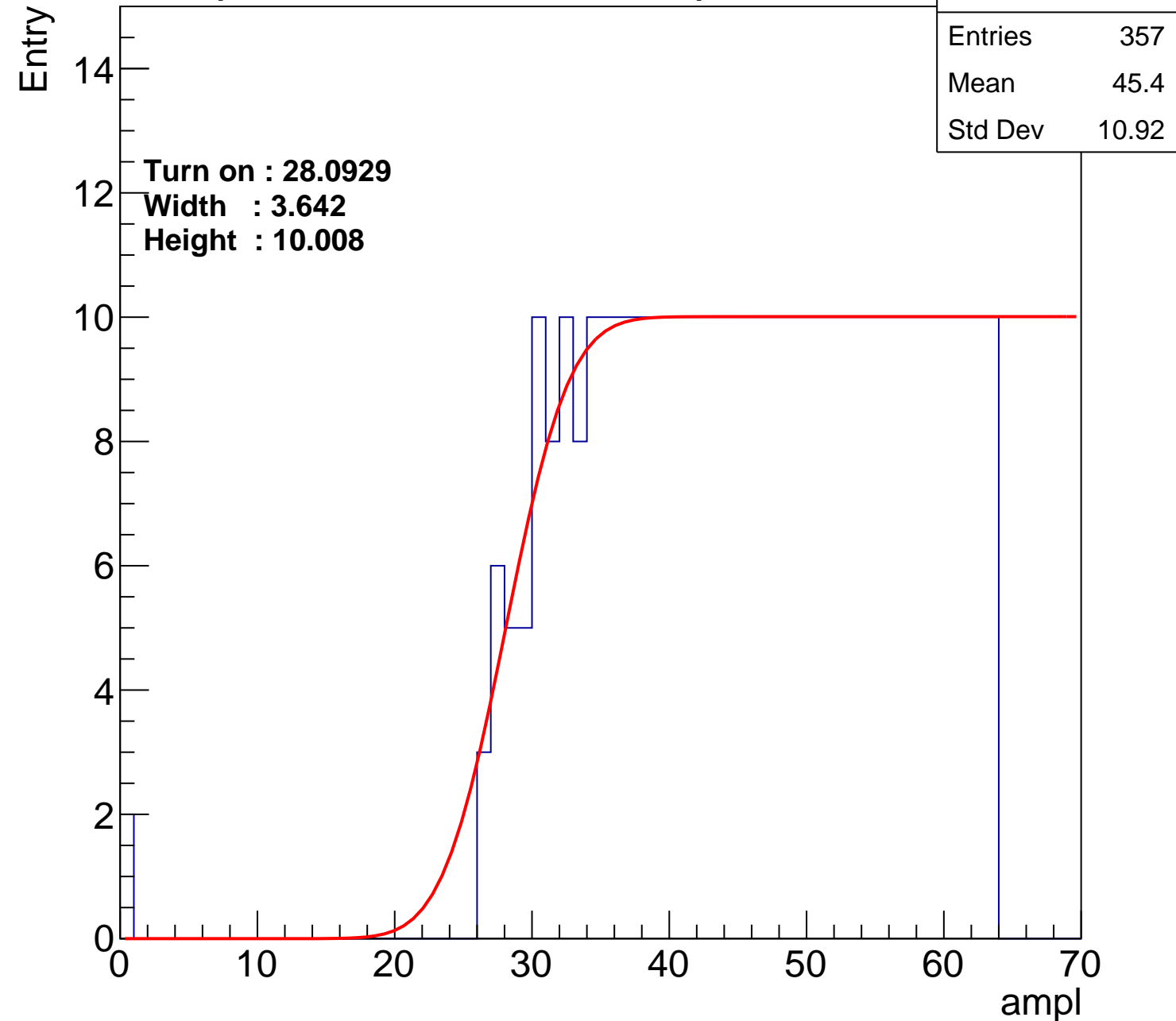
Width : 3.642

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.09
Std Dev	11.63

Turn on : 28.9070

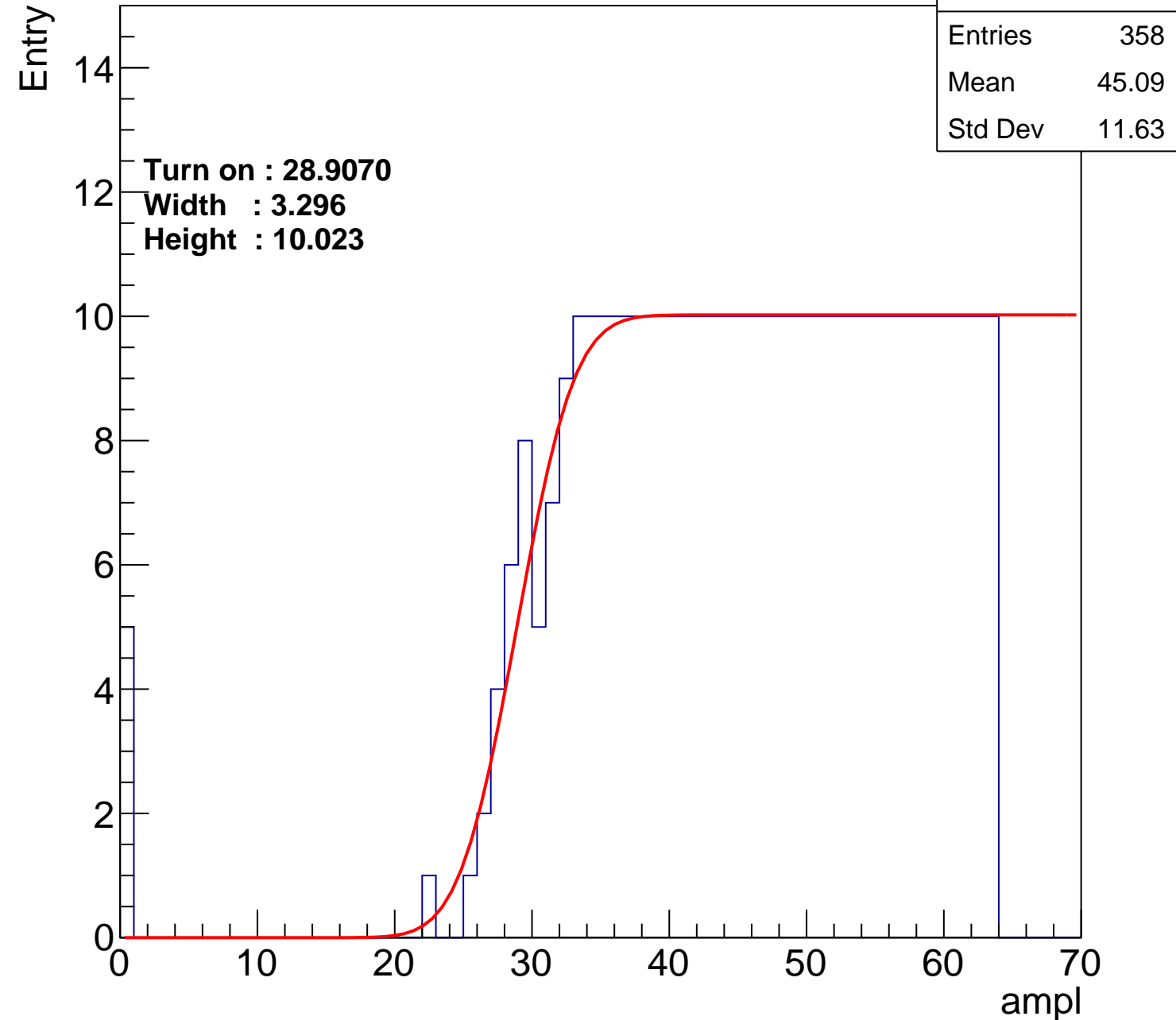
Width : 3.296

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch38

calib_packv5_042523_0143.root, FC#7, port C2

Entries	347
Mean	45.78
Std Dev	10.96

Turn on : 29.7249

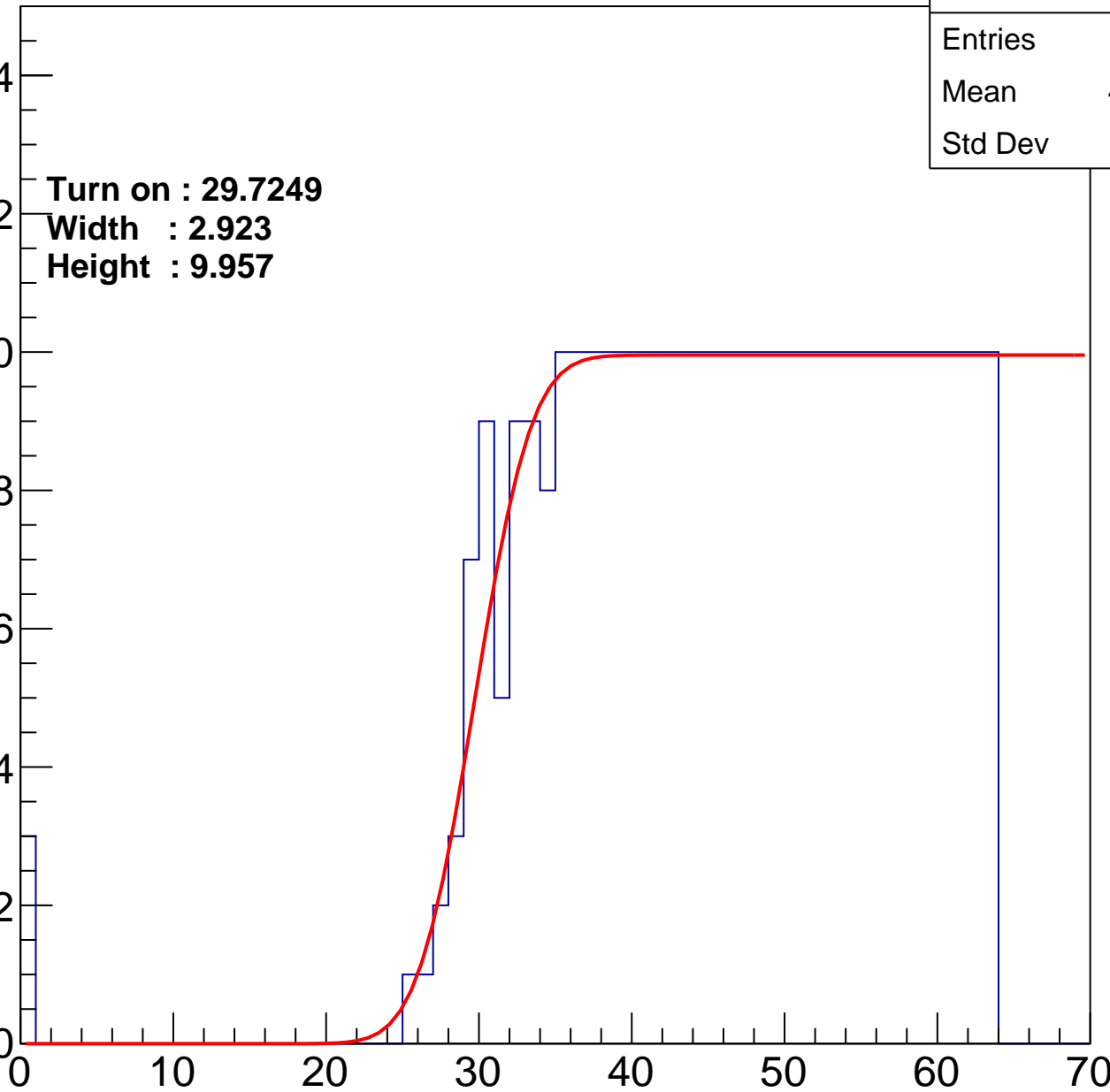
Width : 2.923

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch39

calib_packv5_042523_0143.root, FC#7, port C2

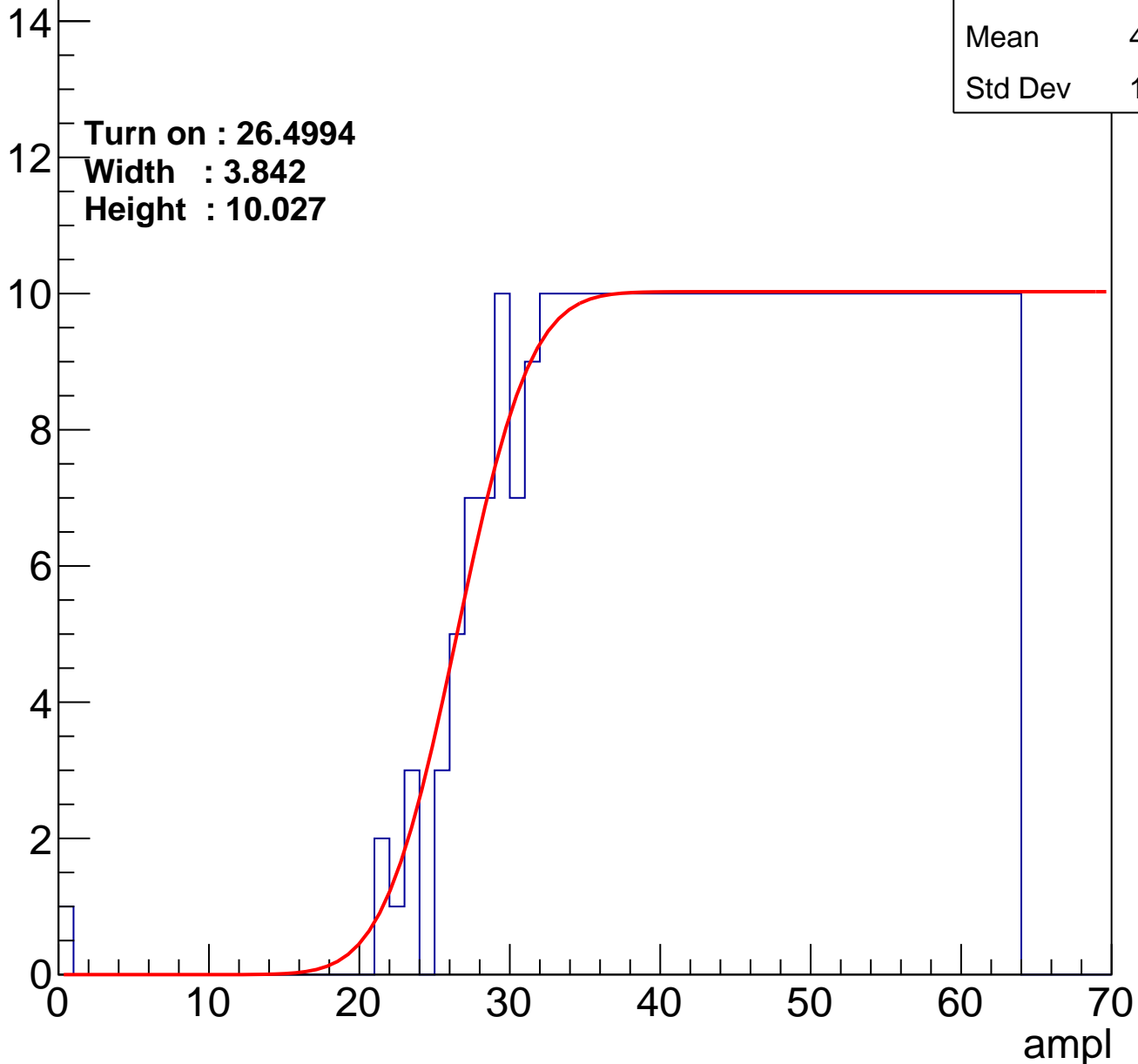
Entries	375
Mean	44.54
Std Dev	11.26

Turn on : 26.4994

Width : 3.842

Height : 10.027

Entry



B1L103S, U13-ch40

calib_packv5_042523_0143.root, FC#7, port C2

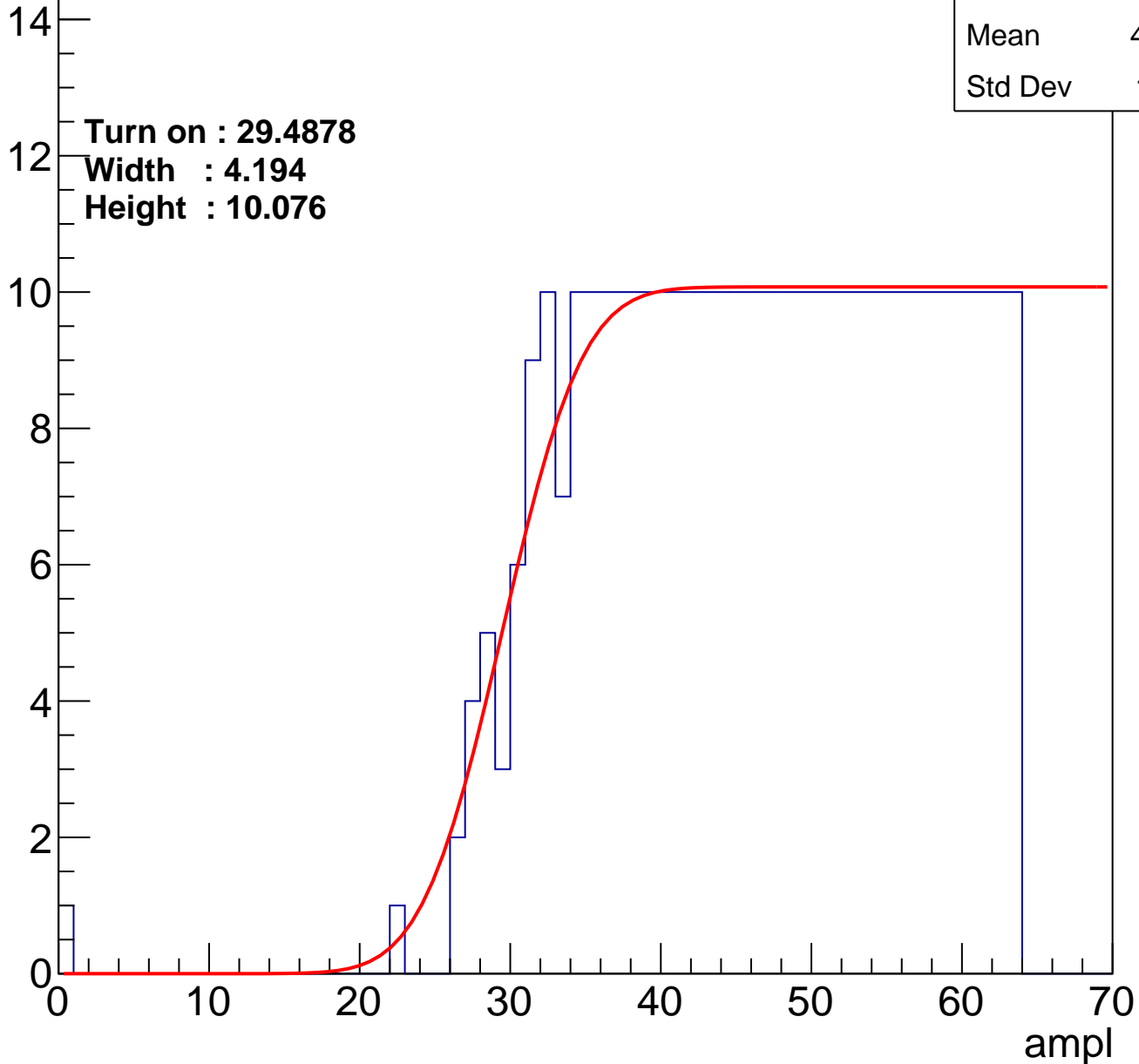
Entries	348
Mean	45.89
Std Dev	10.51

Turn on : 29.4878

Width : 4.194

Height : 10.076

Entry



B1L103S, U13-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.65
Std Dev	11.18

Turn on : 27.3430

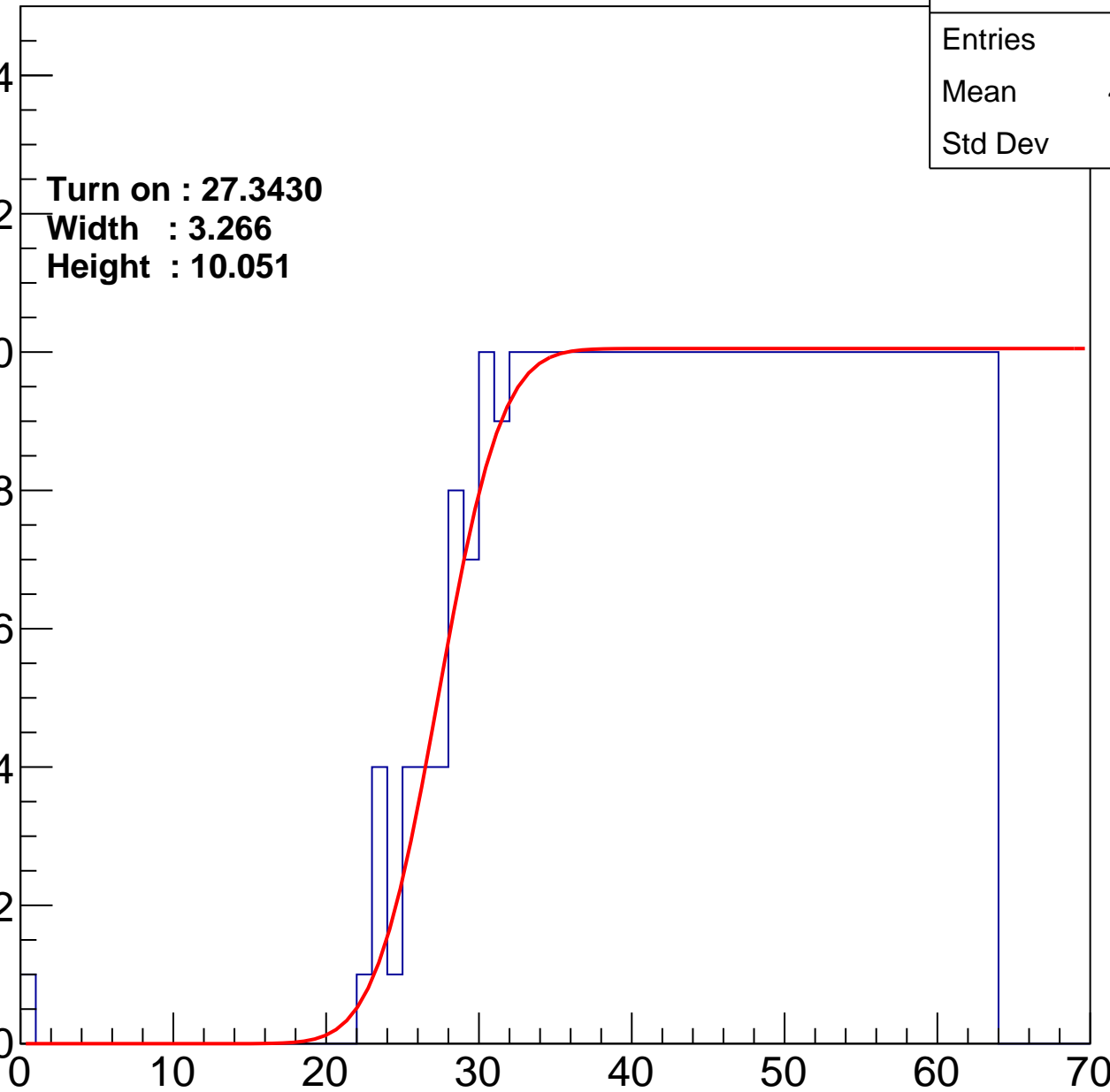
Width : 3.266

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.15
Std Dev	11.47

Turn on : 25.8038

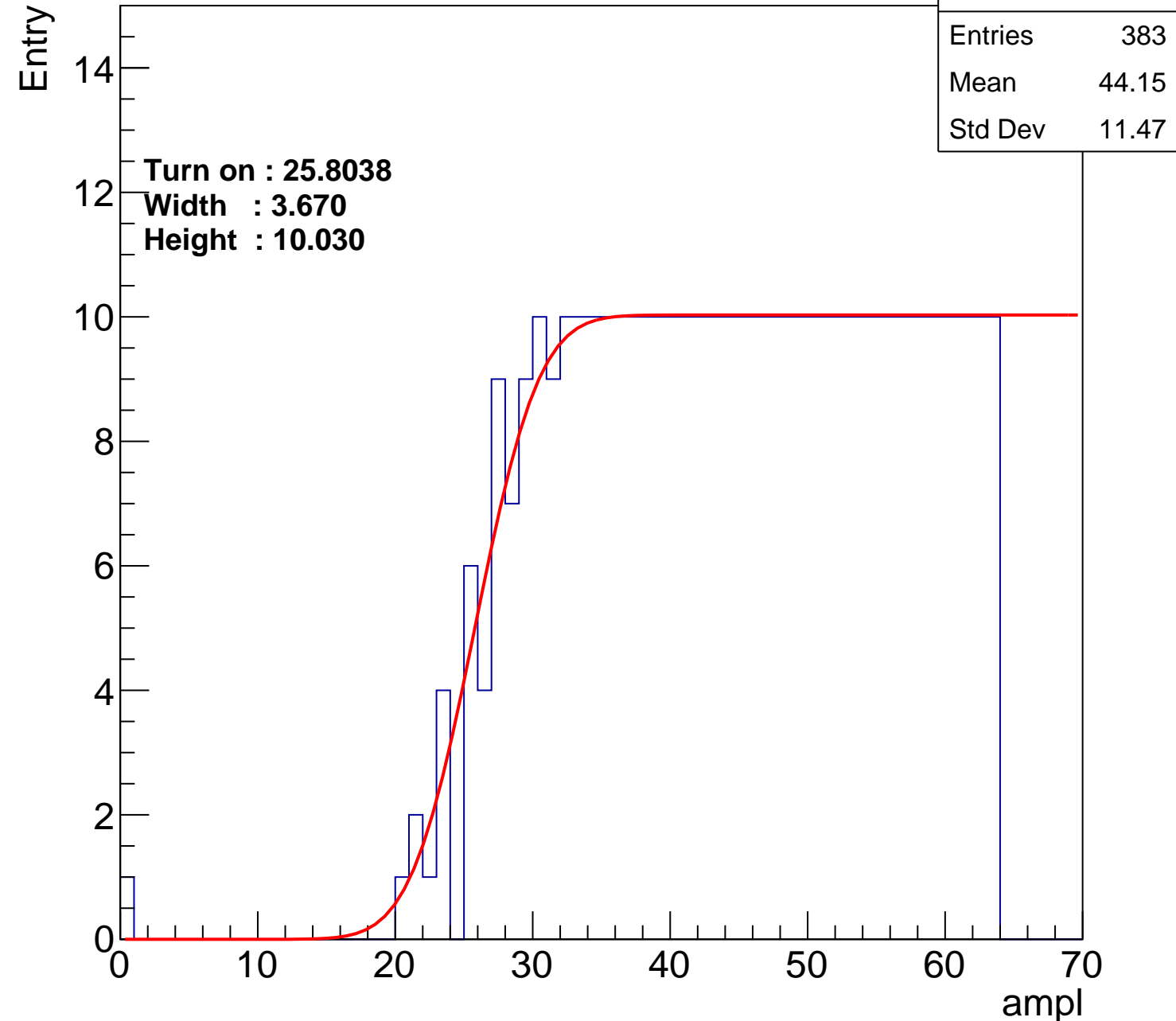
Width : 3.670

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.61
Std Dev	11.23

Turn on : 30.0402

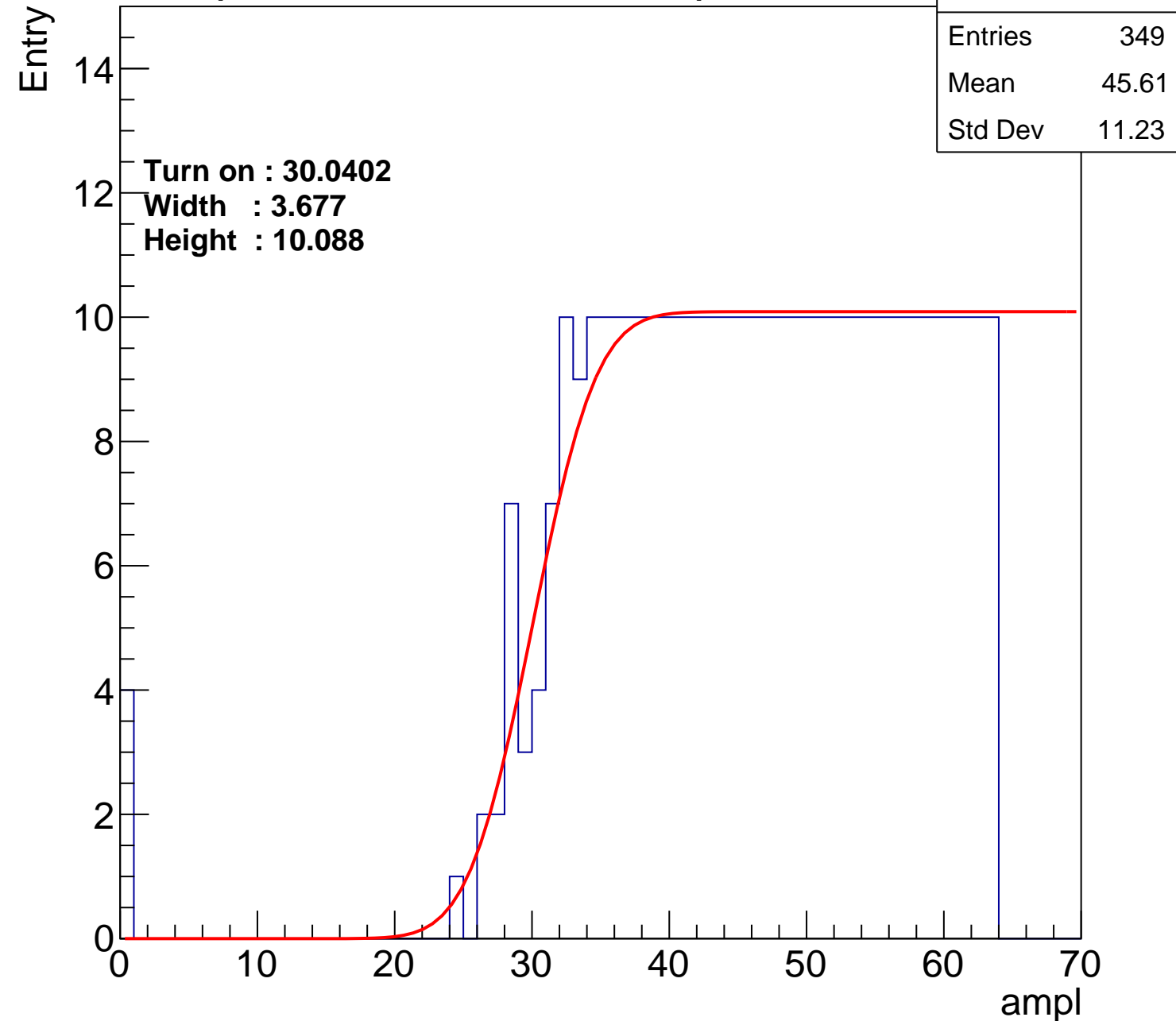
Width : 3.677

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch44

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.7245

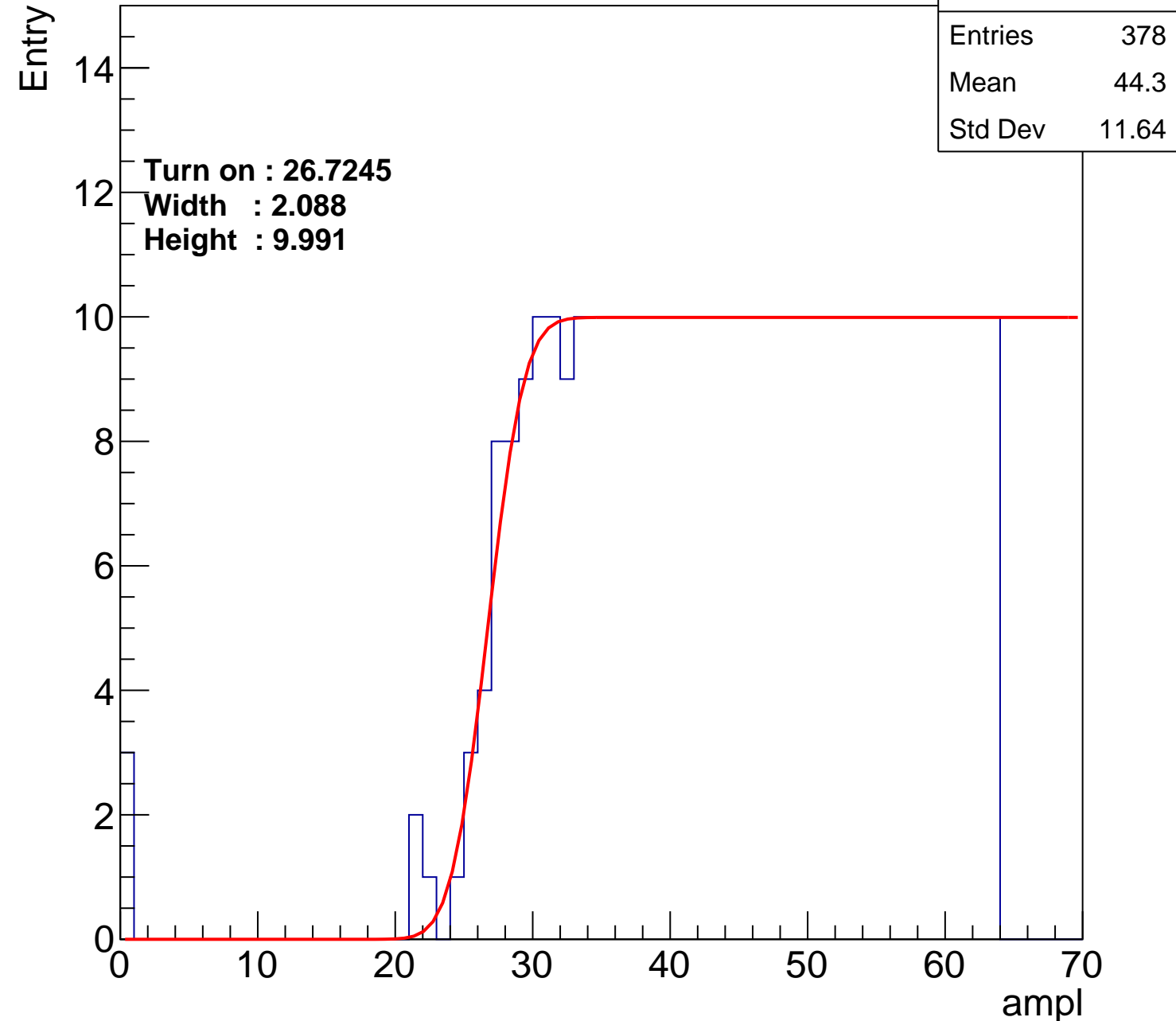
Width : 2.088

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.35
Std Dev	10.79

Turn on : 28.6221

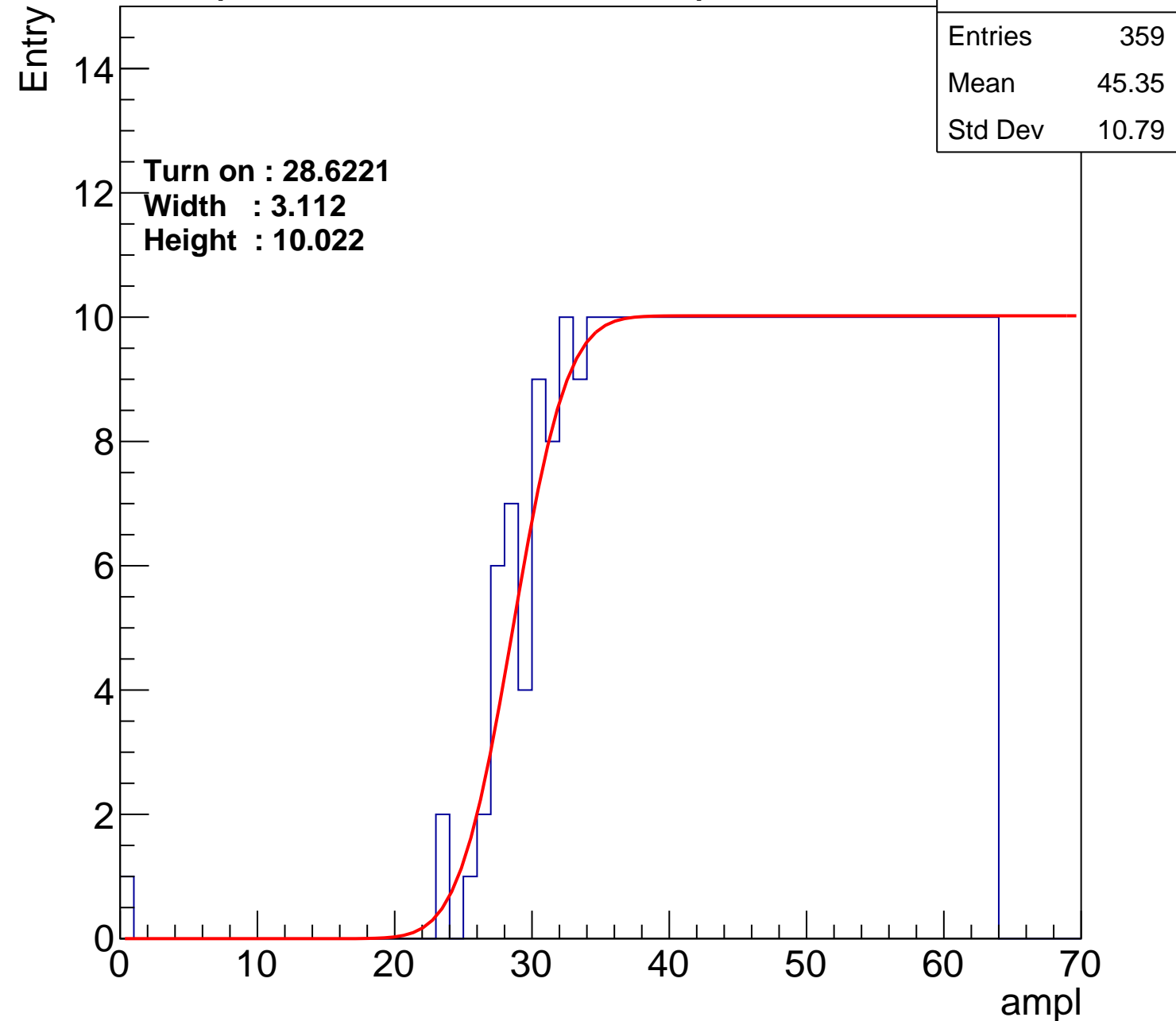
Width : 3.112

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch46

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.14
Std Dev	12.08

Turn on : 27.4446

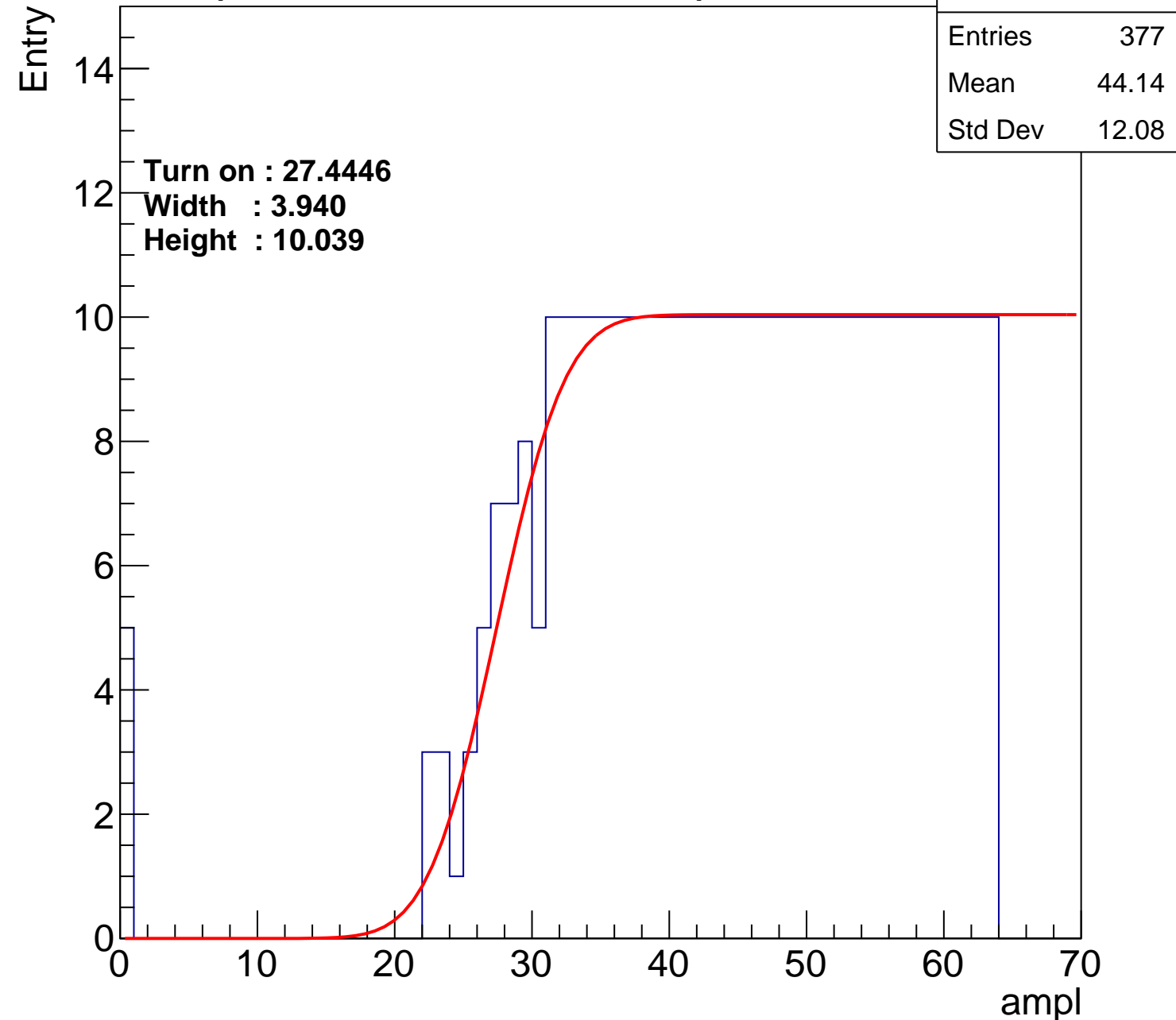
Width : 3.940

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.5
Std Dev	11.73

Turn on : 28.1164

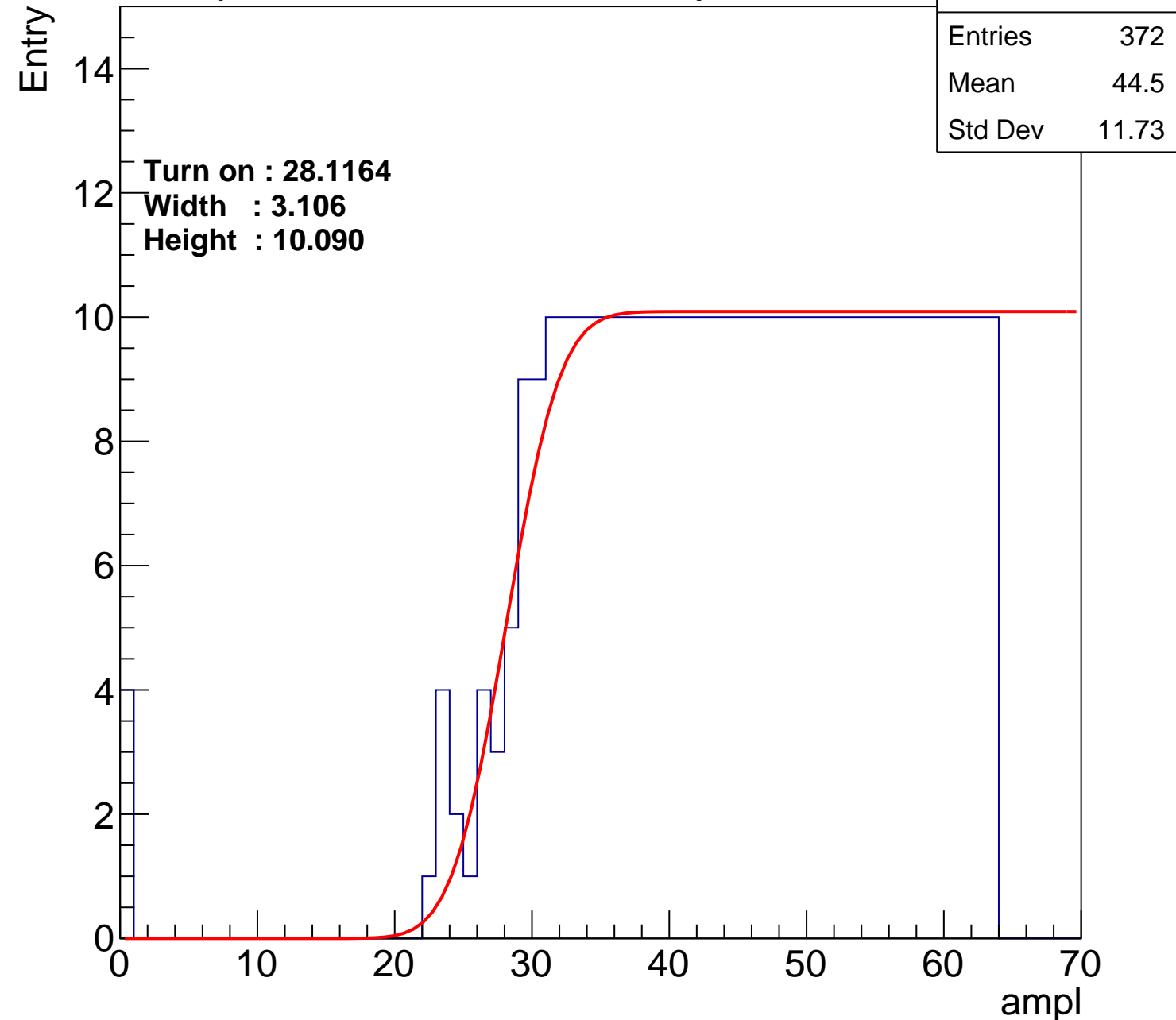
Width : 3.106

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.57
Std Dev	11.59

Turn on : 27.9177

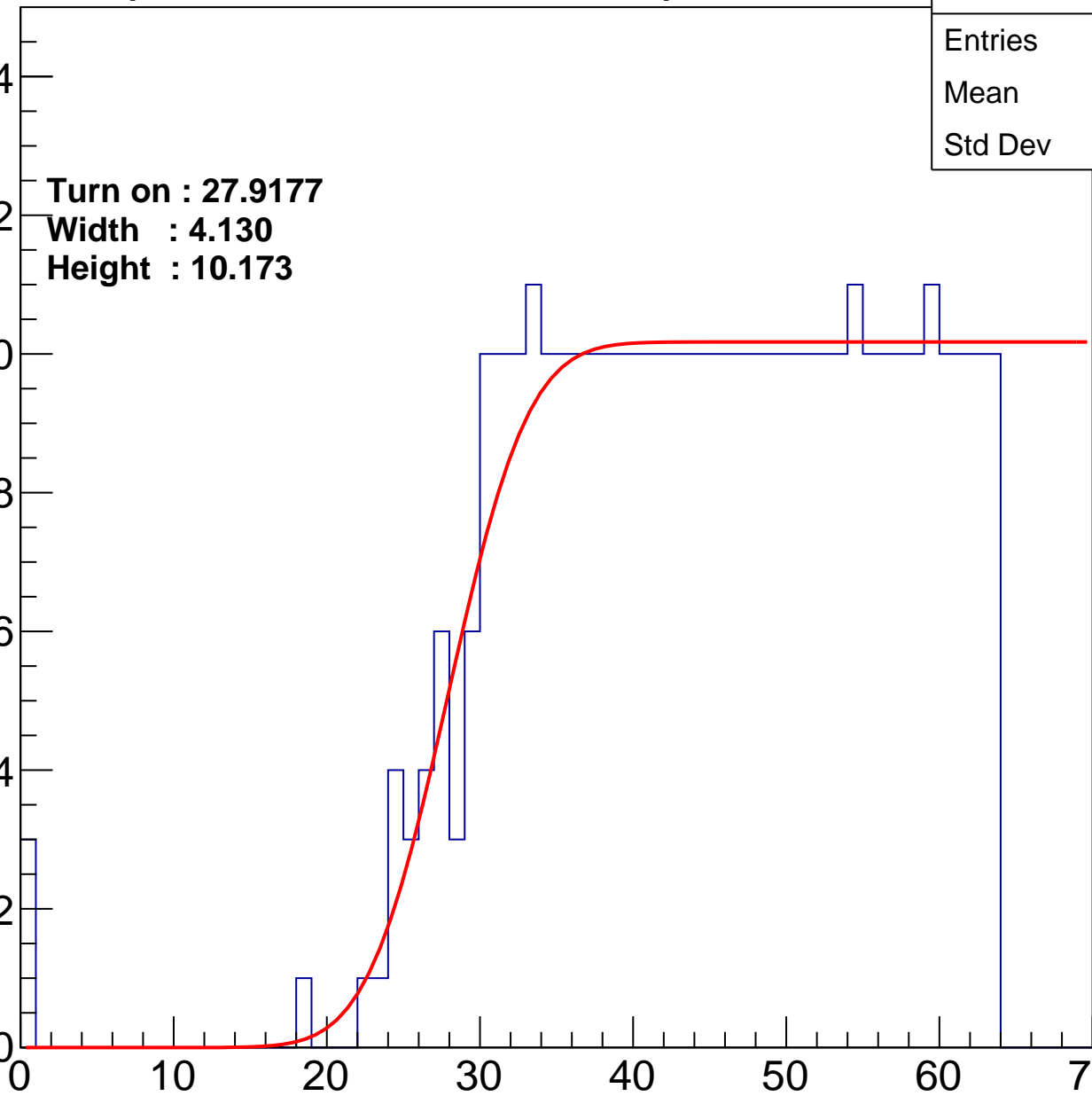
Width : 4.130

Height : 10.173

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch49

calib_packv5_042523_0143.root, FC#7, port C2

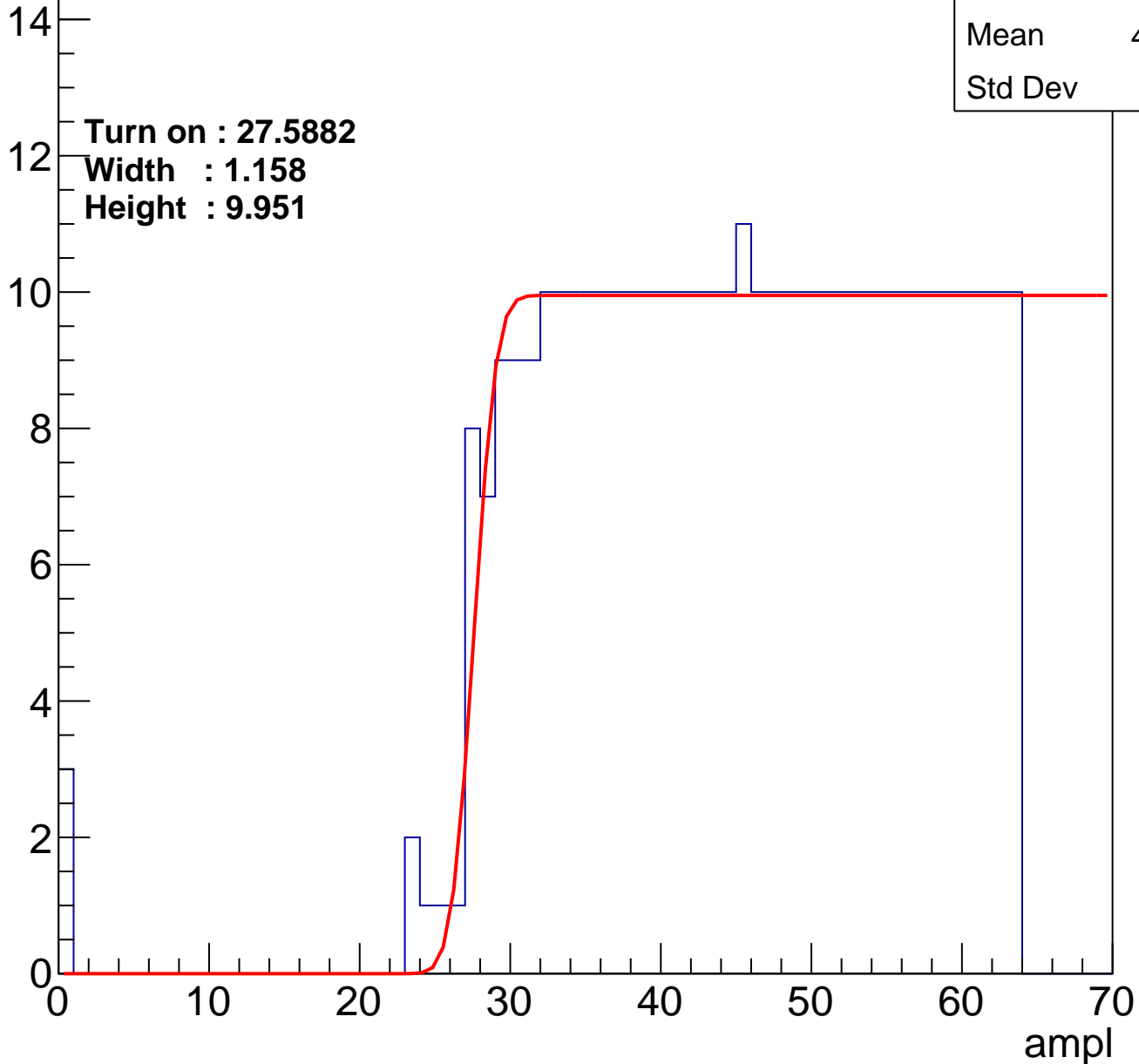
Entries	371
Mean	44.71
Std Dev	11.4

Turn on : 27.5882

Width : 1.158

Height : 9.951

Entry



B1L103S, U13-ch50

calib_packv5_042523_0143.root, FC#7, port C2

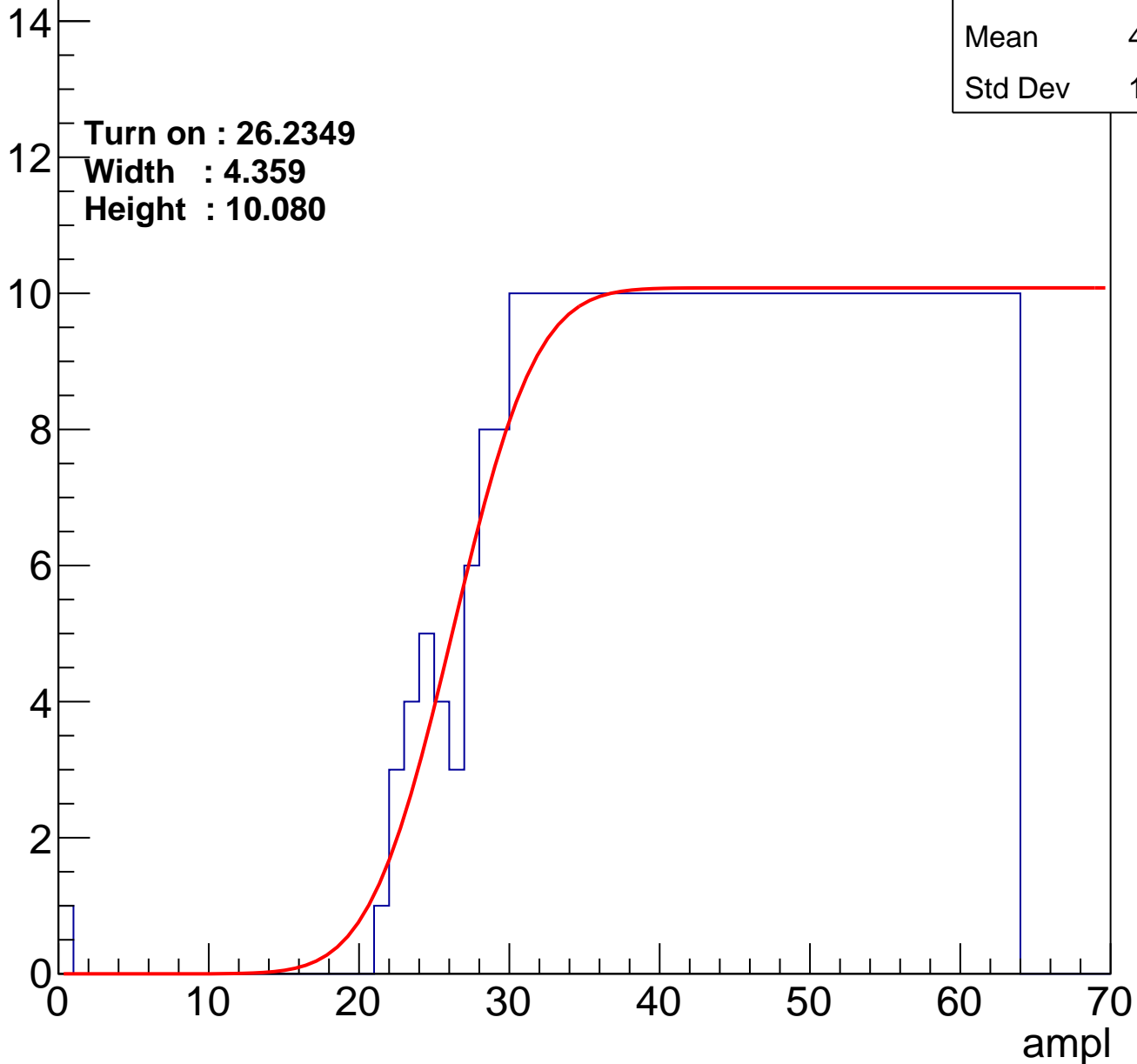
Entries	383
Mean	44.14
Std Dev	11.49

Turn on : 26.2349

Width : 4.359

Height : 10.080

Entry



B1L103S, U13-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.54
Std Dev	11.39

Turn on : 27.2215

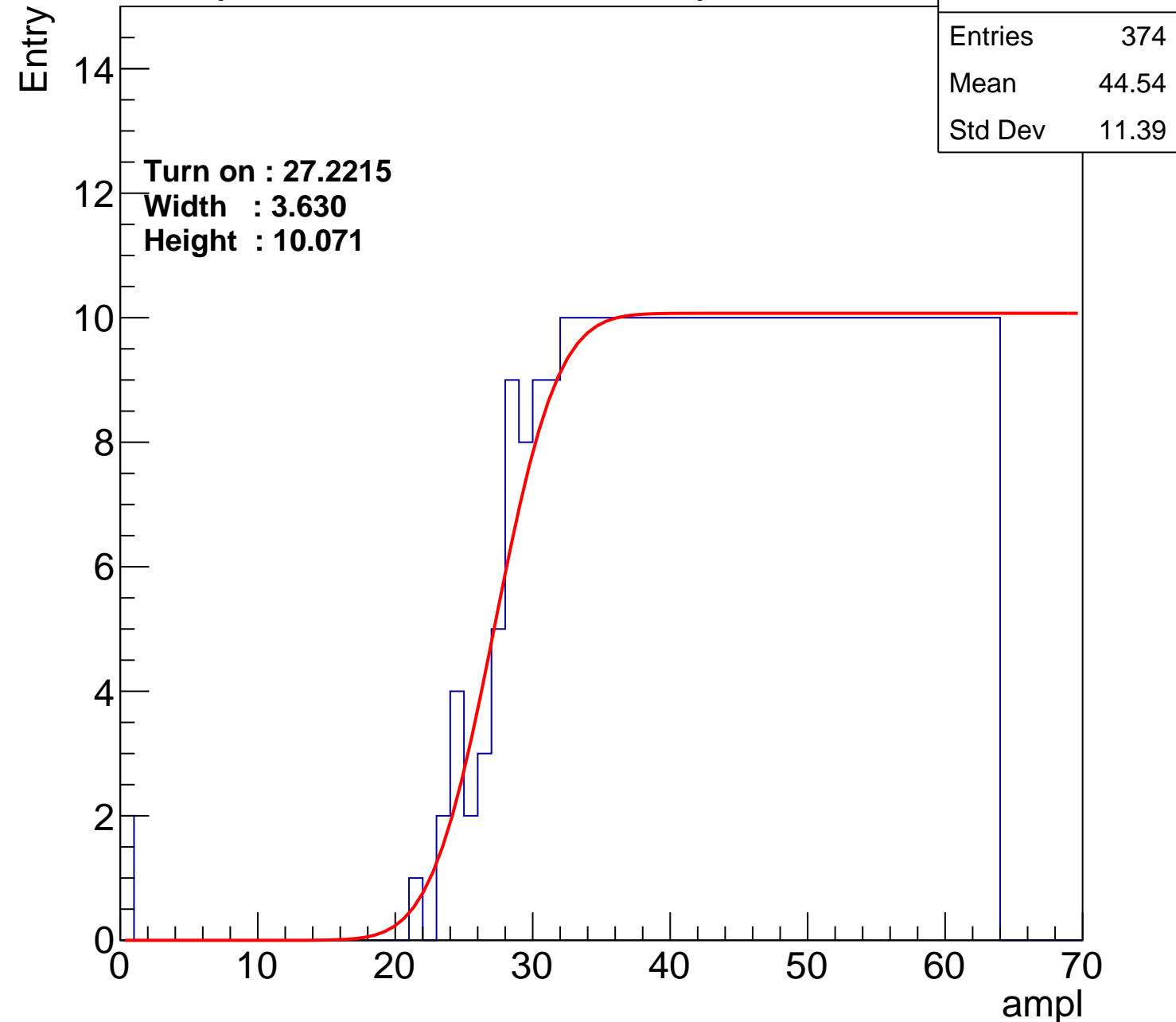
Width : 3.630

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch52

calib_packv5_042523_0143.root, FC#7, port C2

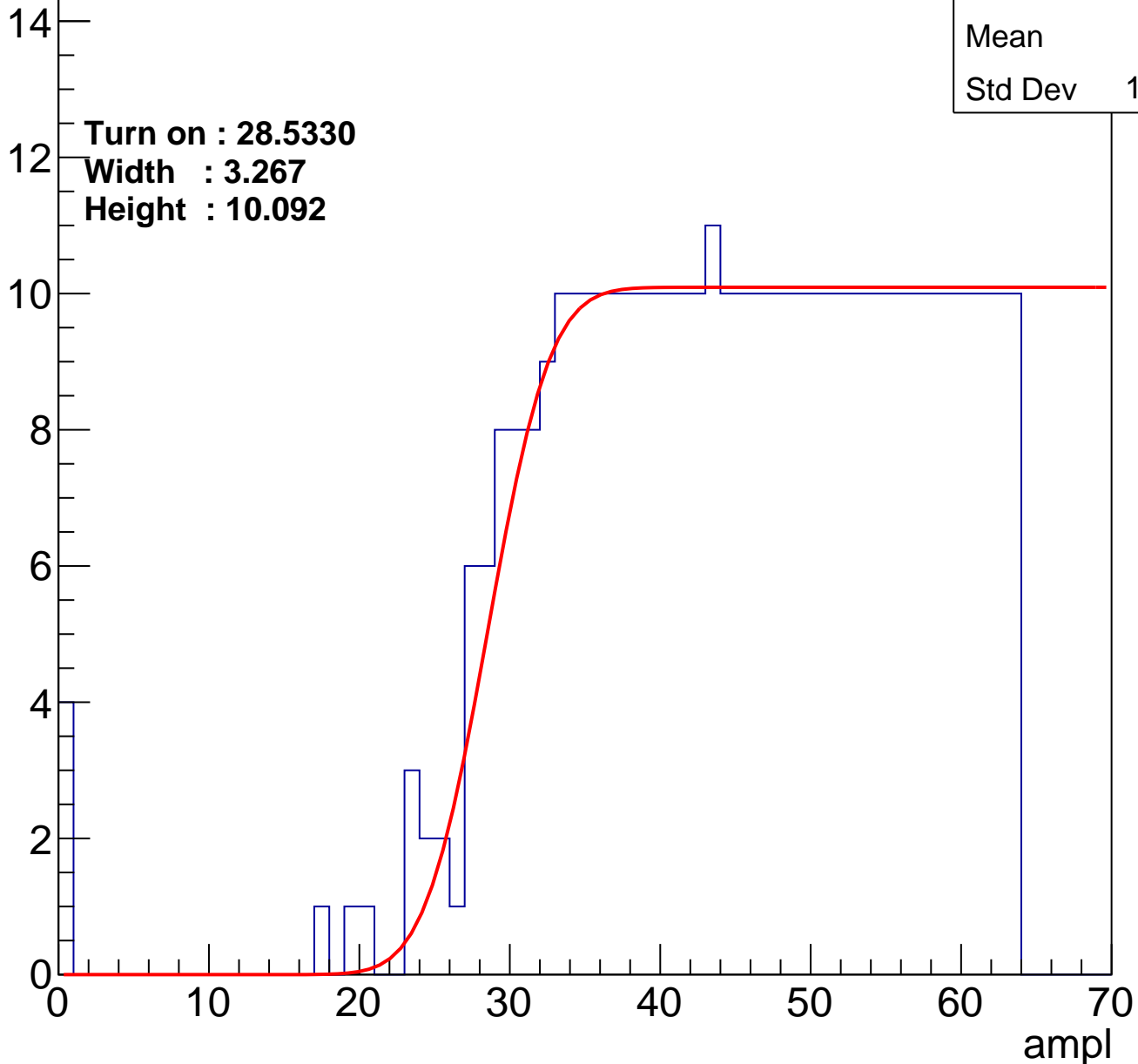
Entries	371
Mean	44.5
Std Dev	11.82

Turn on : 28.5330

Width : 3.267

Height : 10.092

Entry



B1L103S, U13-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.8985

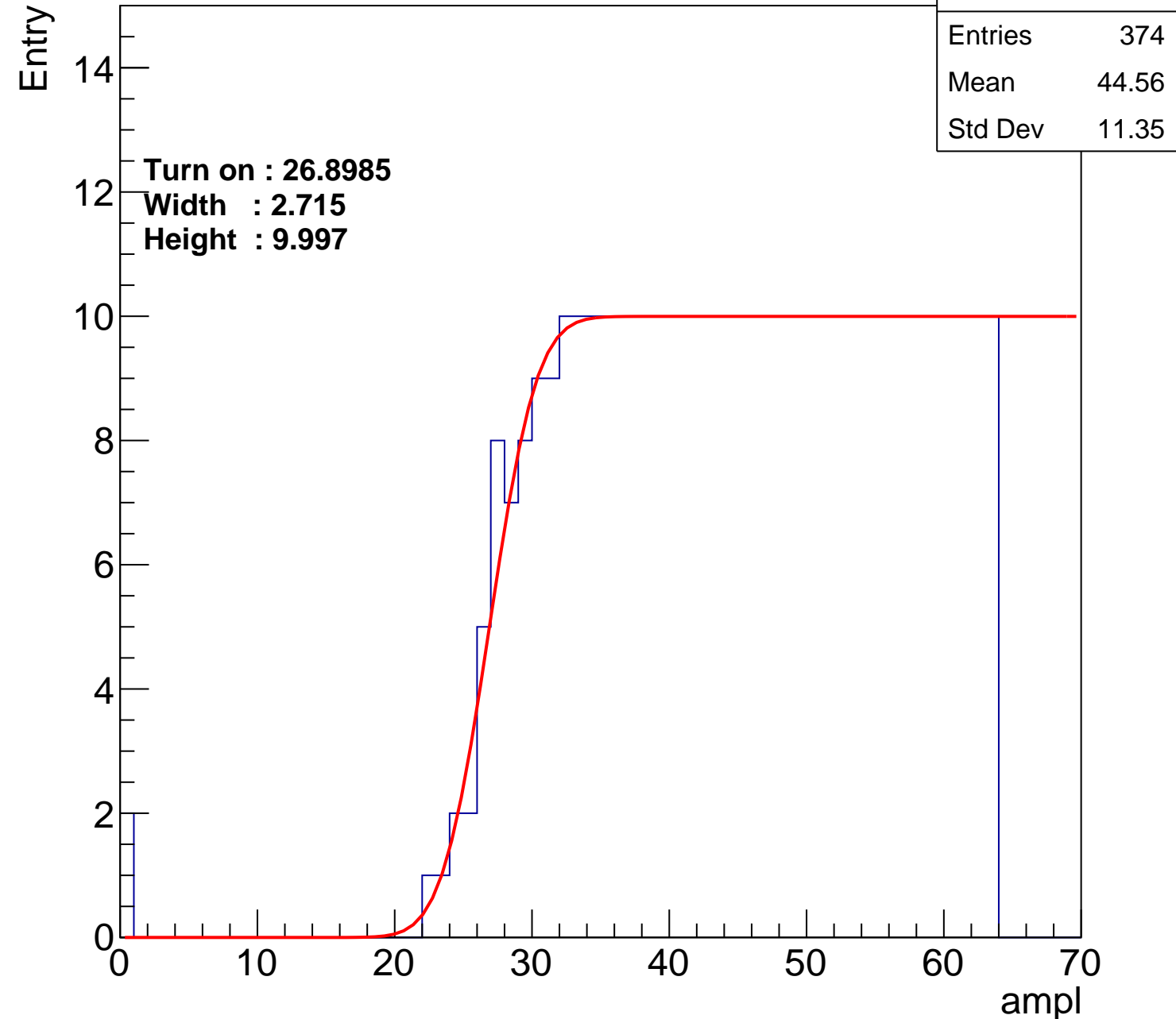
Width : 2.715

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl

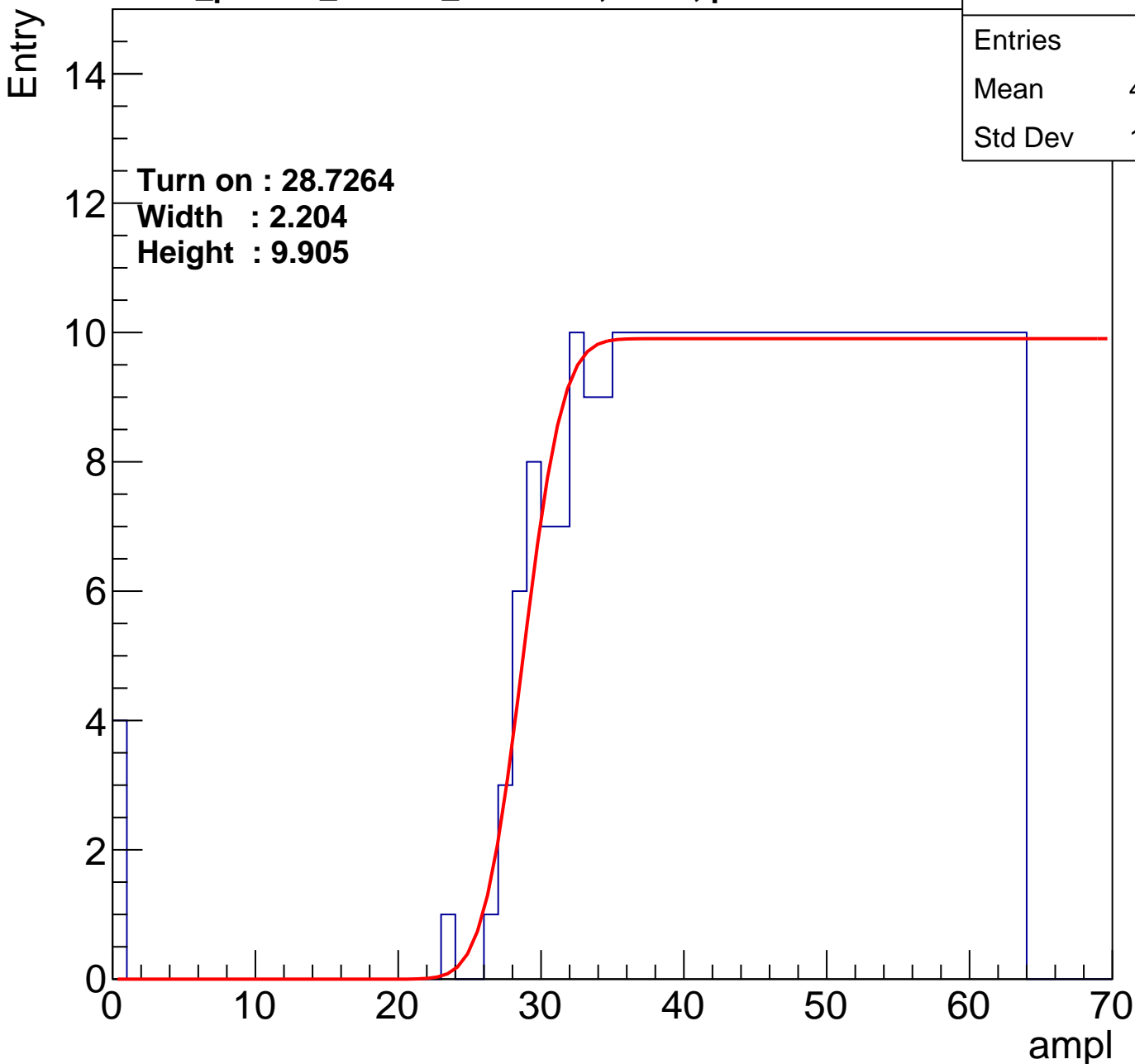


calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	355
Mean	45.32
Std Dev	11.34

Height : 9.905



B1L103S, U13-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.18
Std Dev	11.21

Turn on : 28.4860

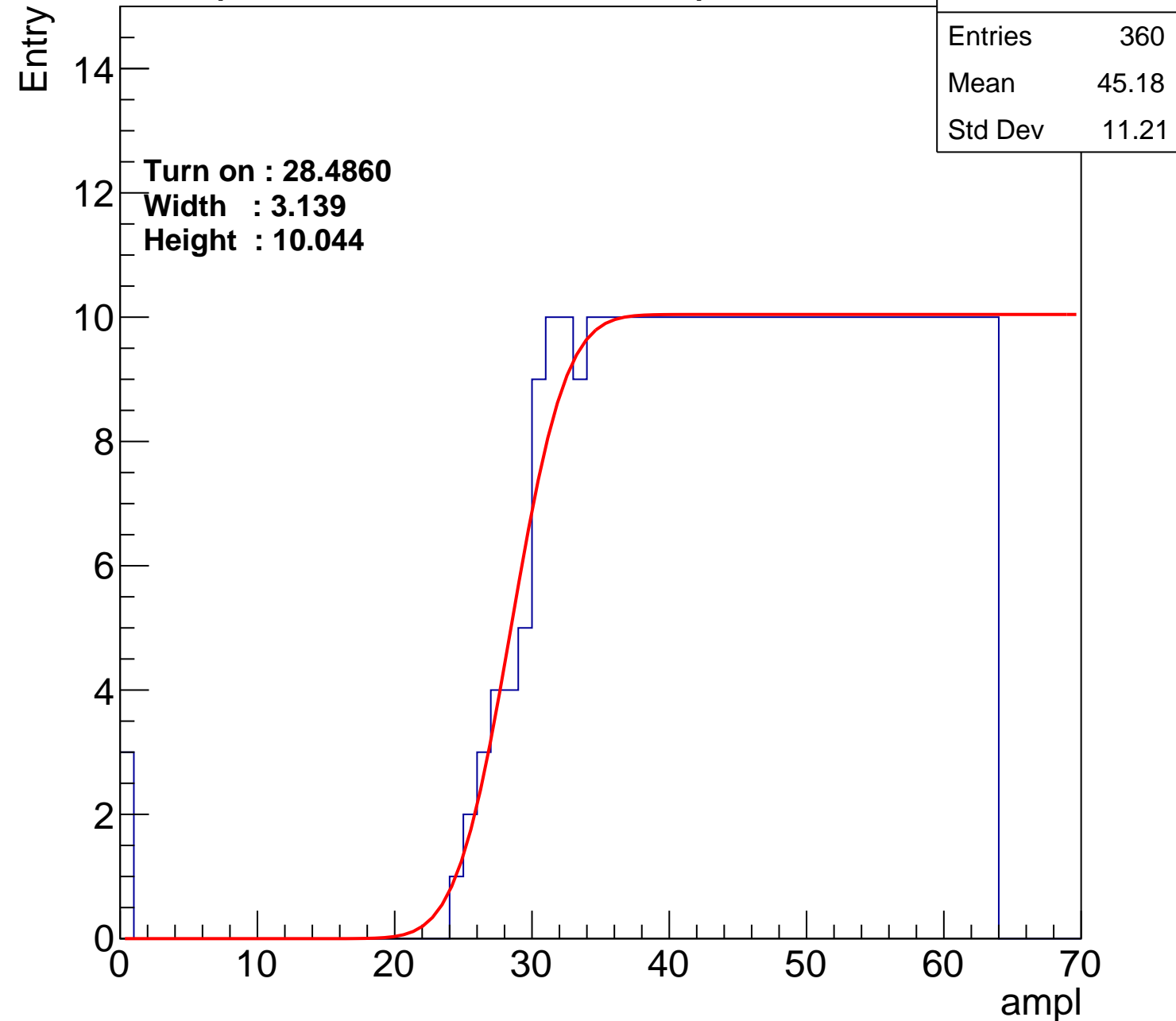
Width : 3.139

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch56

calib_packv5_042523_0143.root, FC#7, port C2

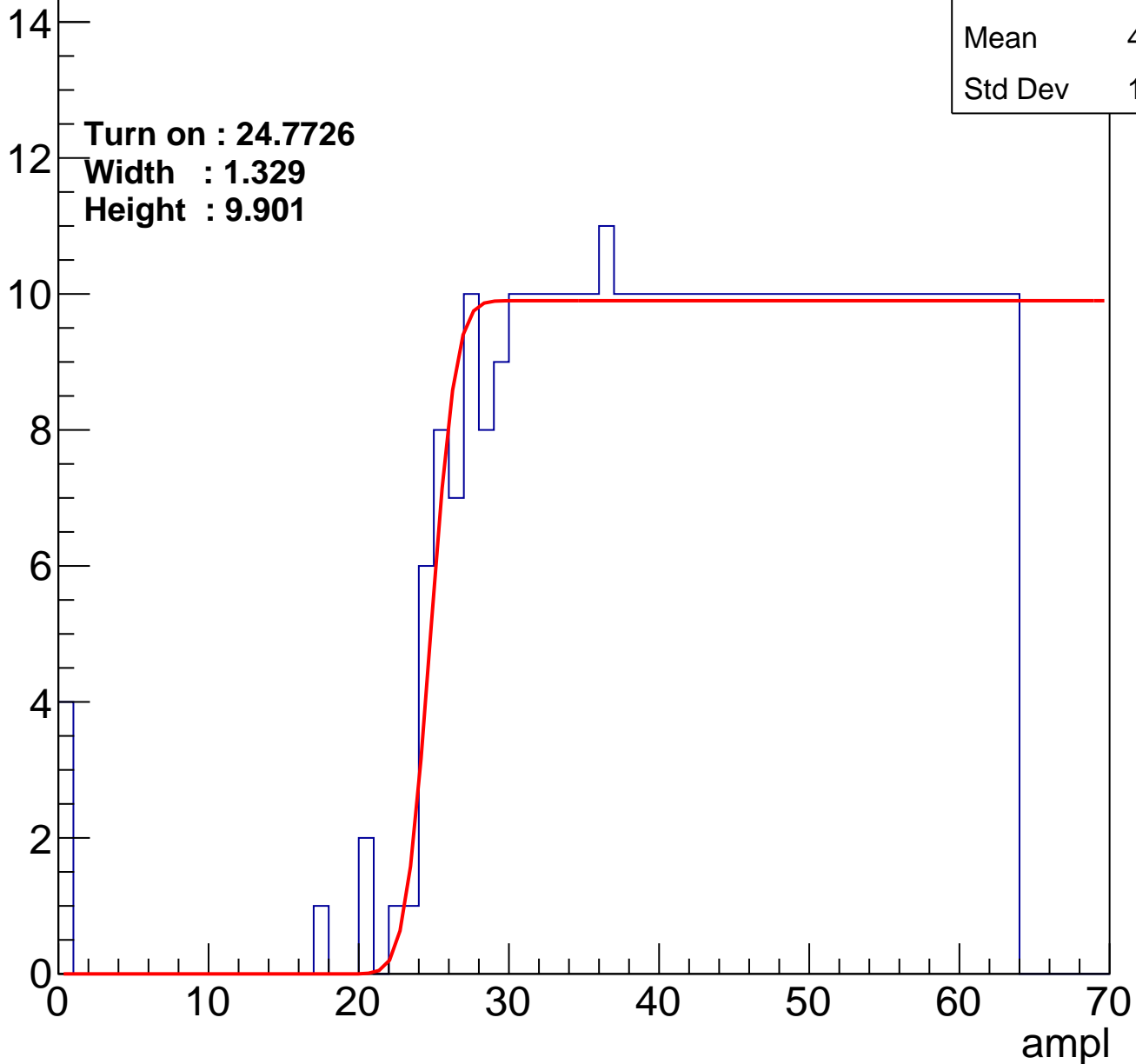
Entries	398
Mean	43.29
Std Dev	12.26

Turn on : 24.7726

Width : 1.329

Height : 9.901

Entry



B1L103S, U13-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.62
Std Dev	11.9

Turn on : 25.4019

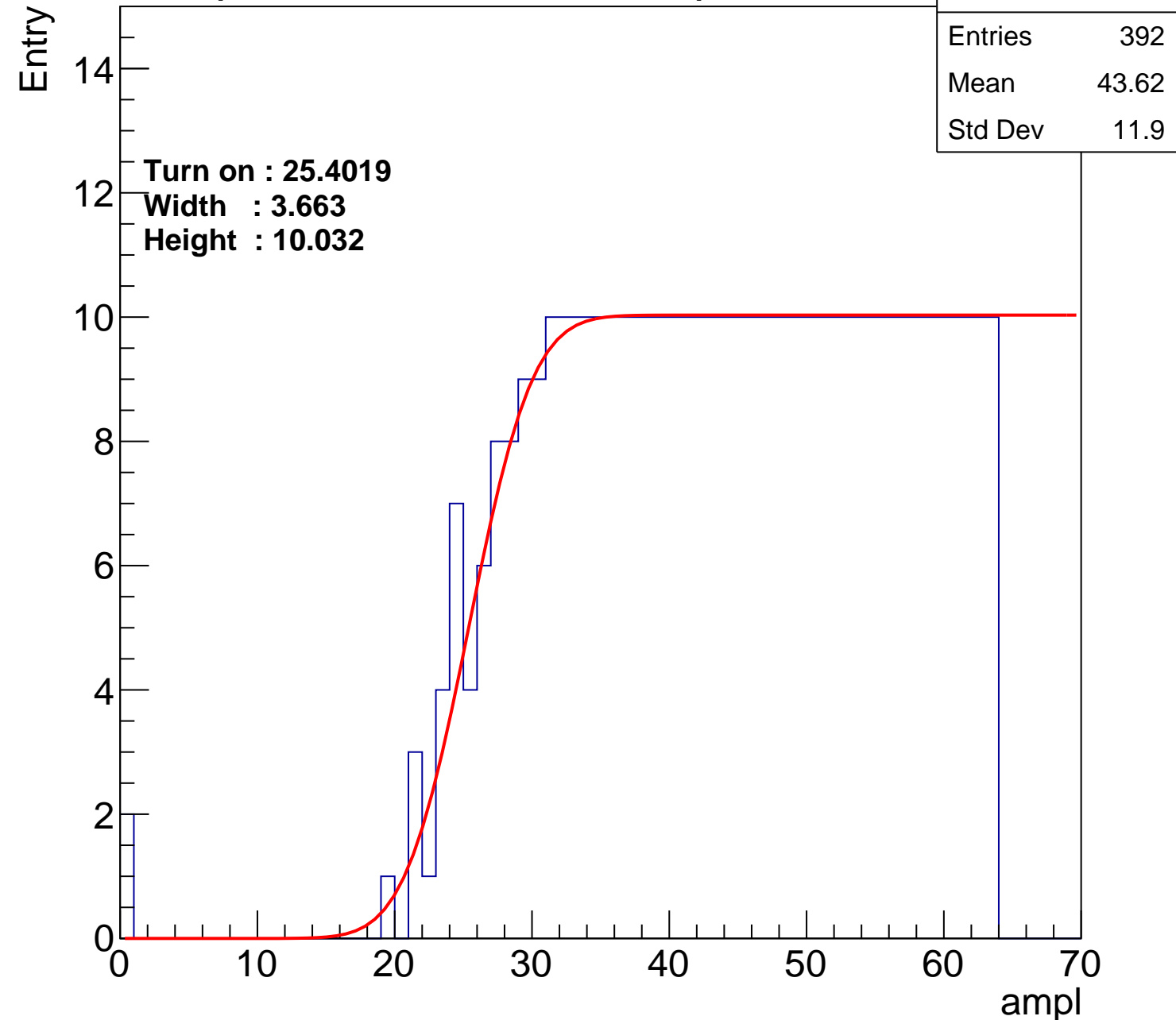
Width : 3.663

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch58

calib_packv5_042523_0143.root, FC#7, port C2

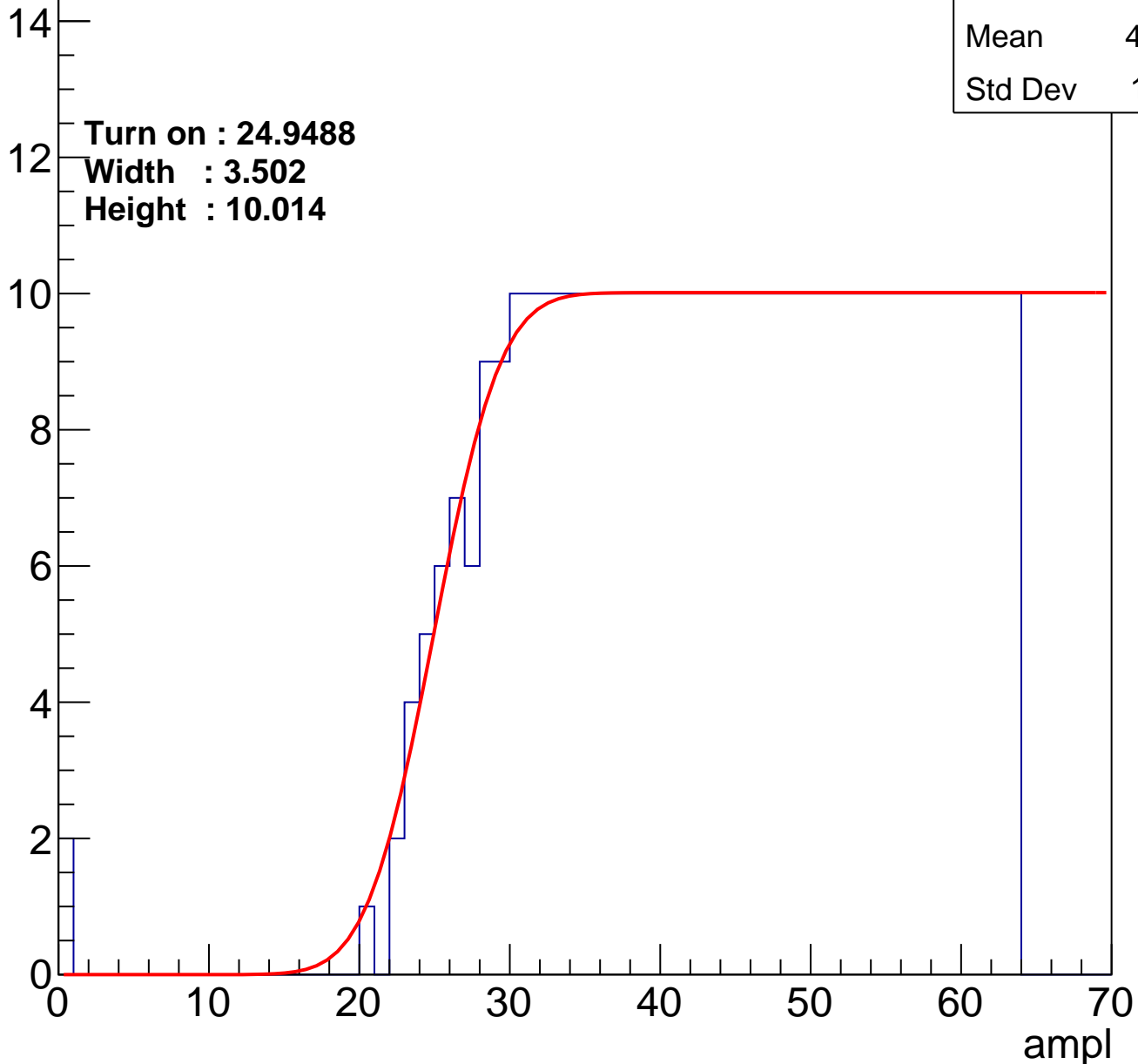
Entries	391
Mean	43.72
Std Dev	11.81

Turn on : 24.9488

Width : 3.502

Height : 10.014

Entry



B1L103S, U13-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	356
Mean	45.44
Std Dev	10.9

Turn on : 28.0855

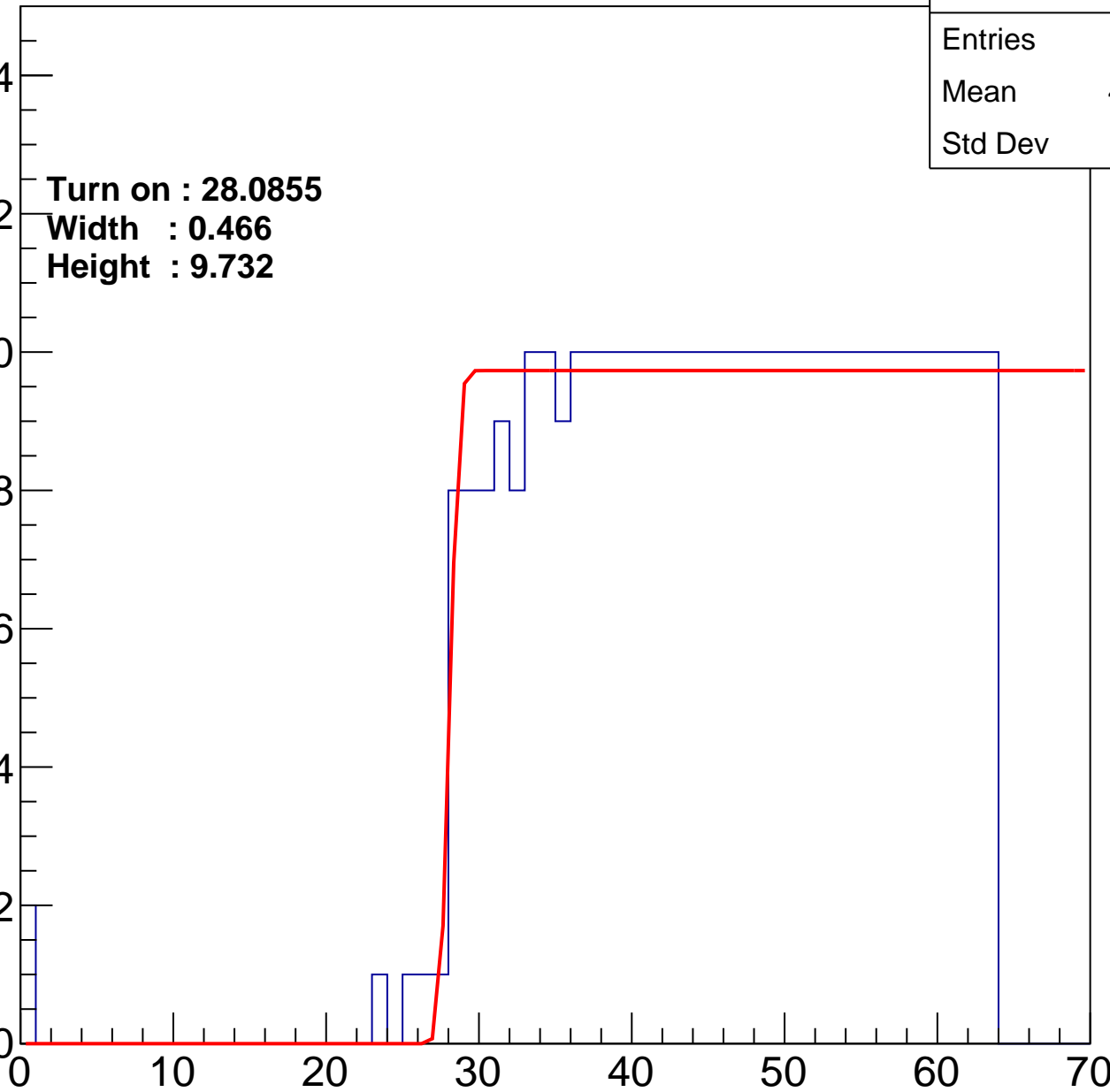
Width : 0.466

Height : 9.732

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch60

calib_packv5_042523_0143.root, FC#7, port C2

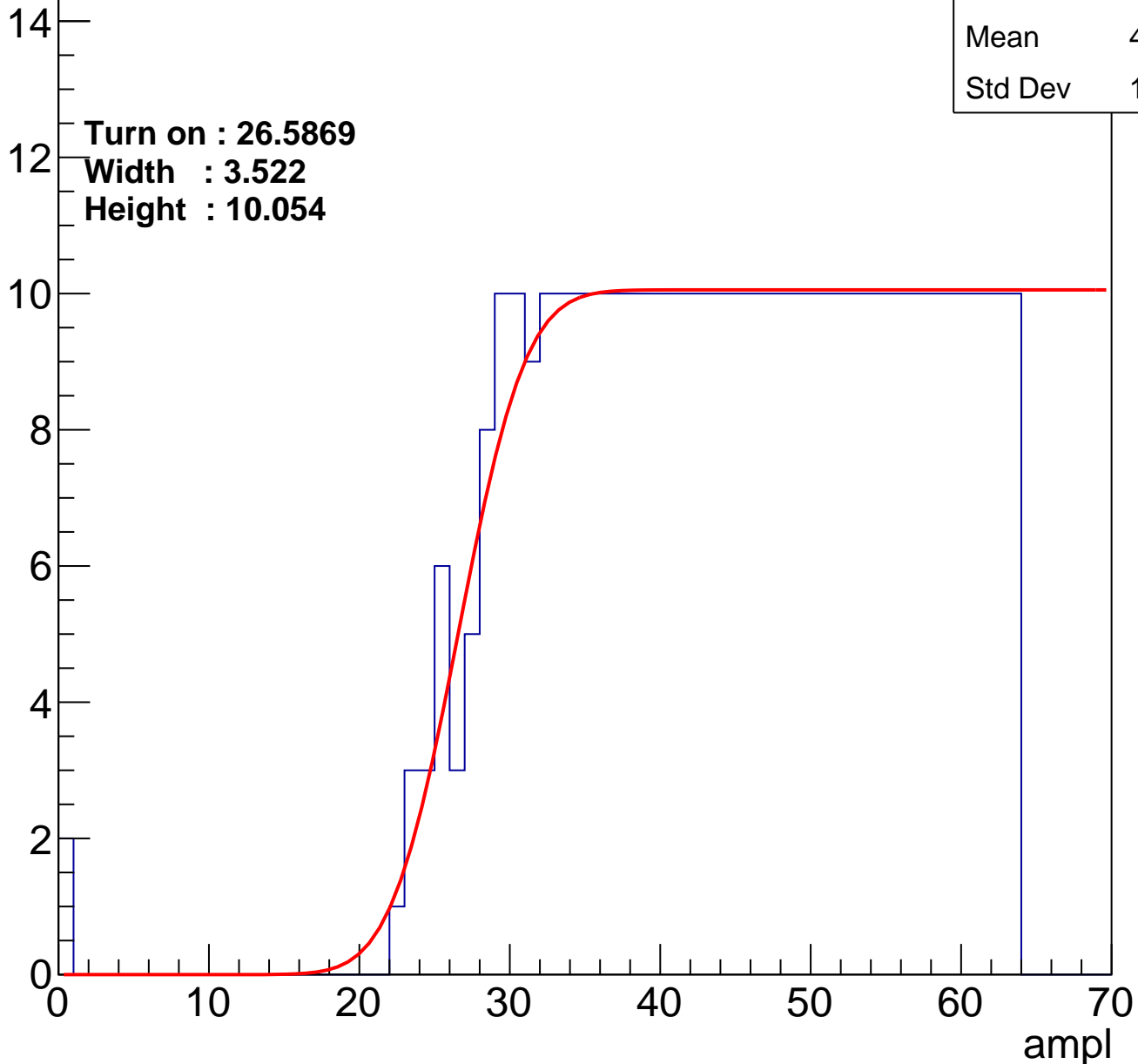
Entries	380
Mean	44.26
Std Dev	11.52

Turn on : 26.5869

Width : 3.522

Height : 10.054

Entry



B1L103S, U13-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	44.98
Std Dev	11.22

Turn on : 28.2867

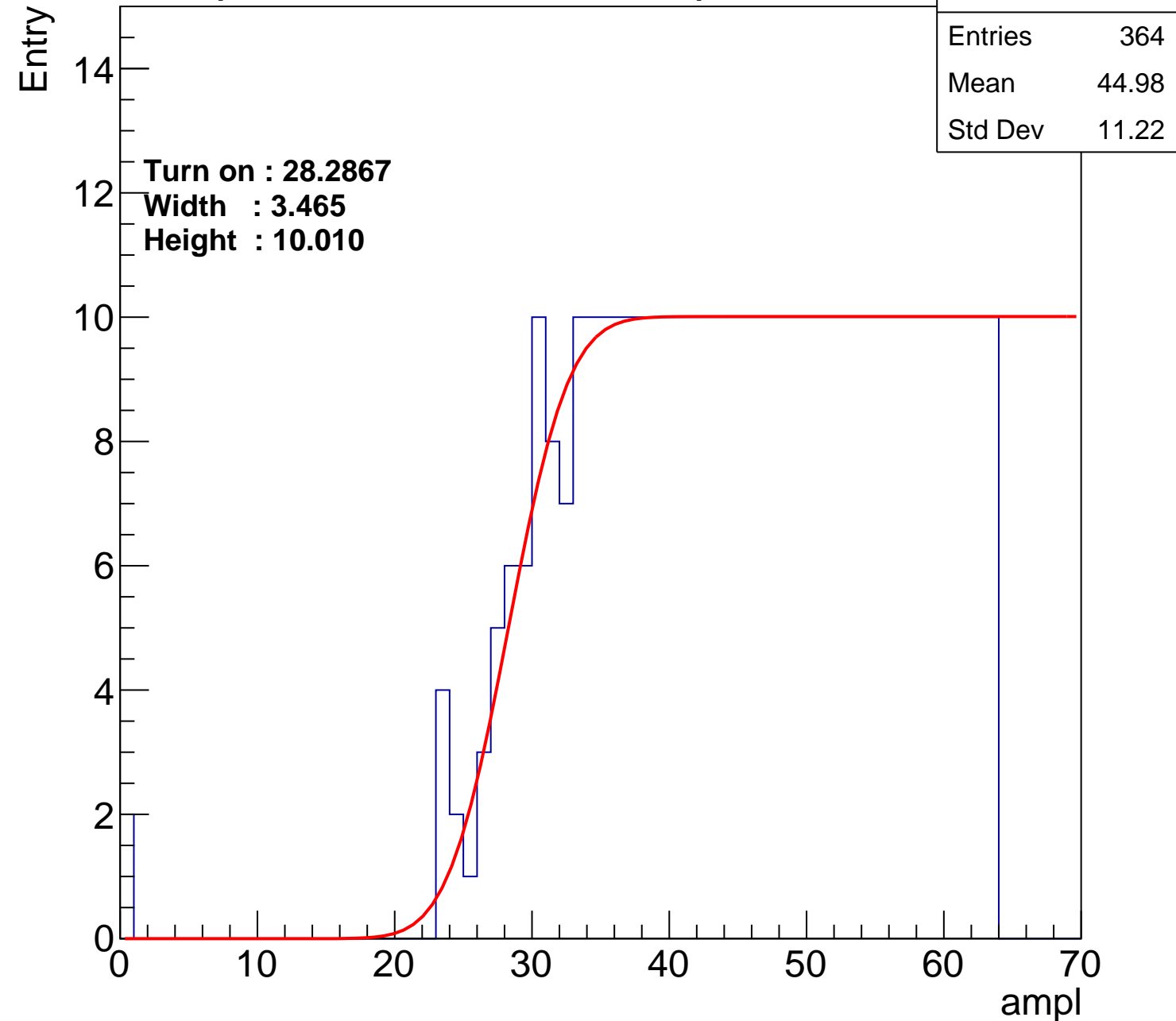
Width : 3.465

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch62

calib_packv5_042523_0143.root, FC#7, port C2

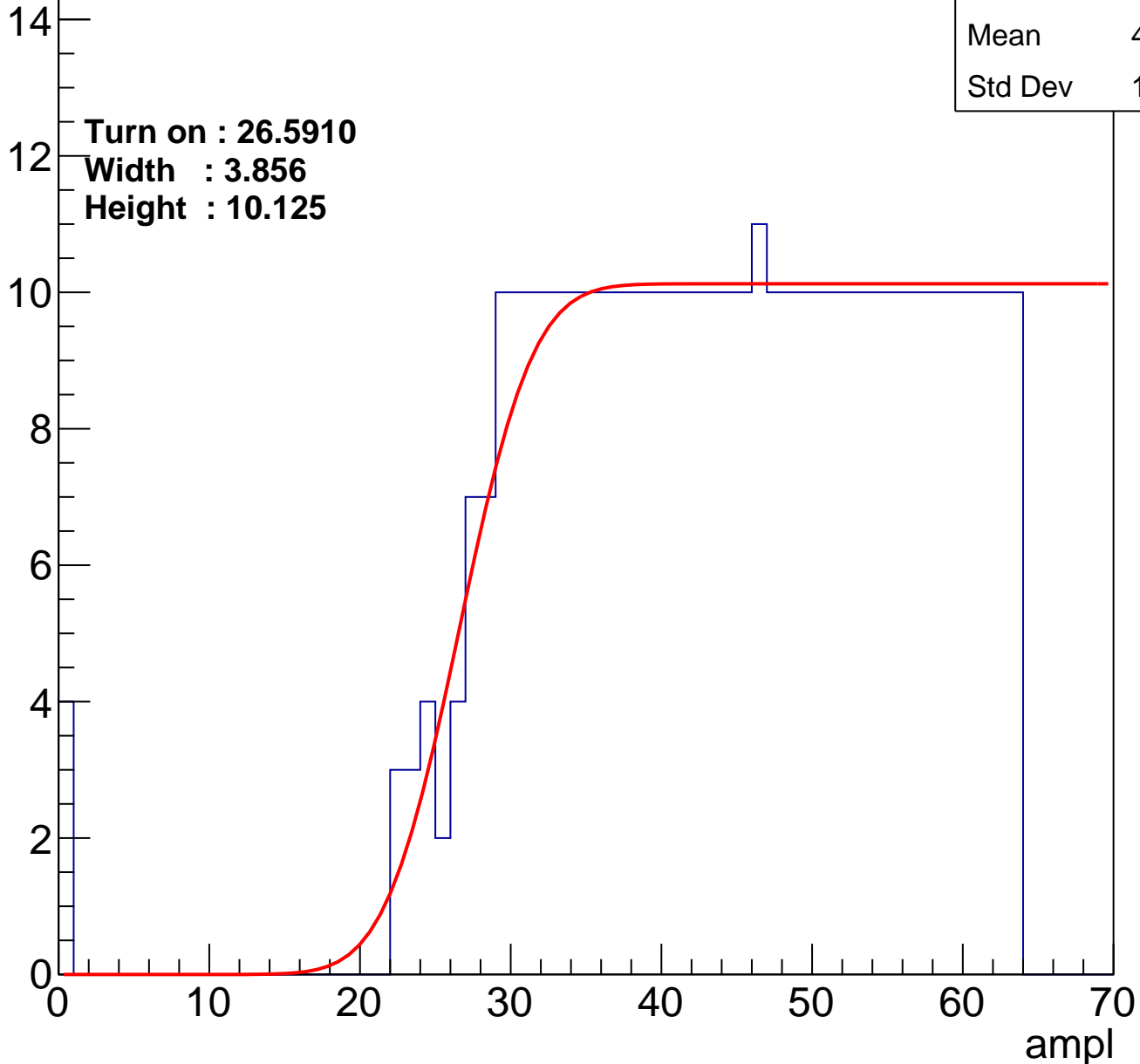
Entries	385
Mean	43.94
Std Dev	11.96

Turn on : 26.5910

Width : 3.856

Height : 10.125

Entry



B1L103S, U13-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.21
Std Dev	11.01

Turn on : 28.1654

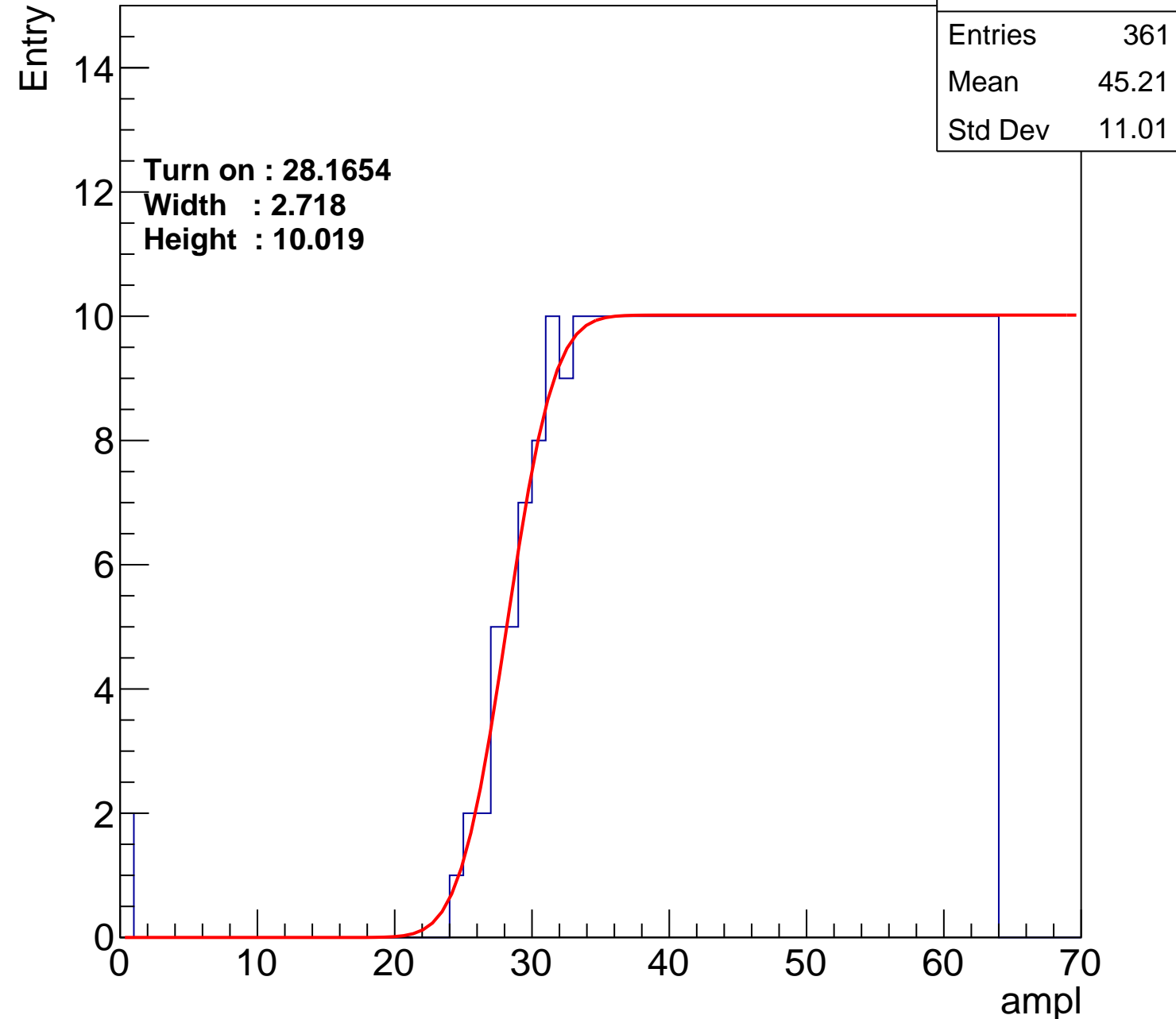
Width : 2.718

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.88
Std Dev	11.23

Turn on : 28.1695

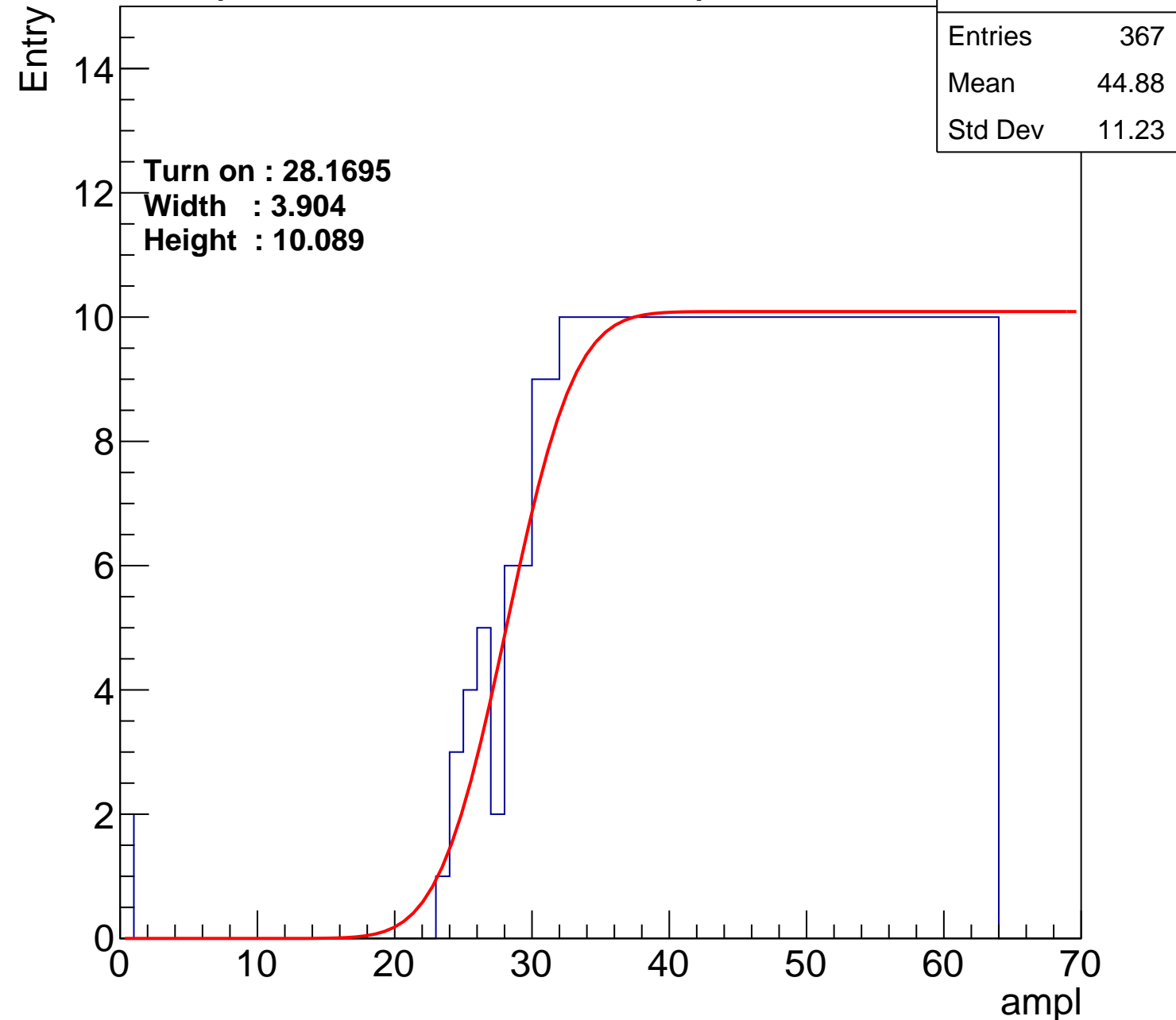
Width : 3.904

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.98
Std Dev	10.92

Turn on : 27.2644

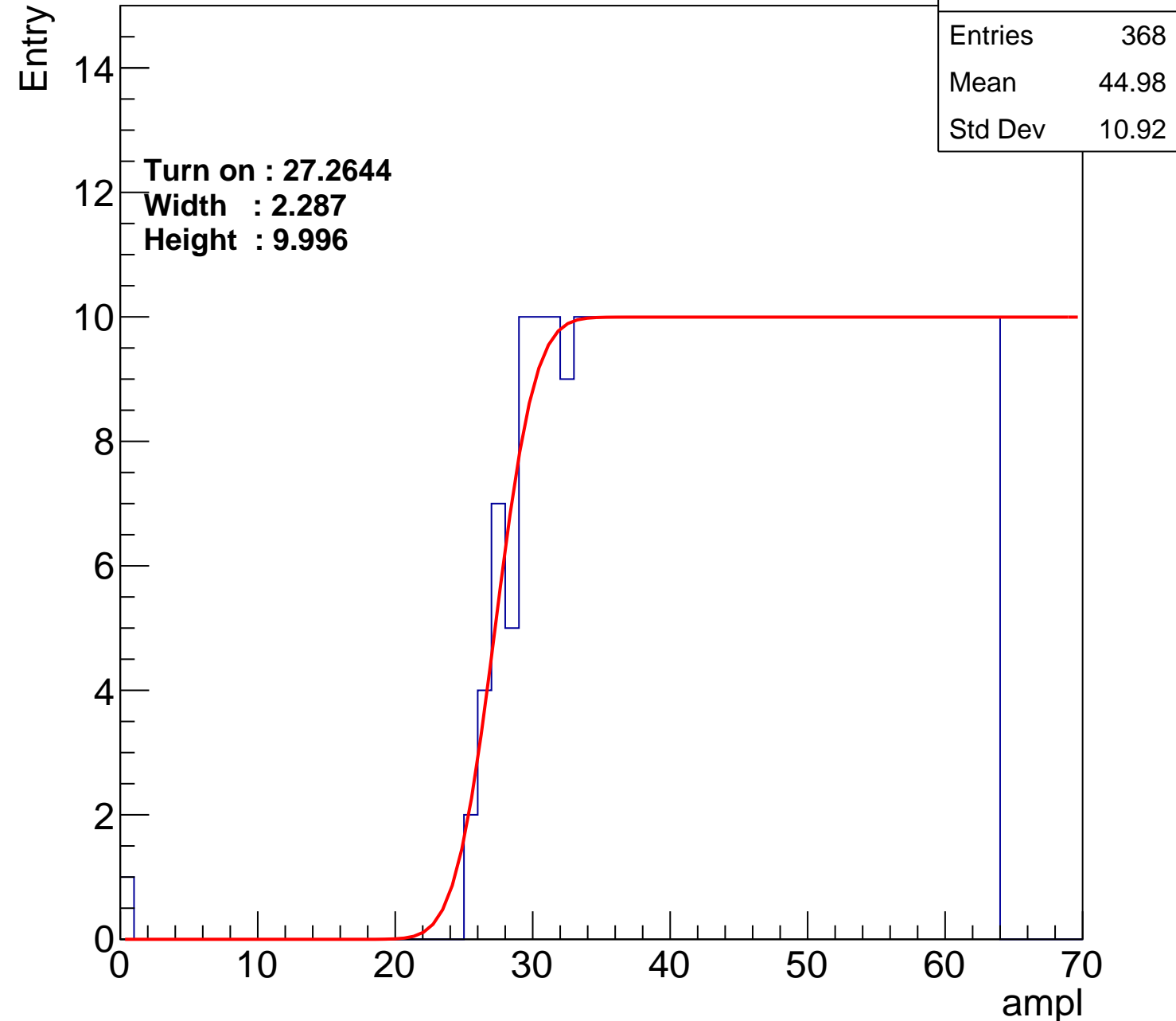
Width : 2.287

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch66

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.02
Std Dev	11.65

Turn on : 29.0601

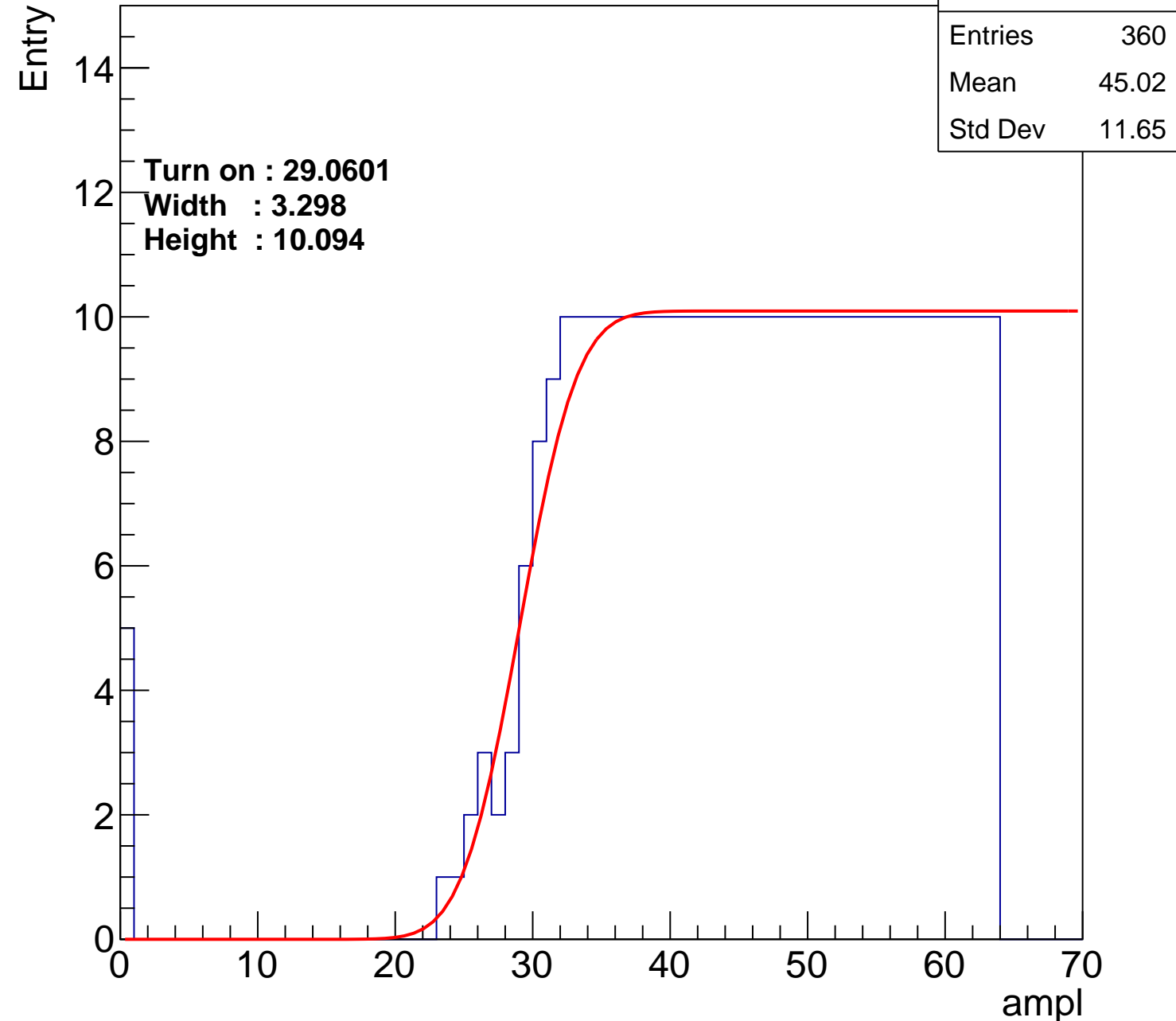
Width : 3.298

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.25
Std Dev	11.49

Turn on : 29.1407

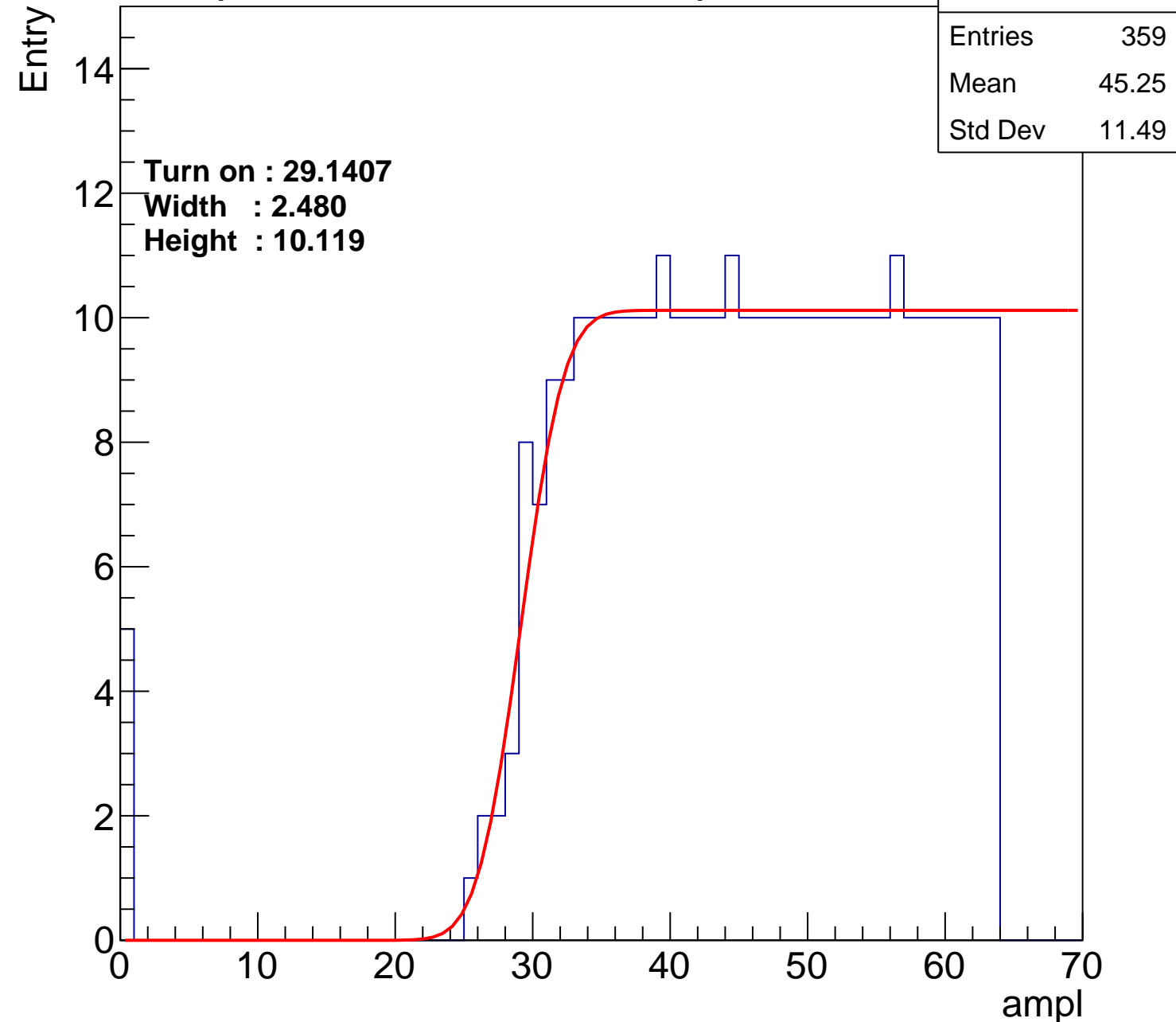
Width : 2.480

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.94
Std Dev	11.32

Turn on : 28.2301

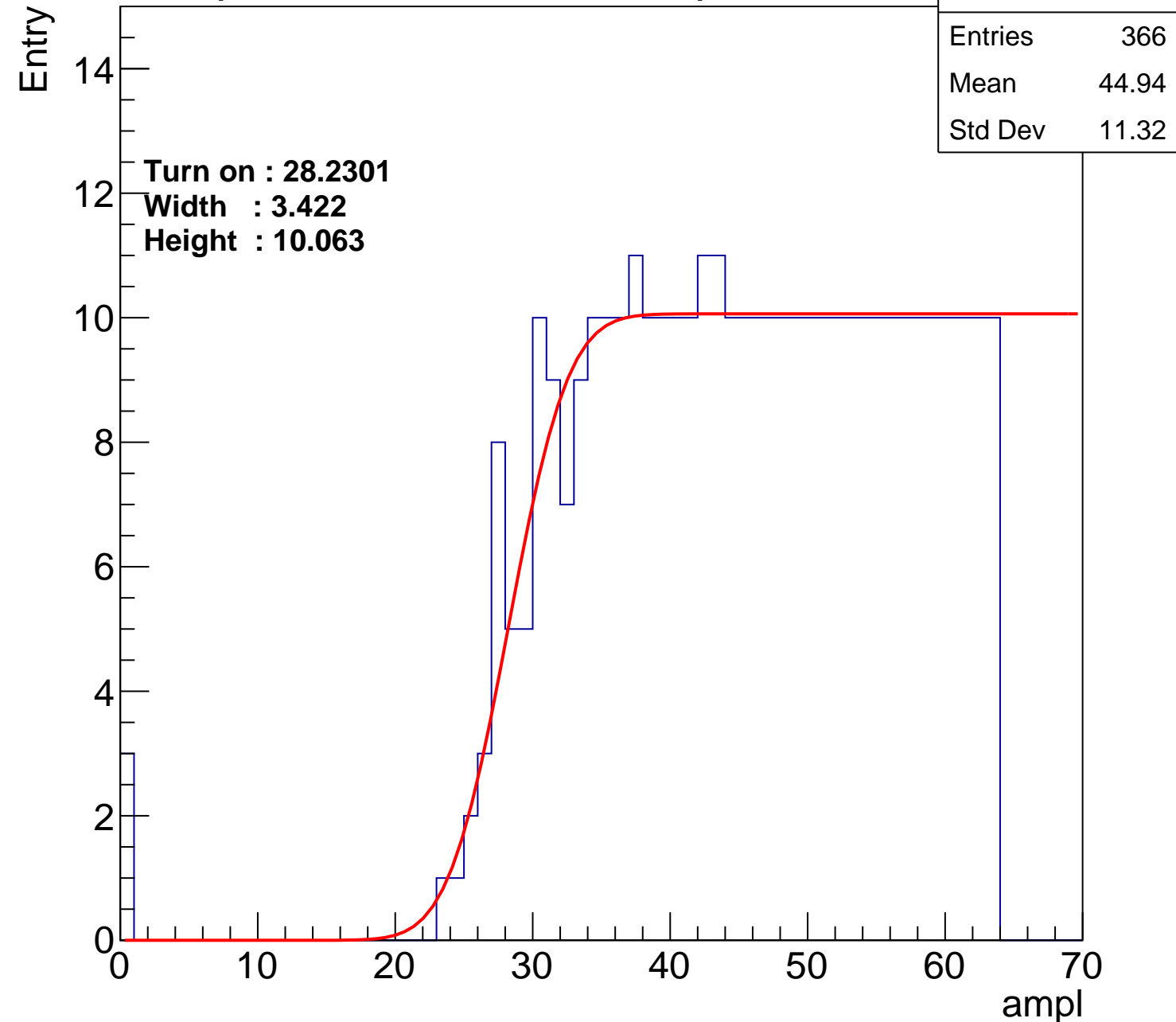
Width : 3.422

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.71
Std Dev	11.51

Turn on : 27.5176

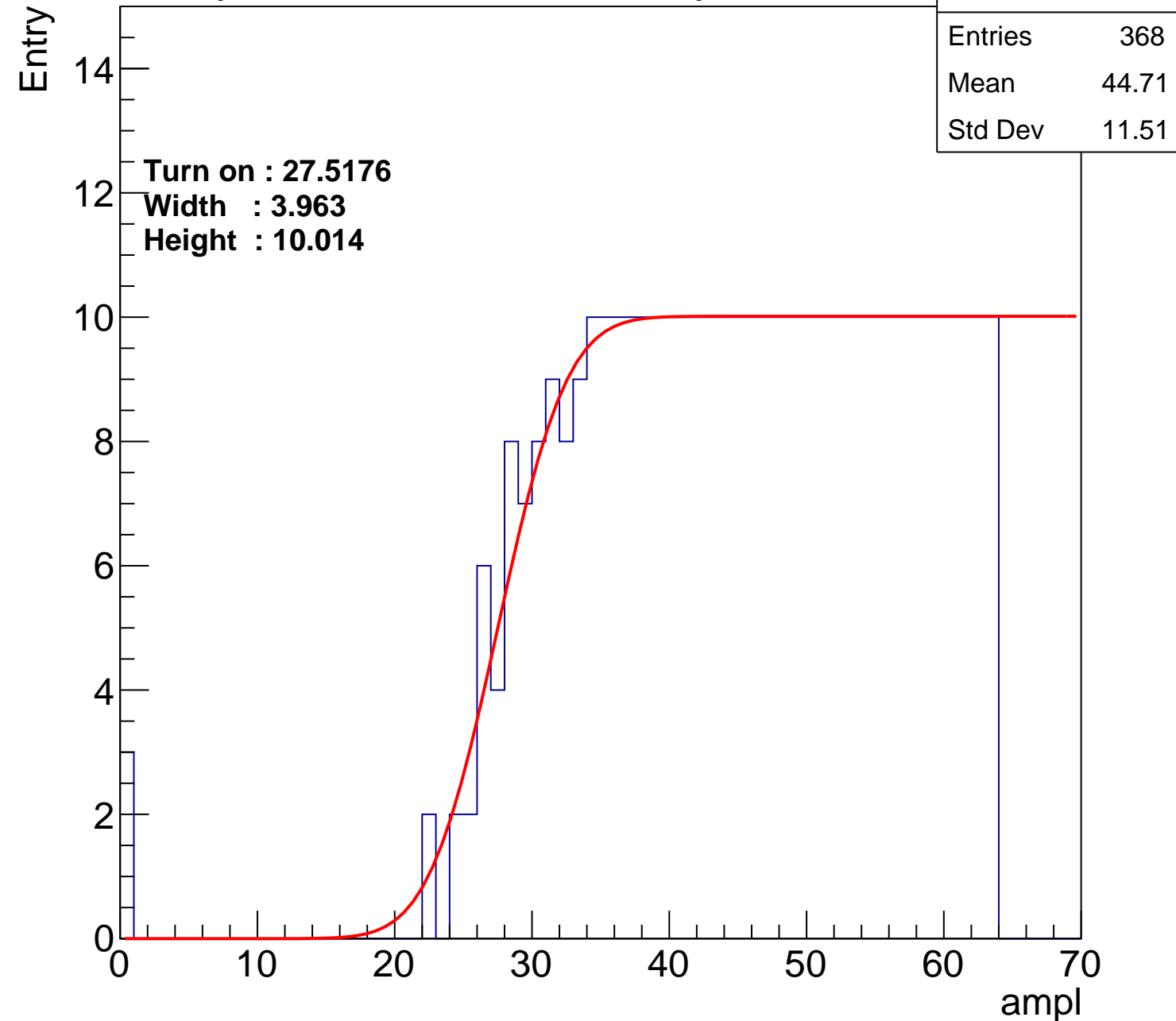
Width : 3.963

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch70

calib_packv5_042523_0143.root, FC#7, port C2

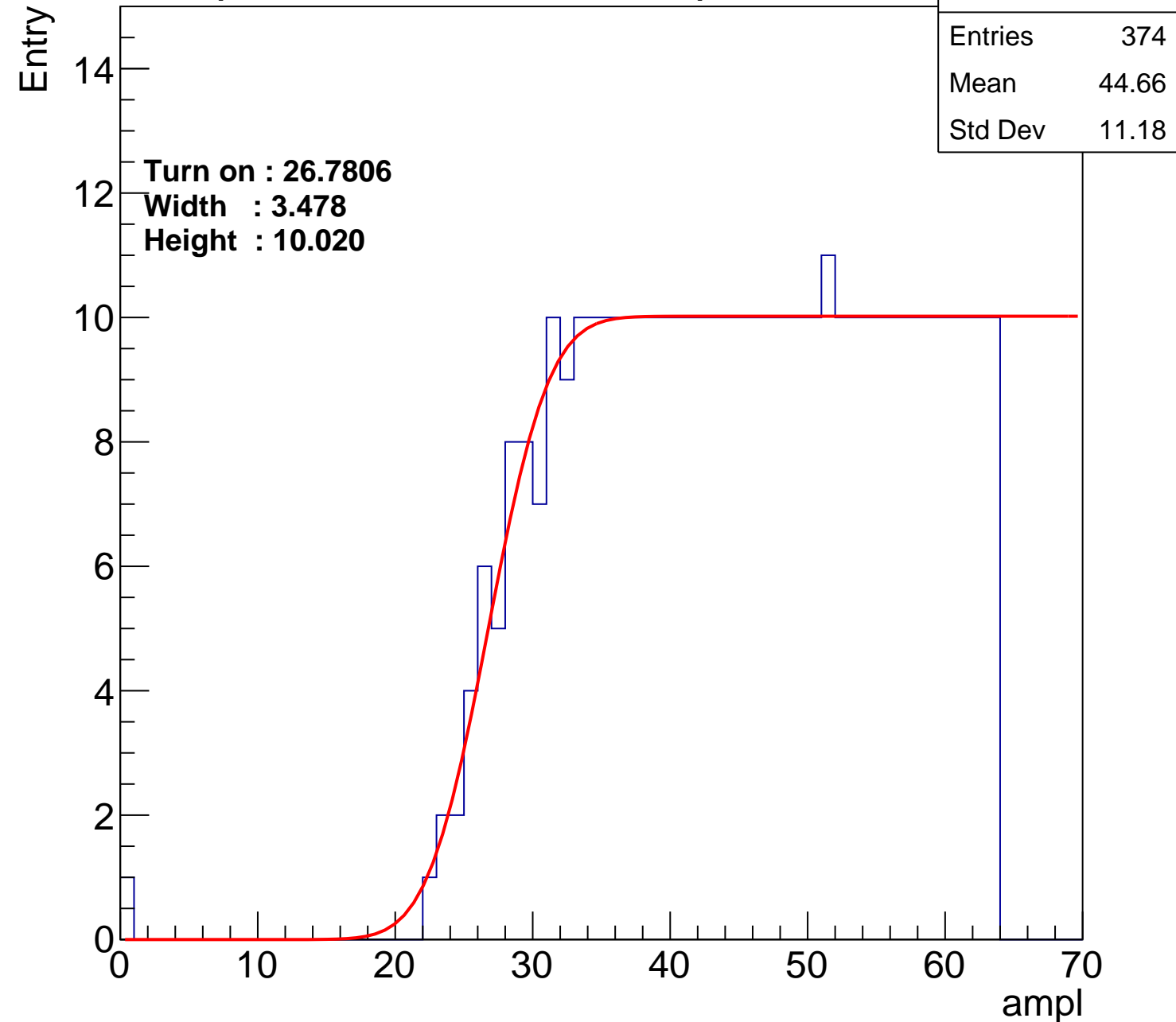
Entry

14
12
10
8
6
4
2
0

Turn on : 26.7806
Width : 3.478
Height : 10.020

Entries	374
Mean	44.66
Std Dev	11.18

ampl



B1L103S, U13-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	358
Mean	45.34
Std Dev	10.97

Turn on : 27.8968

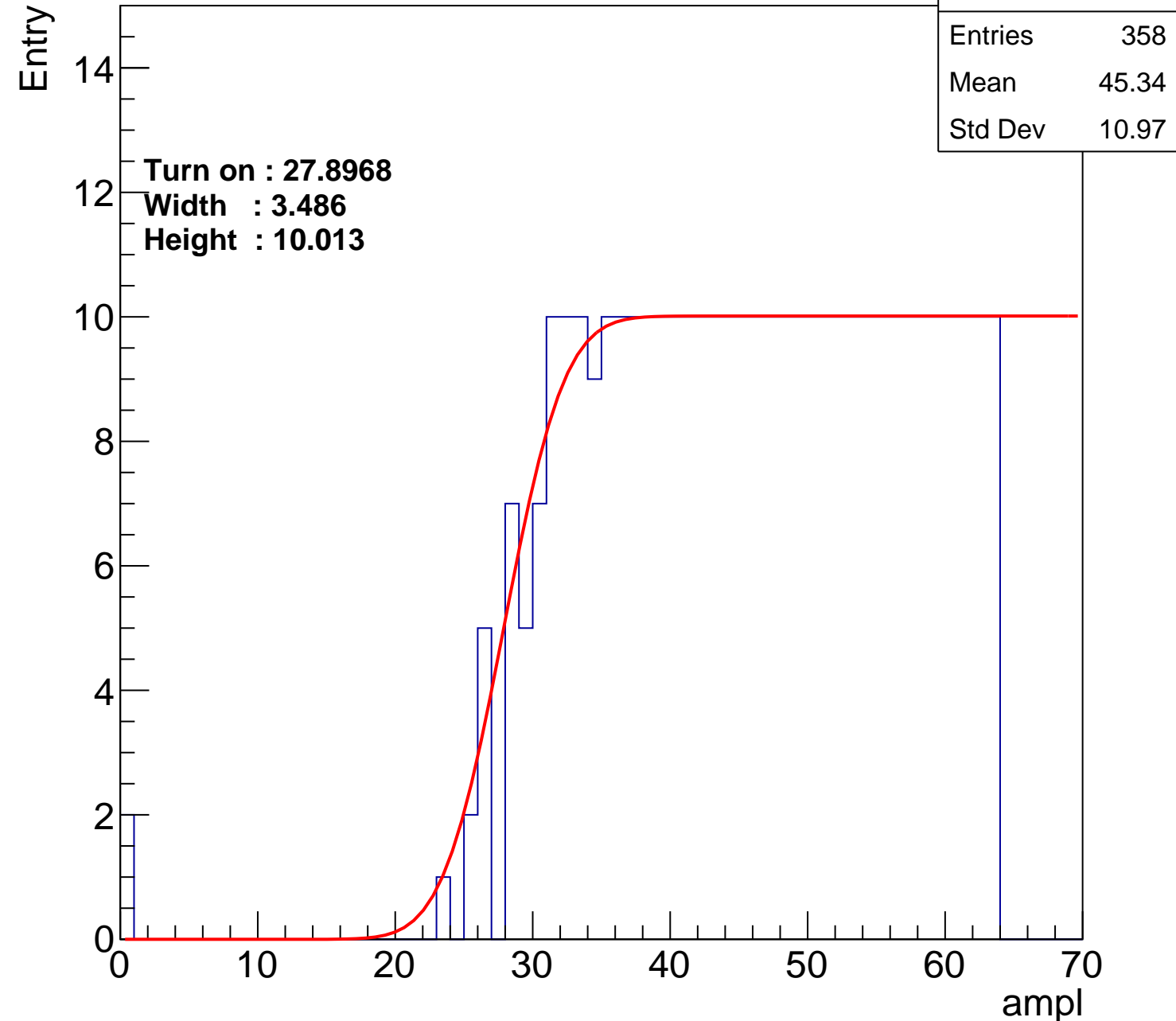
Width : 3.486

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Turn on : 28.2830
Width : 3.251
Height : 10.039



B1L103S, U13-ch73

calib_packv5_042523_0143.root, FC#7, port C2

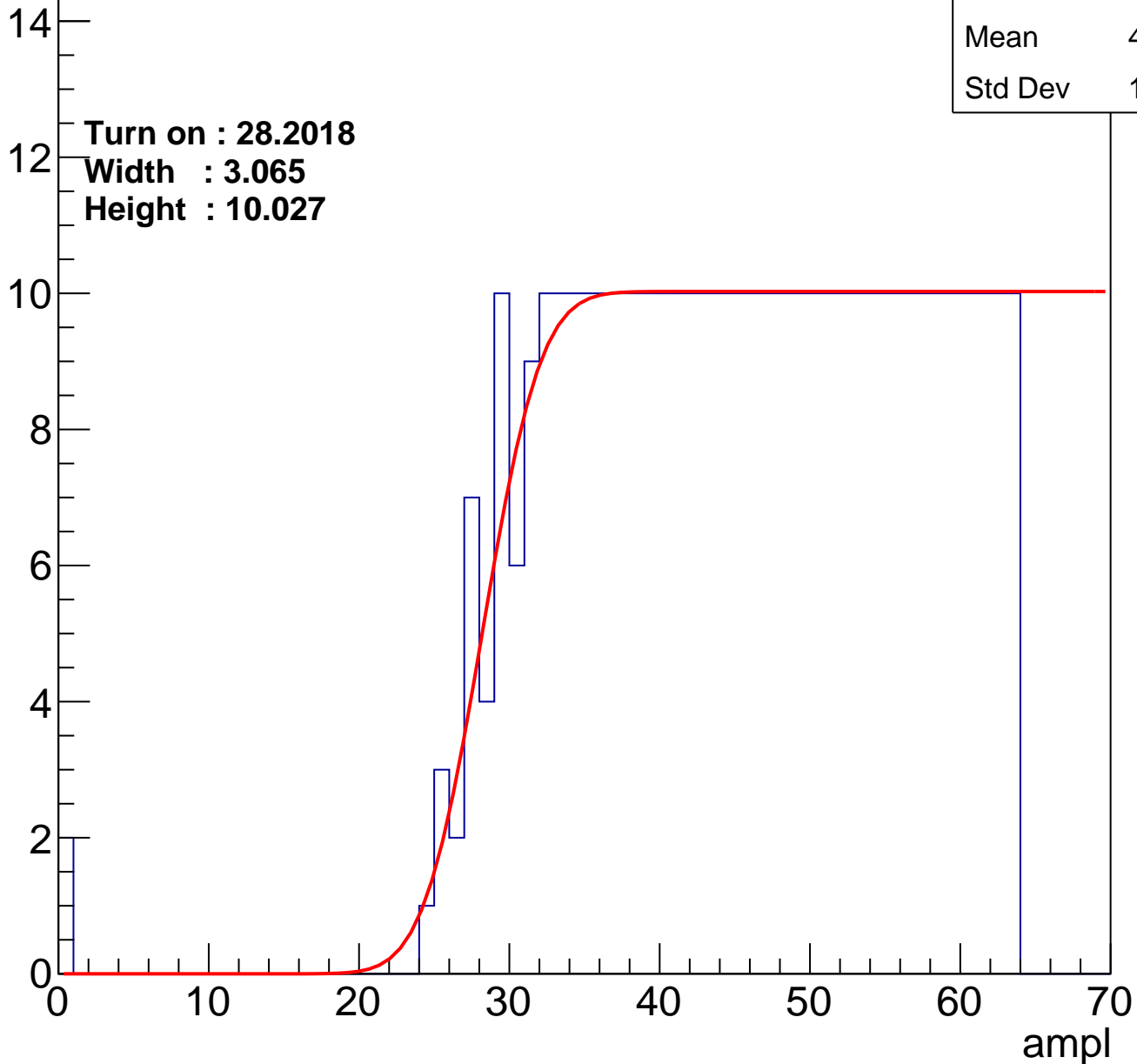
Entries	364
Mean	45.06
Std Dev	11.09

Turn on : 28.2018

Width : 3.065

Height : 10.027

Entry



B1L103S, U13-ch74

calib_packv5_042523_0143.root, FC#7, port C2

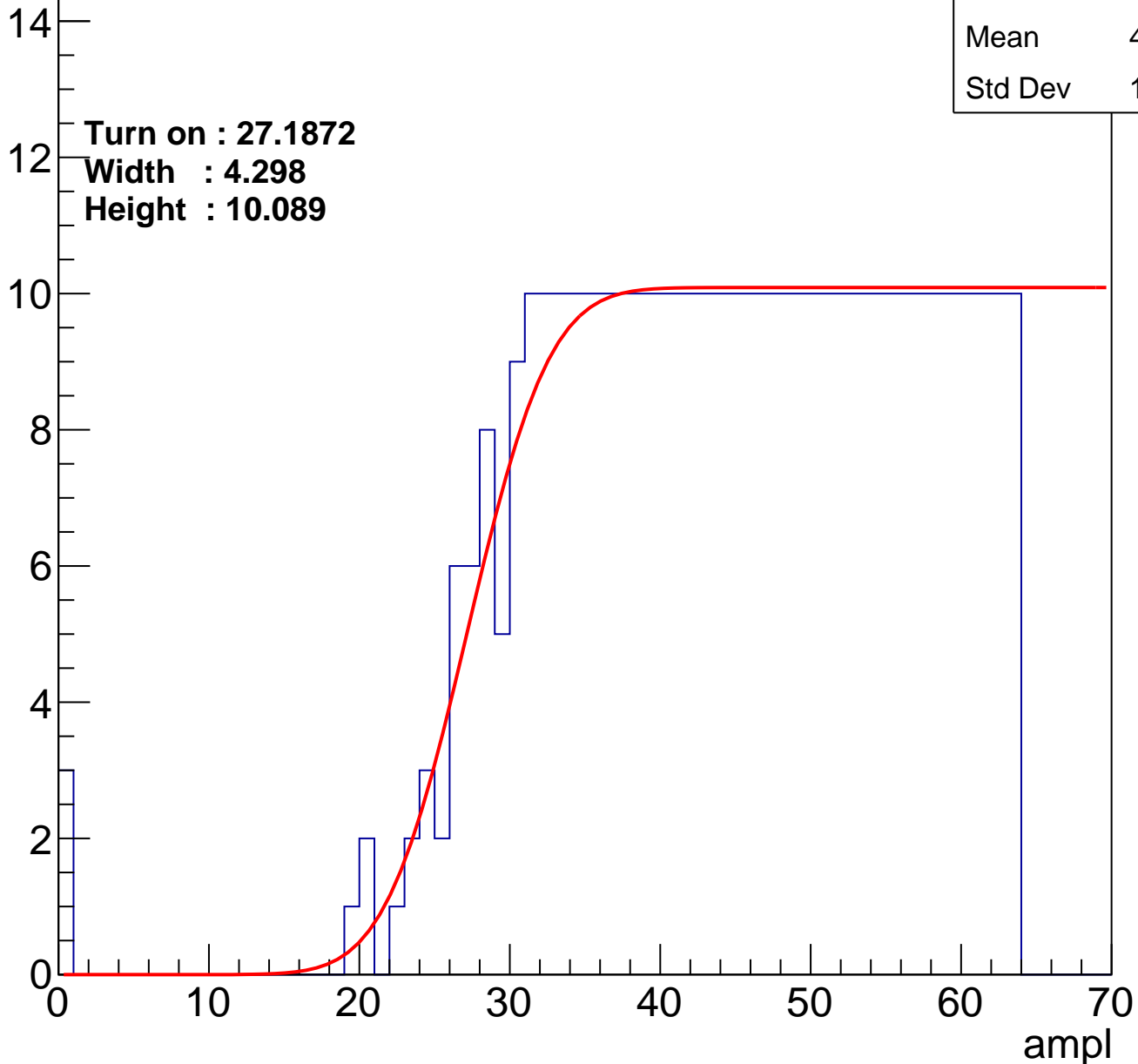
Entries	378
Mean	44.22
Std Dev	11.77

Turn on : 27.1872

Width : 4.298

Height : 10.089

Entry



B1L103S, U13-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.25
Std Dev	11

Turn on : 28.4403

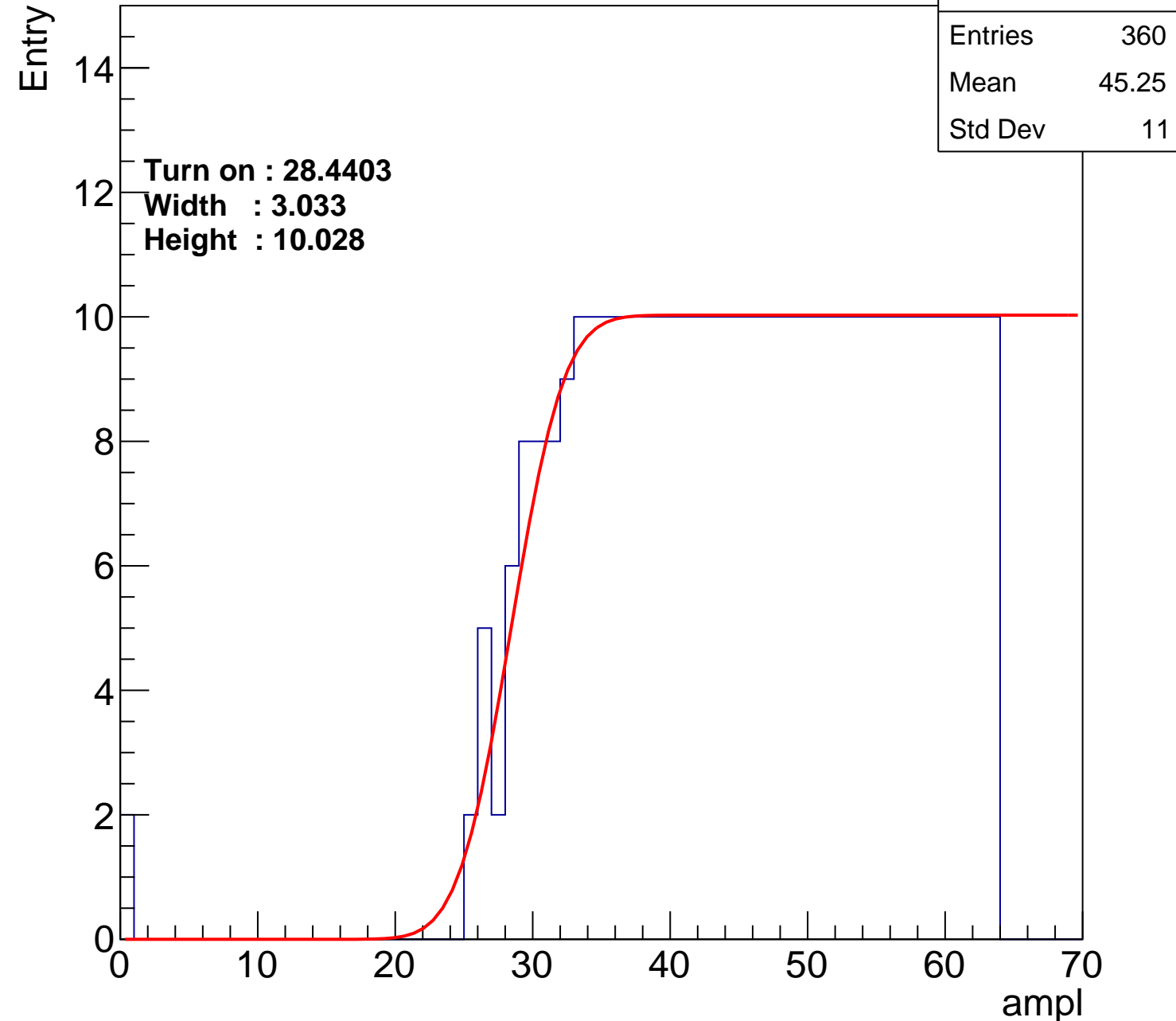
Width : 3.033

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.72
Std Dev	11.14

Turn on : 27.4330

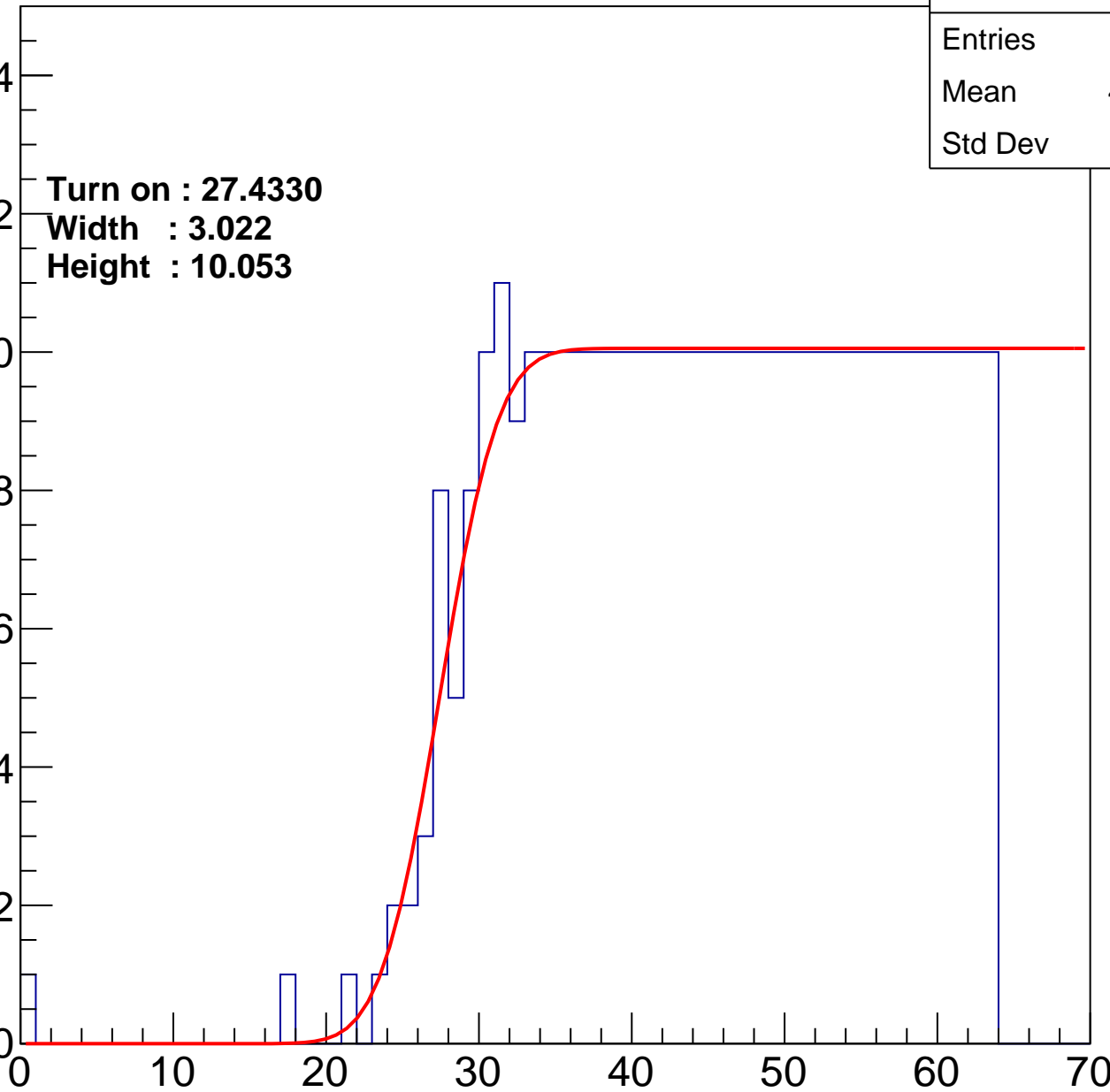
Width : 3.022

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.3178

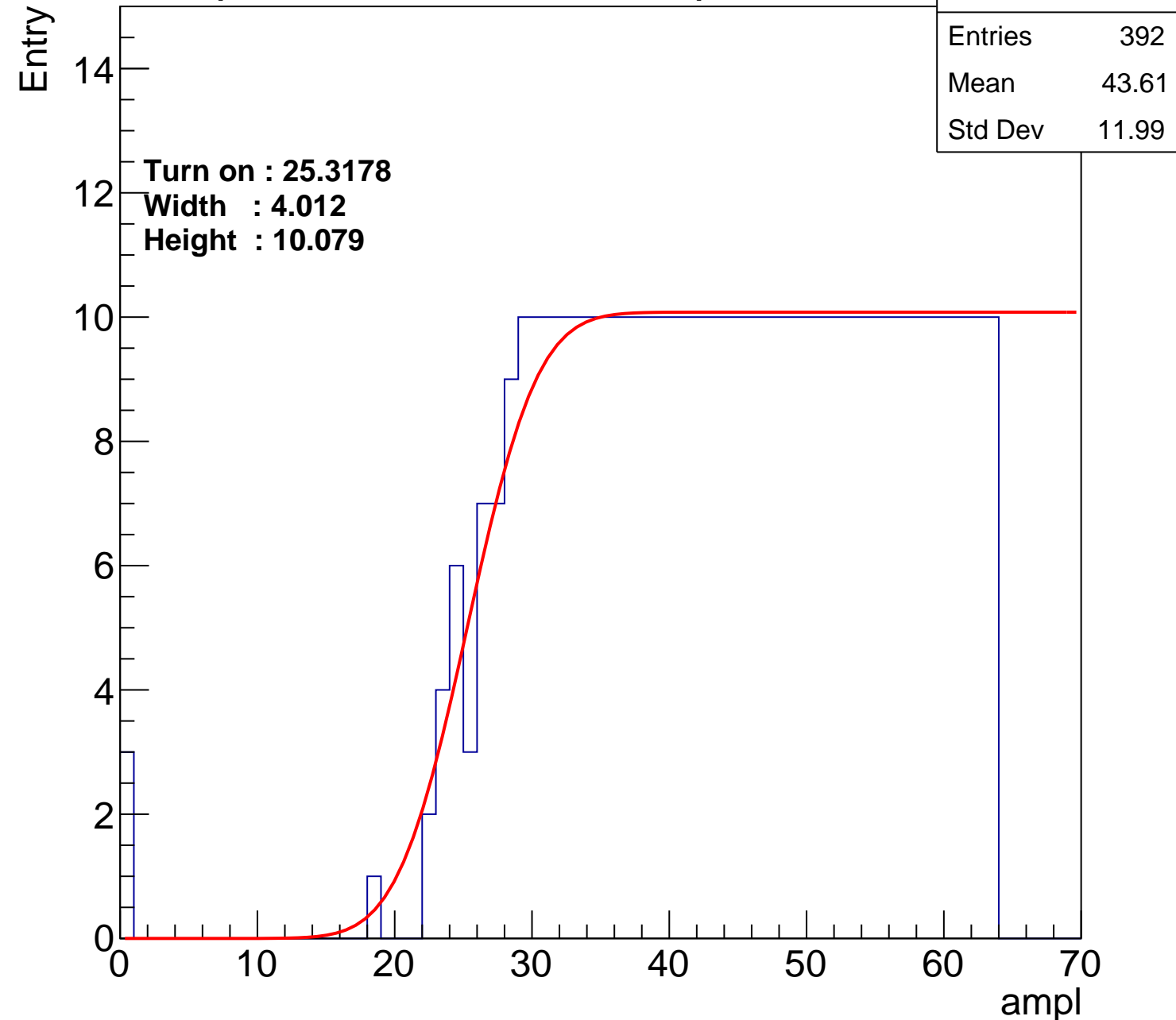
Width : 4.012

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.47
Std Dev	11.63

Turn on : 26.9958

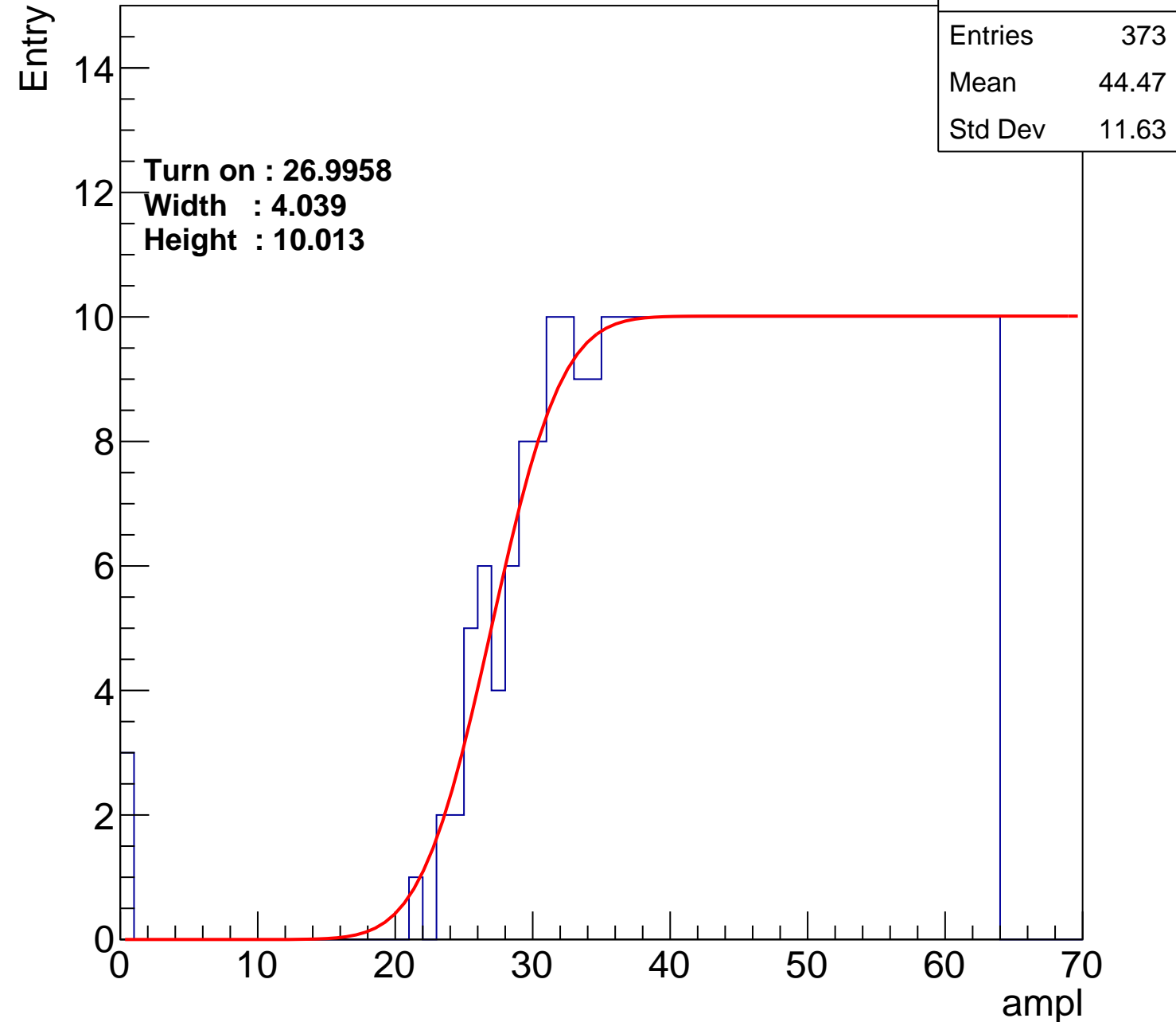
Width : 4.039

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch79

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.88
Std Dev	11.28

Turn on : 28.3911

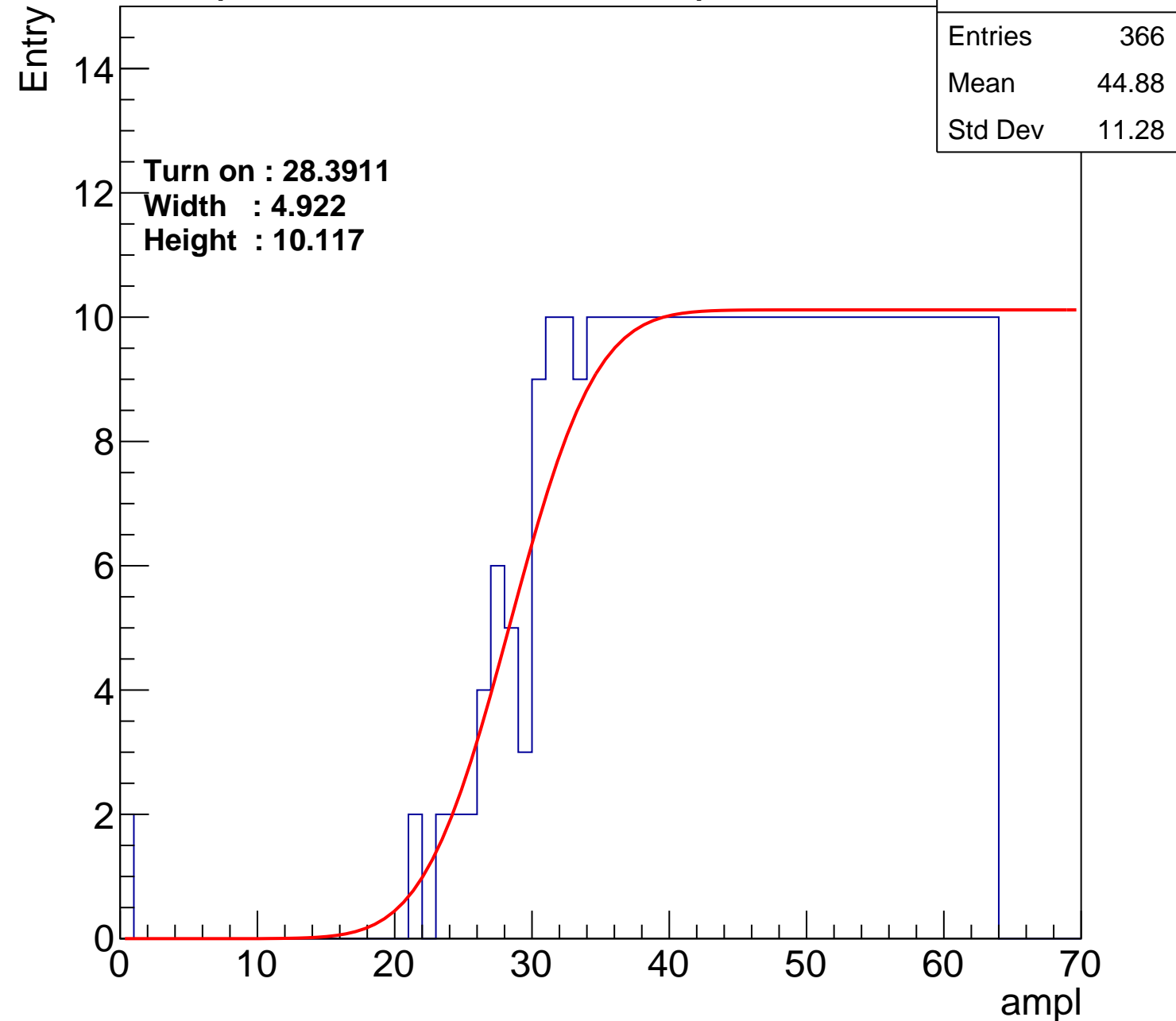
Width : 4.922

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.95
Std Dev	11.22

Turn on : 28.3252

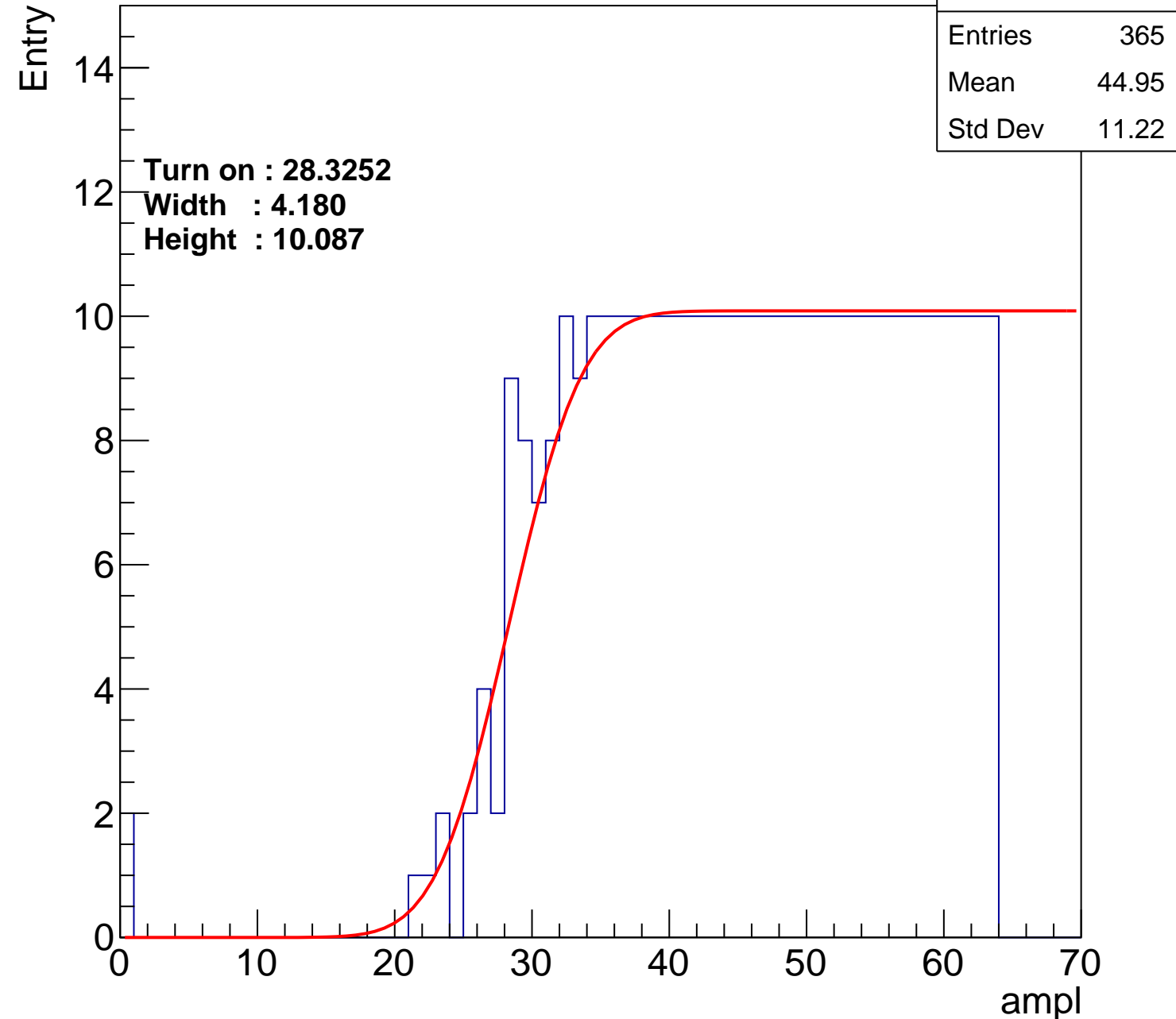
Width : 4.180

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch81

calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.67
Std Dev	10.82

Turn on : 29.5047

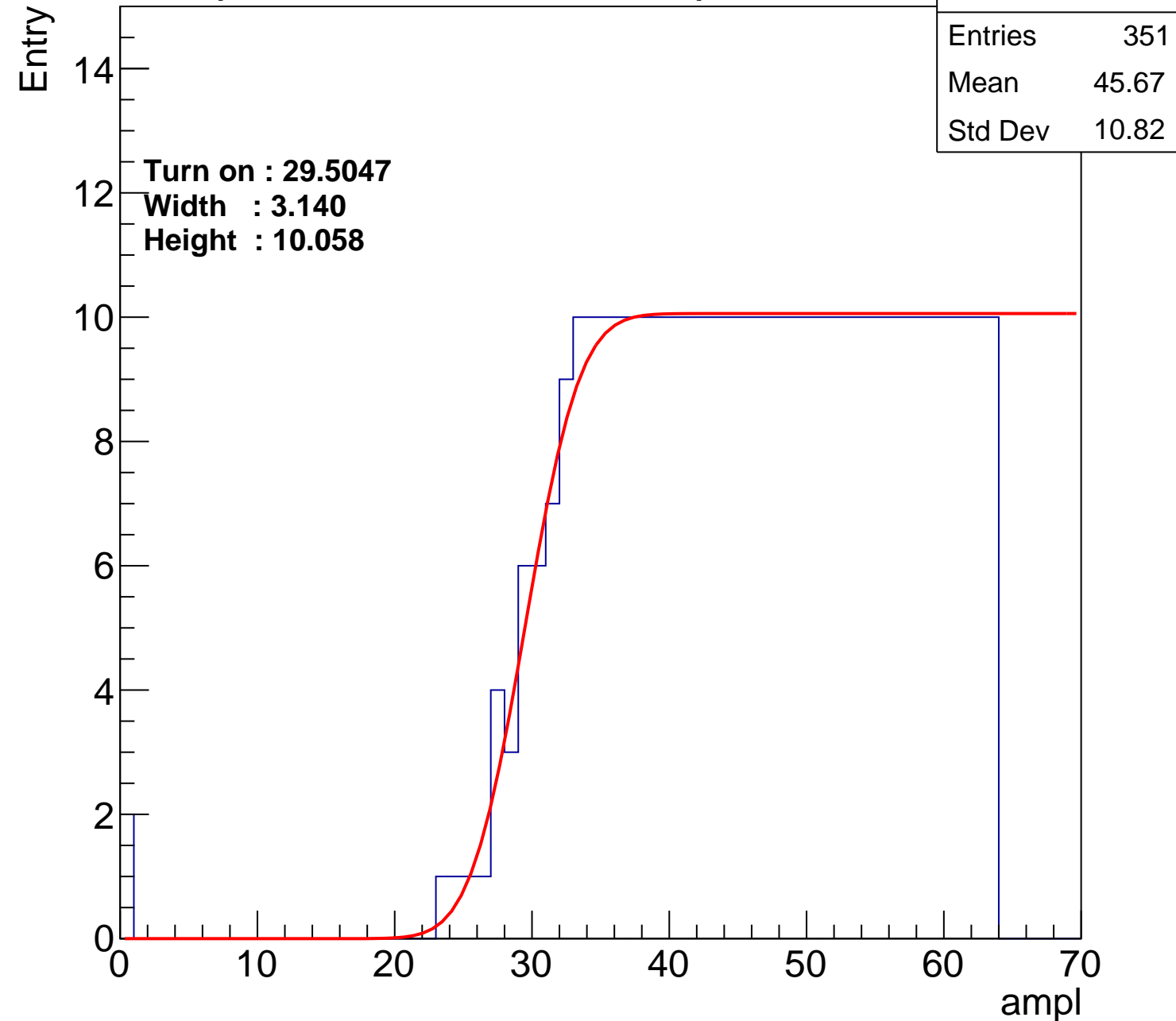
Width : 3.140

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.53
Std Dev	11.41

Turn on : 26.7837

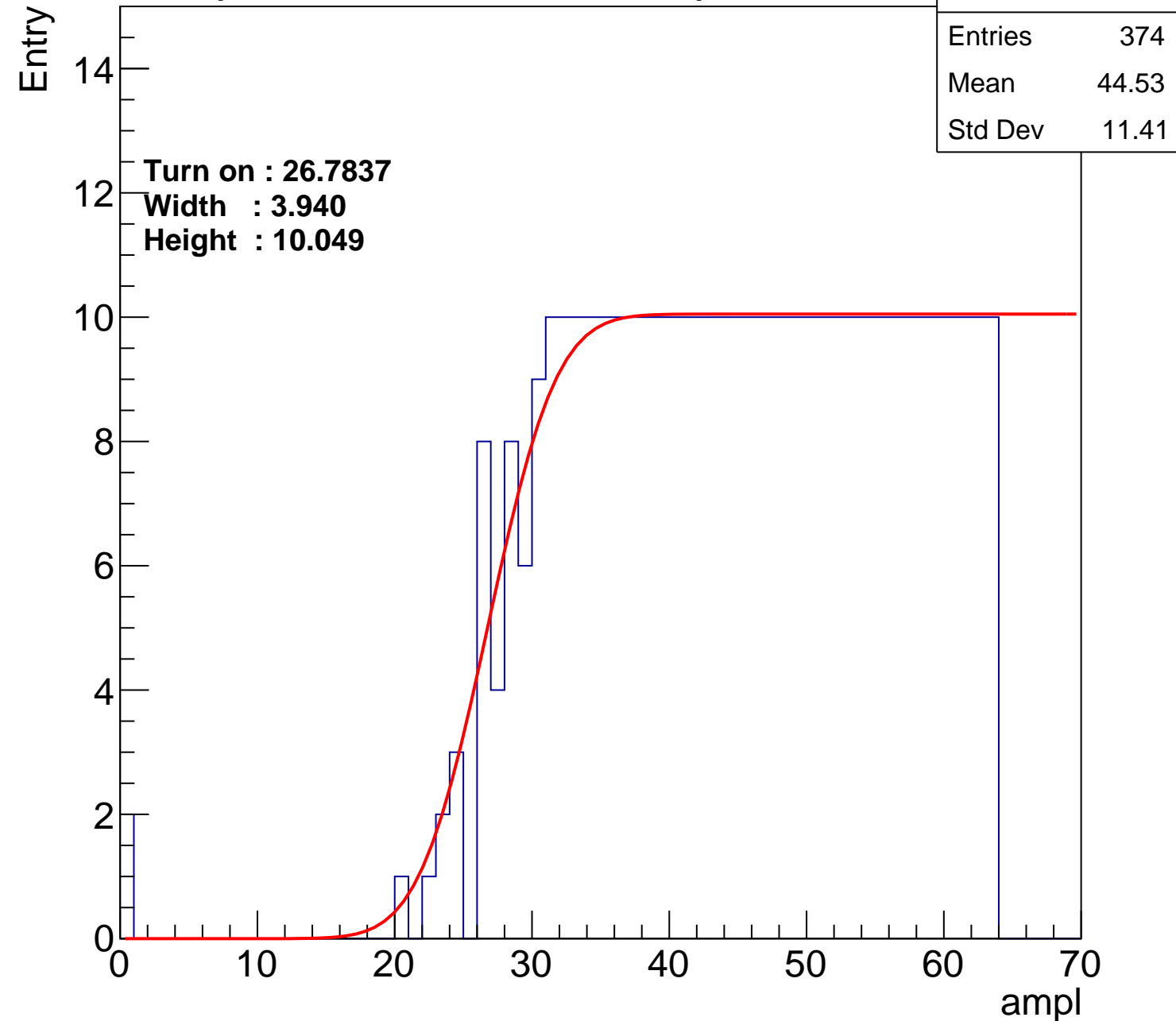
Width : 3.940

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.13
Std Dev	11.23

Turn on : 27.9294

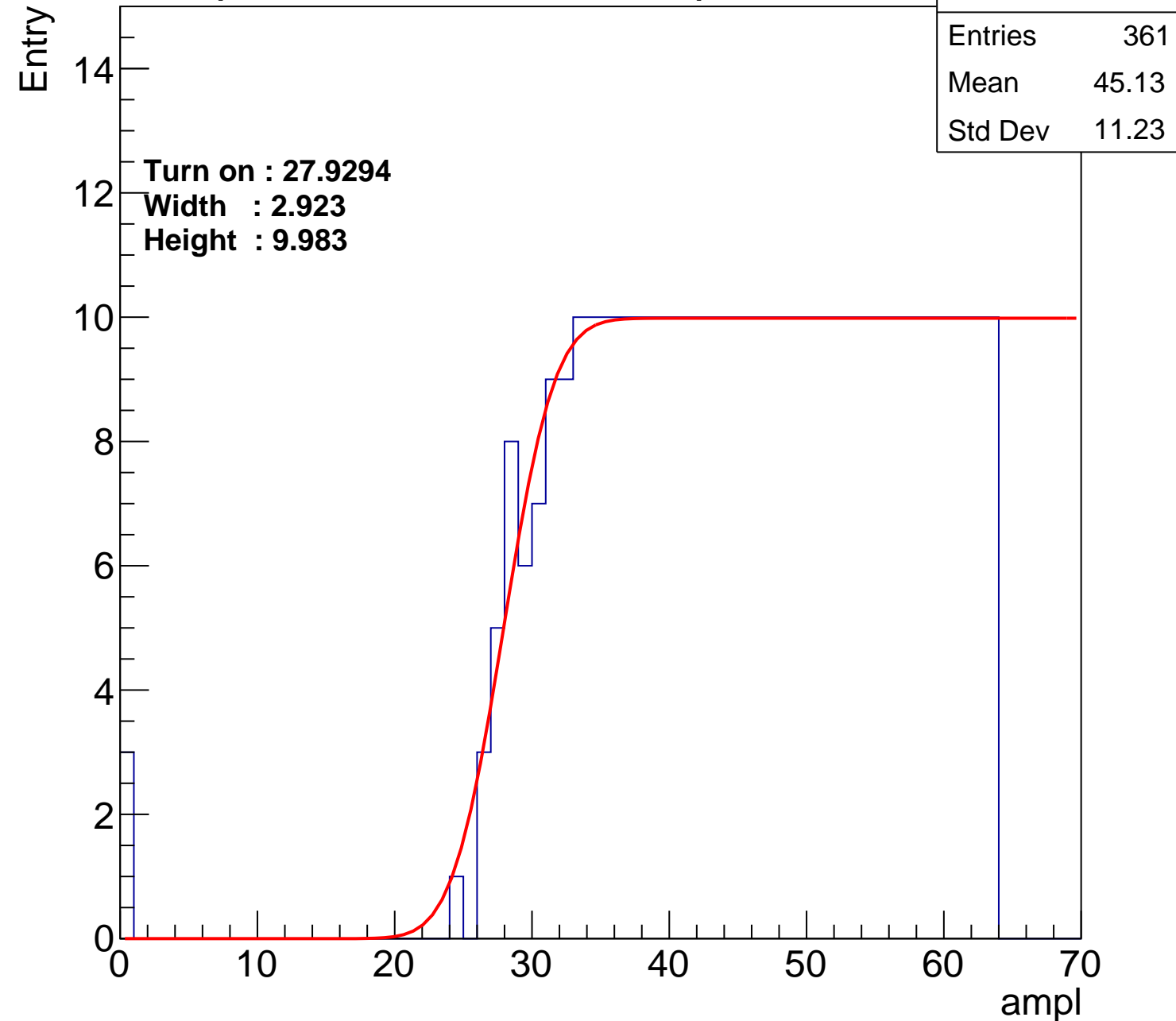
Width : 2.923

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.42
Std Dev	11.58

Turn on : 26.8465

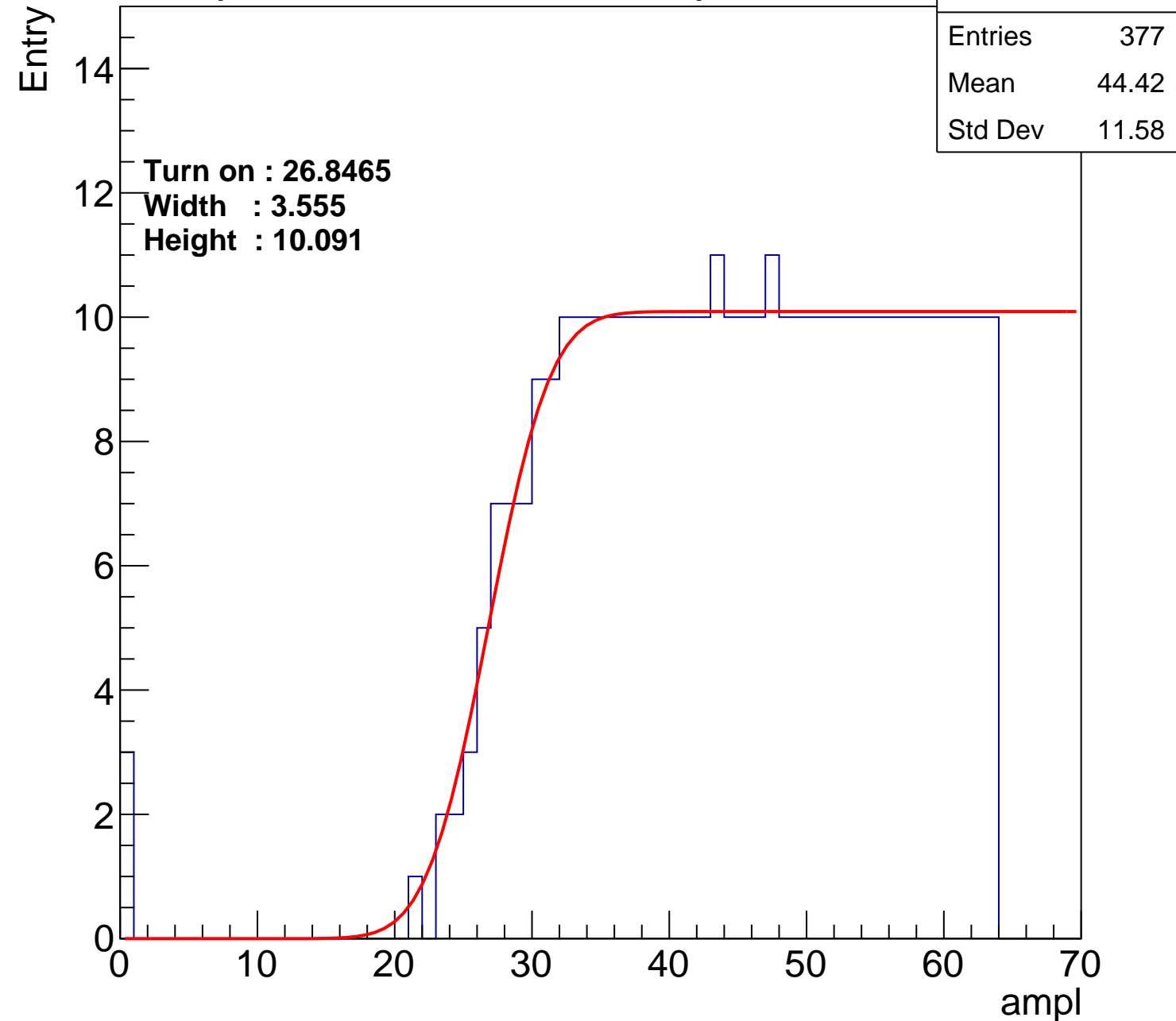
Width : 3.555

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch85

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.43
Std Dev	12.07

Turn on : 24.7119

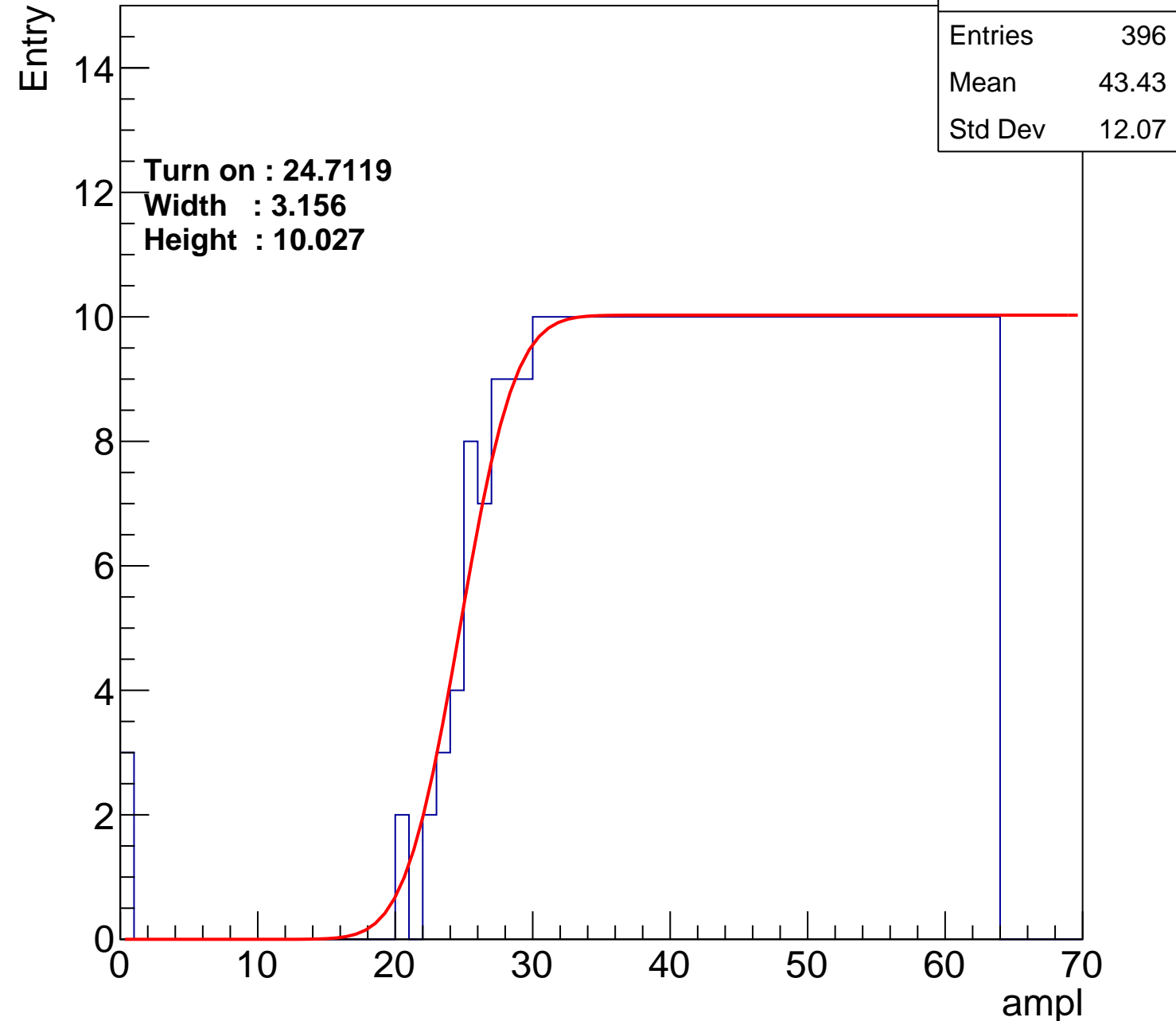
Width : 3.156

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch86

calib_packv5_042523_0143.root, FC#7, port C2

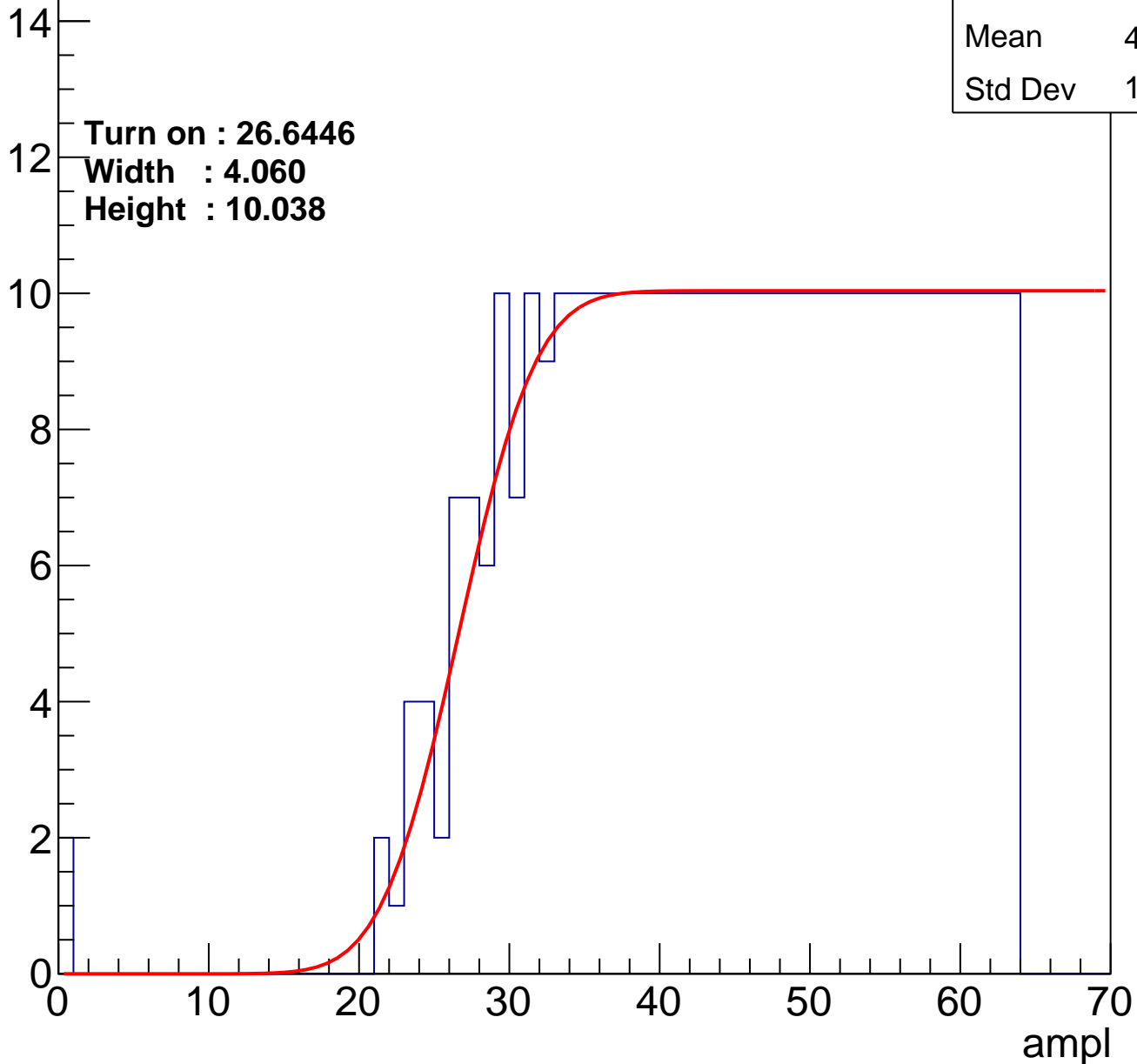
Entries	381
Mean	44.14
Std Dev	11.65

Turn on : 26.6446

Width : 4.060

Height : 10.038

Entry



B1L103S, U13-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 25.9193

Width : 3.541

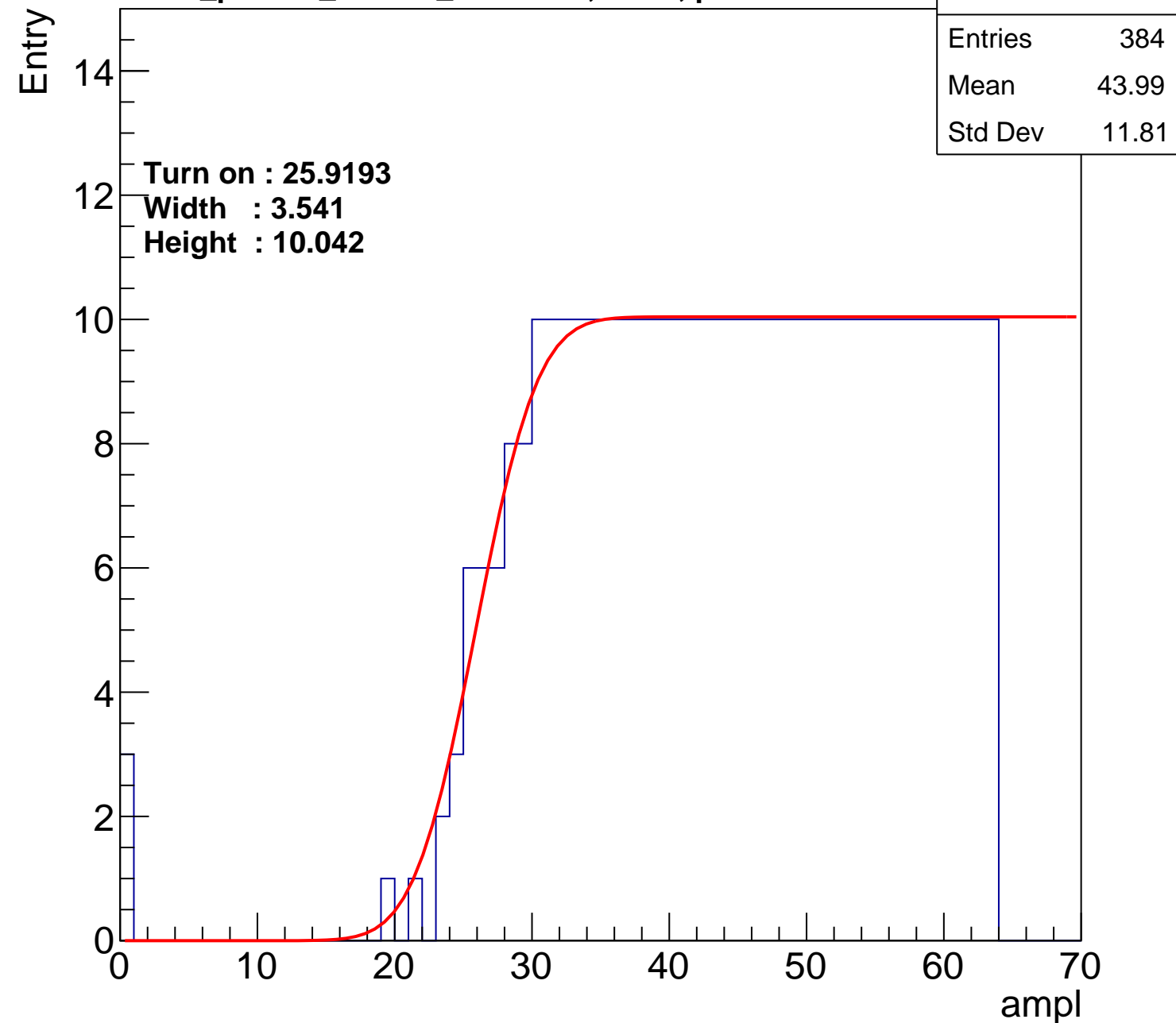
Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.13
Std Dev	12.27

Turn on : 28.4500

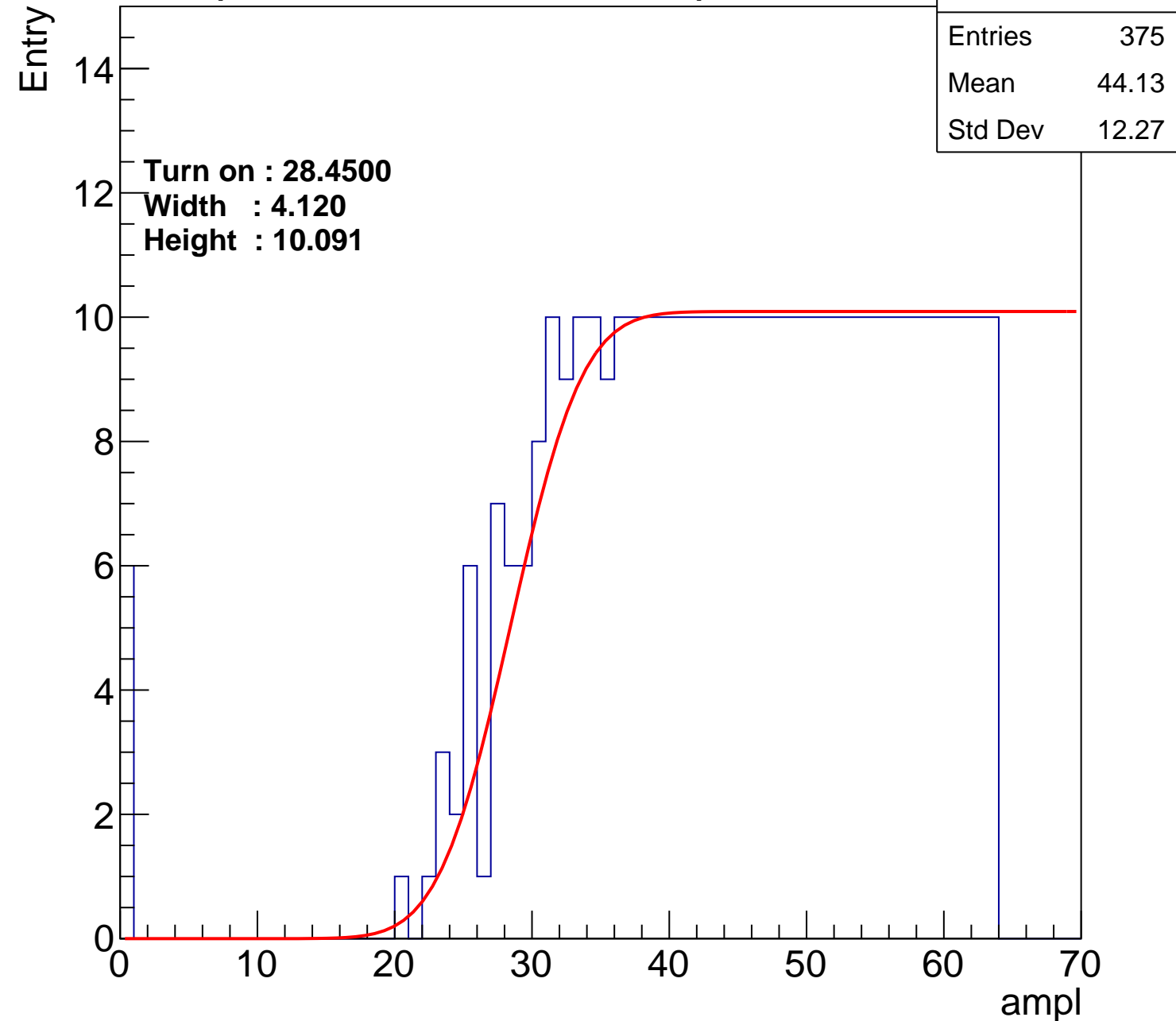
Width : 4.120

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.16
Std Dev	11.68

Turn on : 26.2801

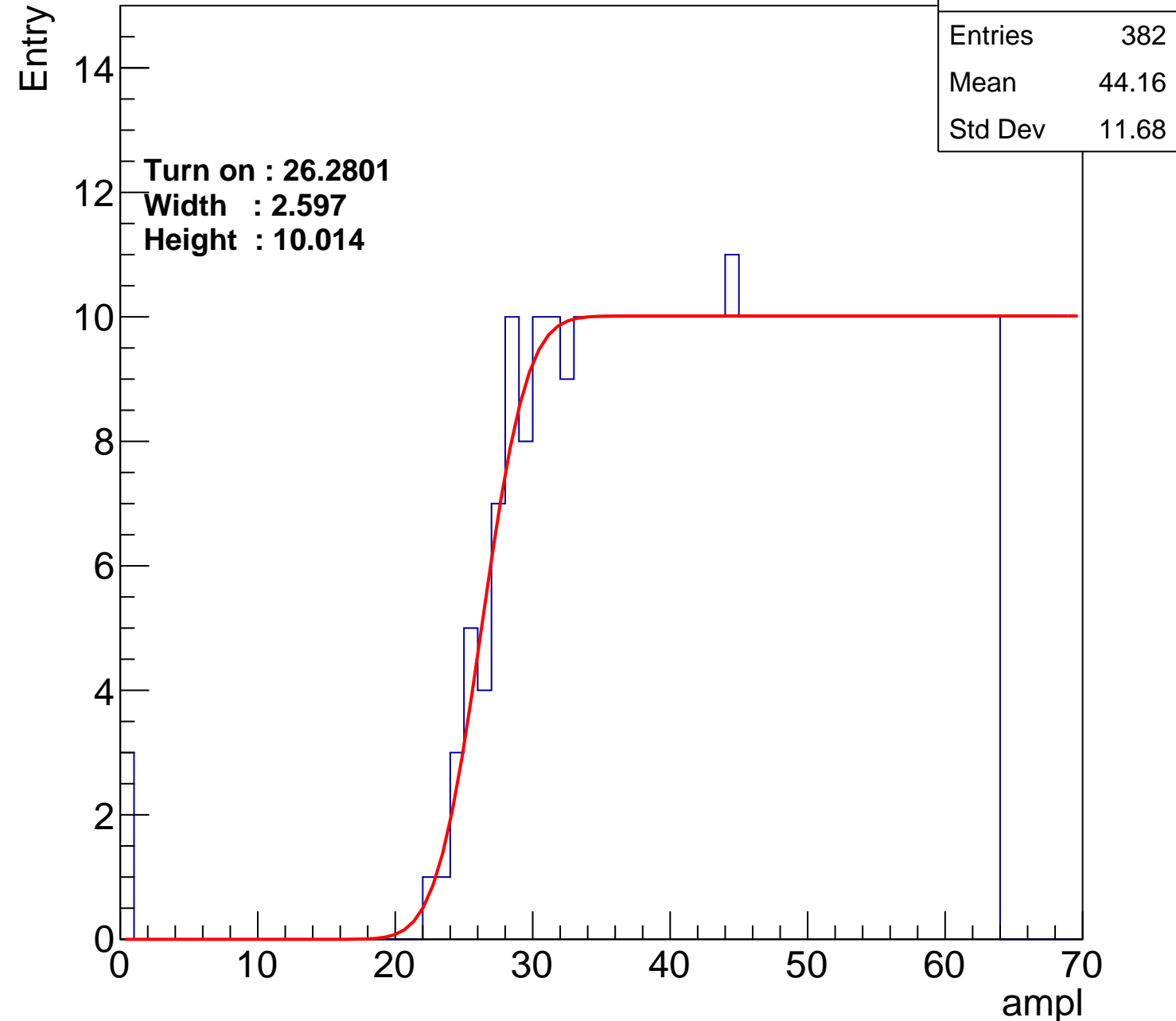
Width : 2.597

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch90

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.33
Std Dev	11.51

Turn on : 26.6530

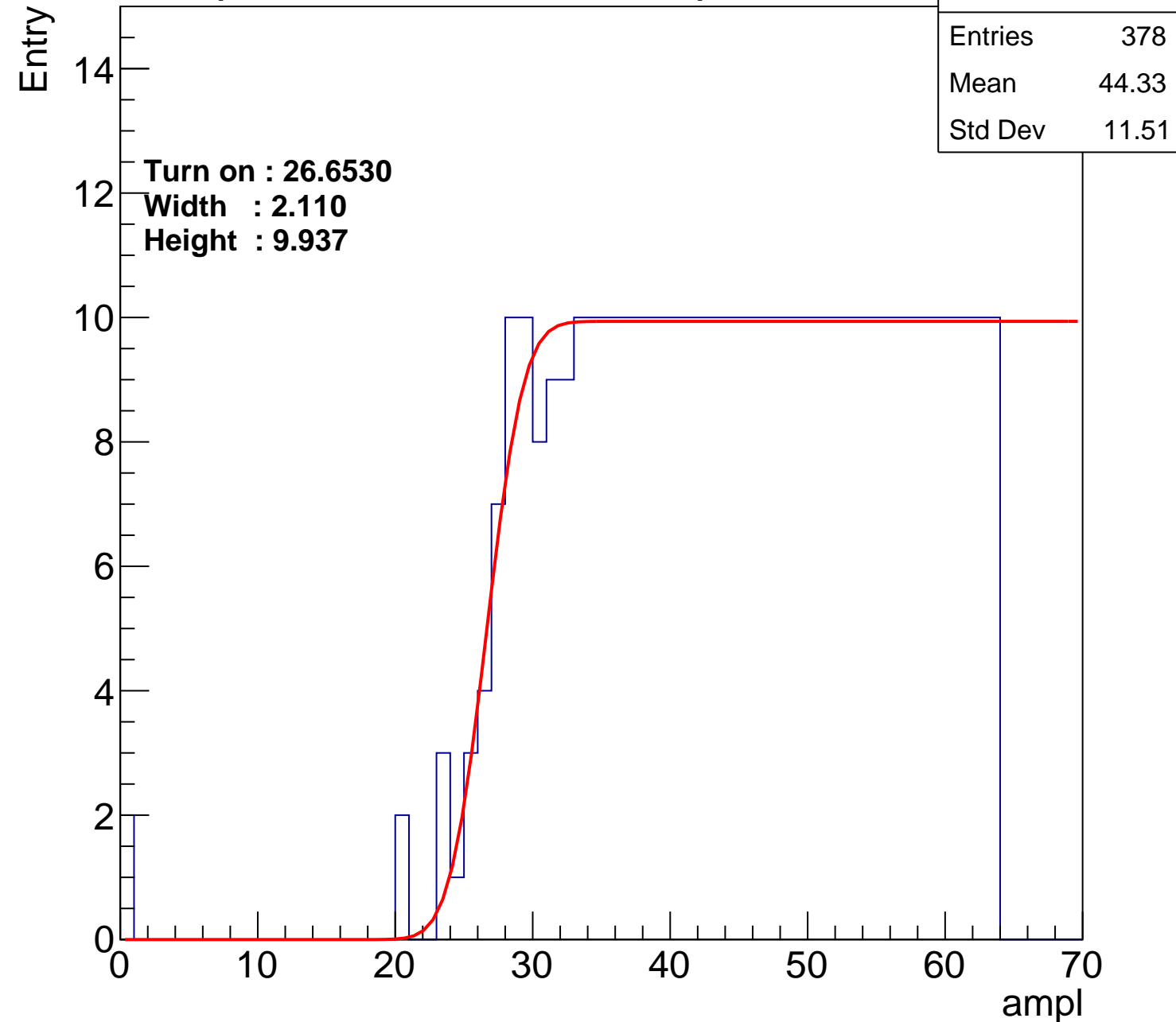
Width : 2.110

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.08
Std Dev	11.78

Turn on : 26.2322

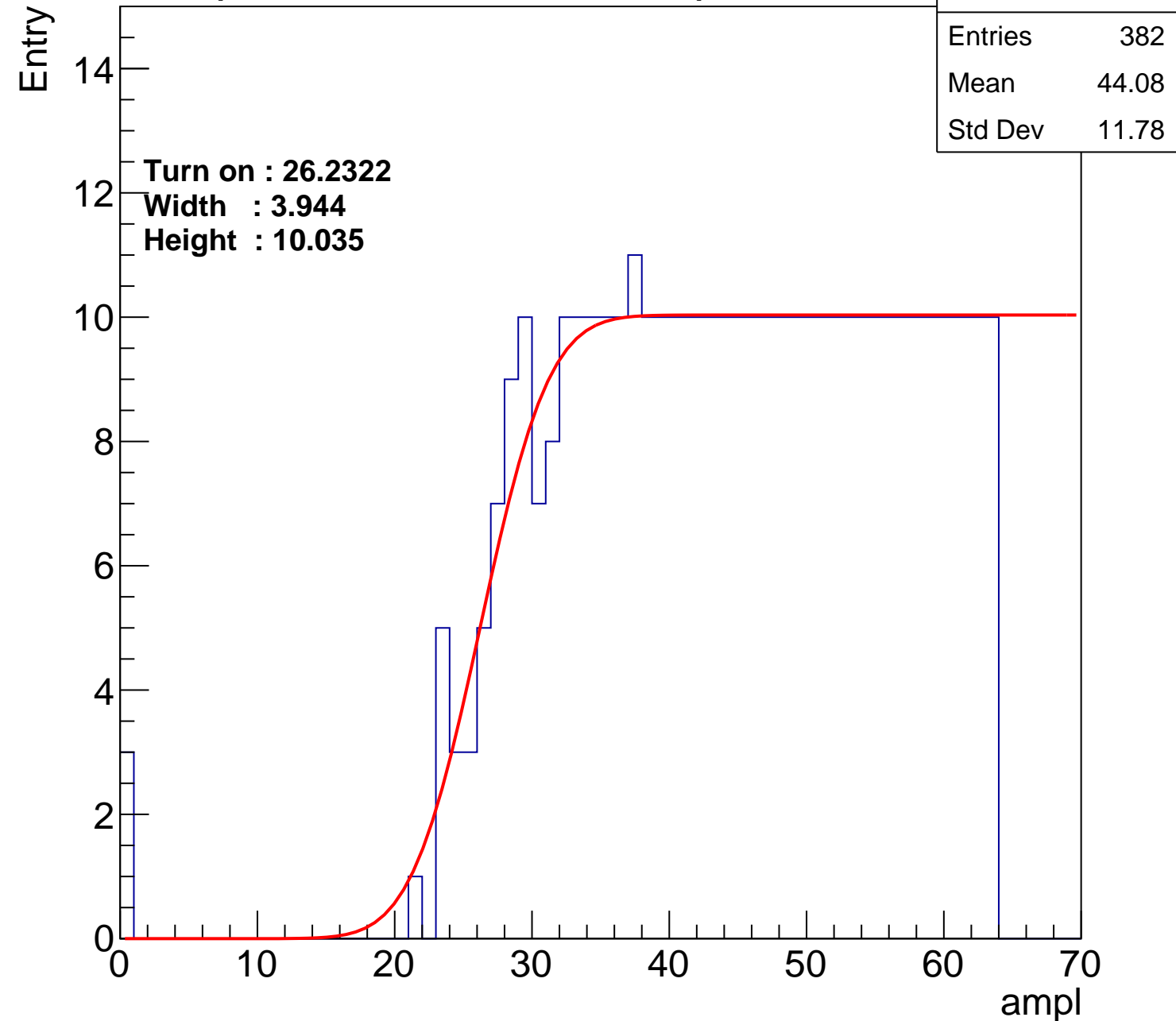
Width : 3.944

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch92

calib_packv5_042523_0143.root, FC#7, port C2

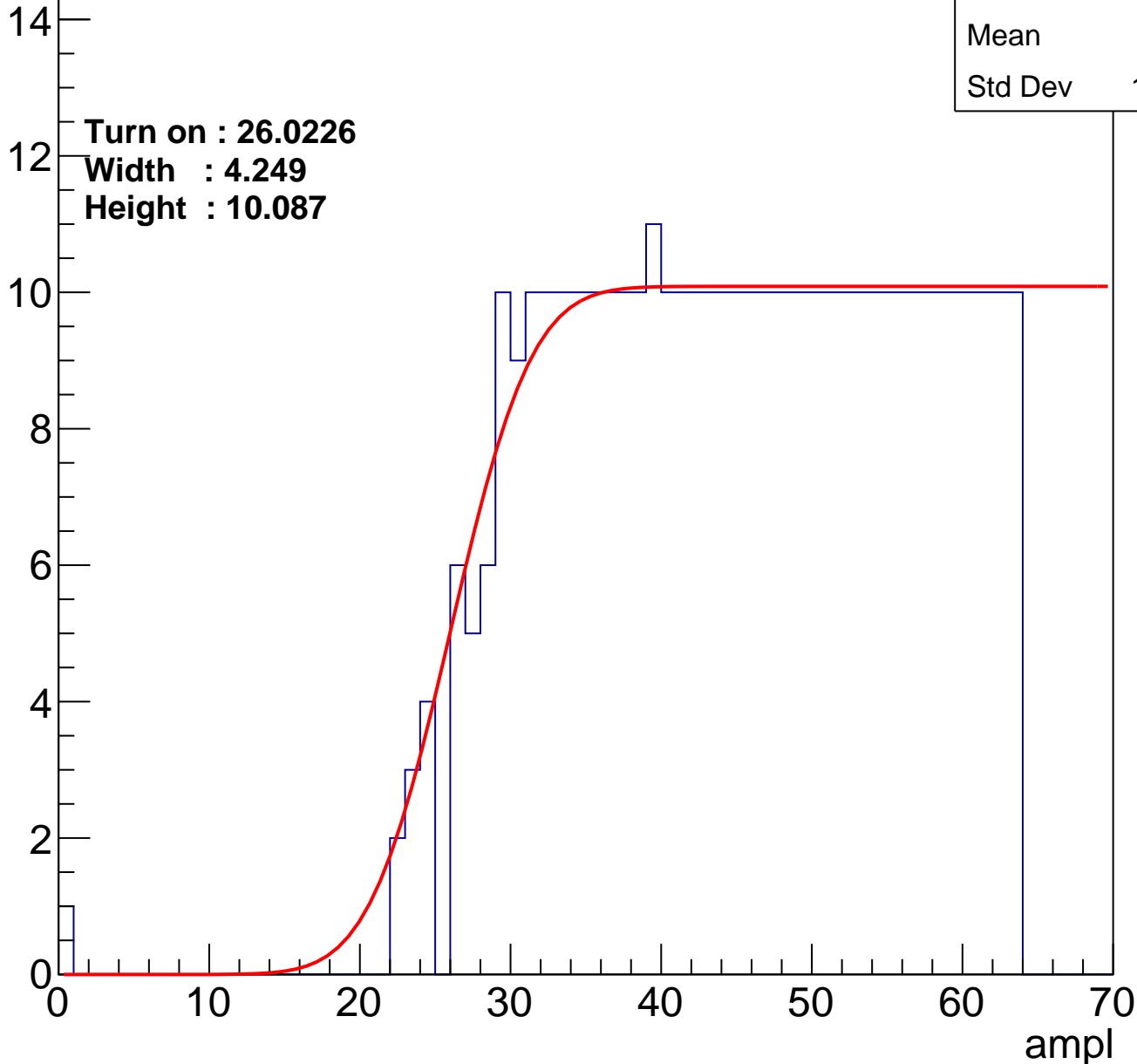
Entries	377
Mean	44.5
Std Dev	11.23

Turn on : 26.0226

Width : 4.249

Height : 10.087

Entry



B1L103S, U13-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.7
Std Dev	11.36

Turn on : 27.5705

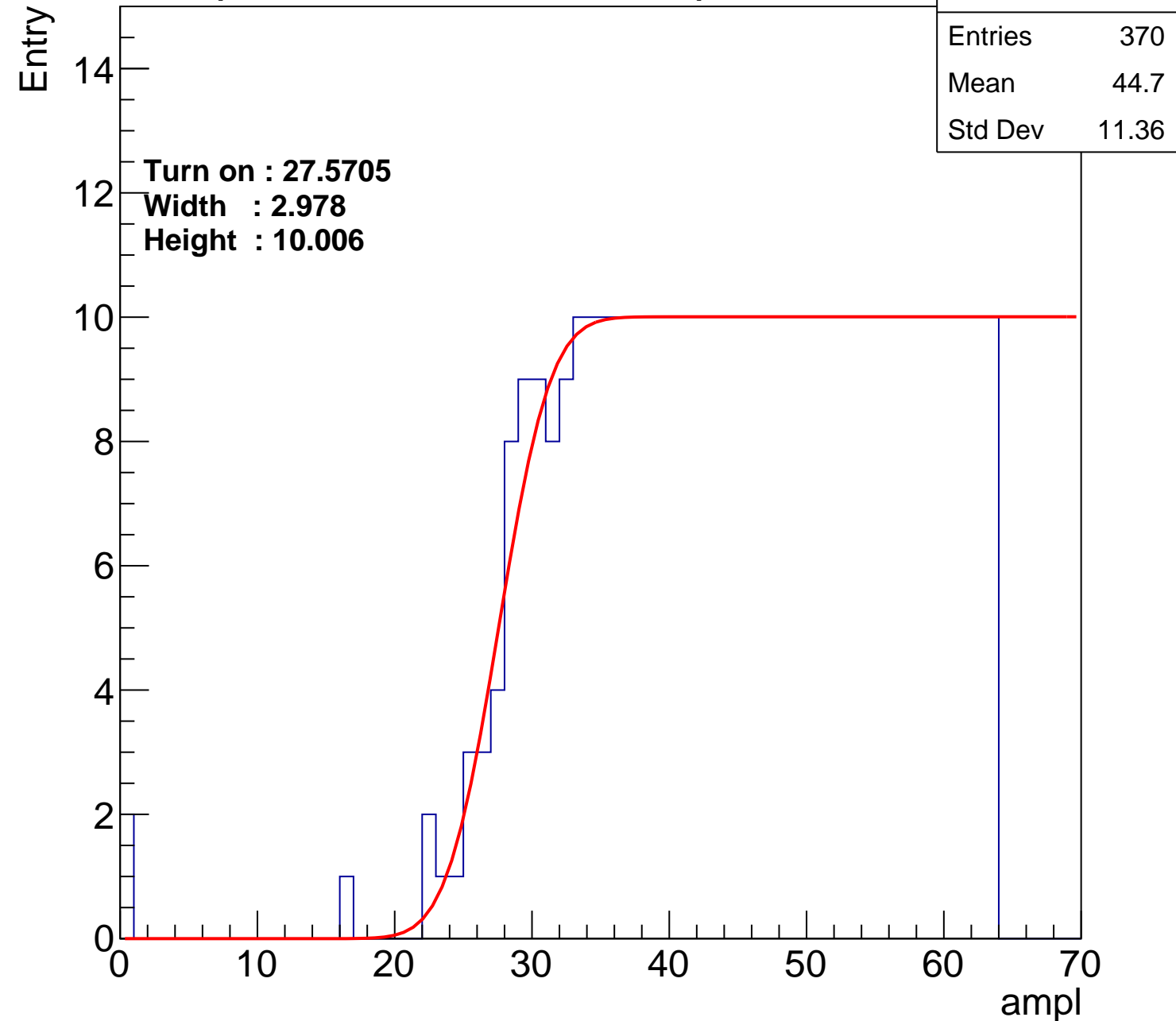
Width : 2.978

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch94

calib_packv5_042523_0143.root, FC#7, port C2

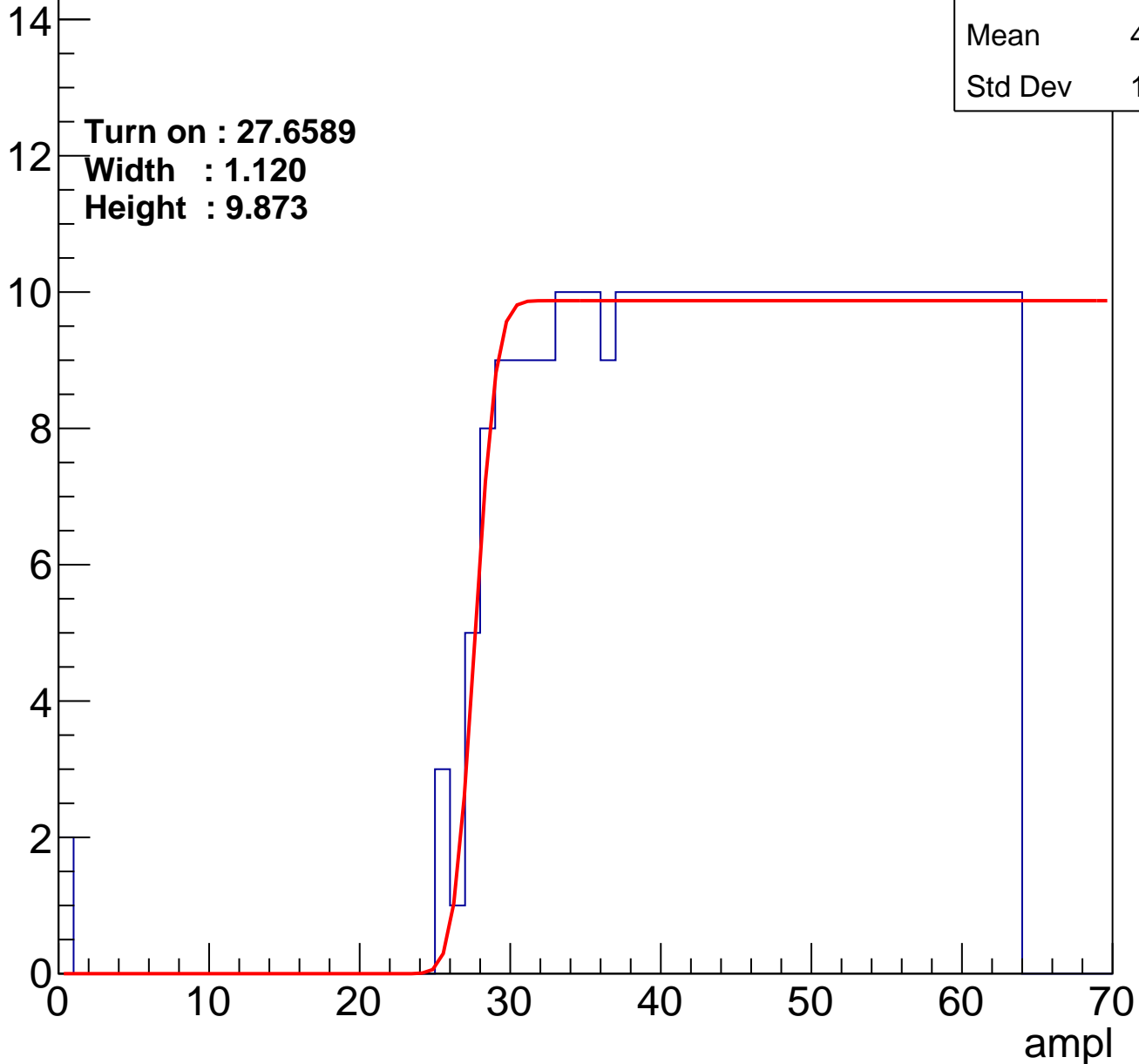
Entries	364
Mean	45.06
Std Dev	11.08

Turn on : 27.6589

Width : 1.120

Height : 9.873

Entry



B1L103S, U13-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.36
Std Dev	10.88

Turn on : 28.8862

Width : 3.877

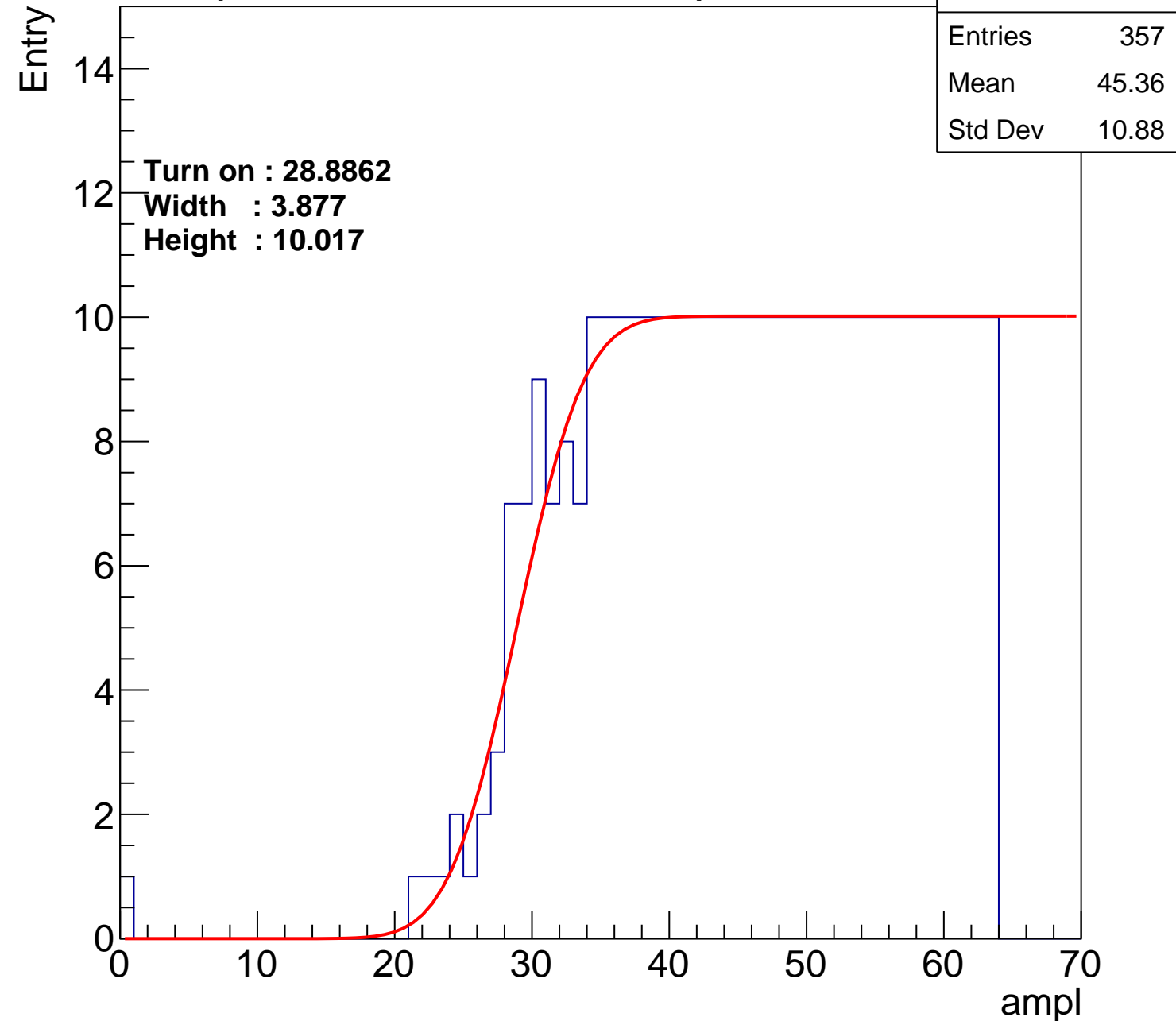
Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch96

calib_packv5_042523_0143.root, FC#7, port C2

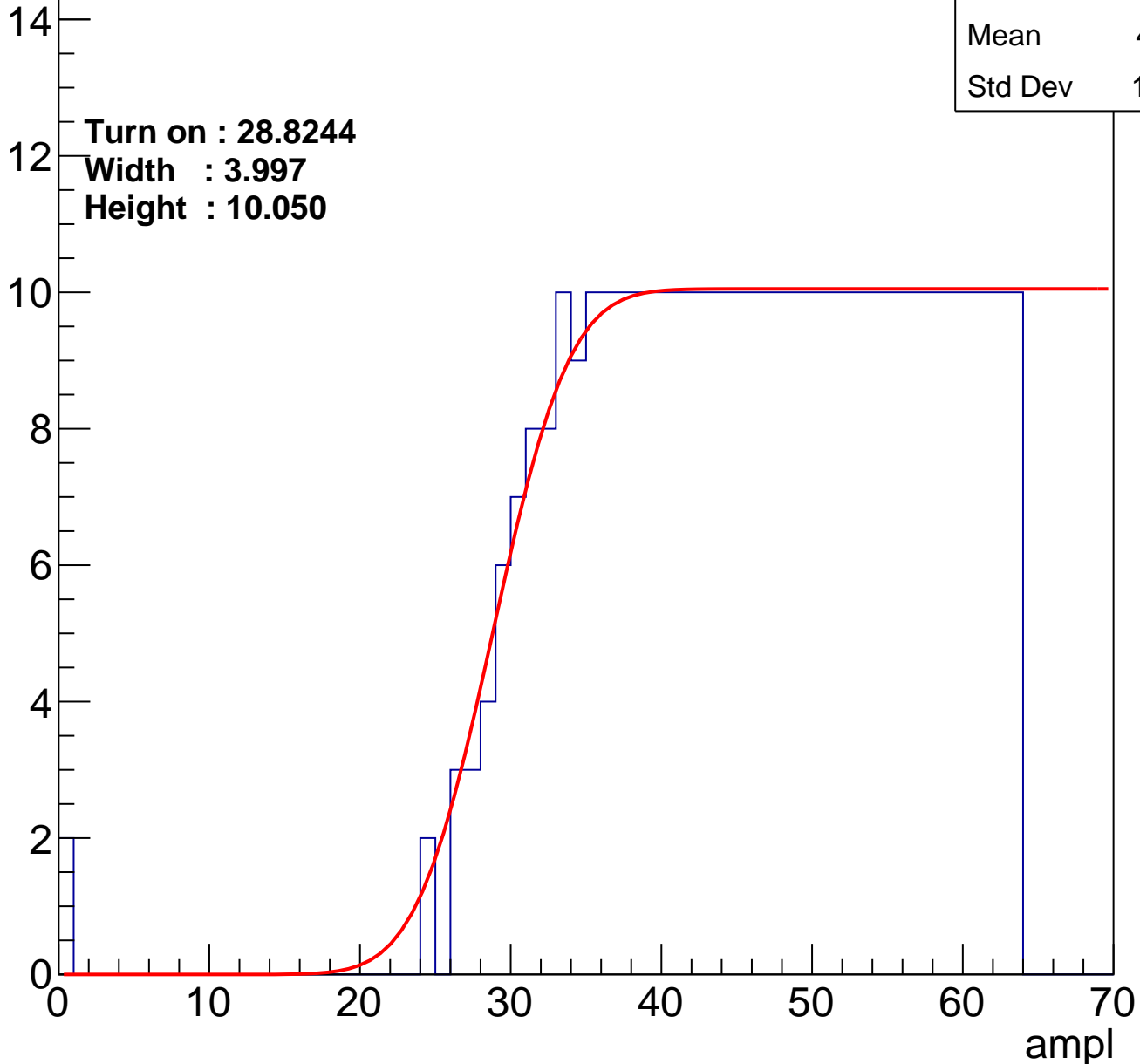
Entries	352
Mean	45.61
Std Dev	10.86

Turn on : 28.8244

Width : 3.997

Height : 10.050

Entry



B1L103S, U13-ch97

calib_packv5_042523_0143.root, FC#7, port C2

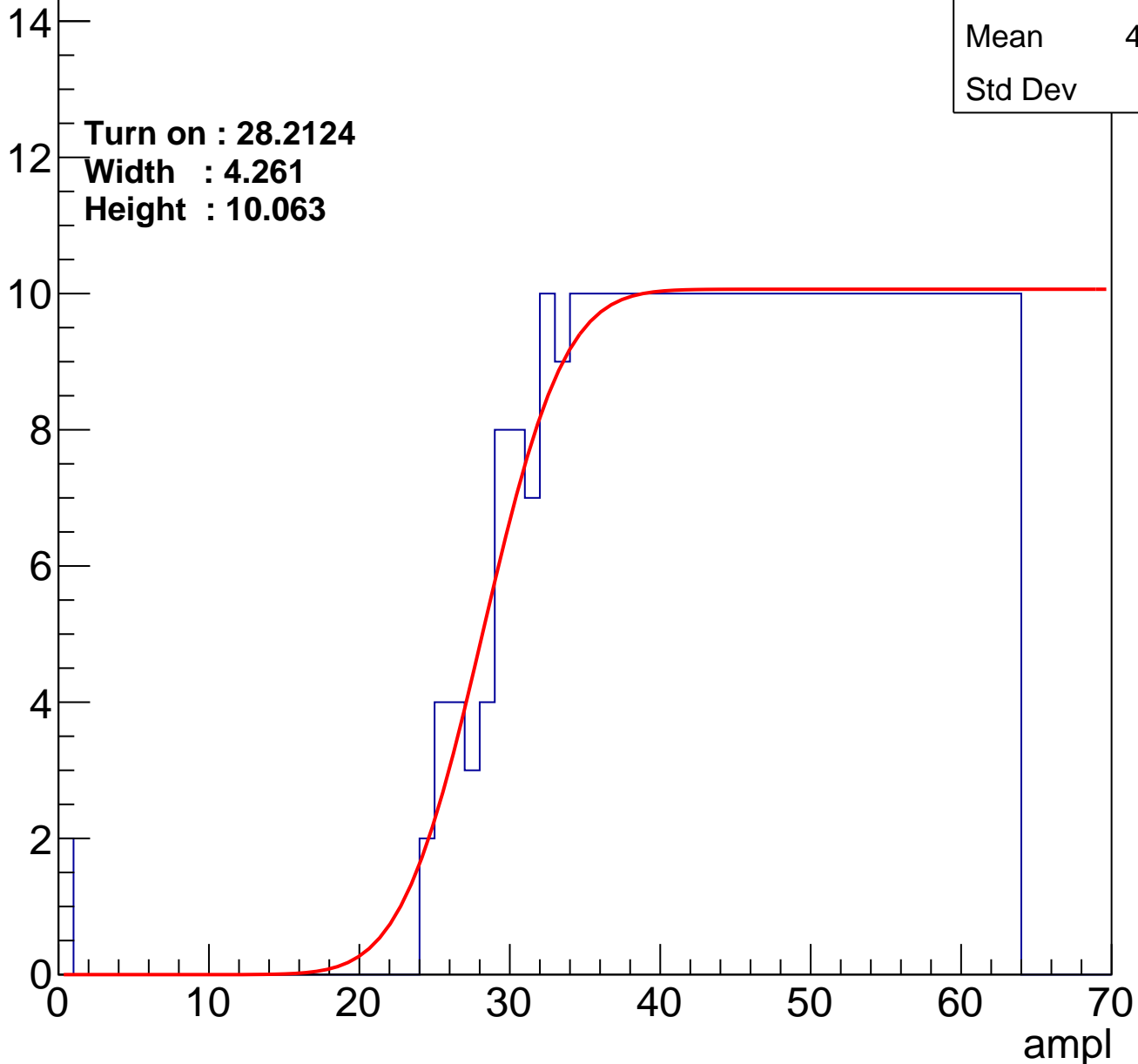
Entries	361
Mean	45.16
Std Dev	11.1

Turn on : 28.2124

Width : 4.261

Height : 10.063

Entry



B1L103S, U13-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.11
Std Dev	12.12

Turn on : 28.3906

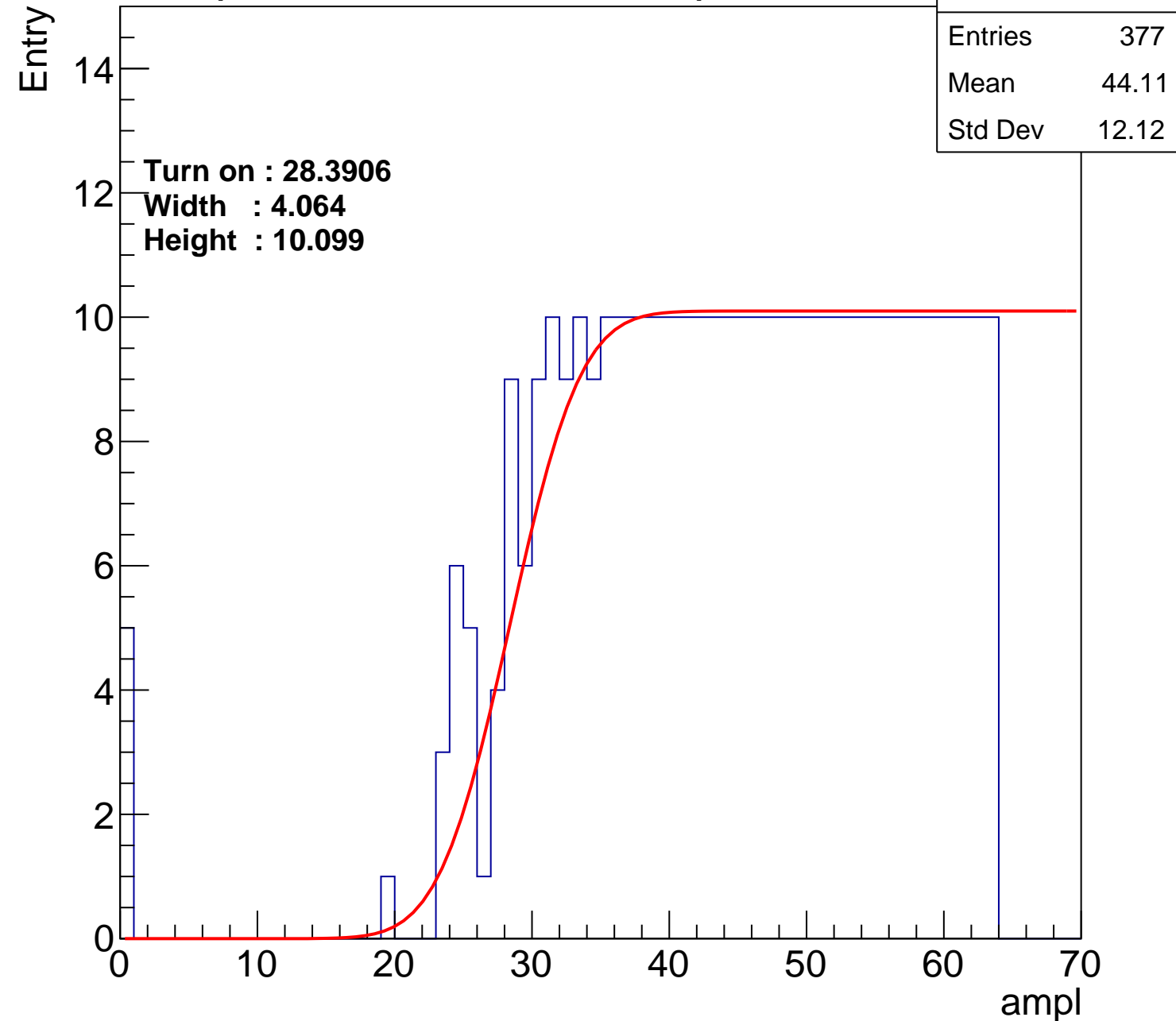
Width : 4.064

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.7287

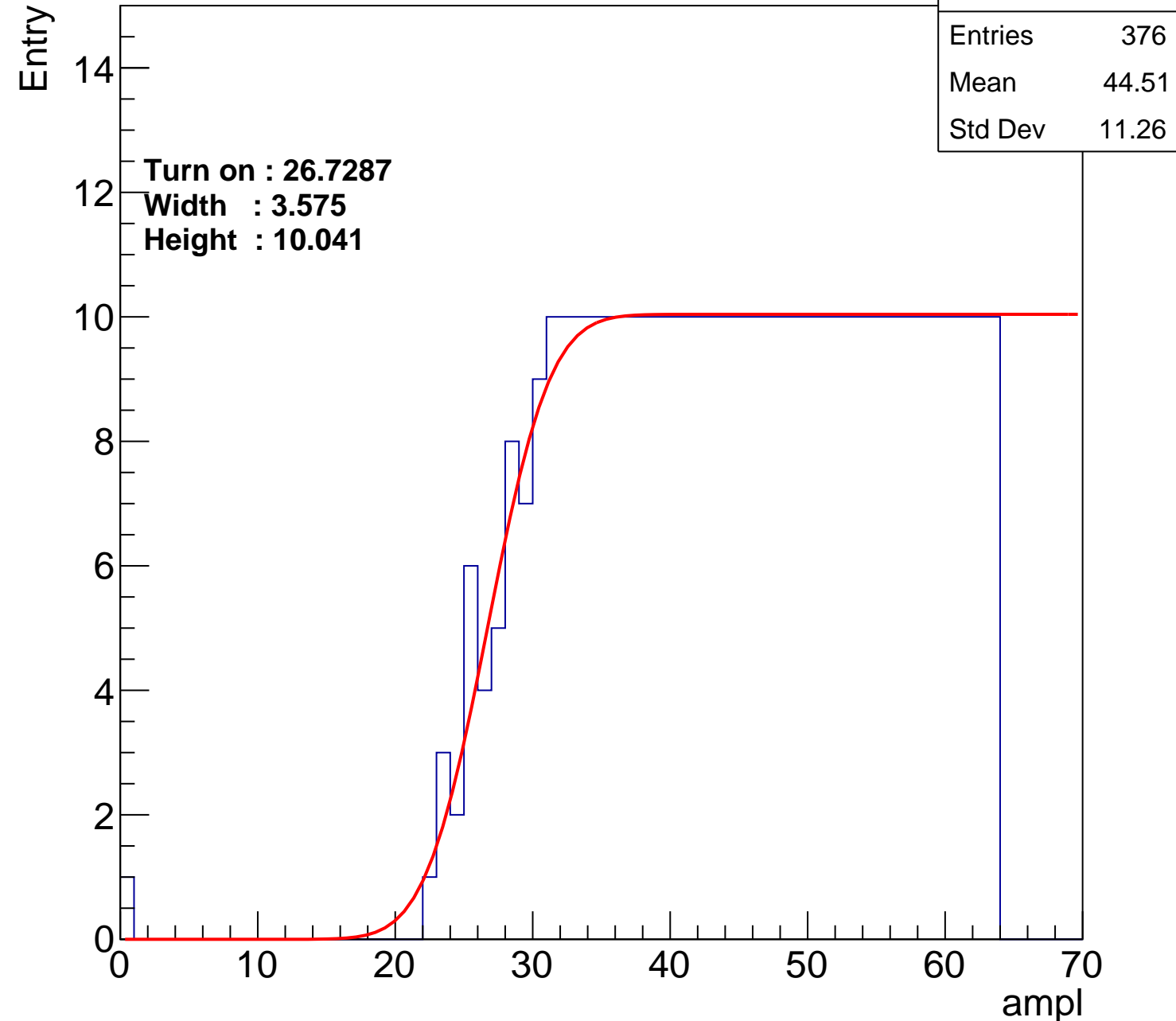
Width : 3.575

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch100

calib_packv5_042523_0143.root, FC#7, port C2

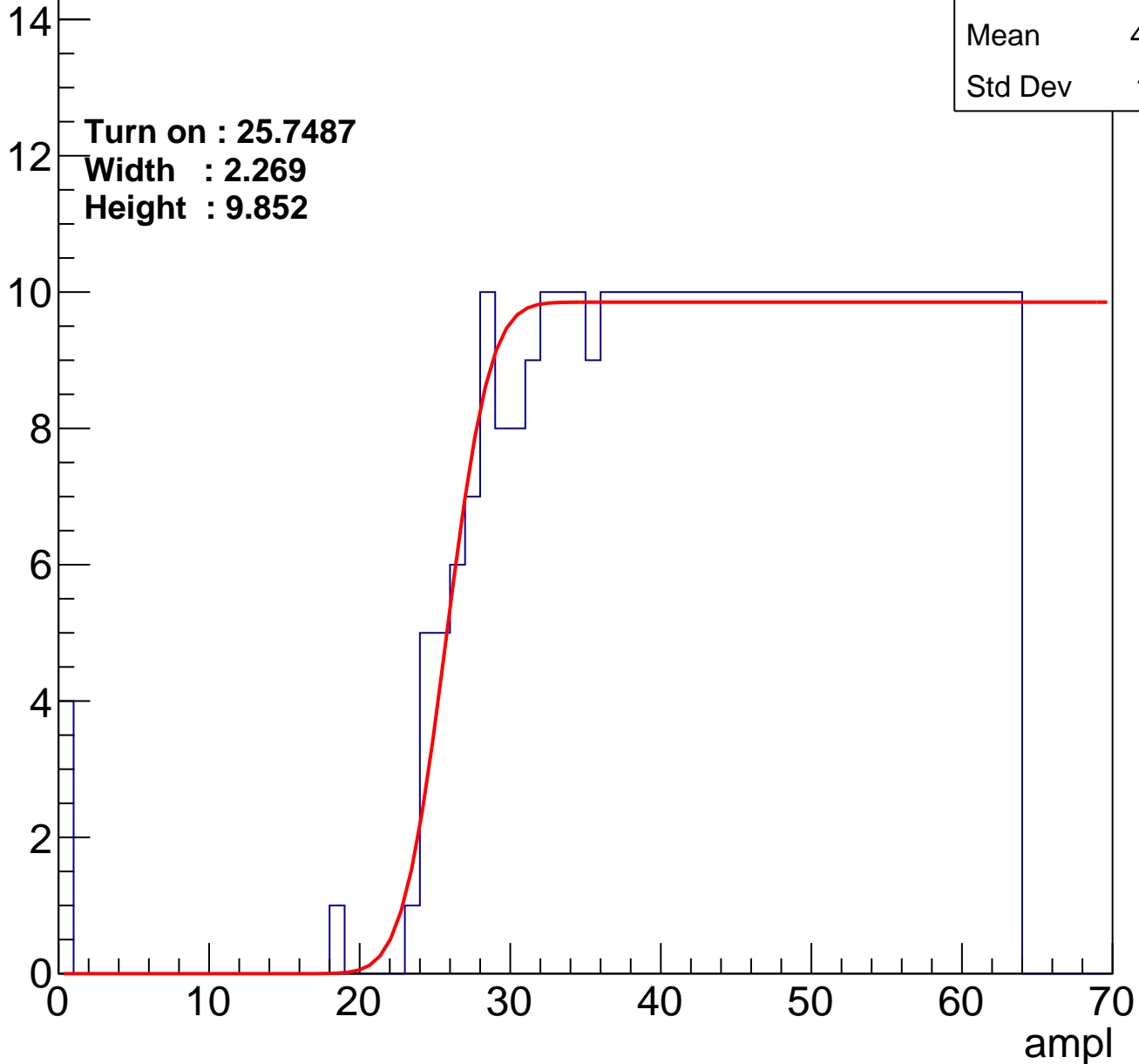
Entries	383
Mean	43.93
Std Dev	12.01

Turn on : 25.7487

Width : 2.269

Height : 9.852

Entry



B1L103S, U13-ch101

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.82
Std Dev	11.07

Turn on : 27.4149

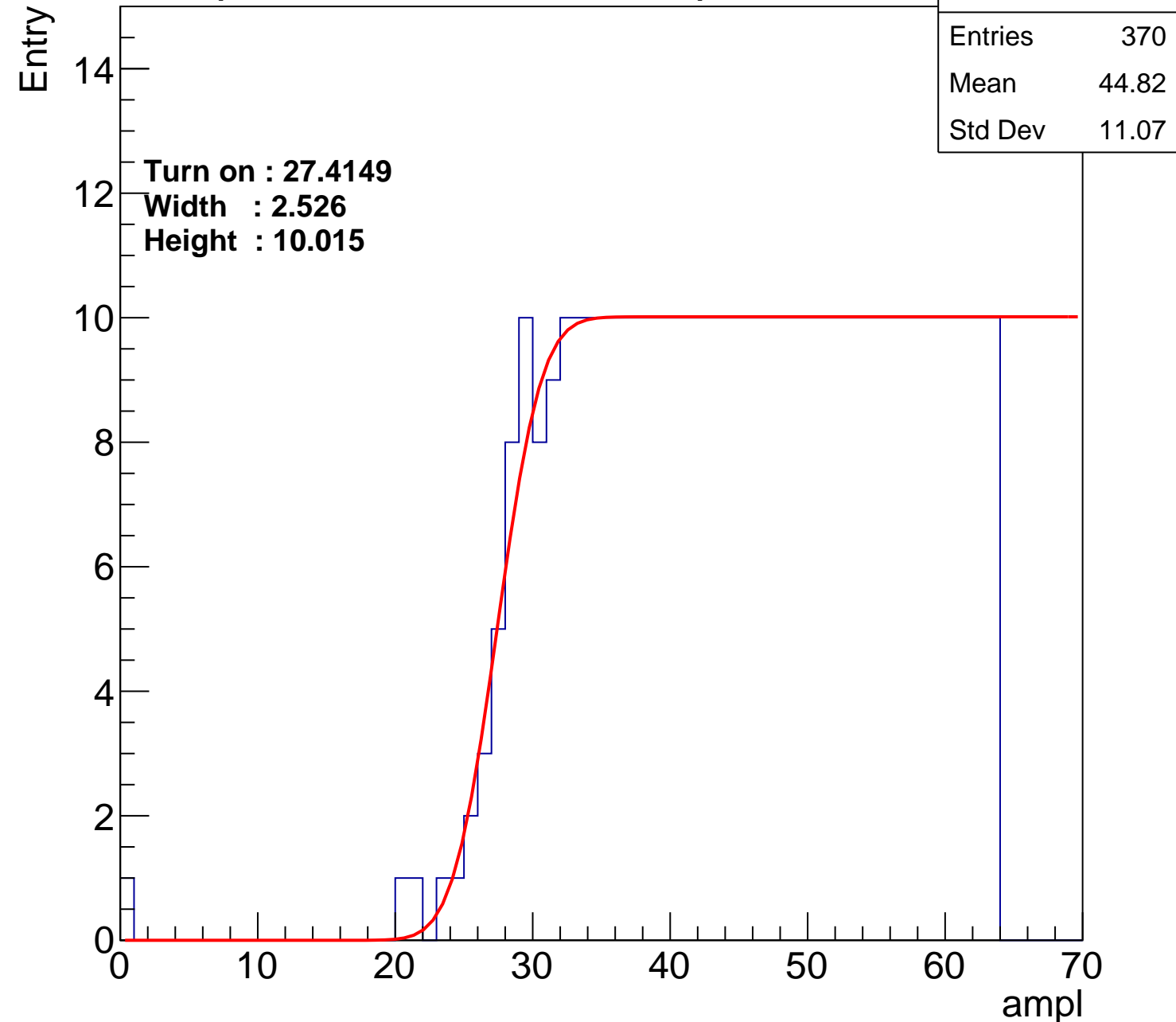
Width : 2.526

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch102

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.72
Std Dev	12.01

Turn on : 25.9935

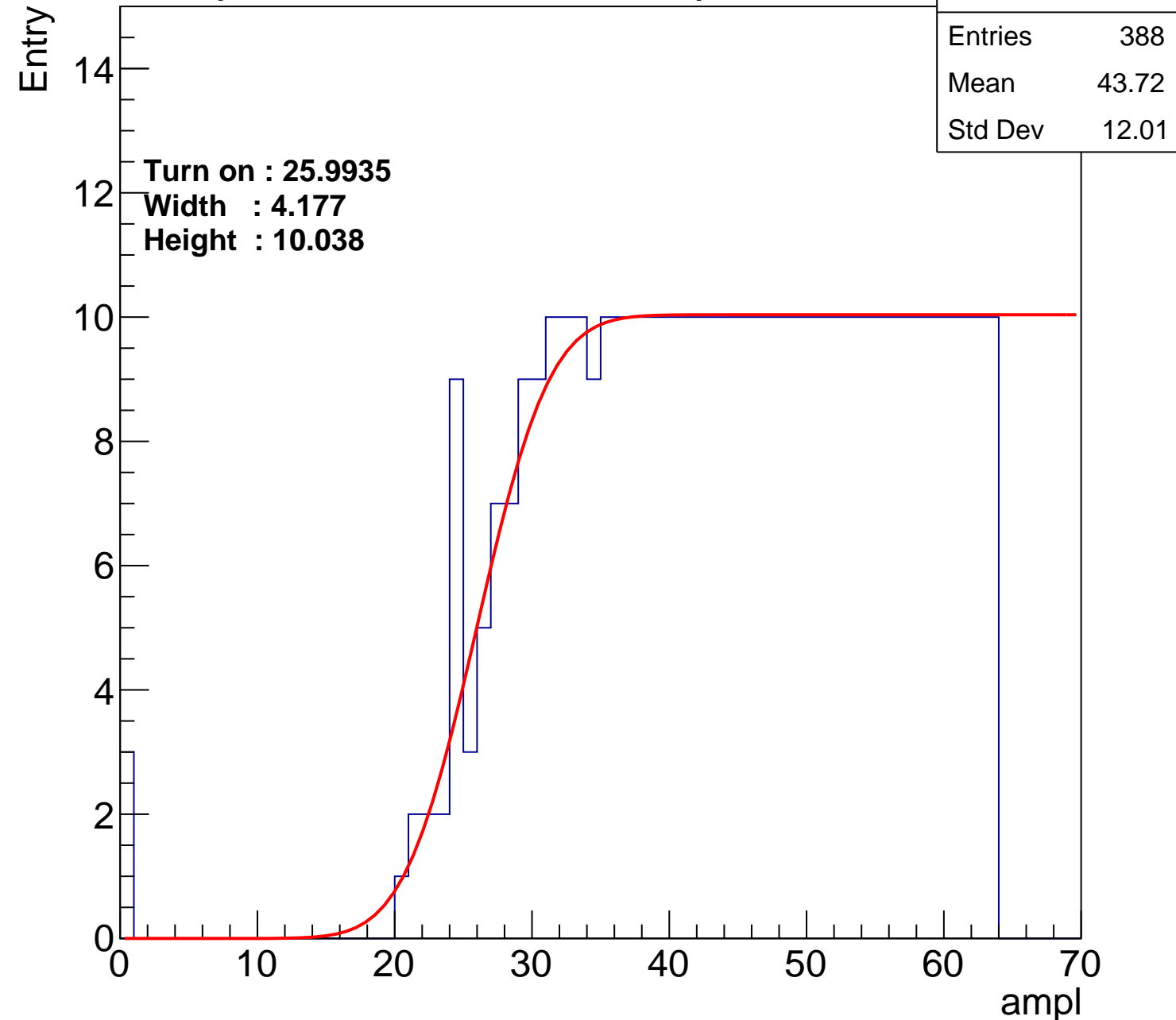
Width : 4.177

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.24
Std Dev	10.86

Turn on : 28.3162

Width : 3.792

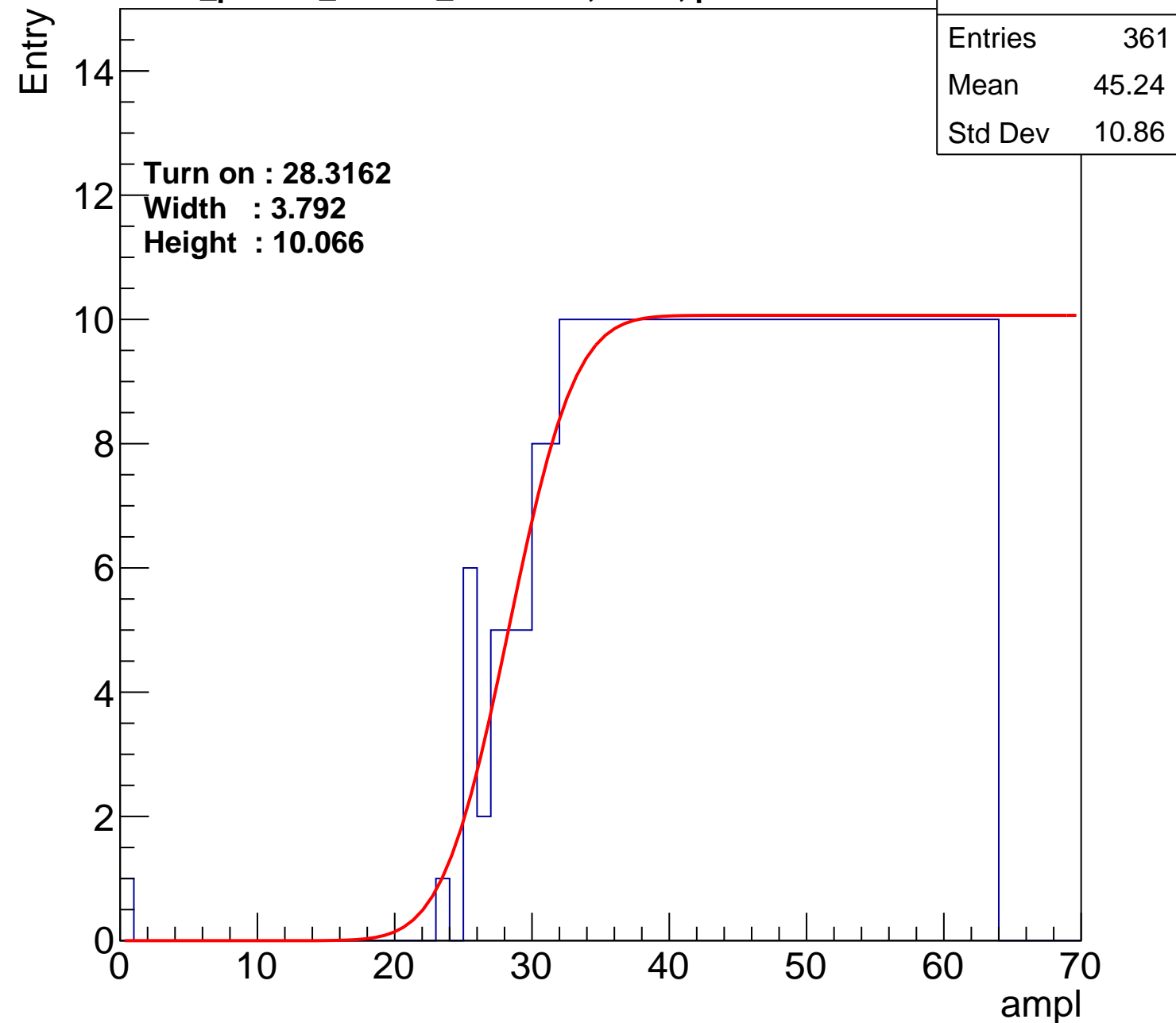
Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U13-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.34
Std Dev	12.37

Turn on : 25.5855

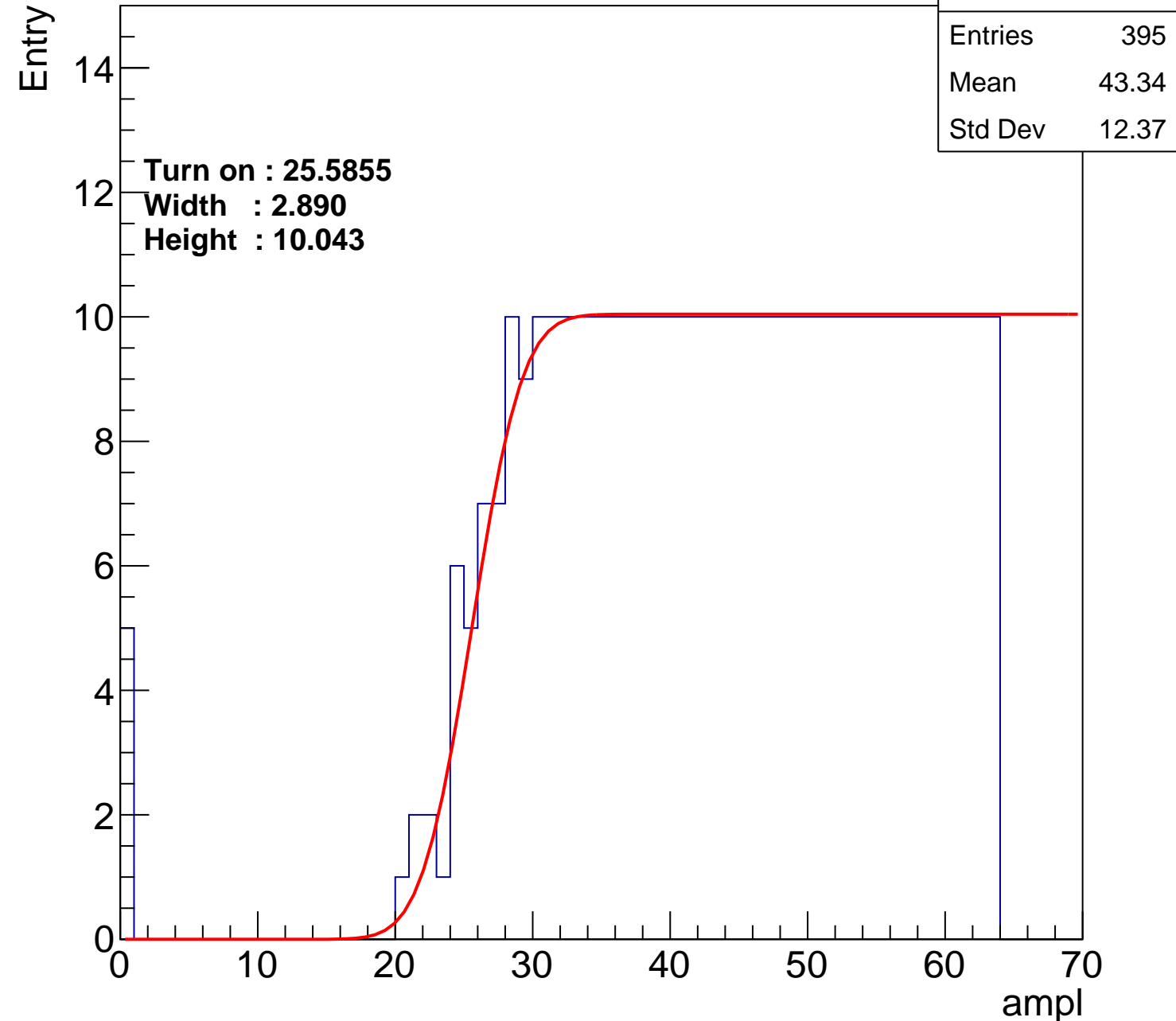
Width : 2.890

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch105

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.94
Std Dev	11.1

Turn on : 27.5134

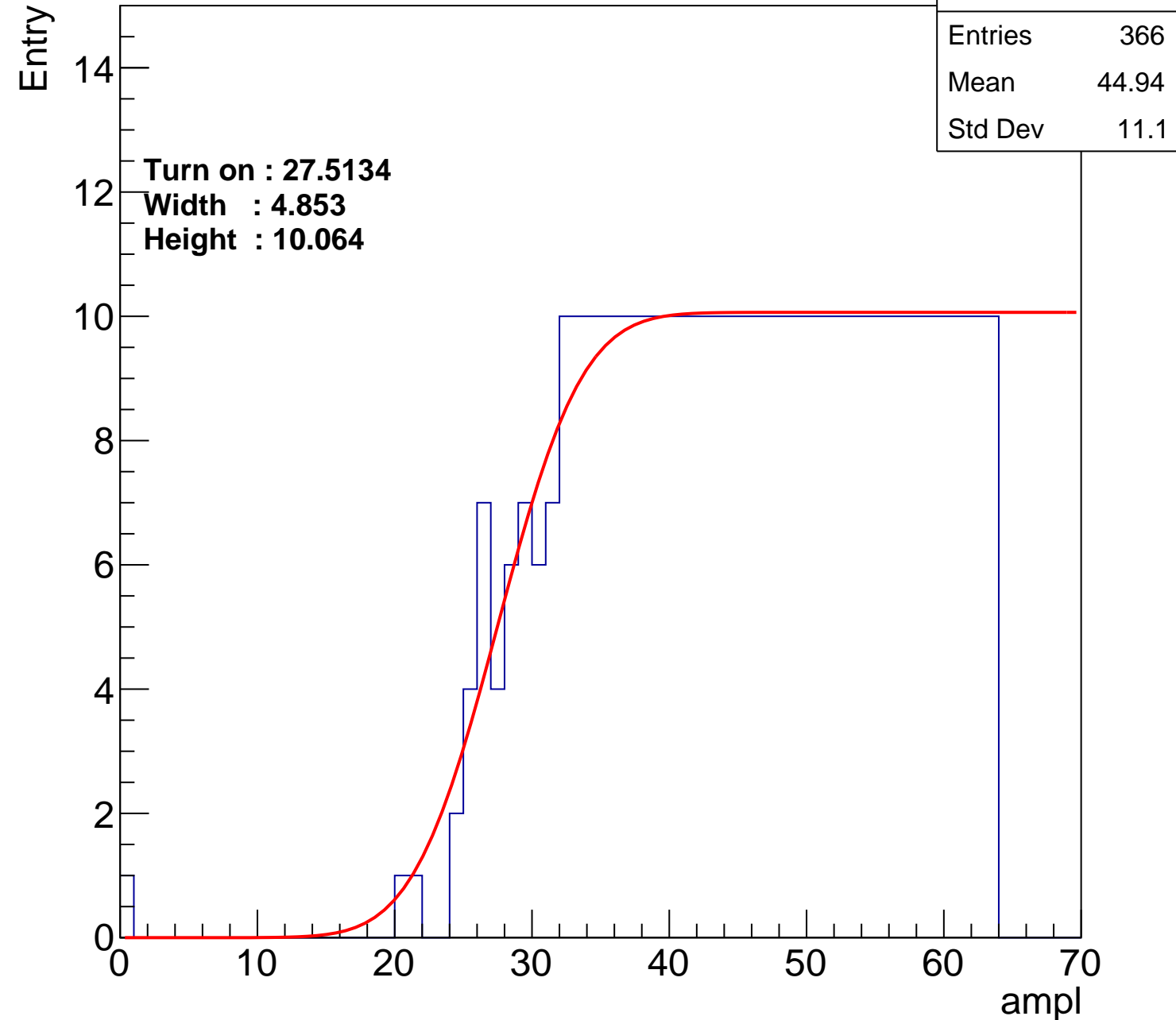
Width : 4.853

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch106

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.68
Std Dev	11.99

Turn on : 25.9939

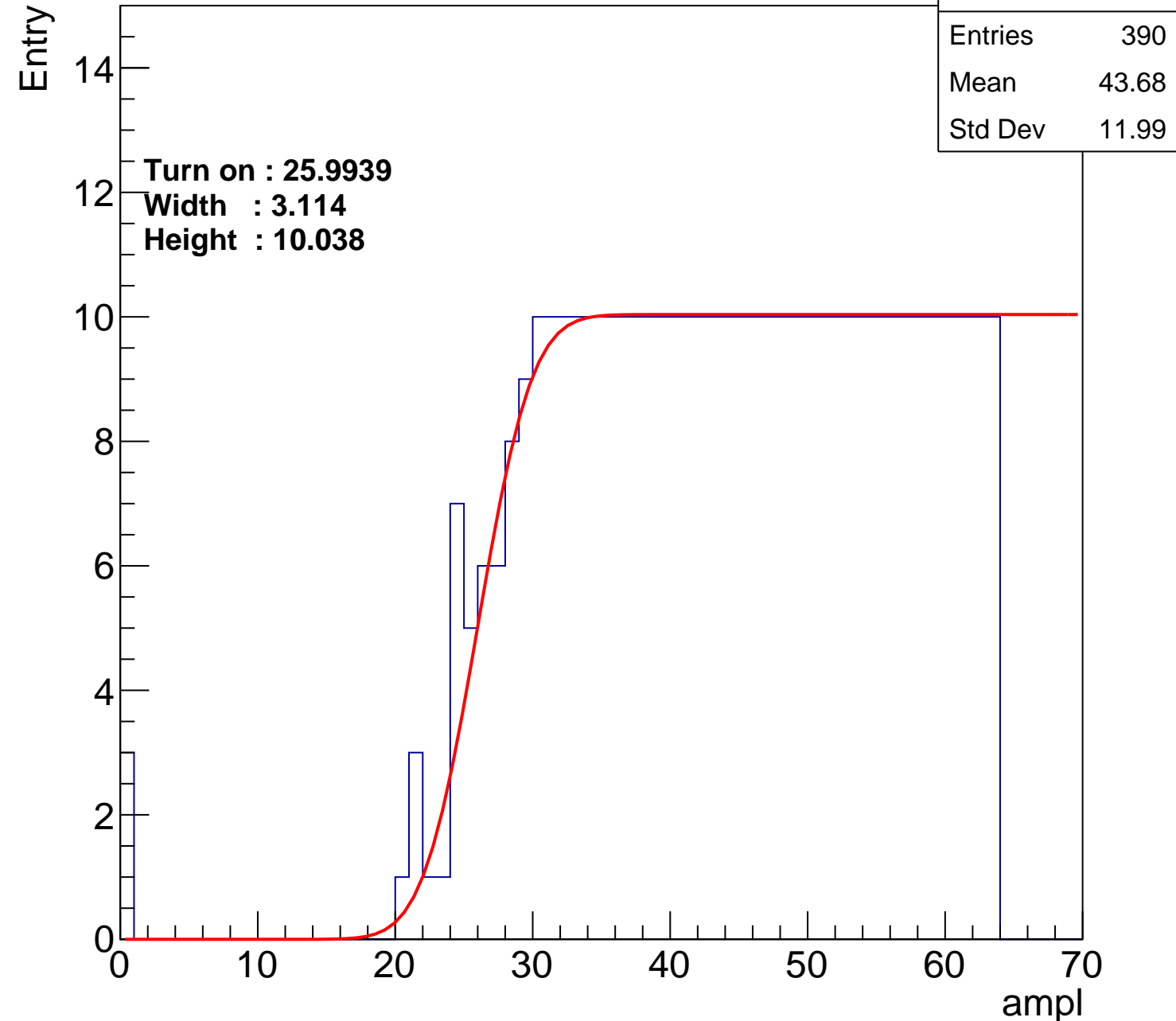
Width : 3.114

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.28
Std Dev	10.81

Turn on : 28.6021

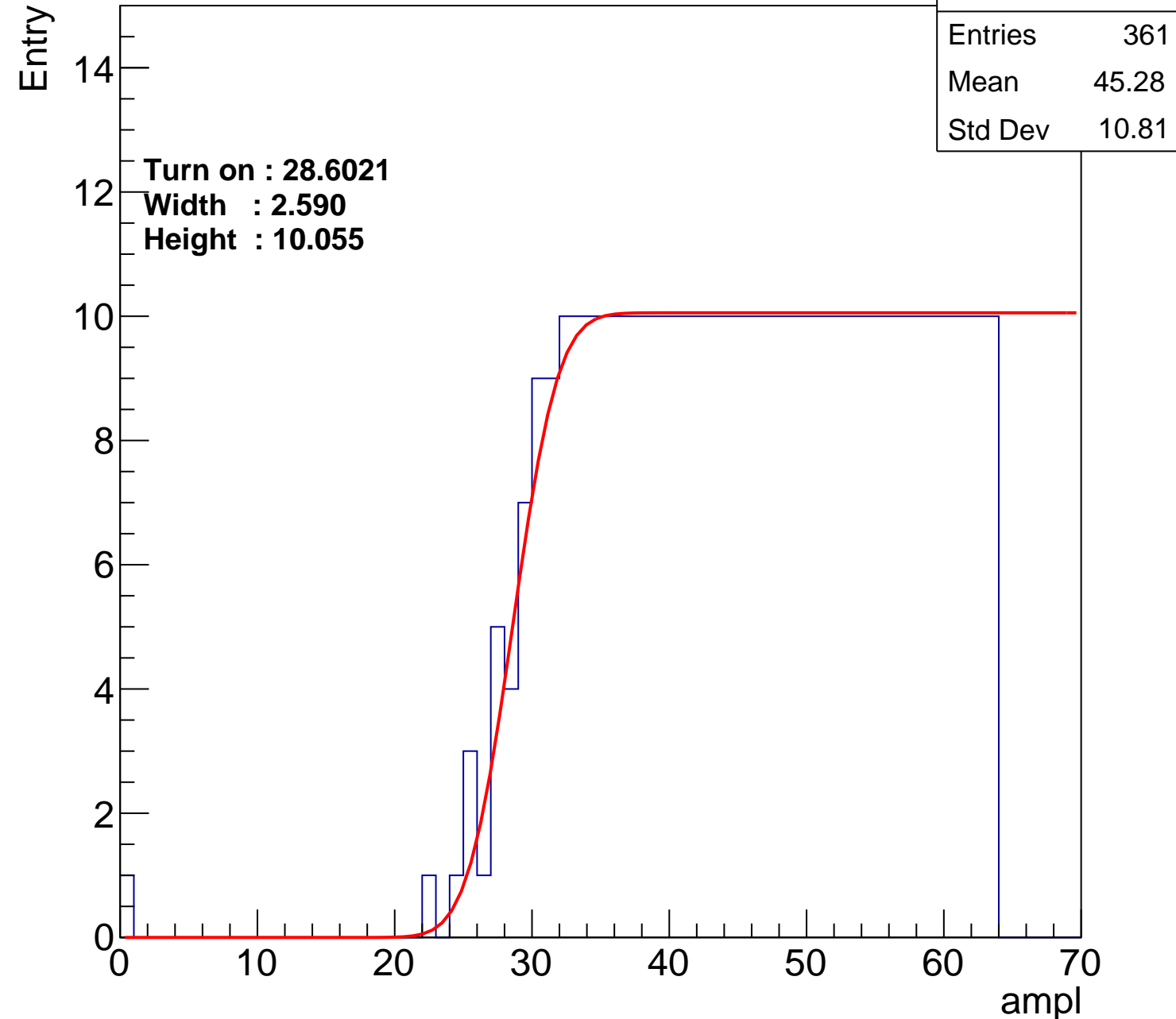
Width : 2.590

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.8914

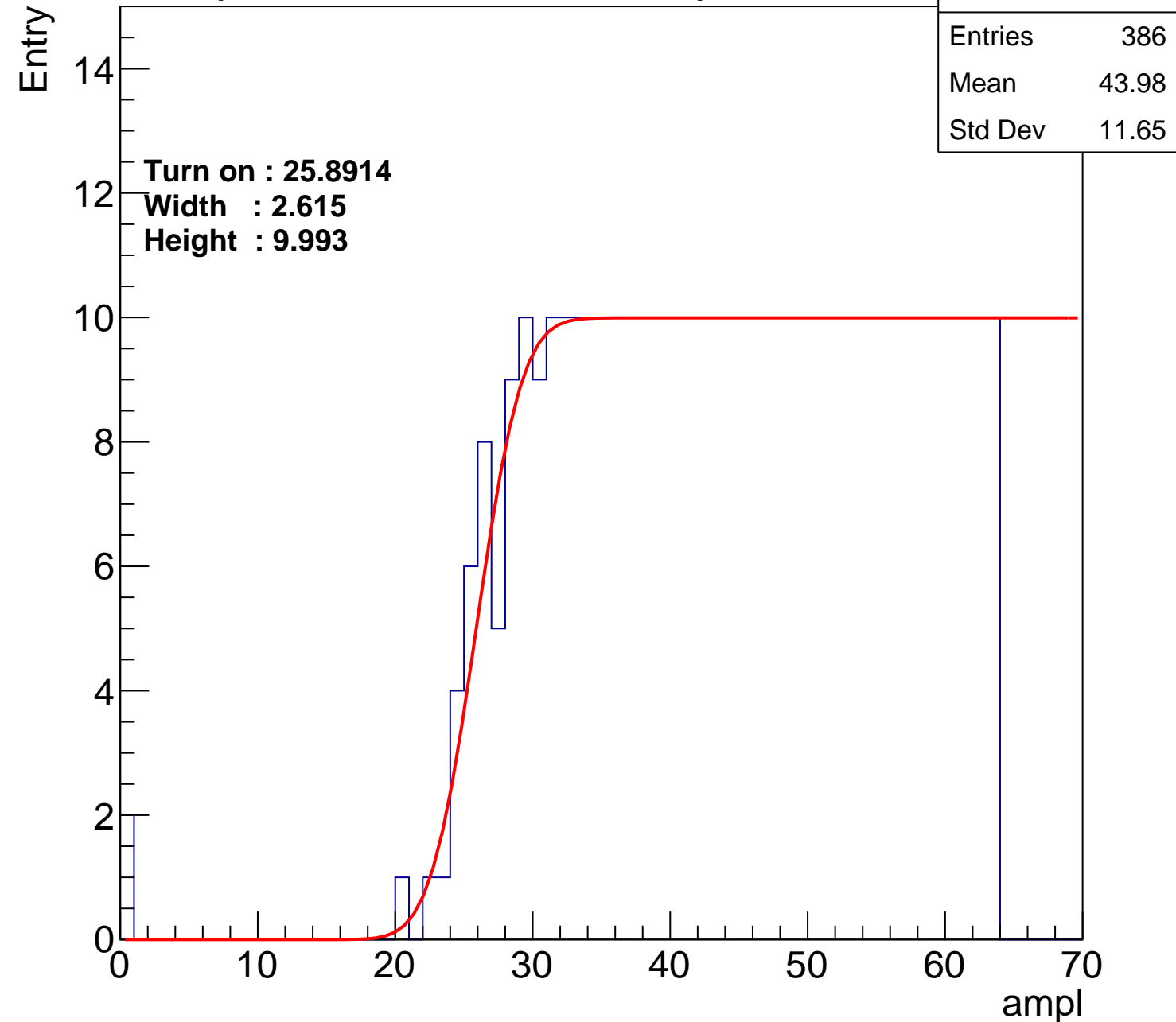
Width : 2.615

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.65
Std Dev	12.1

Turn on : 25.5013

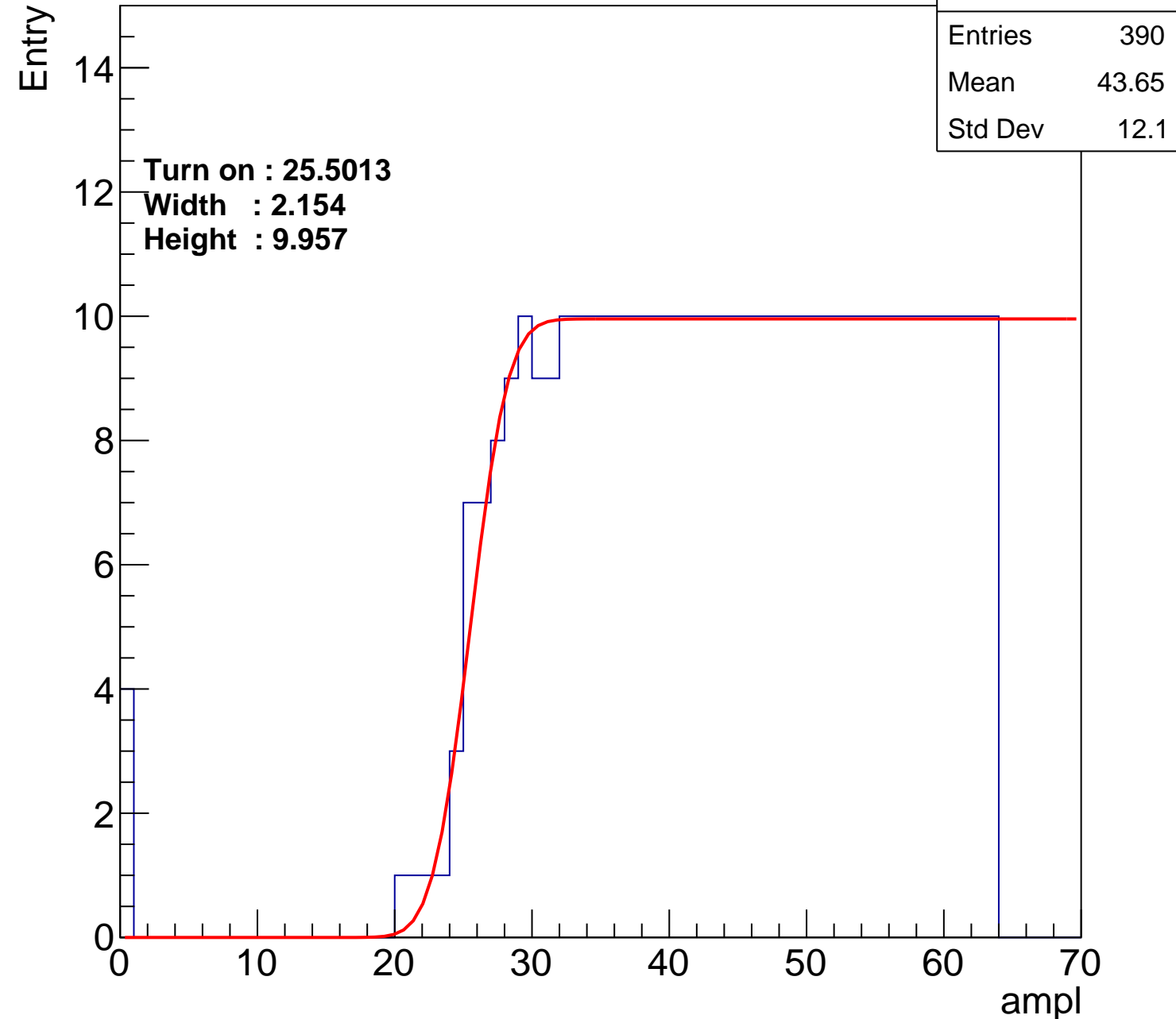
Width : 2.154

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.35
Std Dev	11.8

Turn on : 27.4487

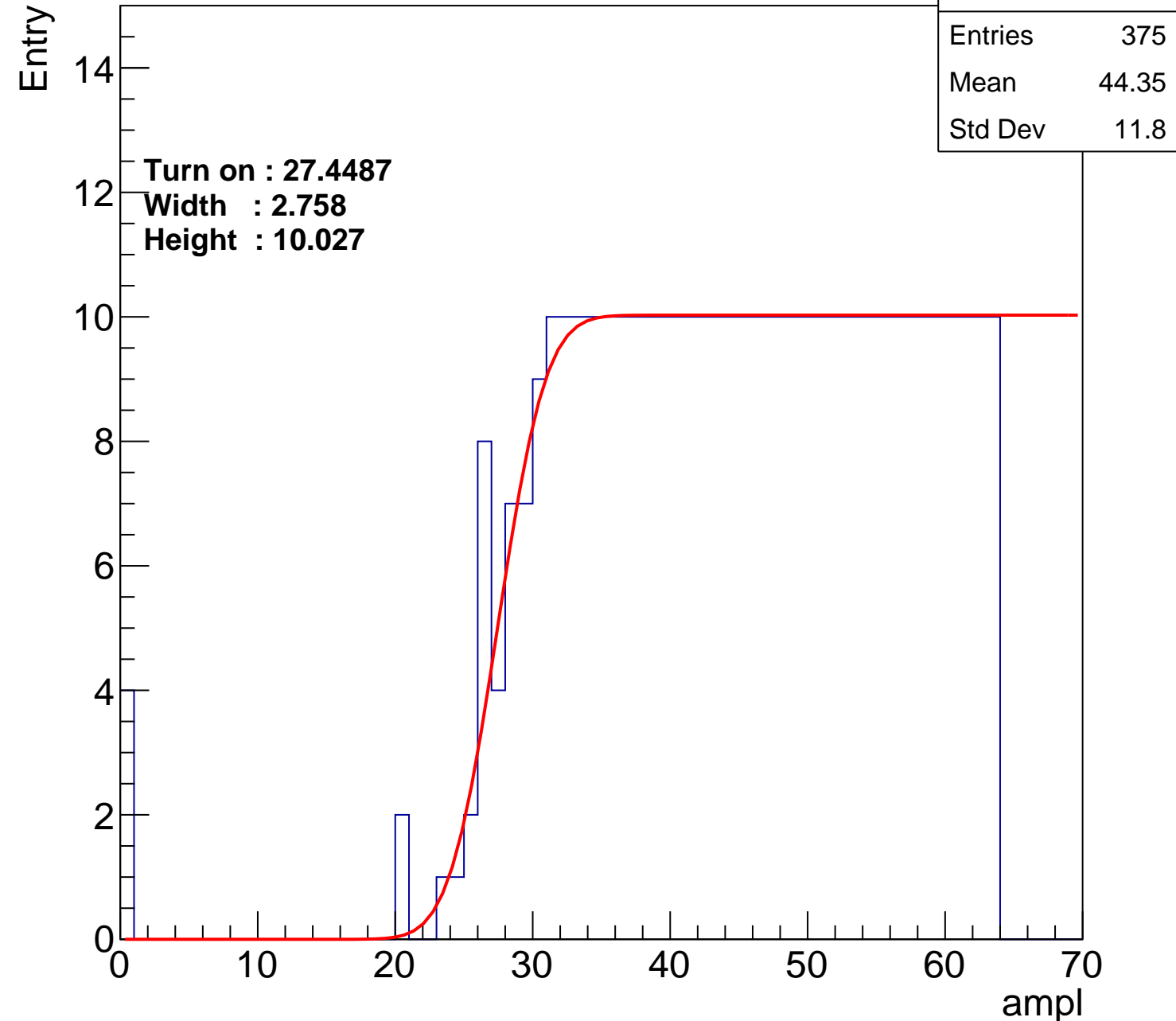
Width : 2.758

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.76
Std Dev	11.31

Turn on : 27.4865

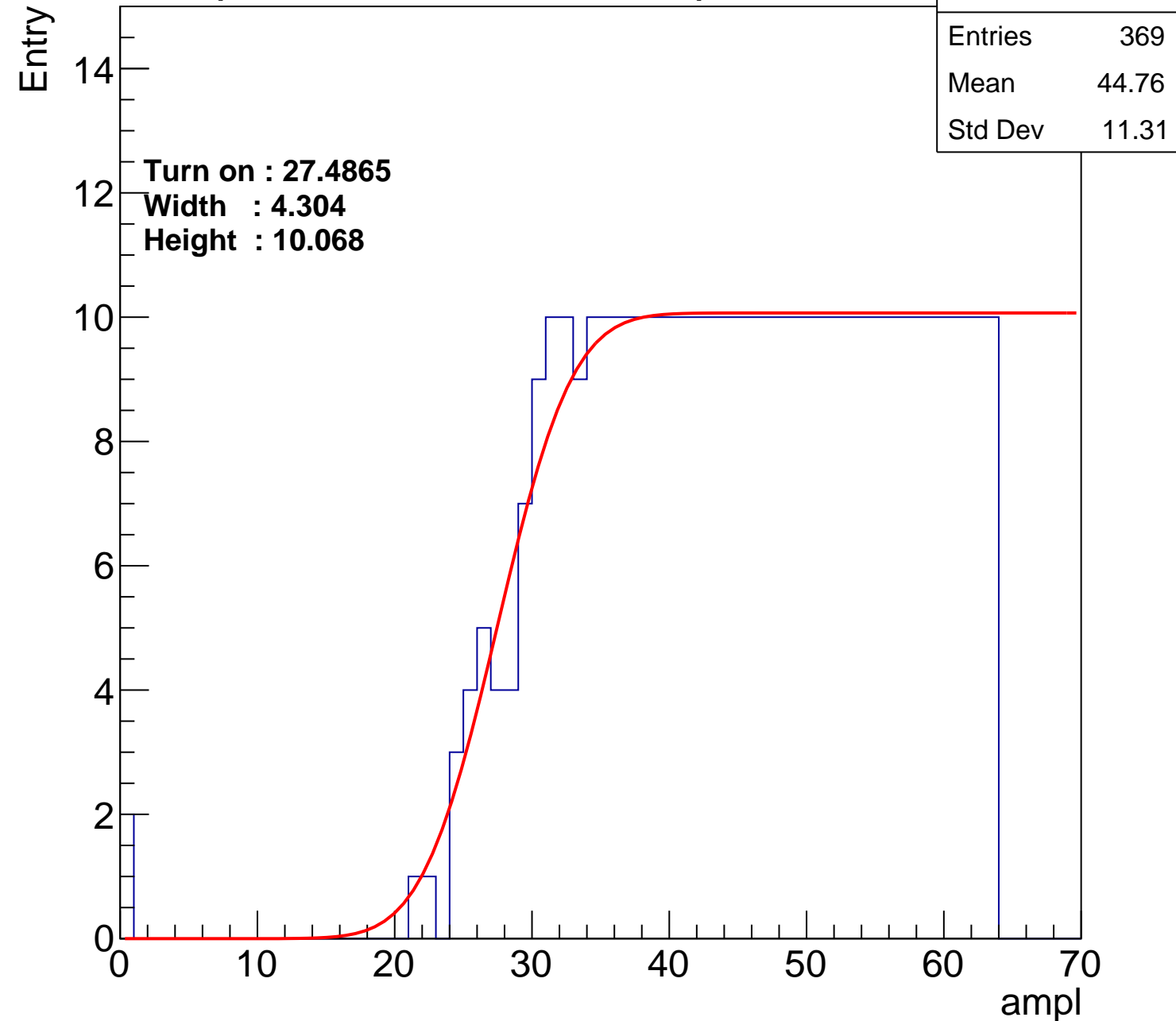
Width : 4.304

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	401
Mean	43.21
Std Dev	12.09

Turn on : 24.0554

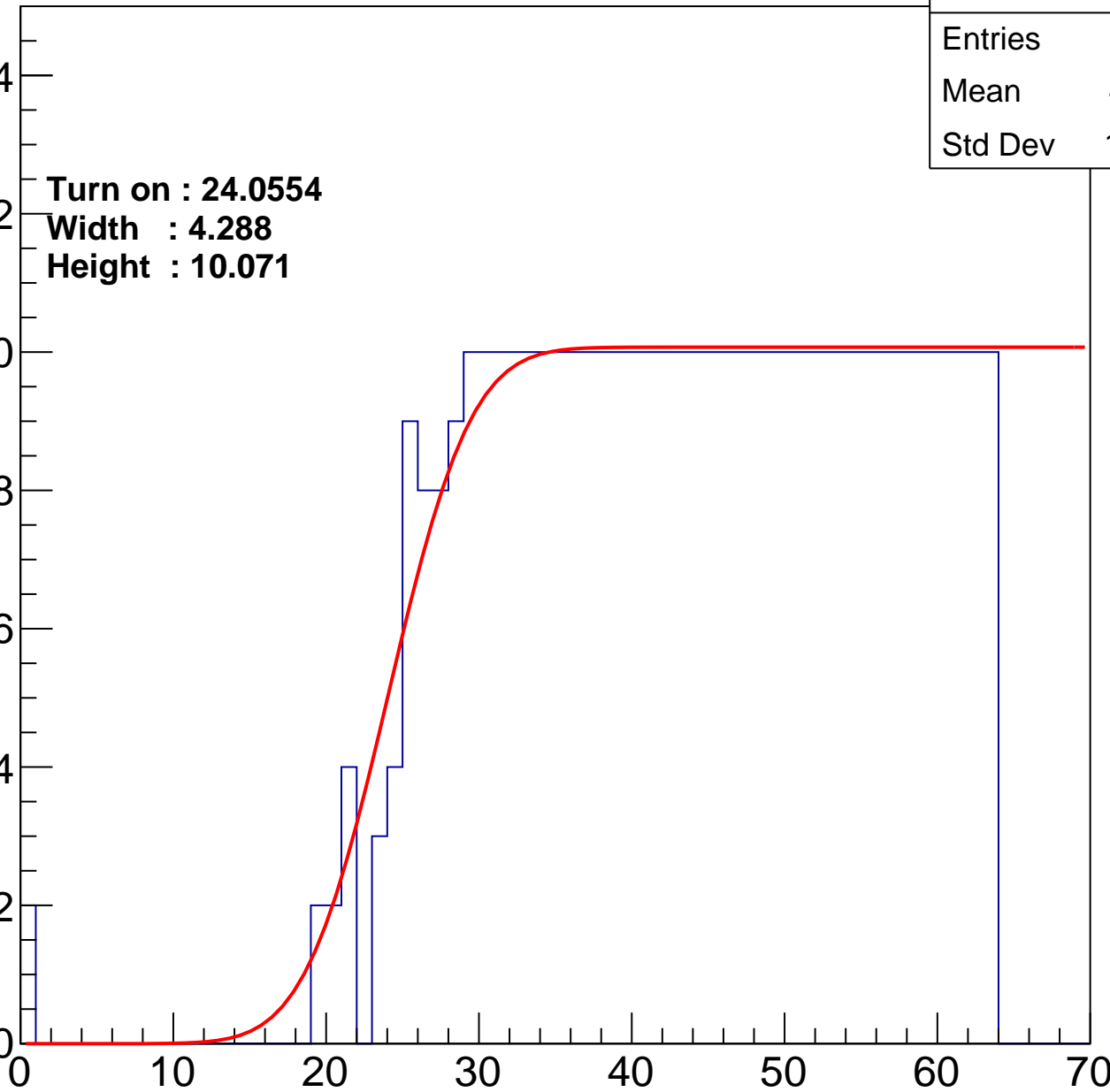
Width : 4.288

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 26.8881

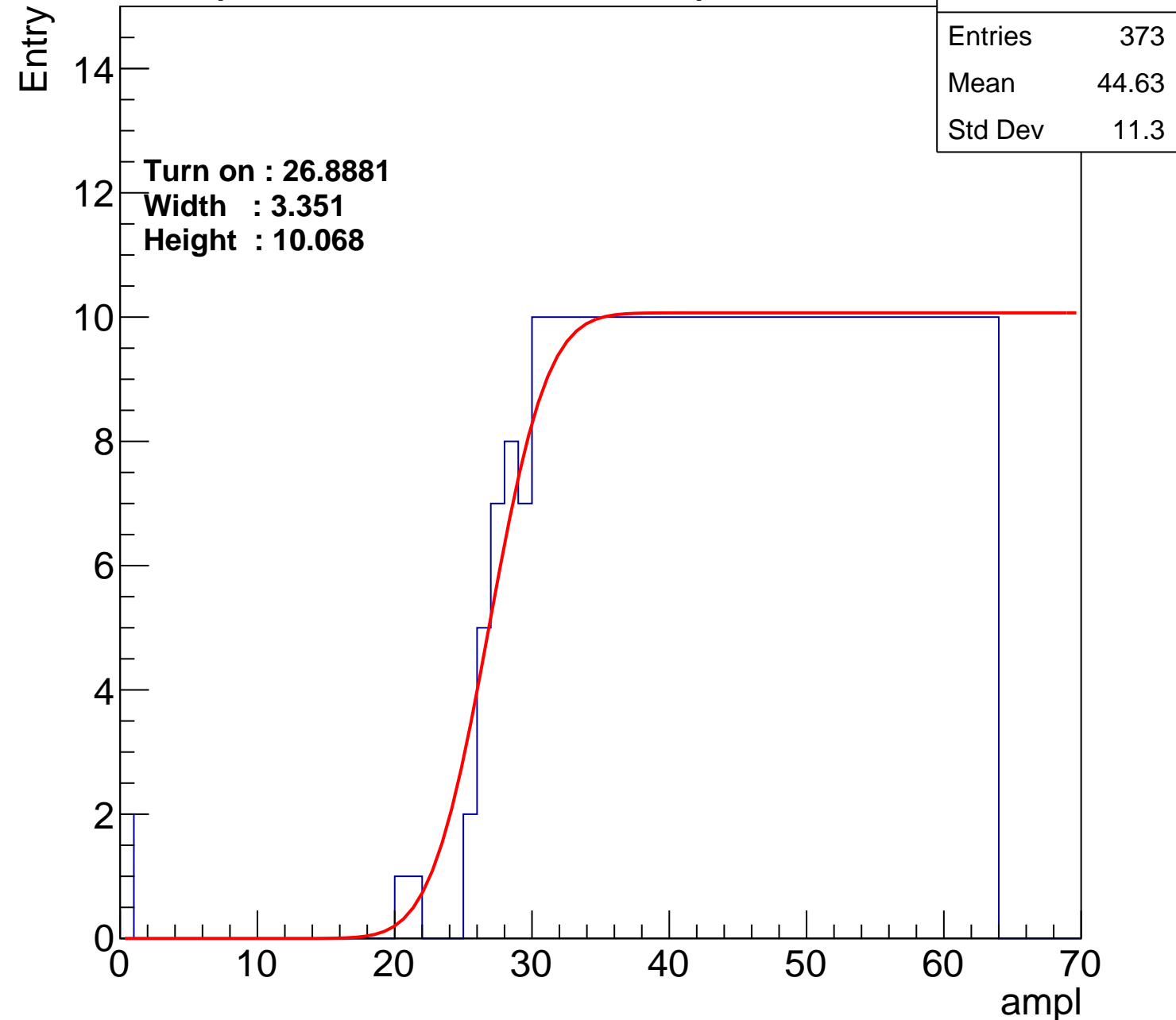
Width : 3.351

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.2
Std Dev	10.93

Turn on : 28.6485

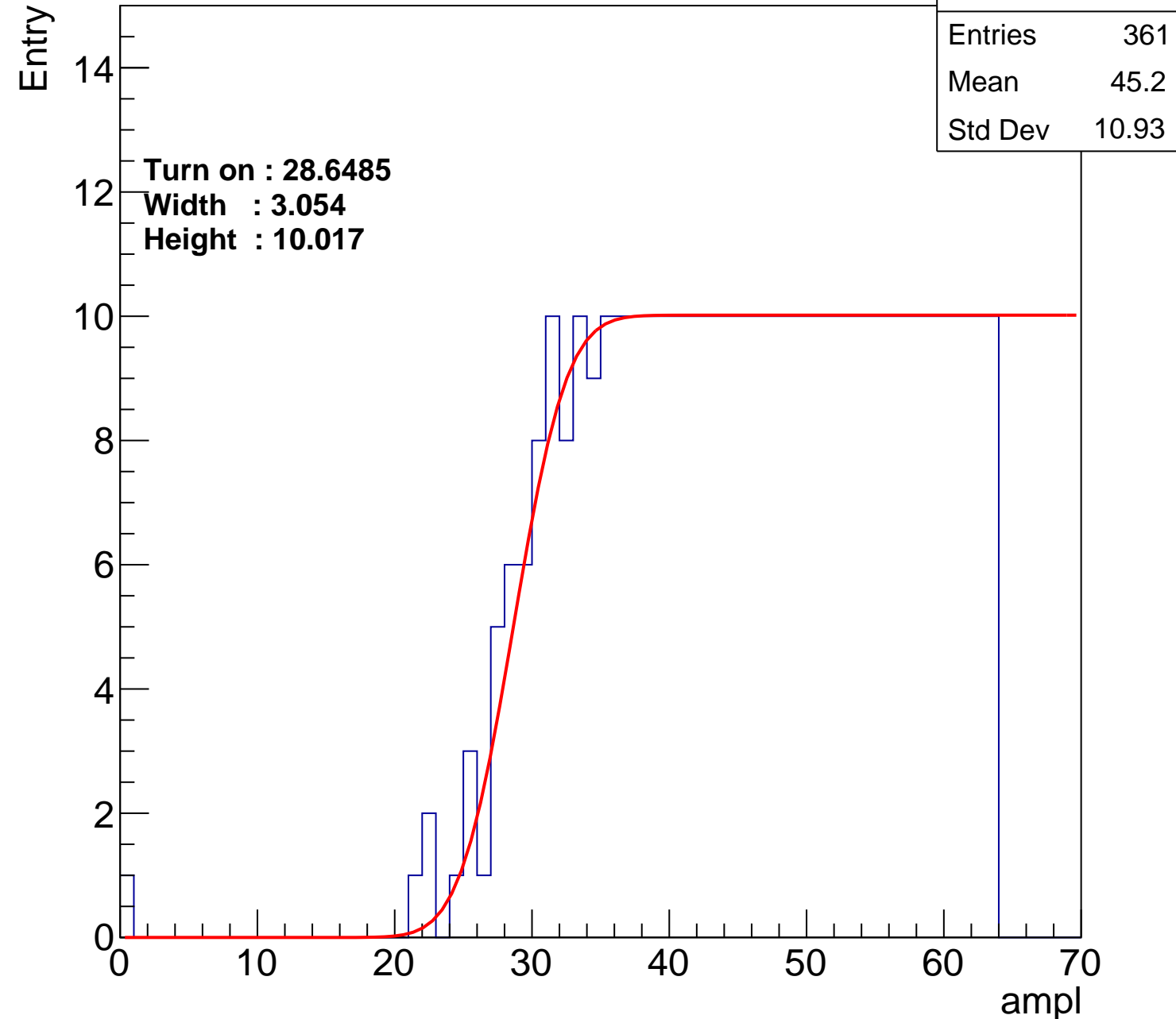
Width : 3.054

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.1
Std Dev	11.79

Turn on : 26.4170

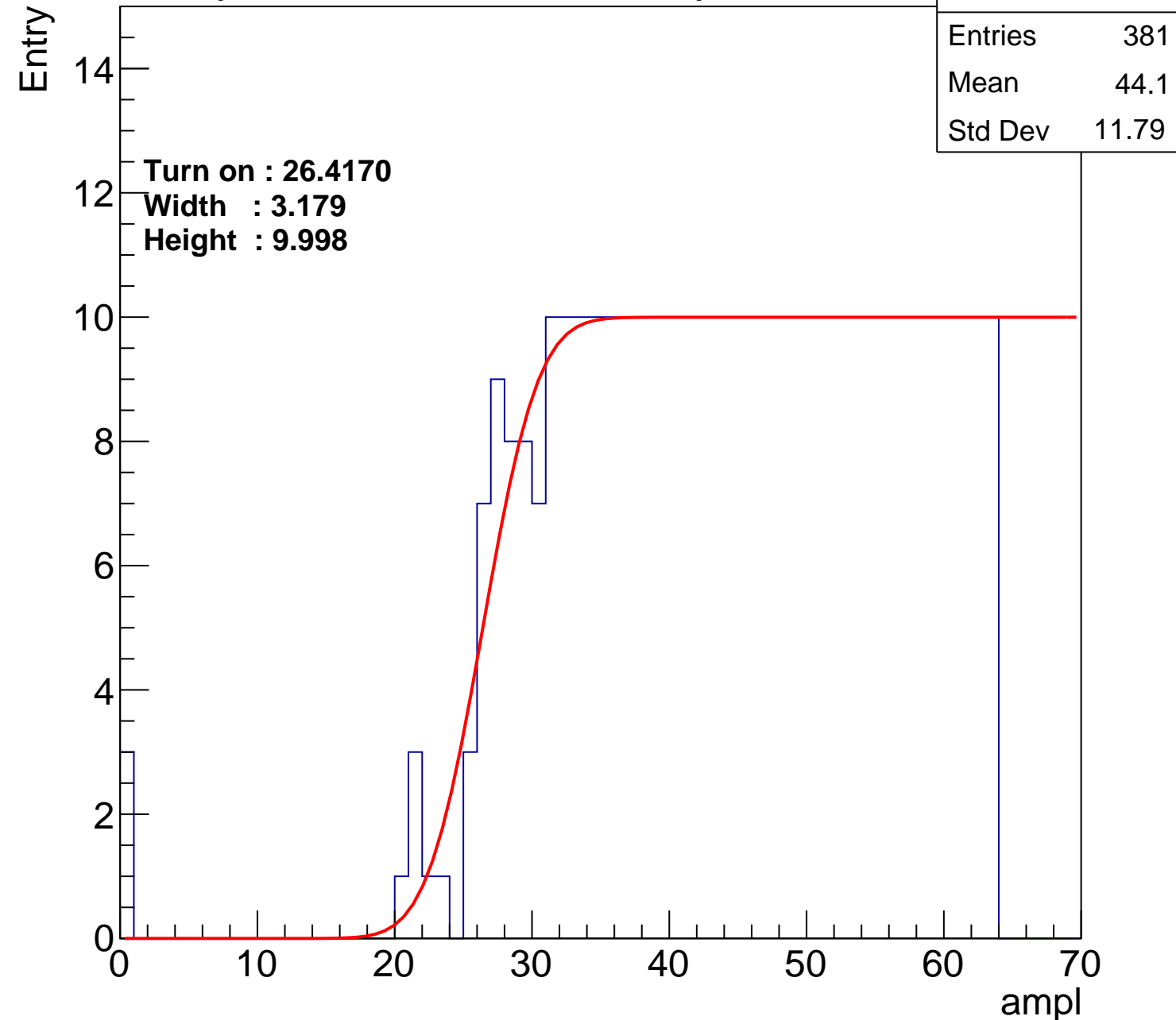
Width : 3.179

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.09
Std Dev	11.94

Turn on : 26.9776

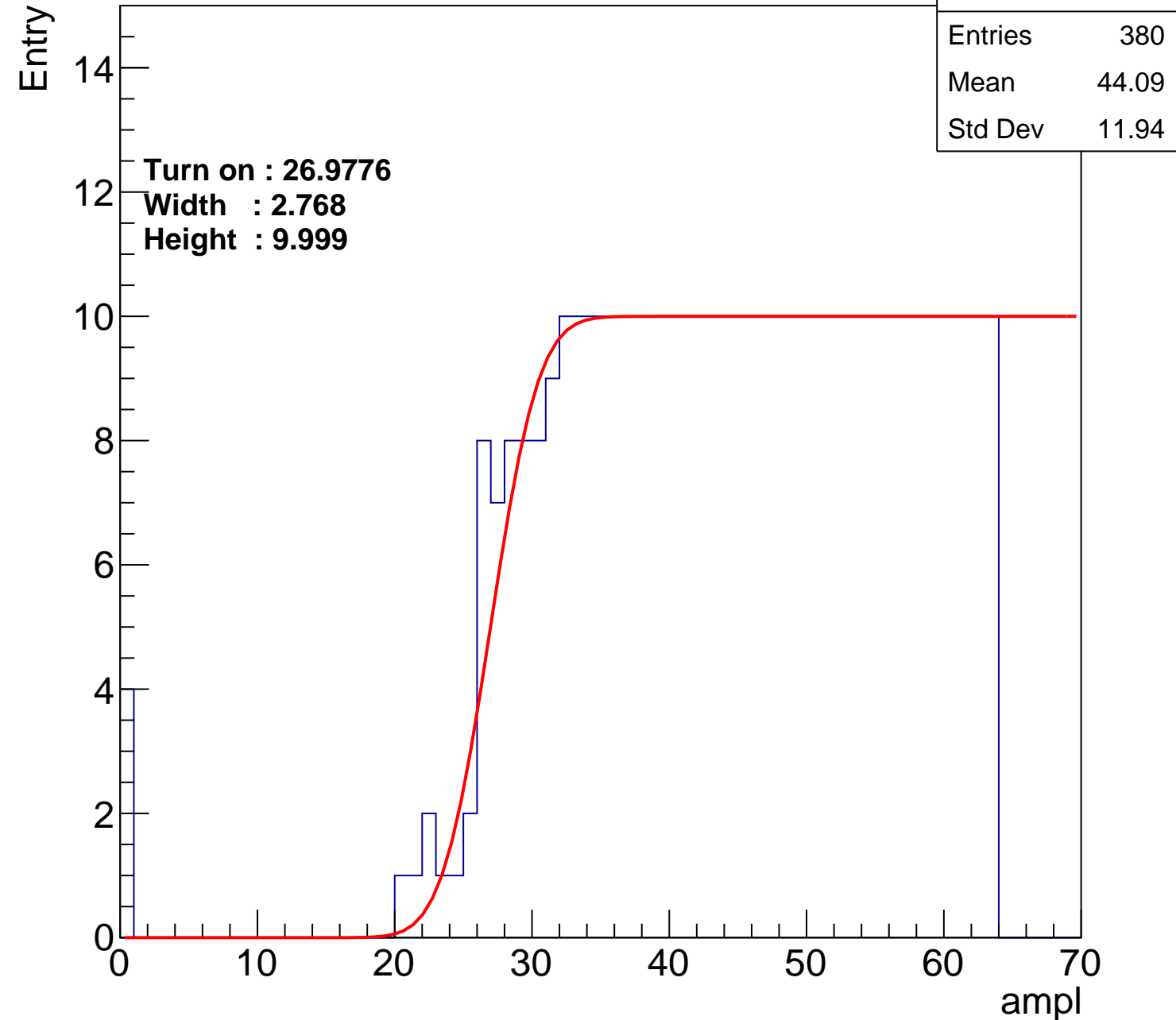
Width : 2.768

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.3630

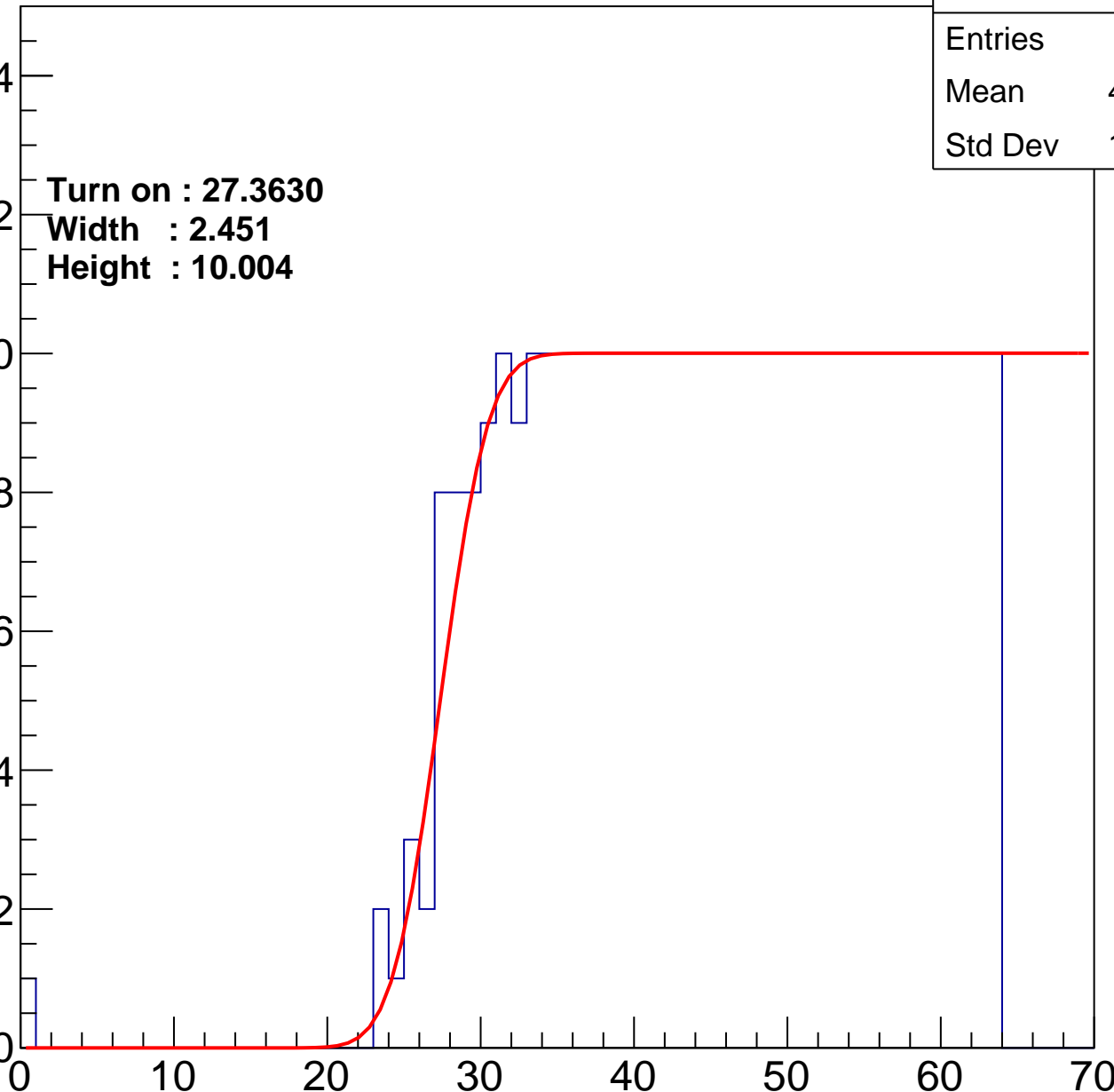
Width : 2.451

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.73
Std Dev	11.16

Turn on : 27.6287

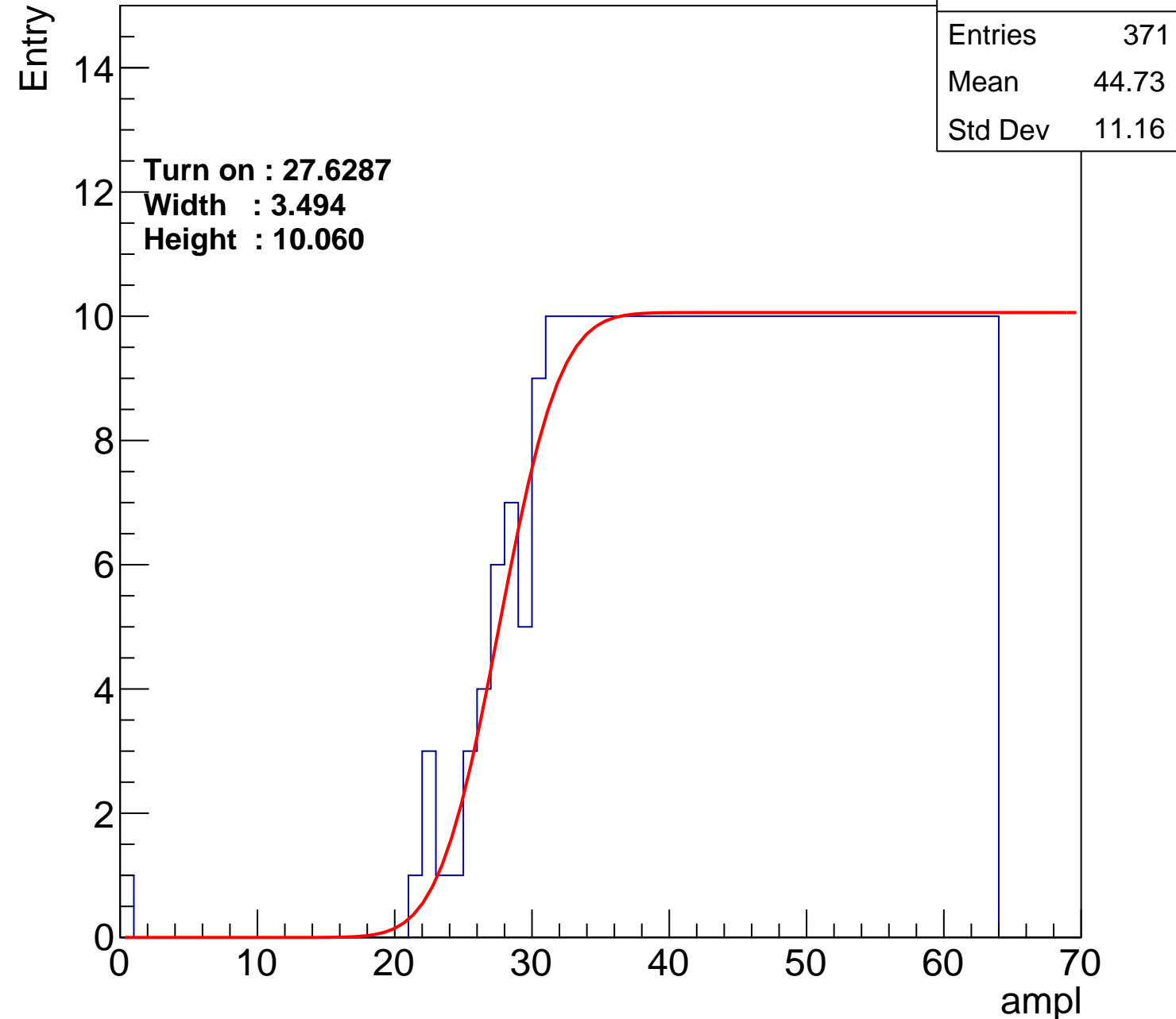
Width : 3.494

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.72
Std Dev	11.78

Turn on : 28.1244

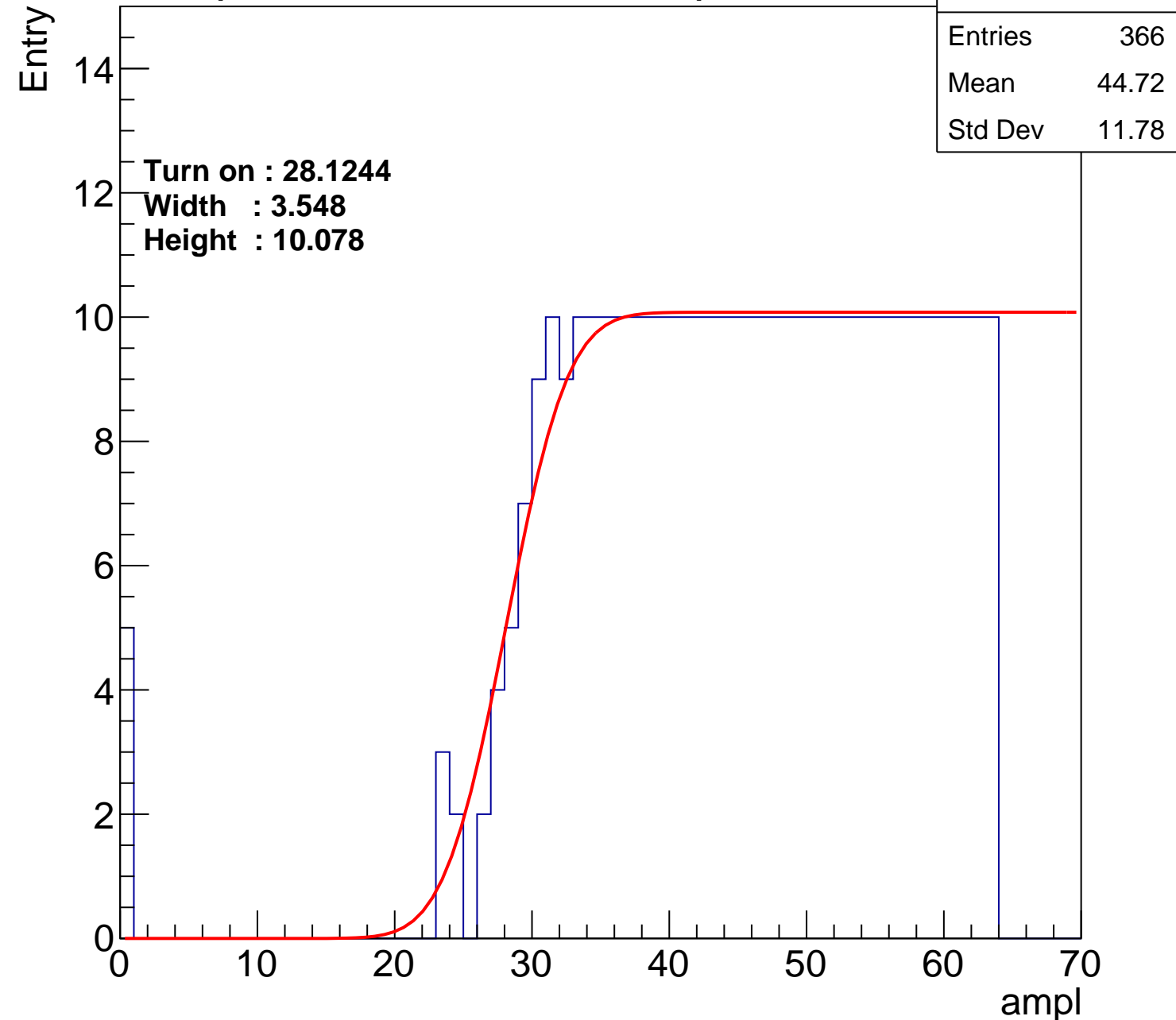
Width : 3.548

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch120

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.22
Std Dev	11.76

Turn on : 26.9976

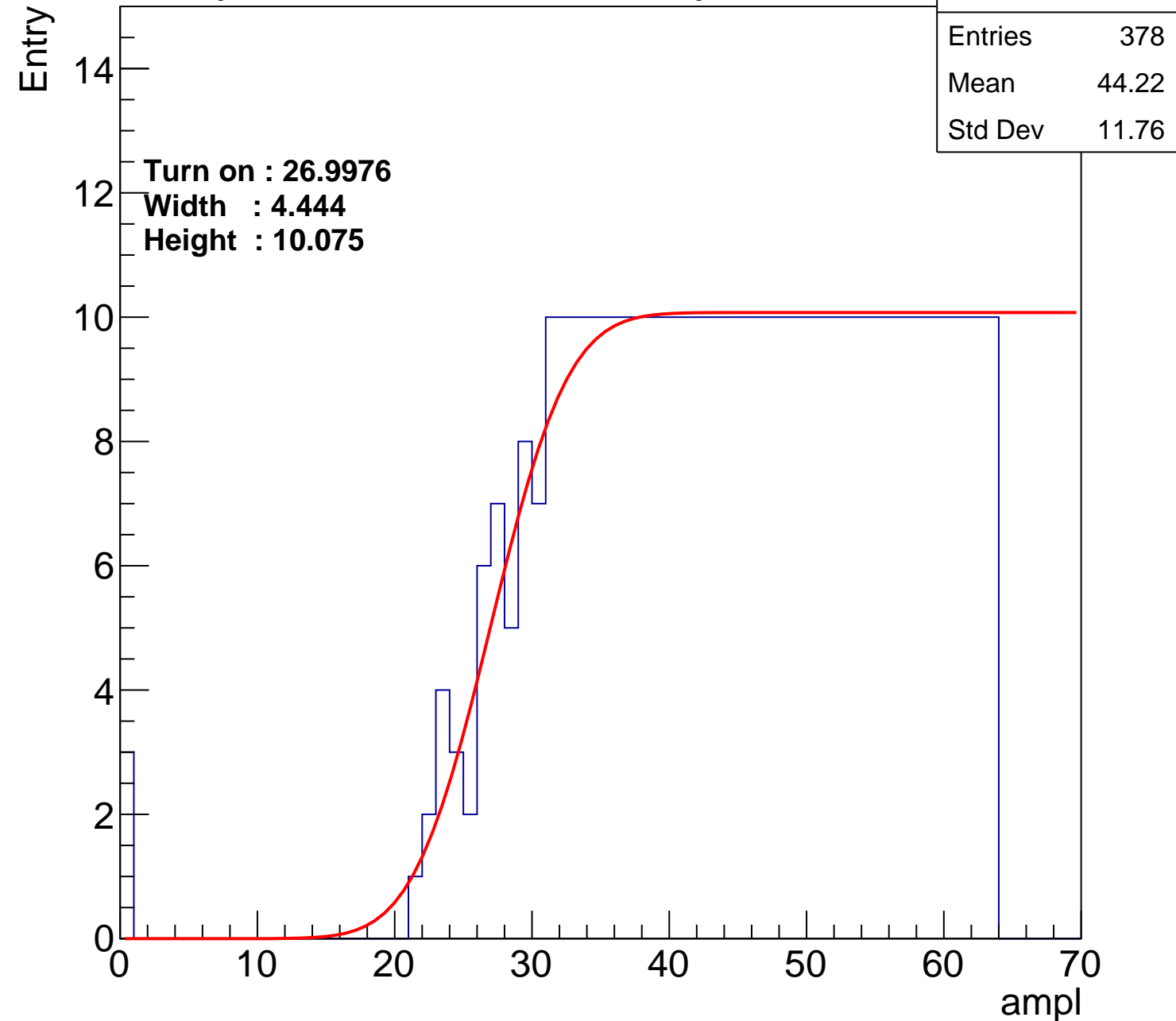
Width : 4.444

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.96
Std Dev	11.98

Turn on : 26.0603

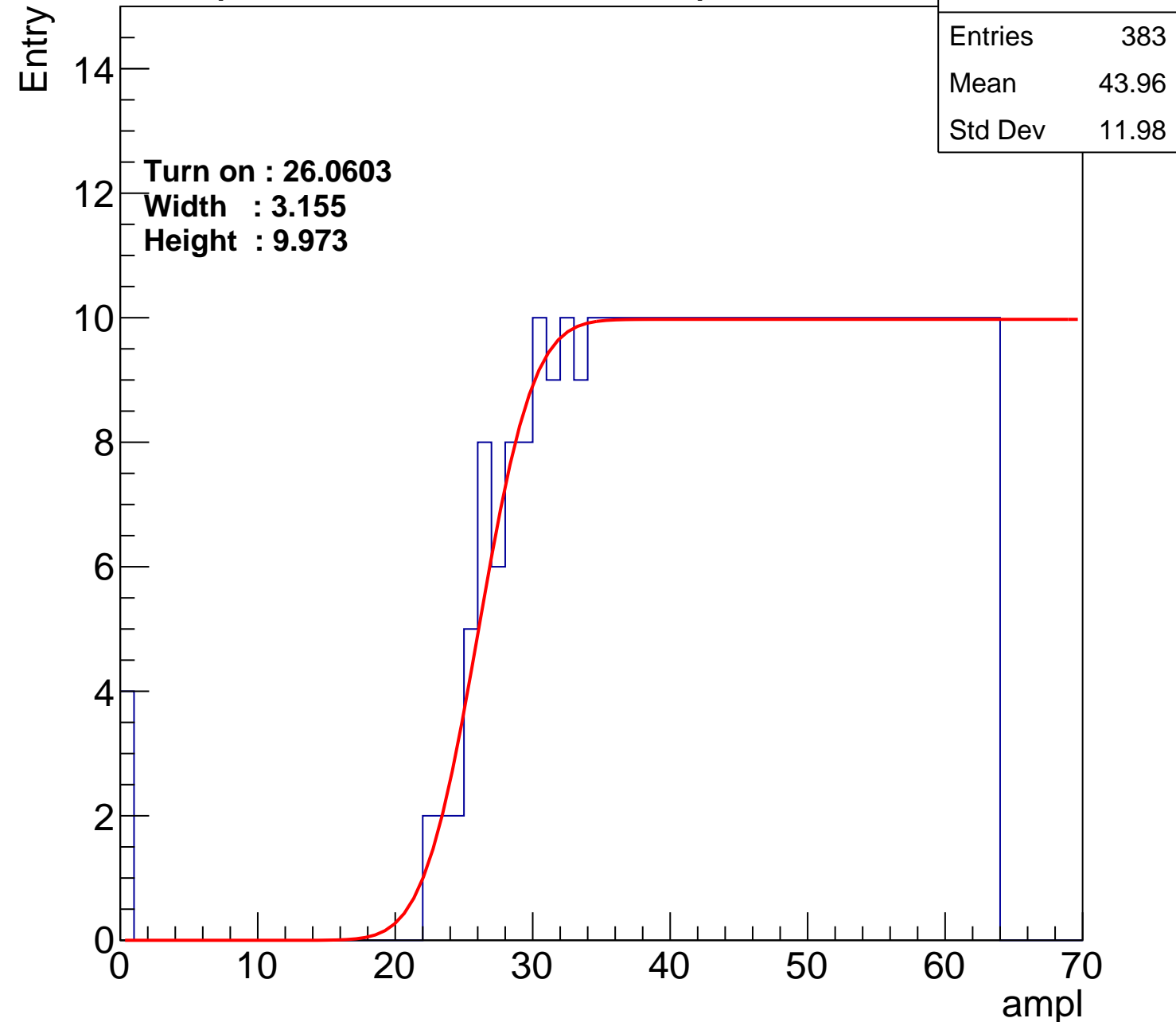
Width : 3.155

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.25
Std Dev	11.84

Turn on : 27.2061

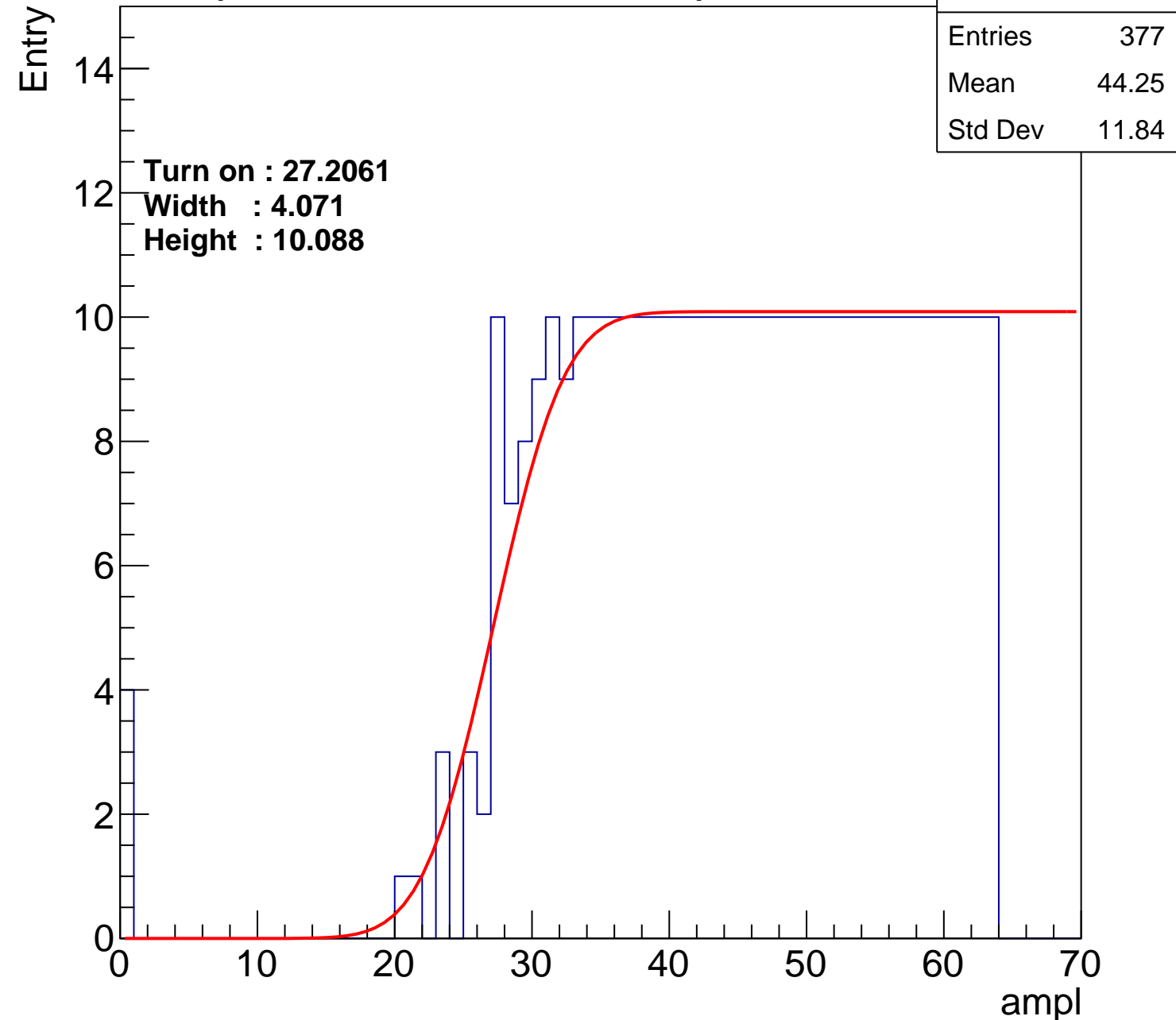
Width : 4.071

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch123

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.15
Std Dev	11.94

Turn on : 26.9984

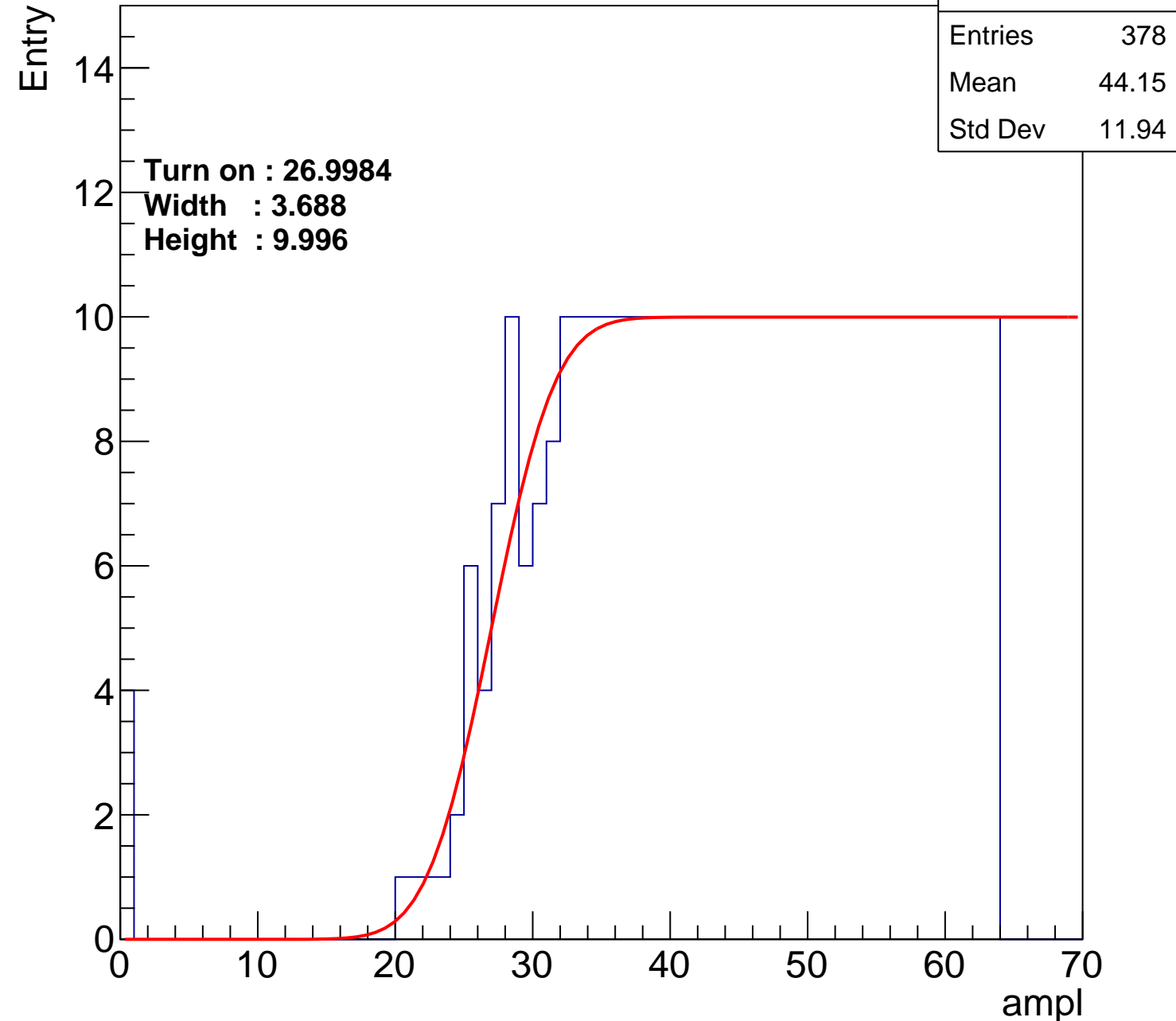
Width : 3.688

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.4
Std Dev	11.65

Turn on : 27.3915

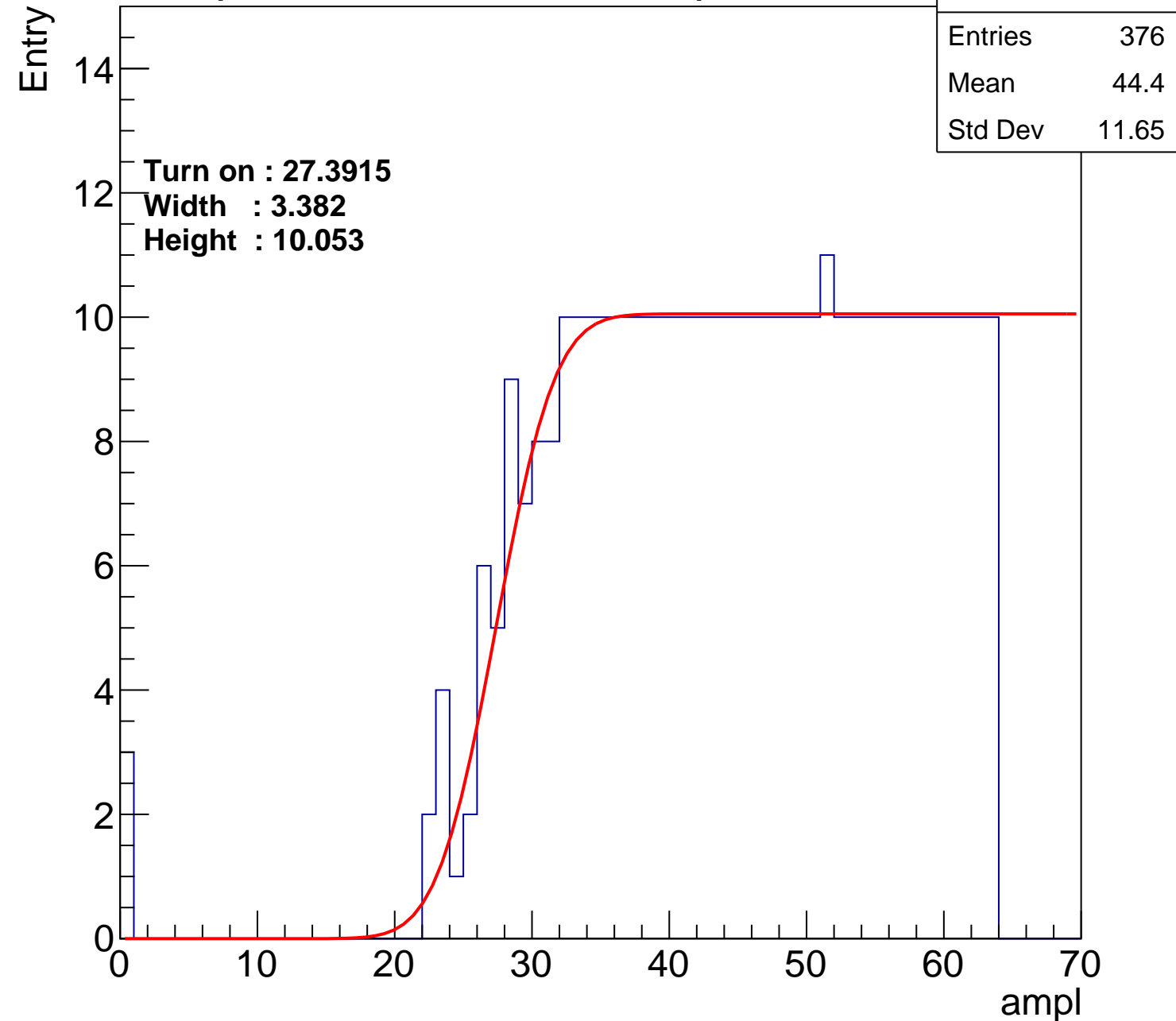
Width : 3.382

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch125

calib_packv5_042523_0143.root, FC#7, port C2

Entries	391
Mean	43.71
Std Dev	11.82

Turn on : 25.3217

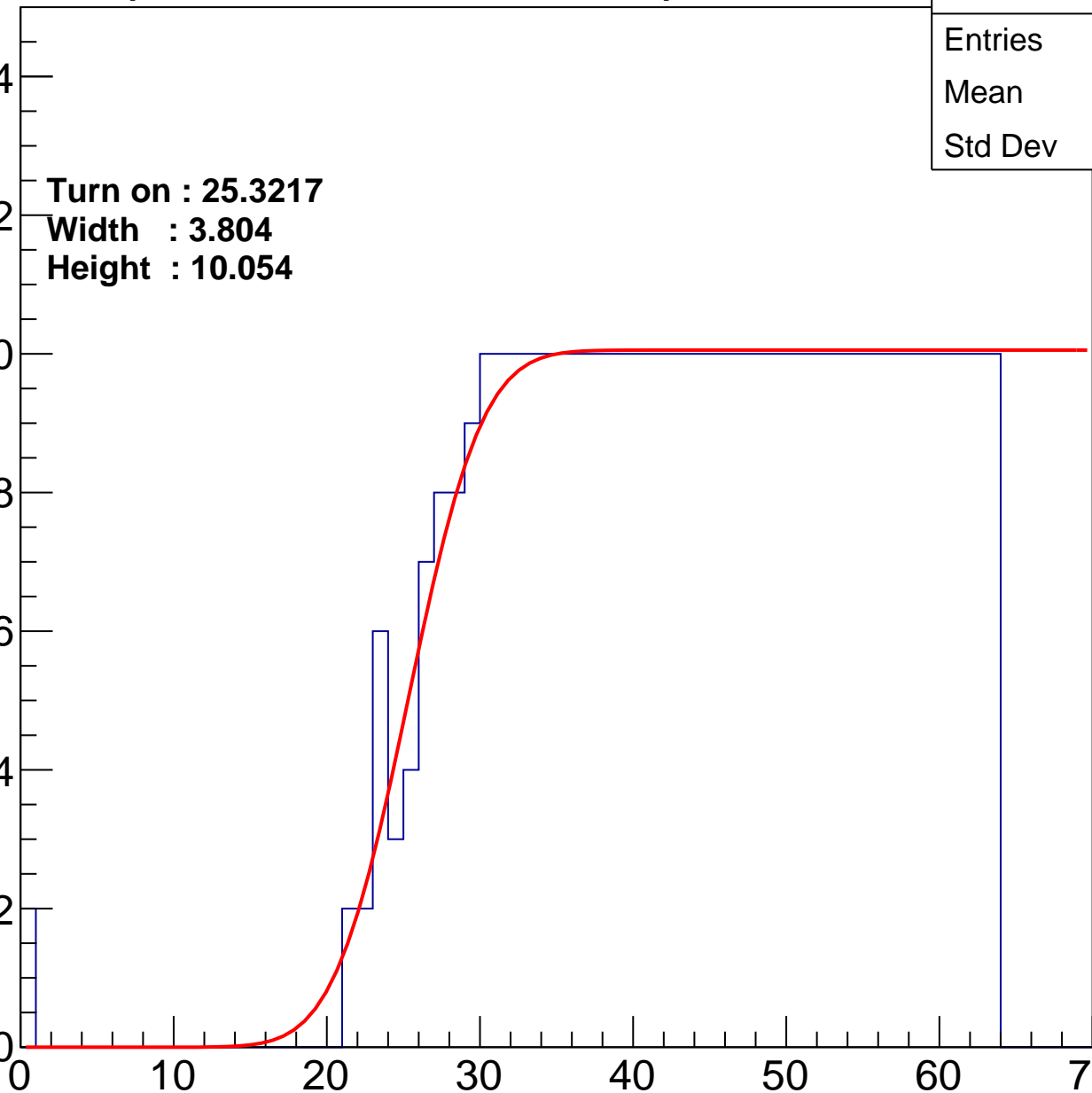
Width : 3.804

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch126

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.32
Std Dev	12.35

Turn on : 25.1037

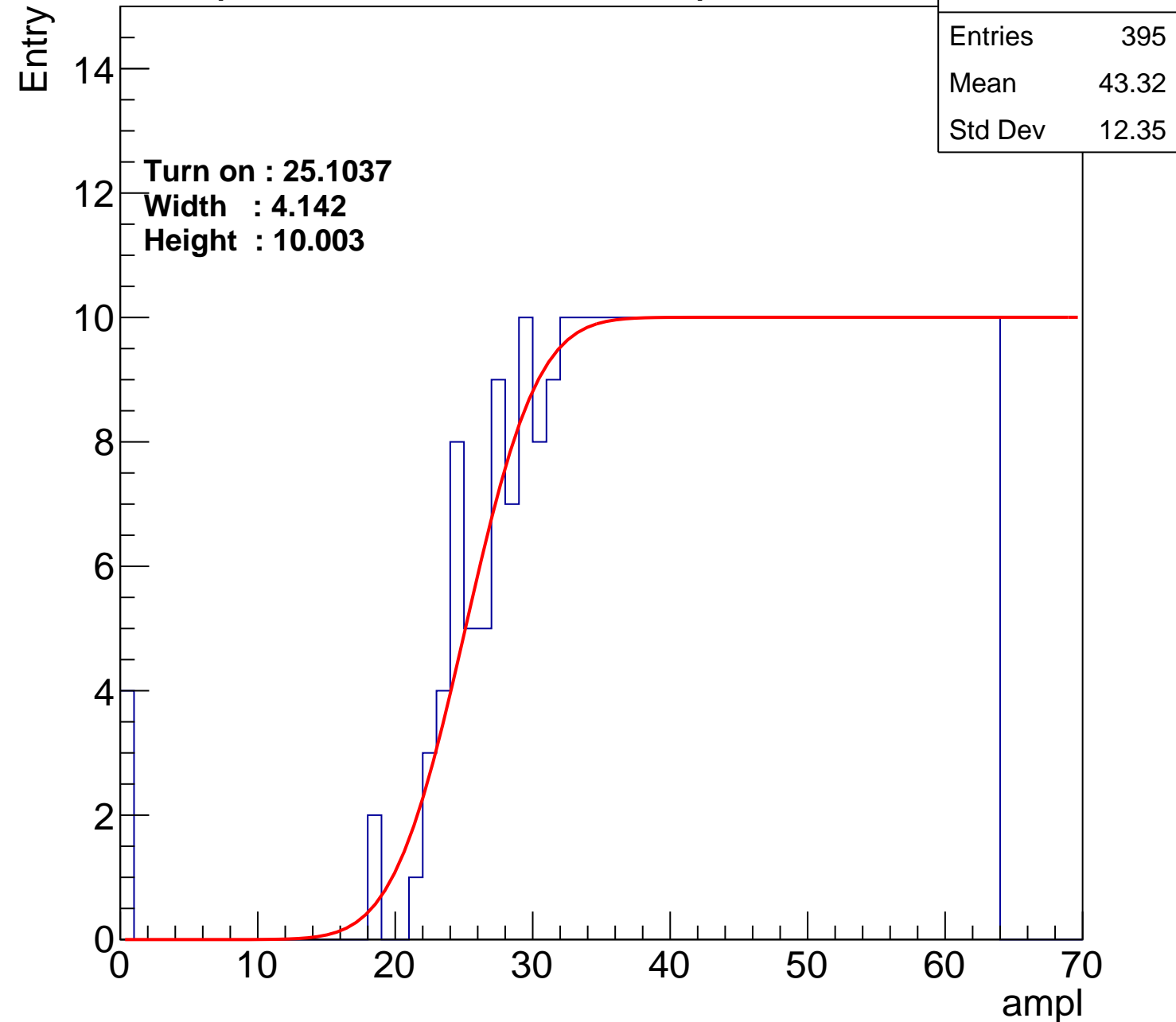
Width : 4.142

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.37
Std Dev	11.33

Turn on : 26.6251

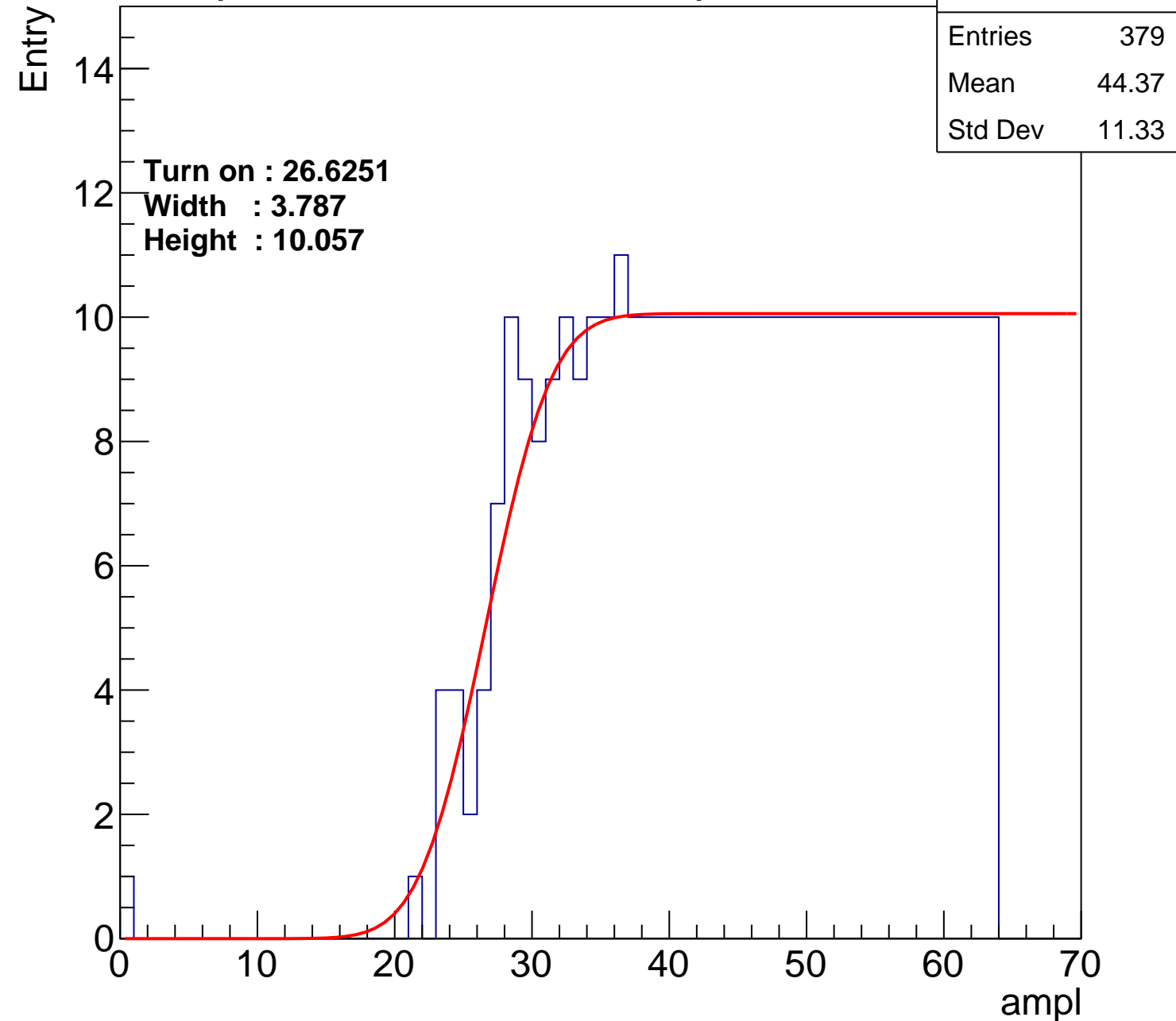
Width : 3.787

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U13-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.37
Std Dev	11.33

Turn on : 26.6251

Width : 3.787

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

