

B0L103S, U13-ch0

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.24
Std Dev	10.86

Turn on : 51.5230

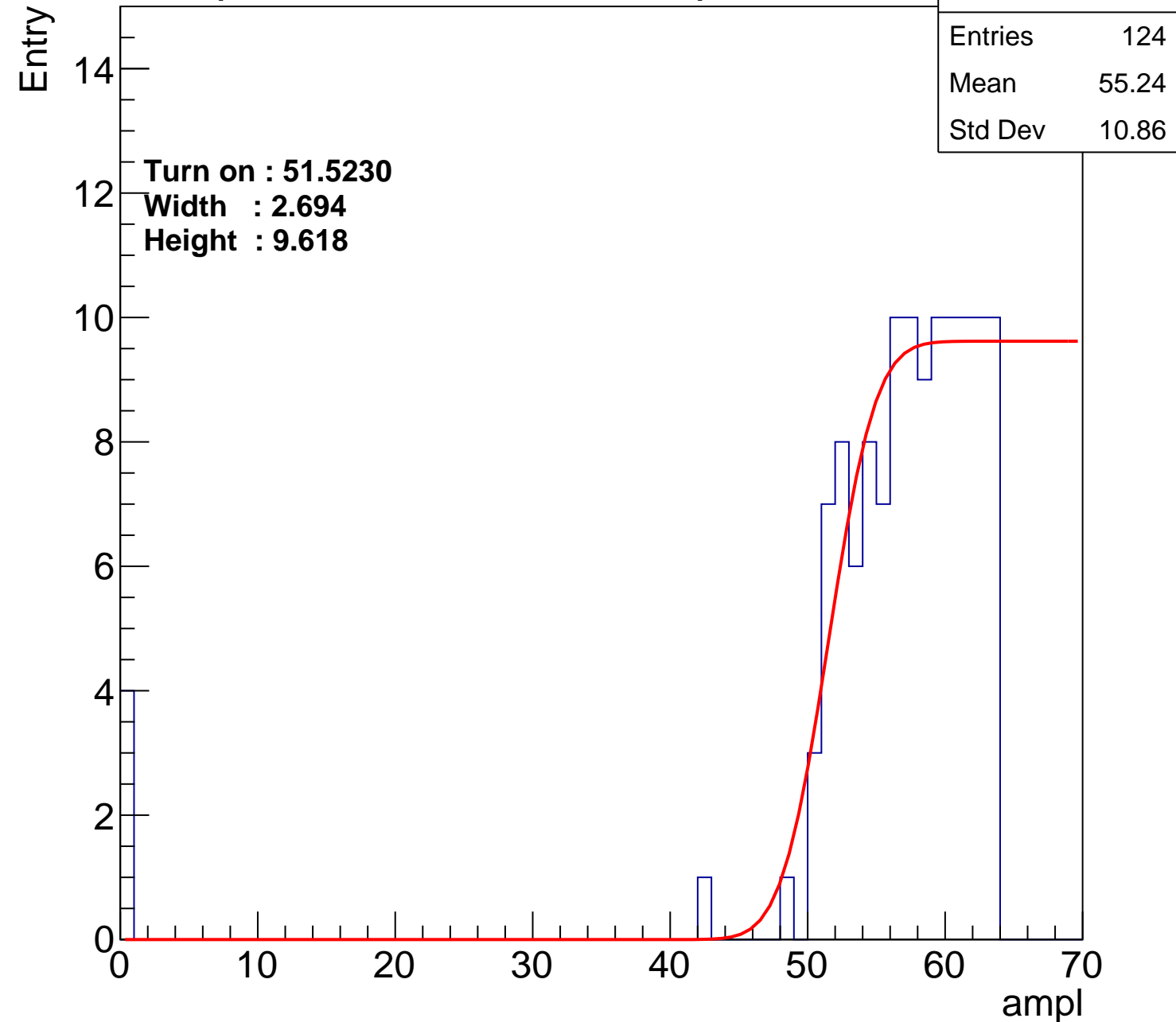
Width : 2.694

Height : 9.618

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch1

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	55.17
Std Dev	12.48

Turn on : 54.6399

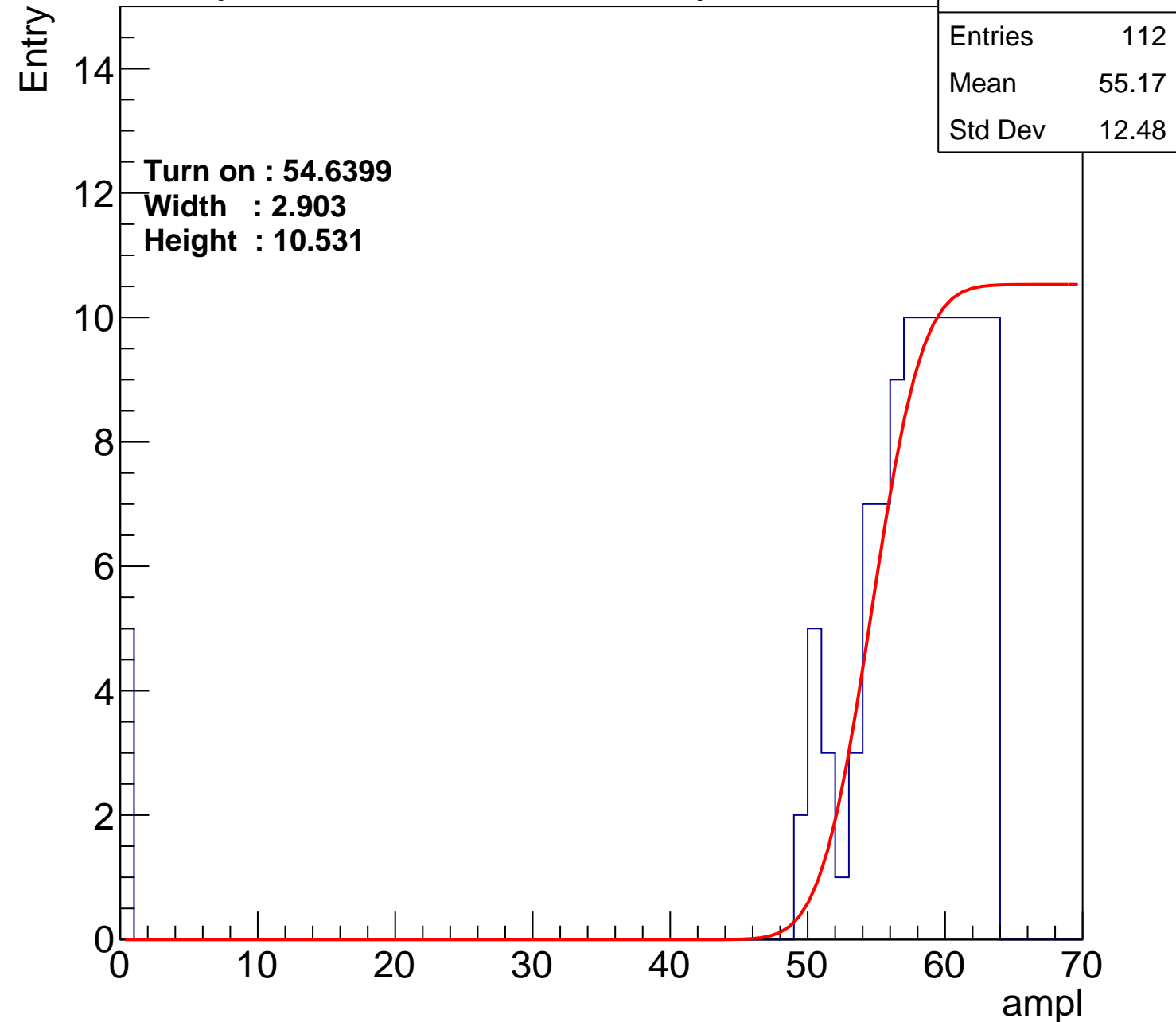
Width : 2.903

Height : 10.531

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch2

calib_packv5_040323_1717.root, FC#2, port C3

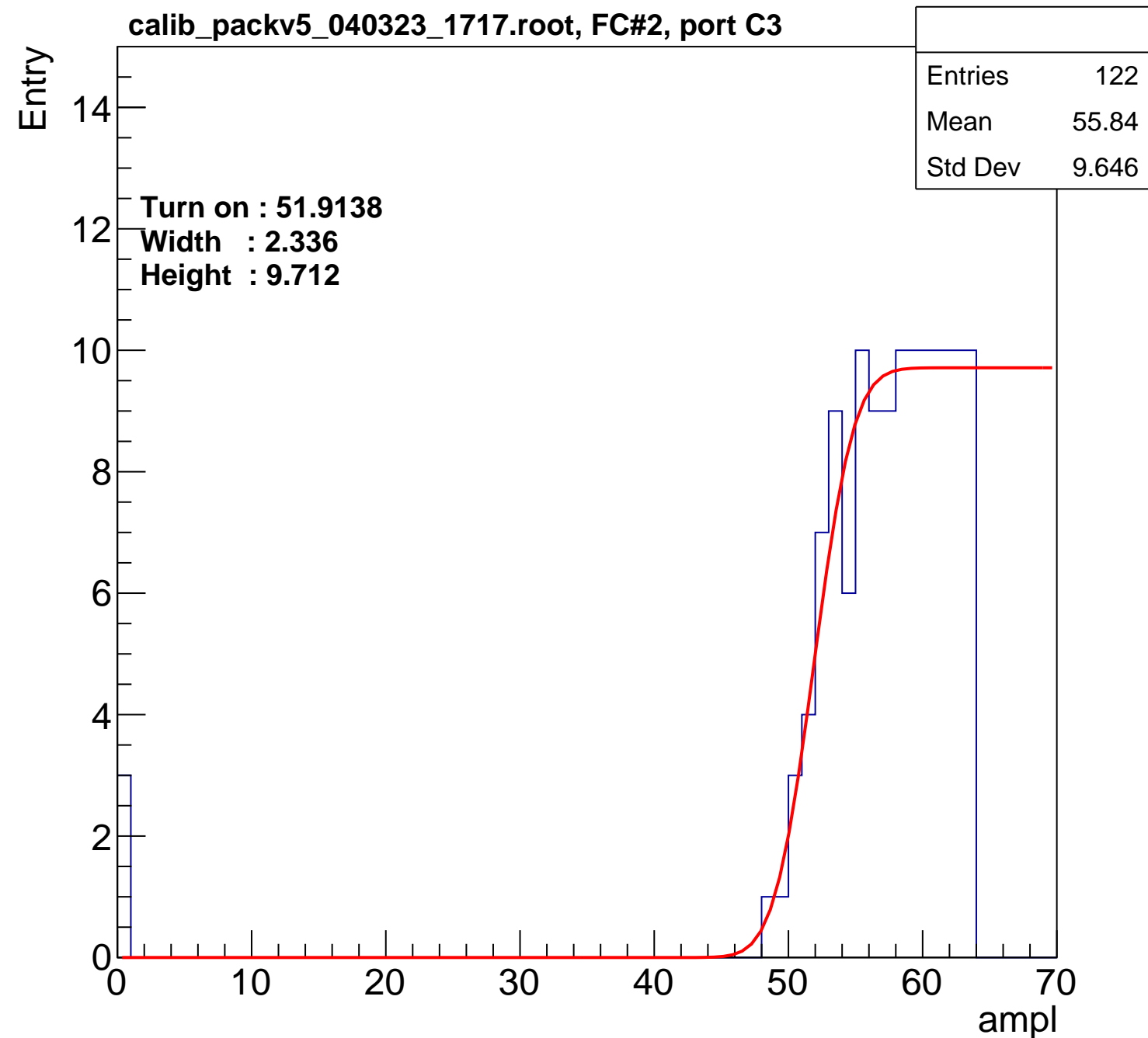
Entry

14
12
10
8
6
4
2
0

Turn on : 51.9138
Width : 2.336
Height : 9.712

Entries	122
Mean	55.84
Std Dev	9.646

ampl



B0L103S, U13-ch3

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	56.29
Std Dev	6.558

Turn on : 51.9315

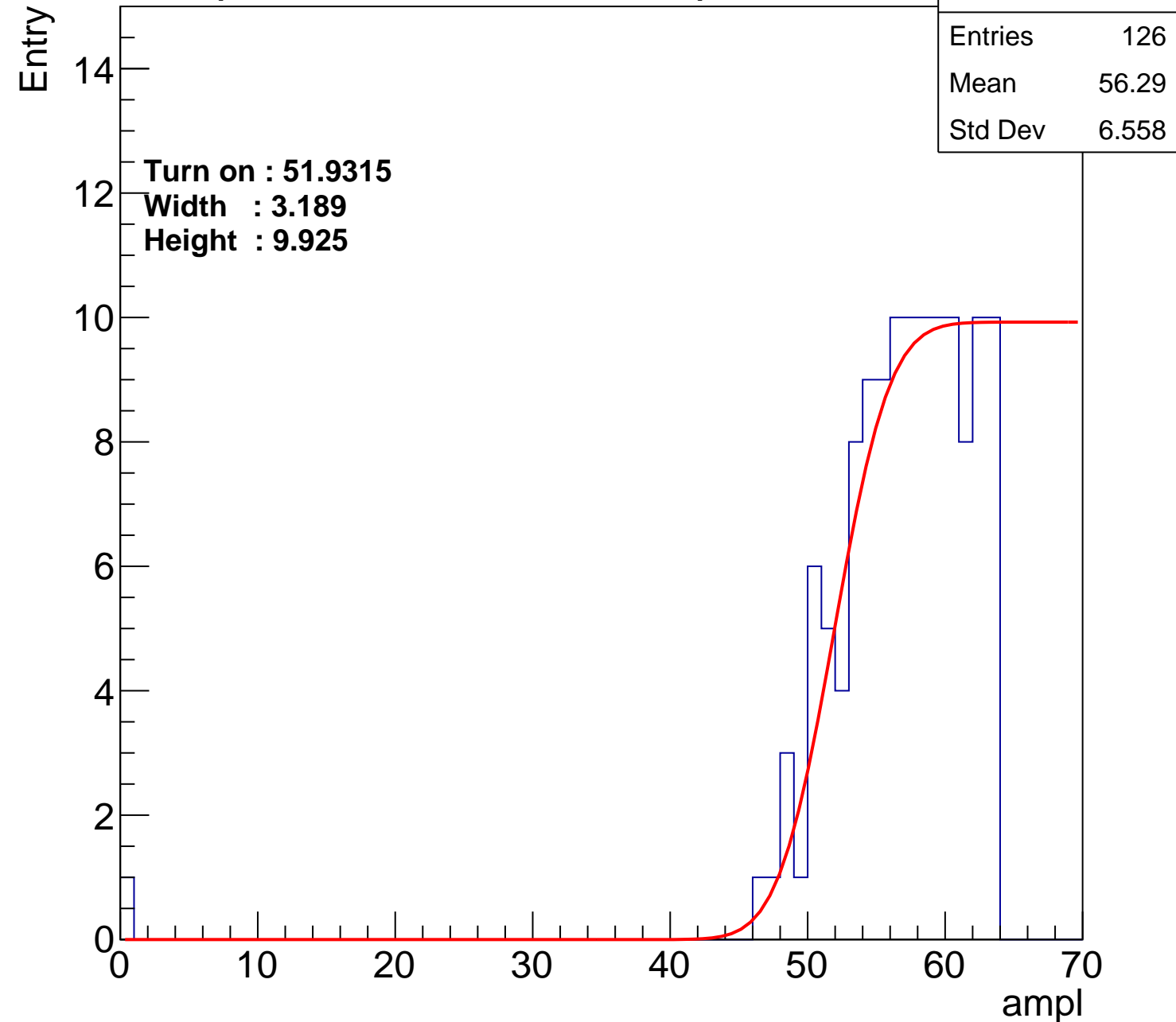
Width : 3.189

Height : 9.925

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch4

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.78
Std Dev	11.55

Turn on : 50.7773

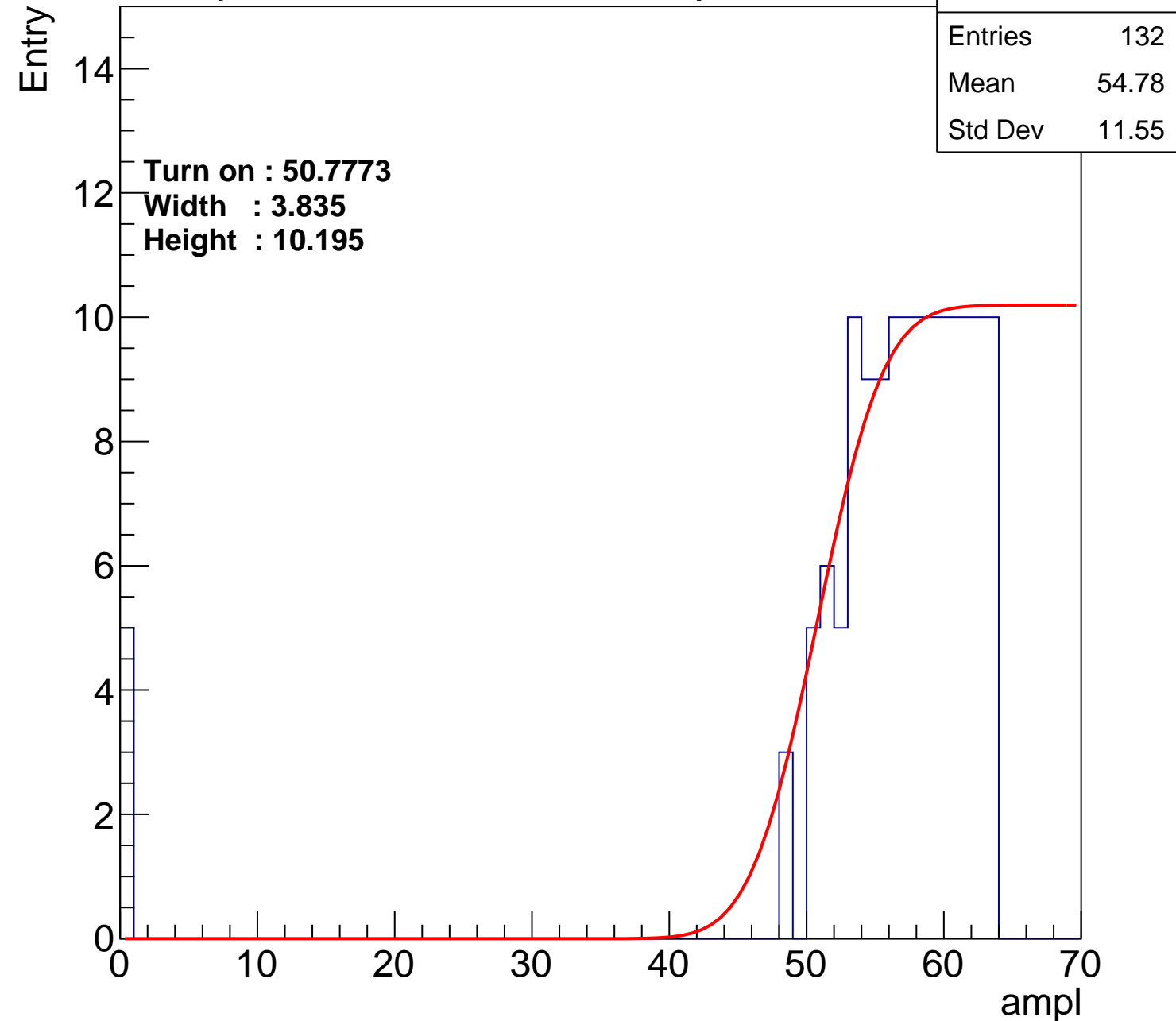
Width : 3.835

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch5

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.85
Std Dev	9.652

Turn on : 51.7735

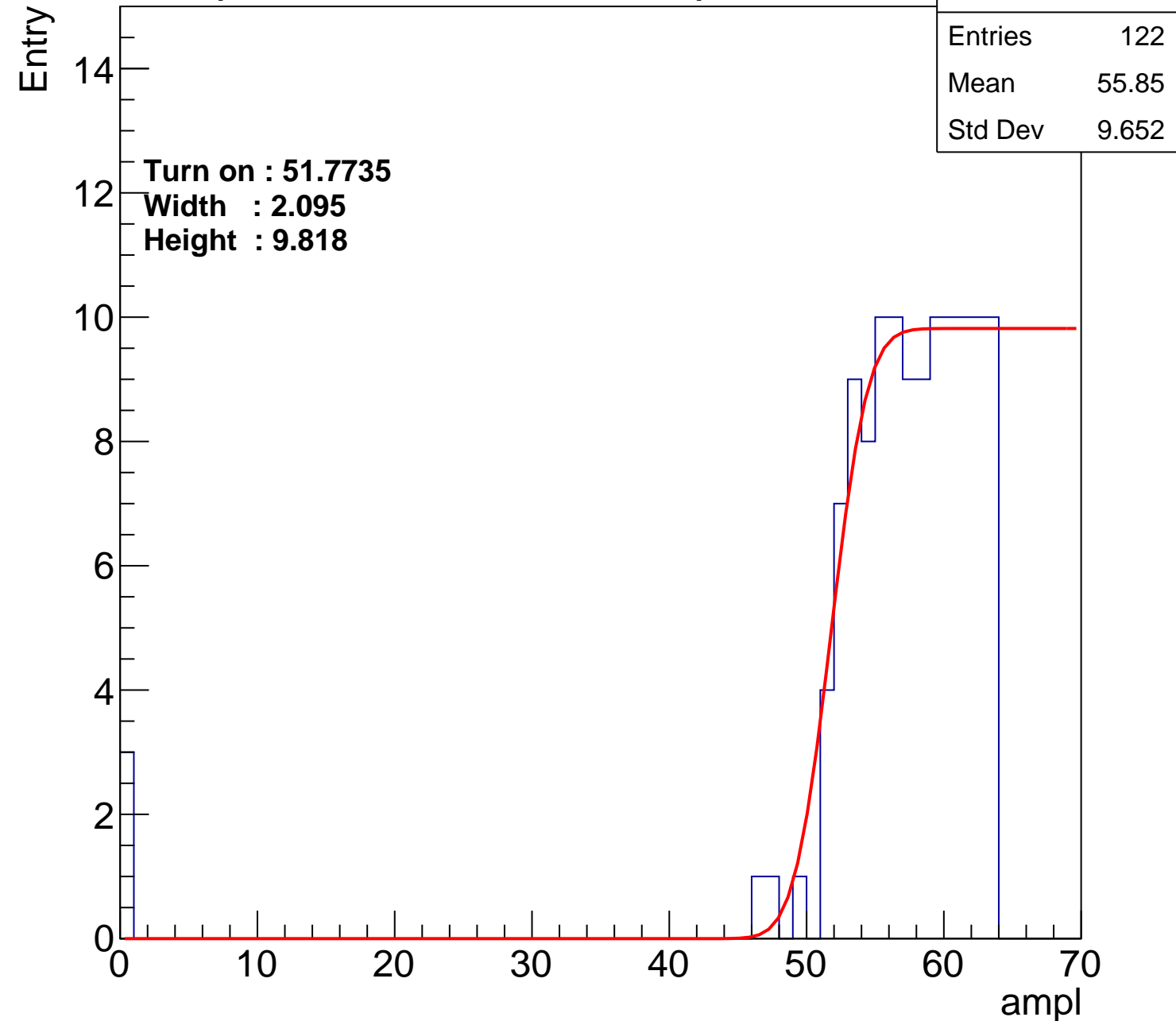
Width : 2.095

Height : 9.818

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch6

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.17
Std Dev	10.04

Turn on : 53.8890

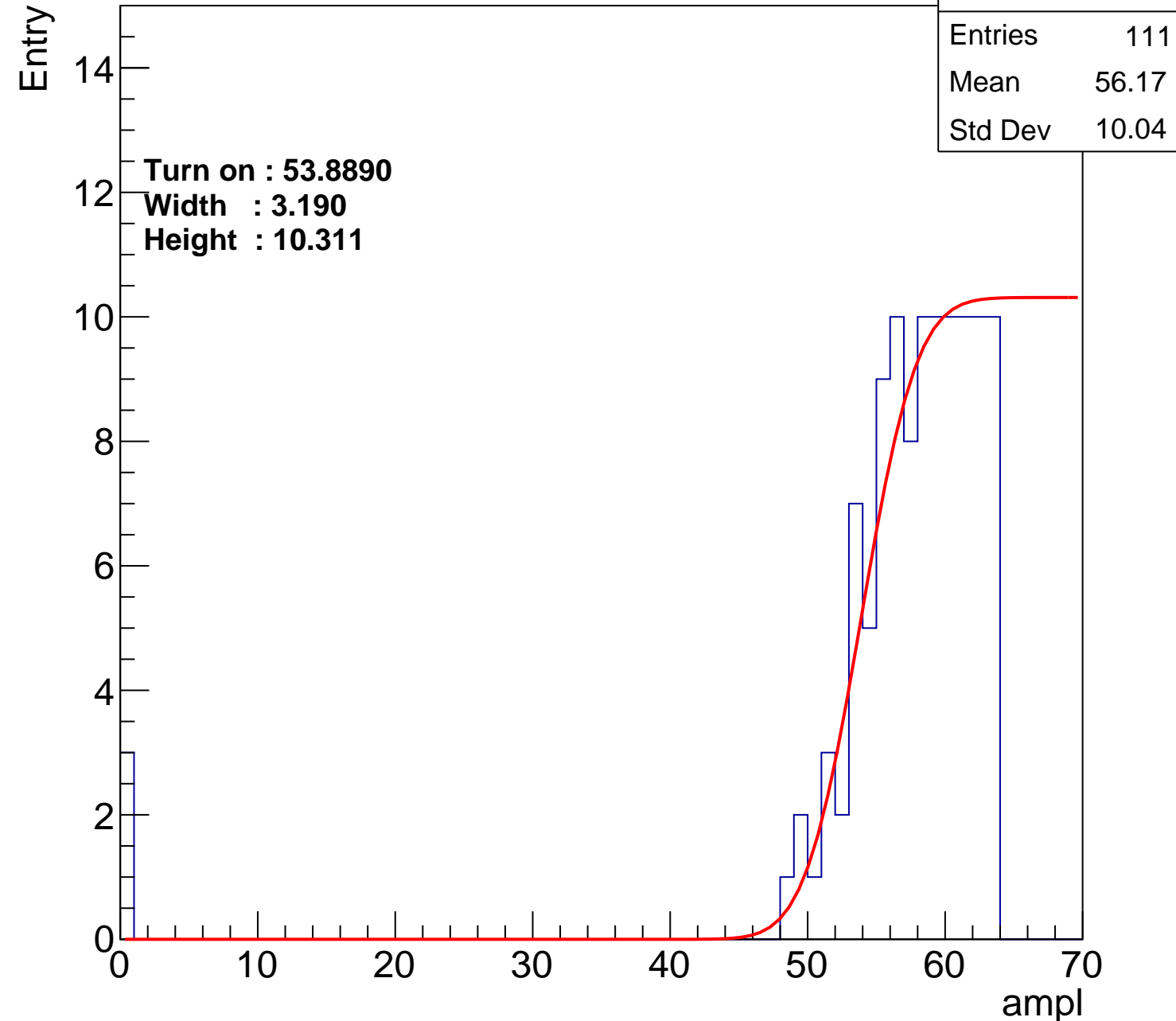
Width : 3.190

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch7

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.12
Std Dev	9.921

Turn on : 53.1523

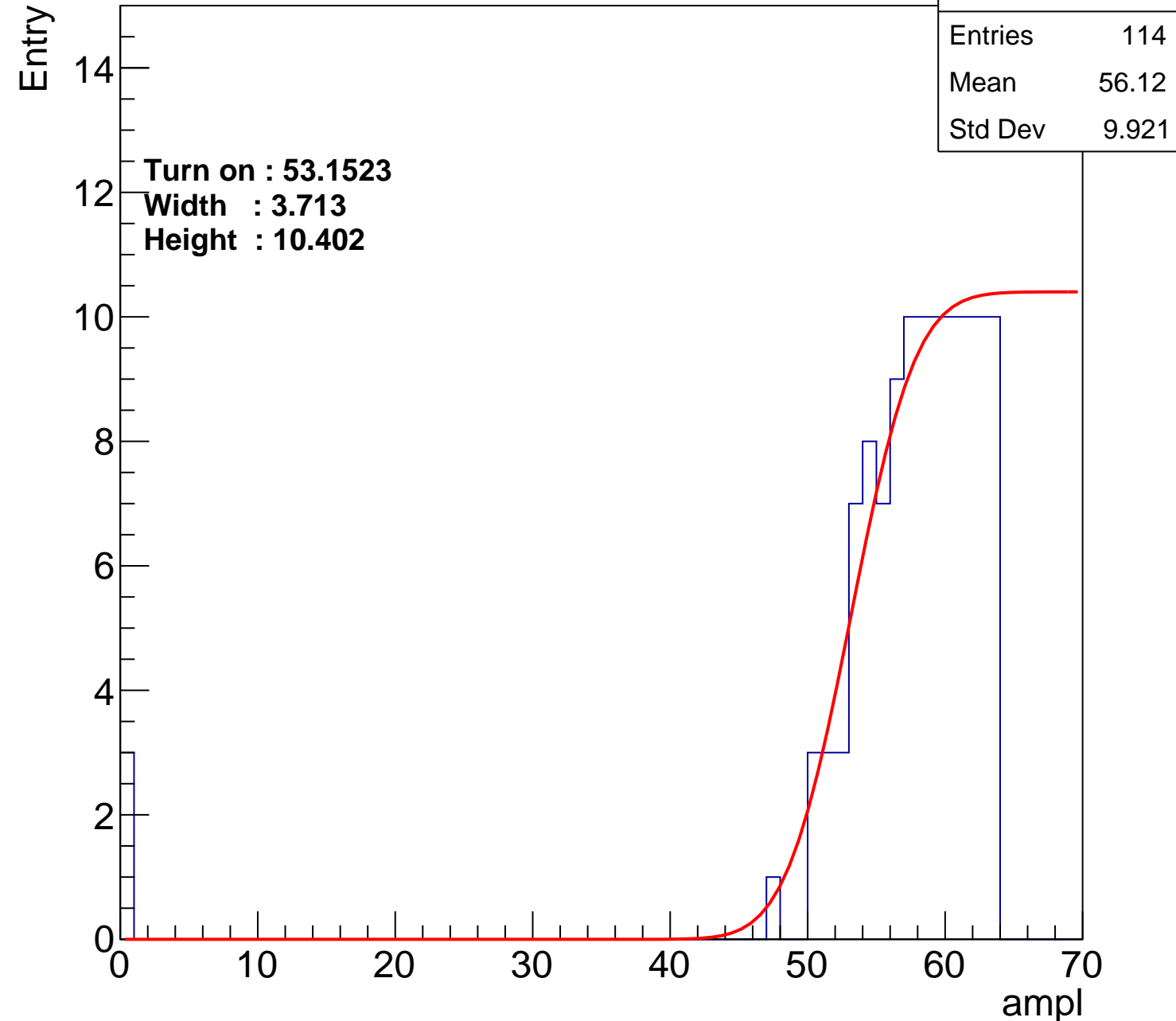
Width : 3.713

Height : 10.402

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch8

calib_packv5_040323_1717.root, FC#2, port C3

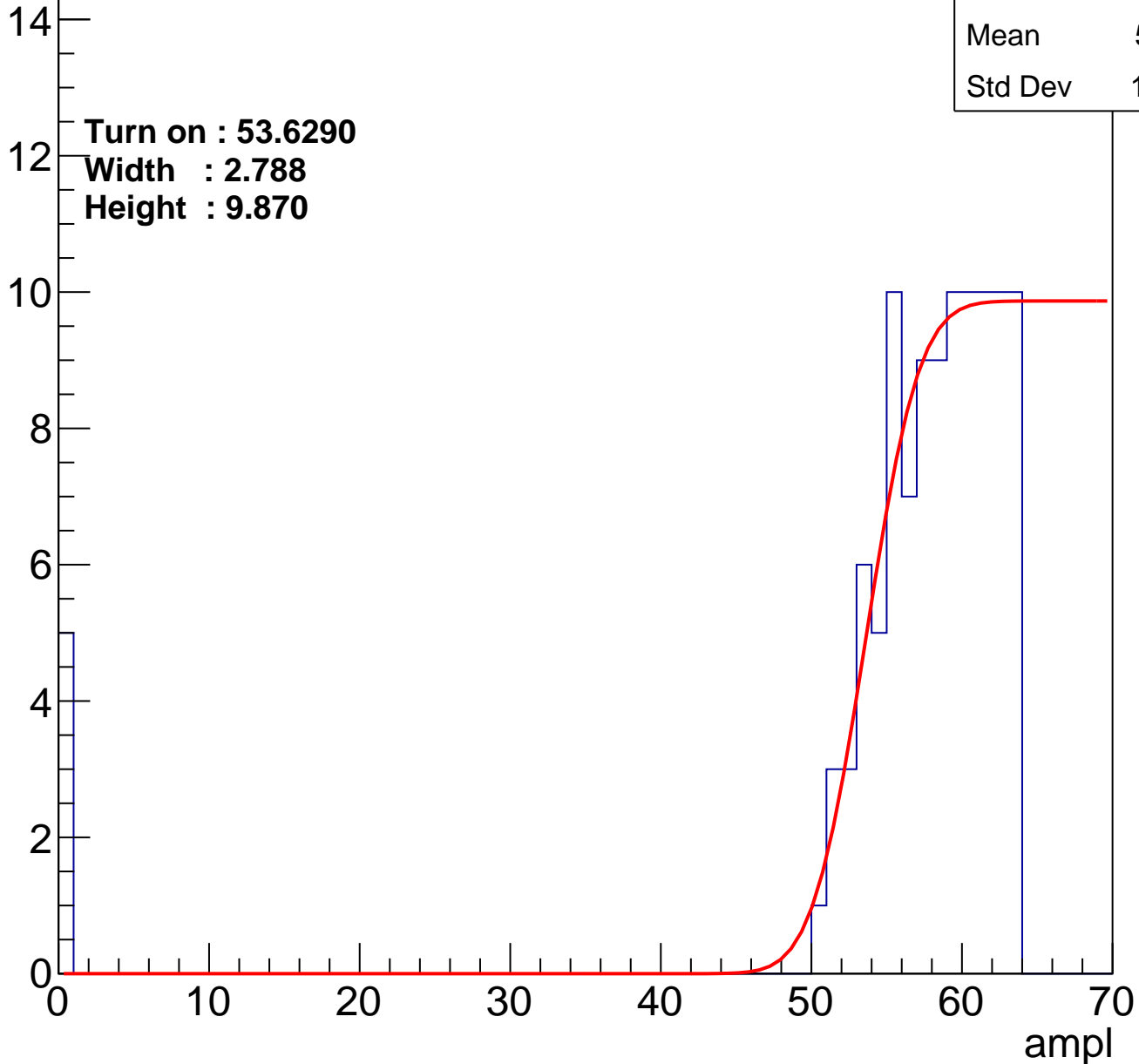
Entries	108
Mean	55.31
Std Dev	12.64

Turn on : 53.6290

Width : 2.788

Height : 9.870

Entry



B0L103S, U13-ch9

calib_packv5_040323_1717.root, FC#2, port C3

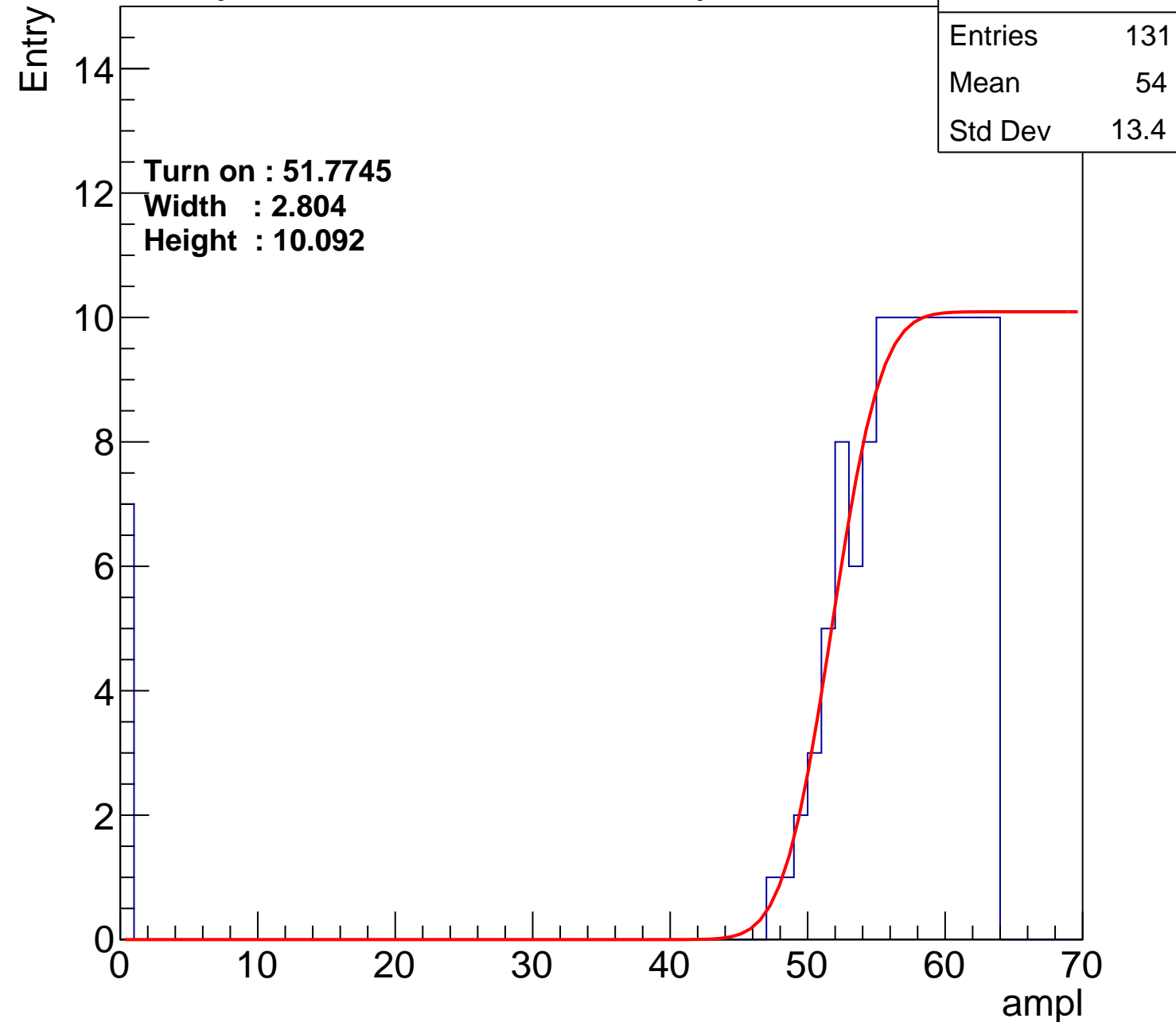
Entry

14
12
10
8
6
4
2
0

Turn on : 51.7745
Width : 2.804
Height : 10.092

Entries	131
Mean	54
Std Dev	13.4

ampl



B0L103S, U13-ch10

calib_packv5_040323_1717.root, FC#2, port C3

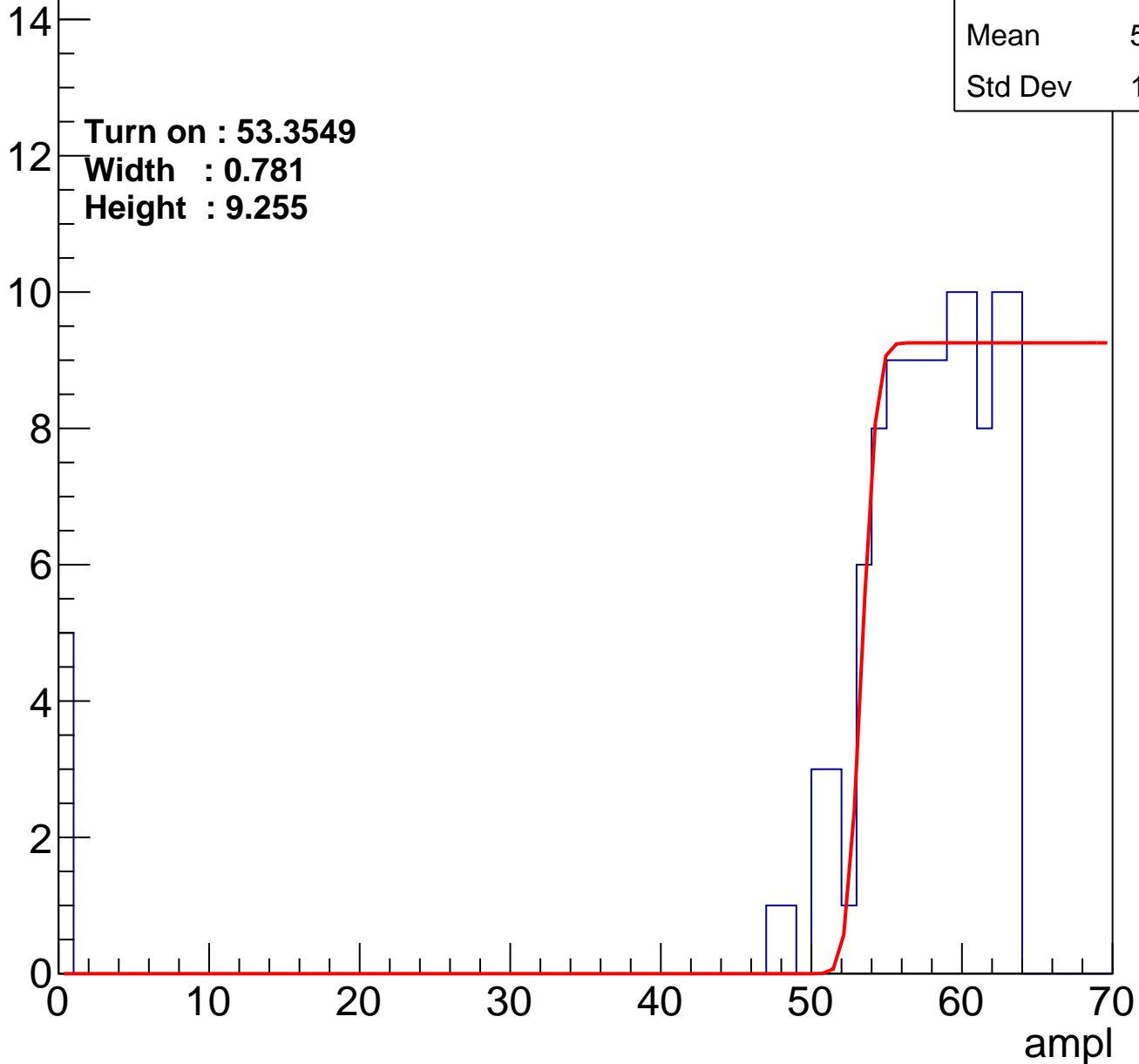
Entries	112
Mean	55.02
Std Dev	12.45

Turn on : 53.3549

Width : 0.781

Height : 9.255

Entry



B0L103S, U13-ch11

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	56.35
Std Dev	6.408

Turn on : 51.0048

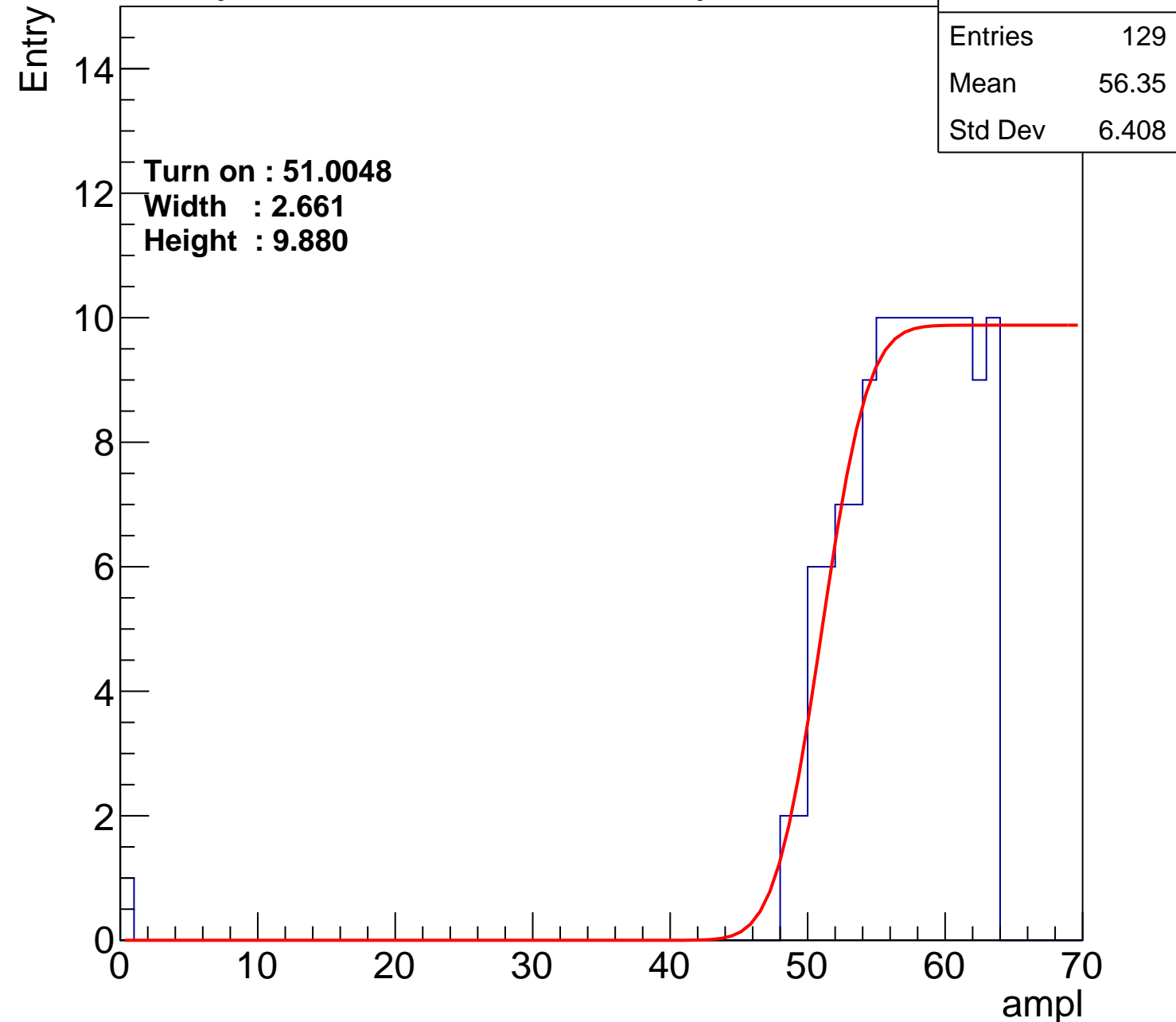
Width : 2.661

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch12

calib_packv5_040323_1717.root, FC#2, port C3

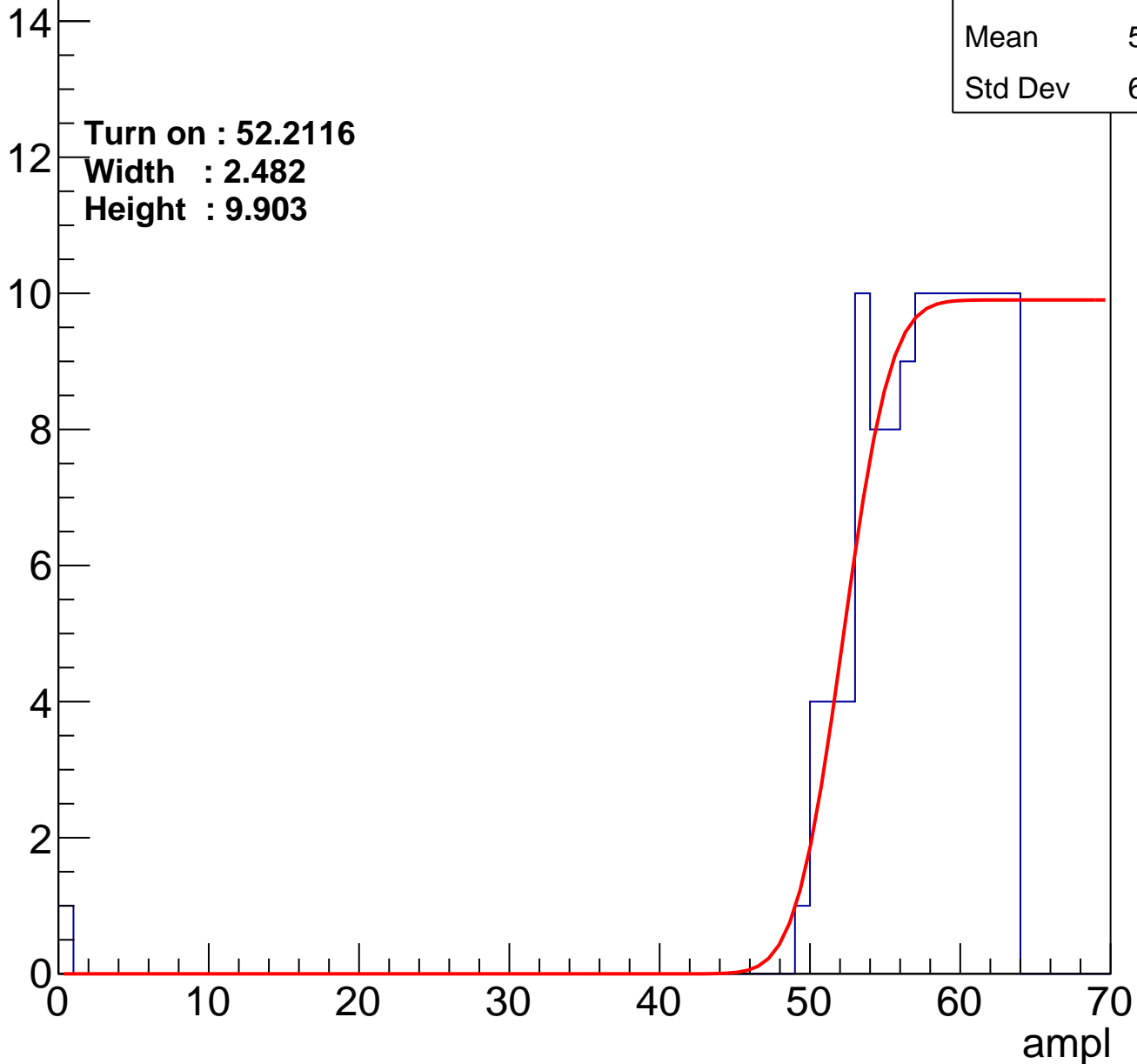
Entries	119
Mean	56.87
Std Dev	6.439

Turn on : 52.2116

Width : 2.482

Height : 9.903

Entry



B0L103S, U13-ch13

calib_packv5_040323_1717.root, FC#2, port C3

Entries	99
Mean	55.47
Std Dev	13.18

Turn on : 54.8264

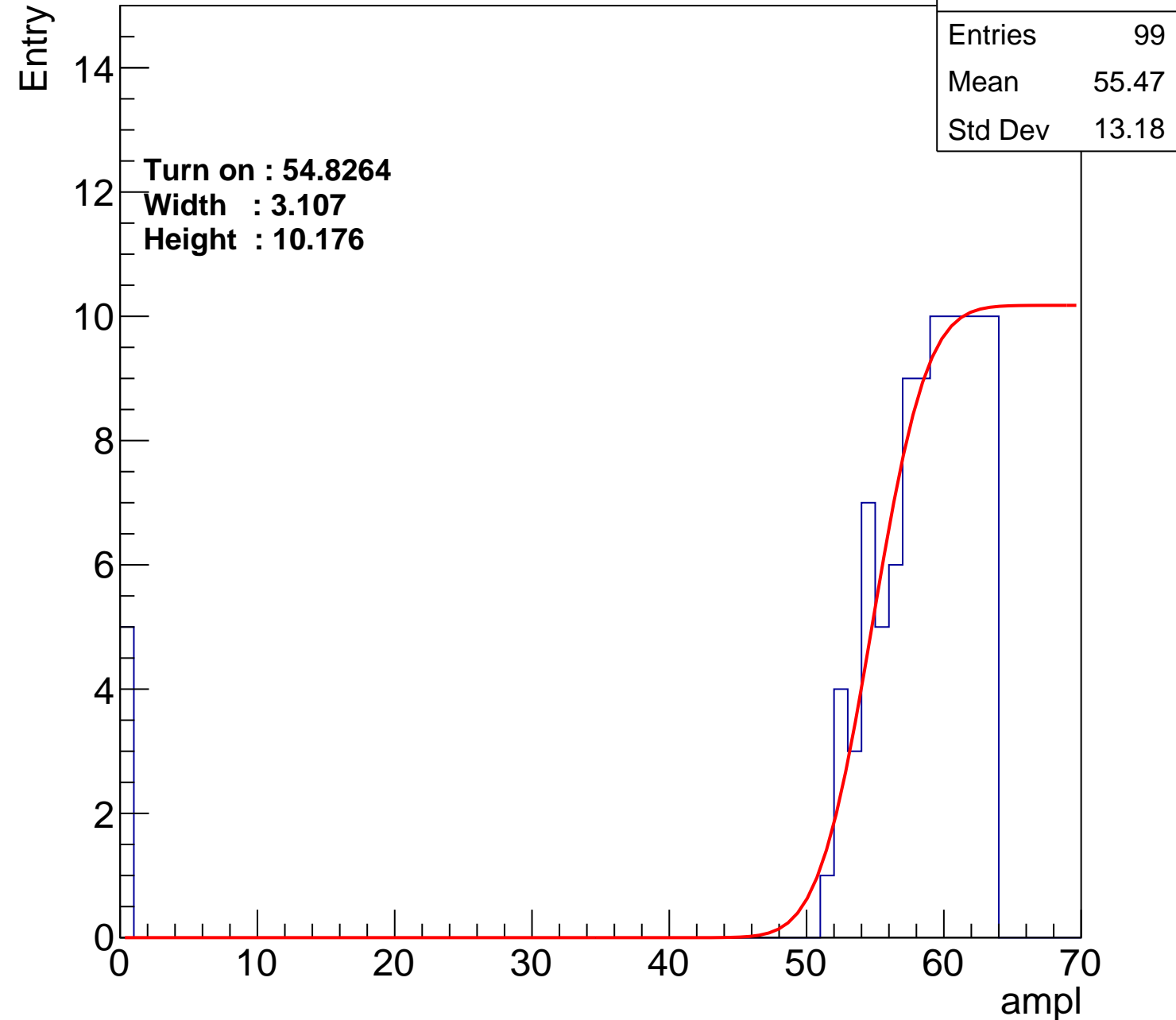
Width : 3.107

Height : 10.176

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch14

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.89
Std Dev	6.443

Turn on : 50.5320

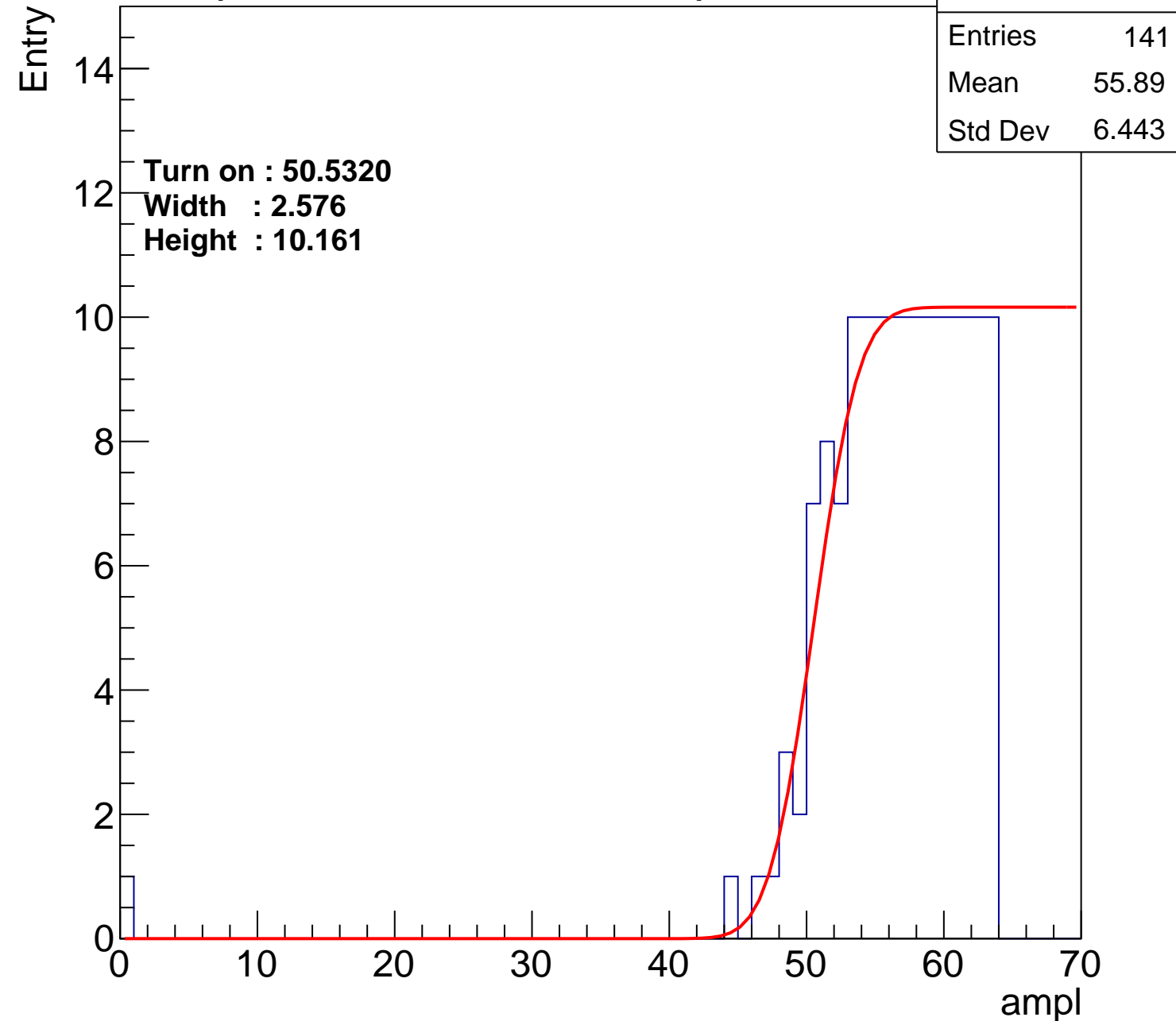
Width : 2.576

Height : 10.161

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch15

calib_packv5_040323_1717.root, FC#2, port C3

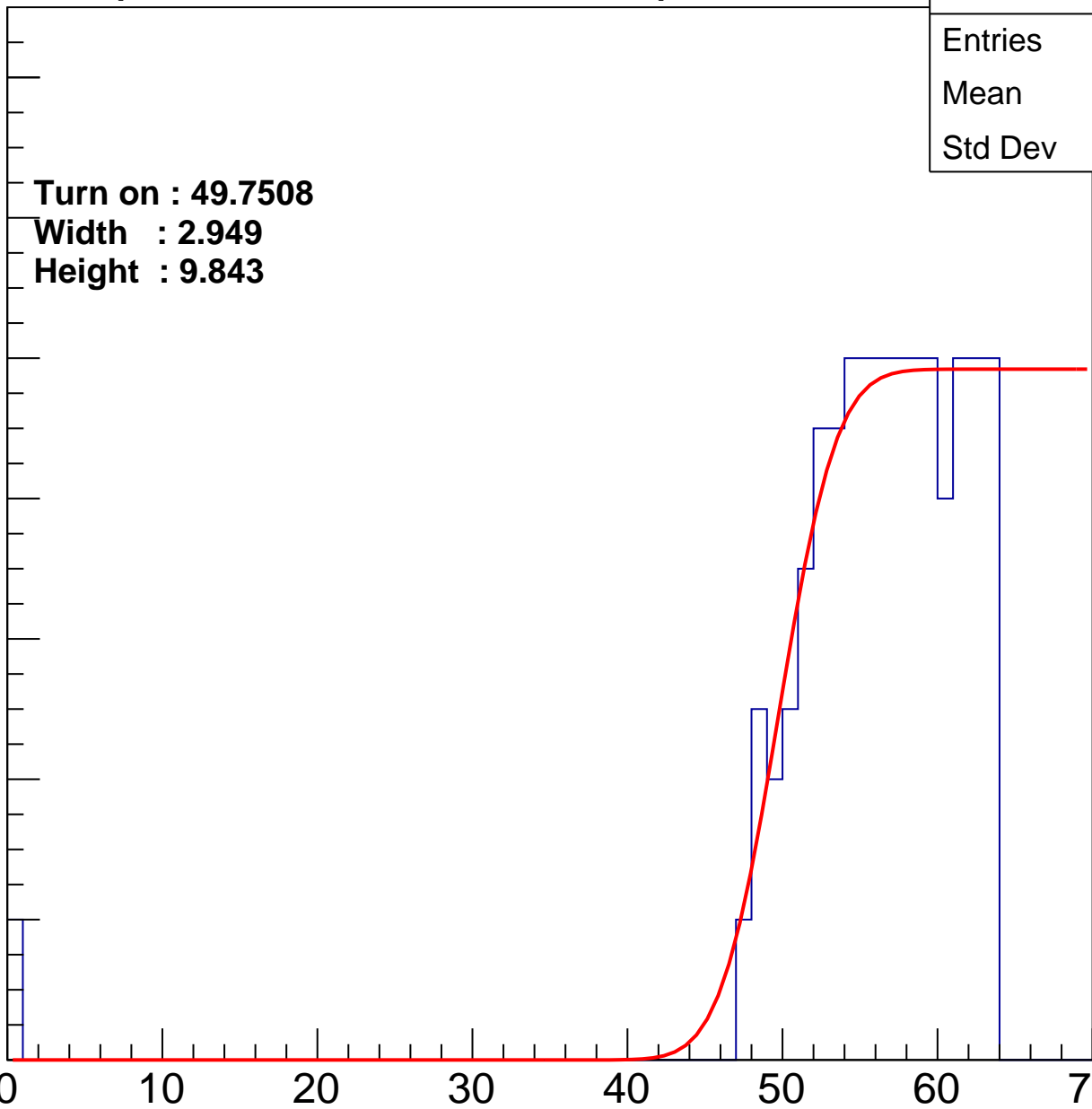
Entry

14
12
10
8
6
4
2
0

Turn on : 49.7508
Width : 2.949
Height : 9.843

Entries	141
Mean	55.4
Std Dev	7.944

ampl



B0L103S, U13-ch16

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	54.72
Std Dev	11.76

Turn on : 51.9855

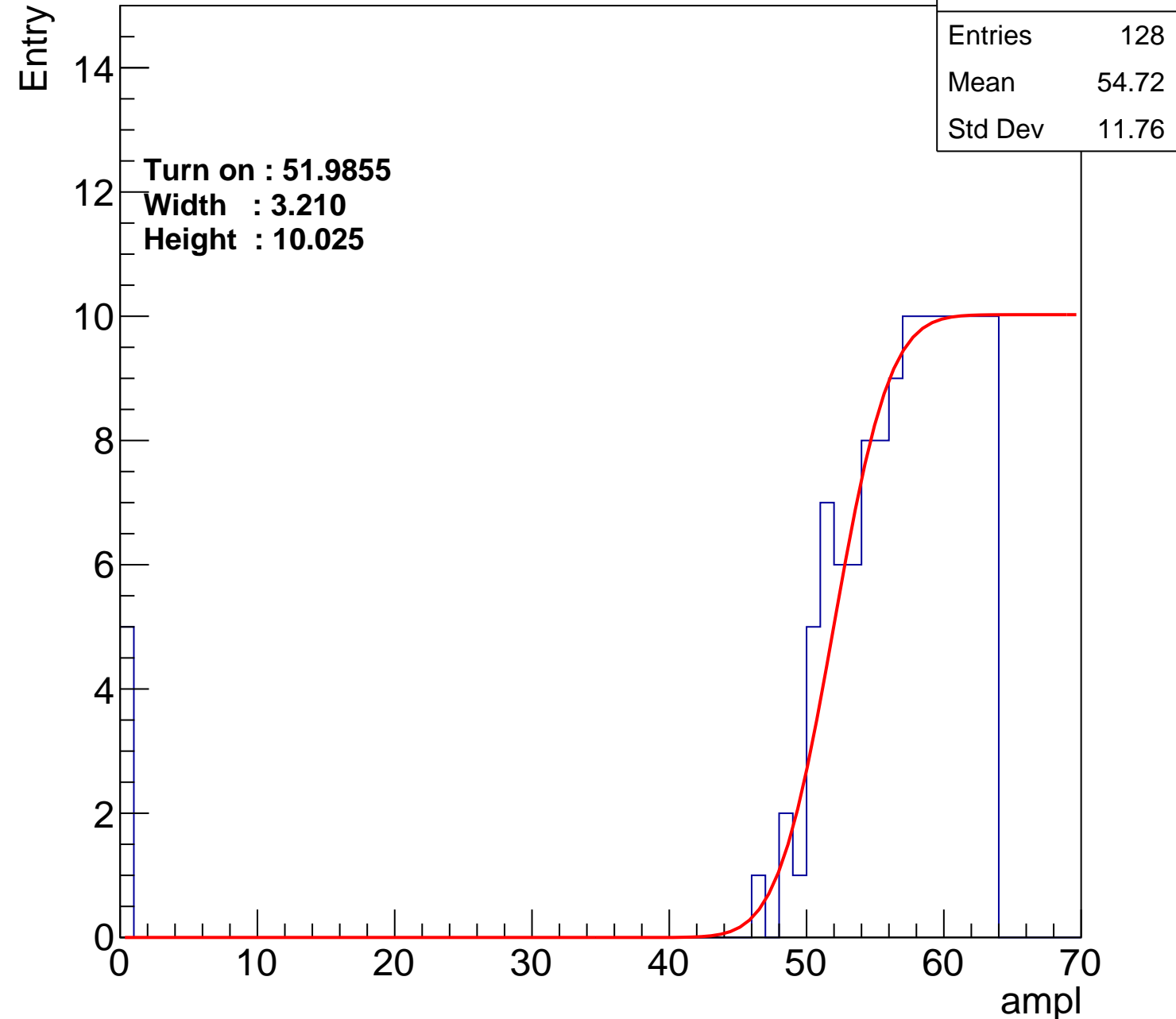
Width : 3.210

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch17

calib_packv5_040323_1717.root, FC#2, port C3

Entries	113
Mean	55.16
Std Dev	12.43

Turn on : 53.7688

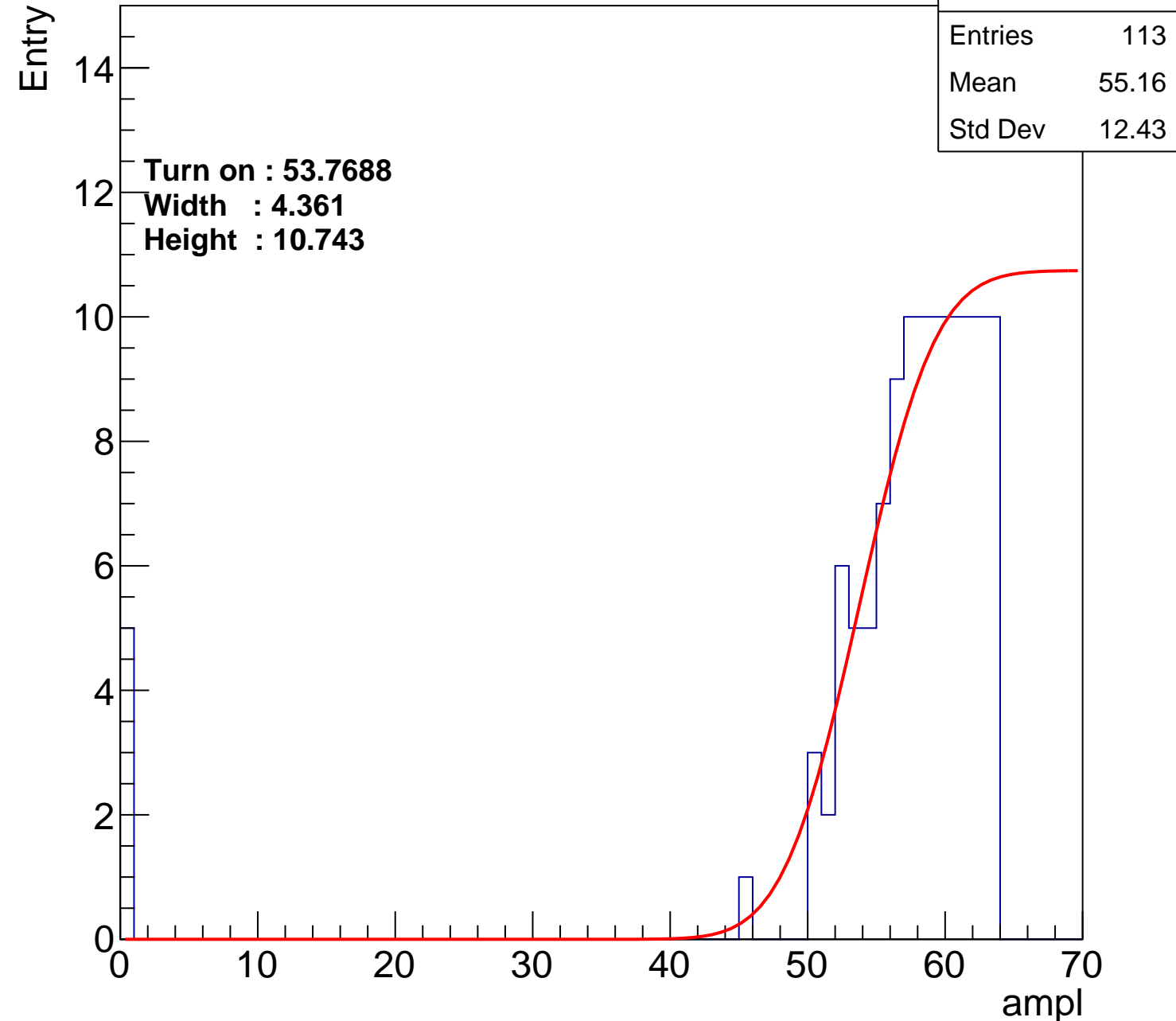
Width : 4.361

Height : 10.743

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch18

calib_packv5_040323_1717.root, FC#2, port C3

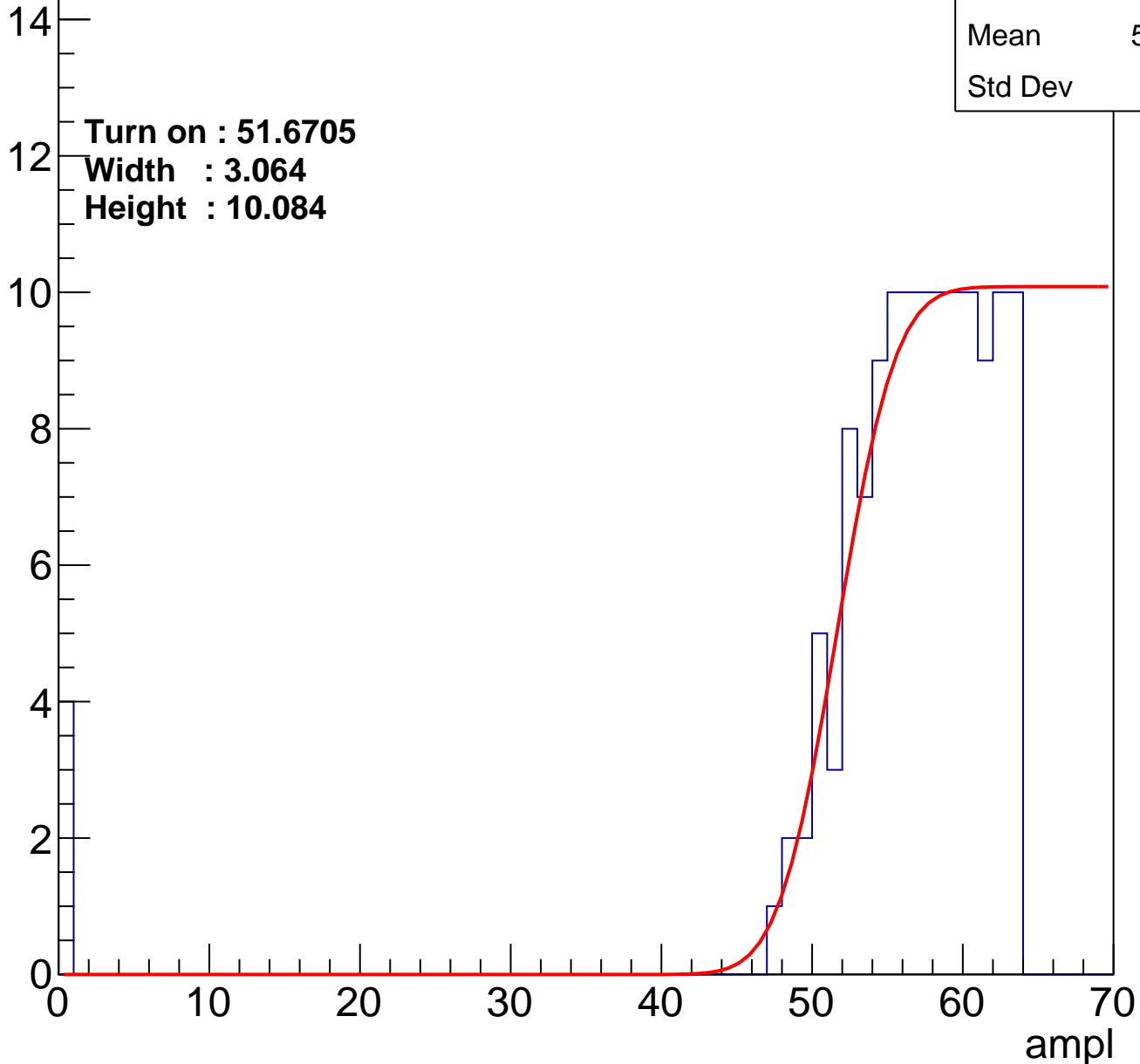
Entries	130
Mean	55.12
Std Dev	10.6

Turn on : 51.6705

Width : 3.064

Height : 10.084

Entry



B0L103S, U13-ch19

calib_packv5_040323_1717.root, FC#2, port C3

Entries	100
Mean	55.36
Std Dev	13.14

Turn on : 55.6443

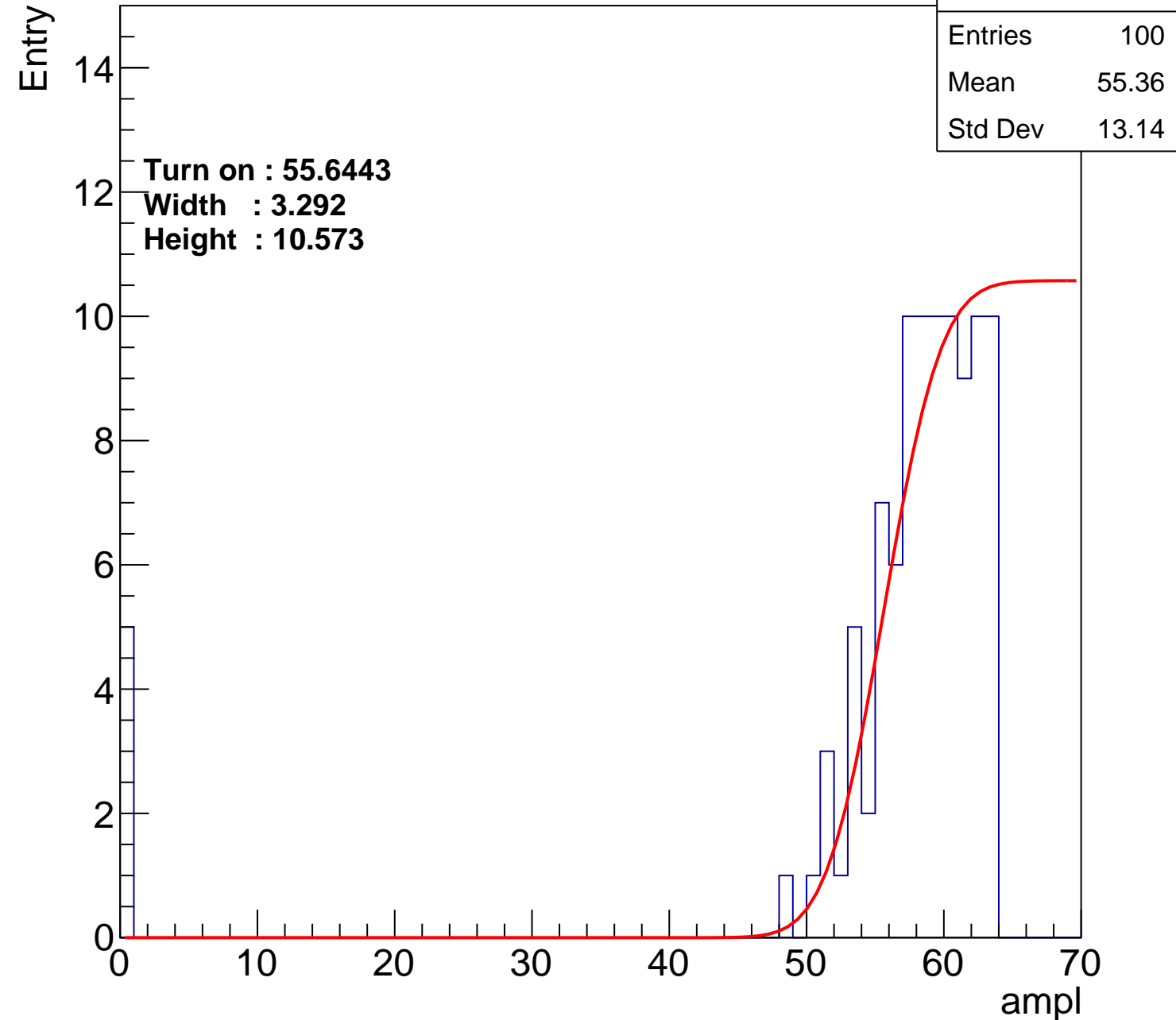
Width : 3.292

Height : 10.573

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch20

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	53.88
Std Dev	13.13

Turn on : 51.3617

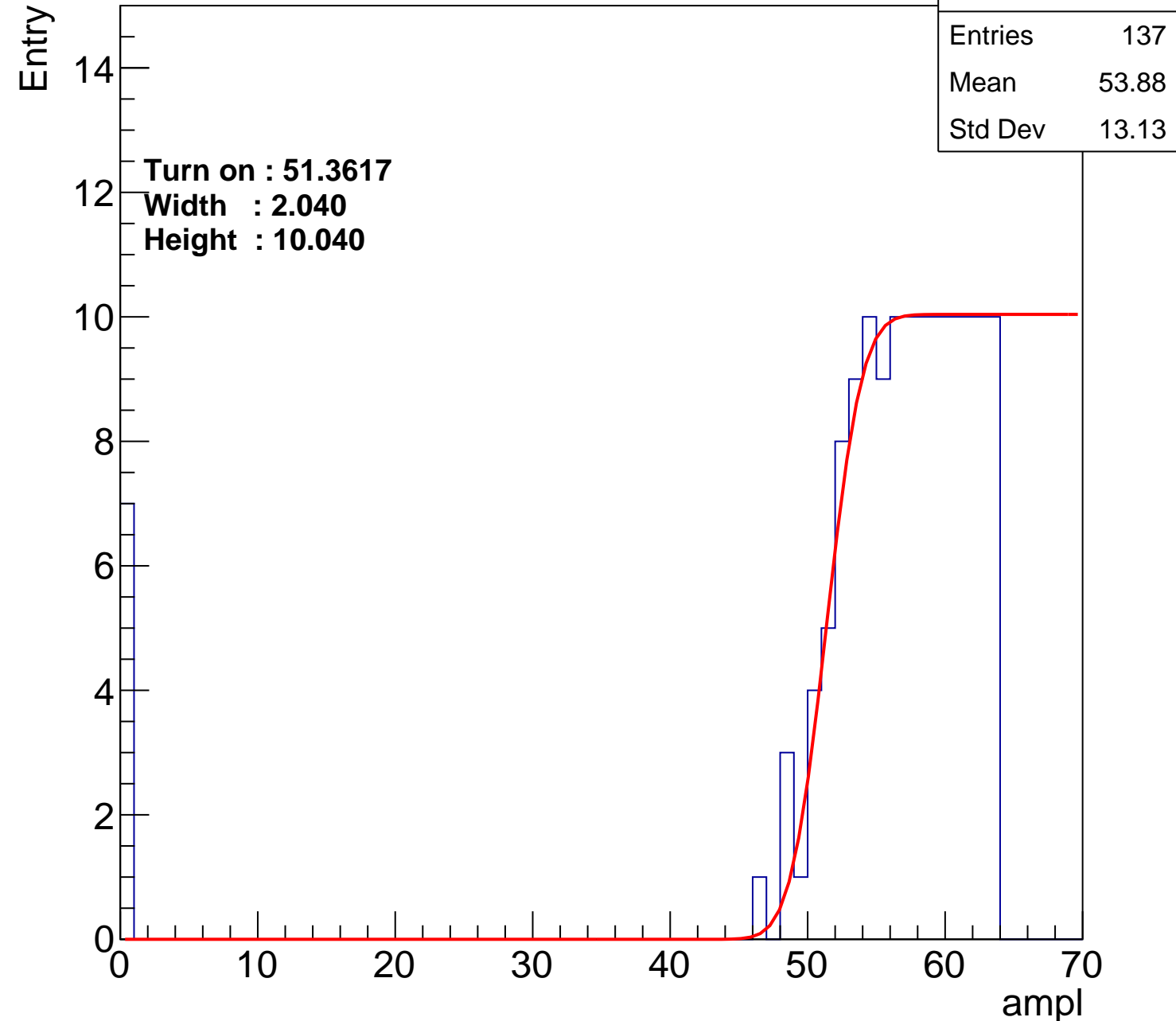
Width : 2.040

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch21

calib_packv5_040323_1717.root, FC#2, port C3

Entries	118
Mean	55.06
Std Dev	12.17

Turn on : 53.0891

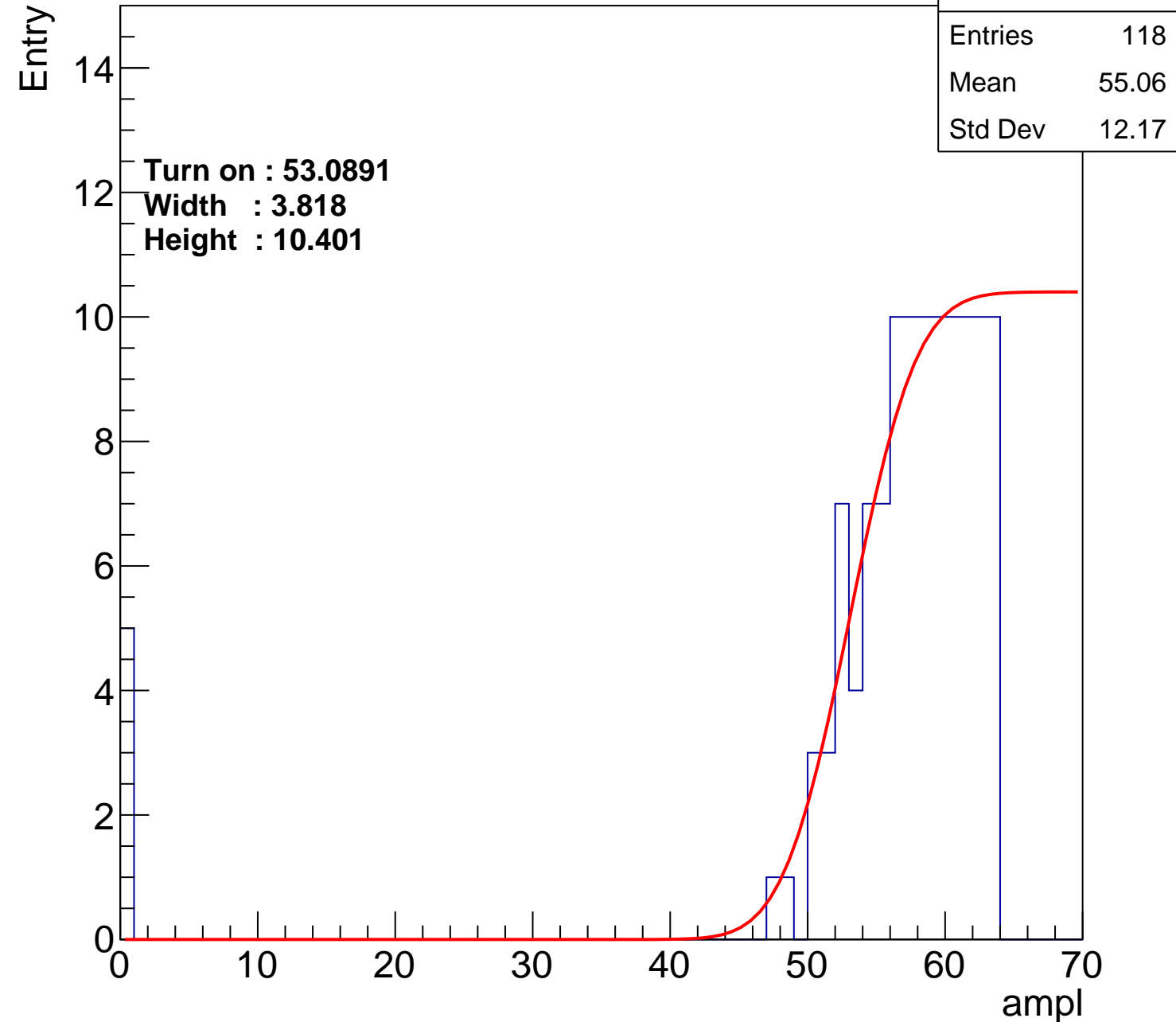
Width : 3.818

Height : 10.401

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch22

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	55.24
Std Dev	12.52

Turn on : 53.8820

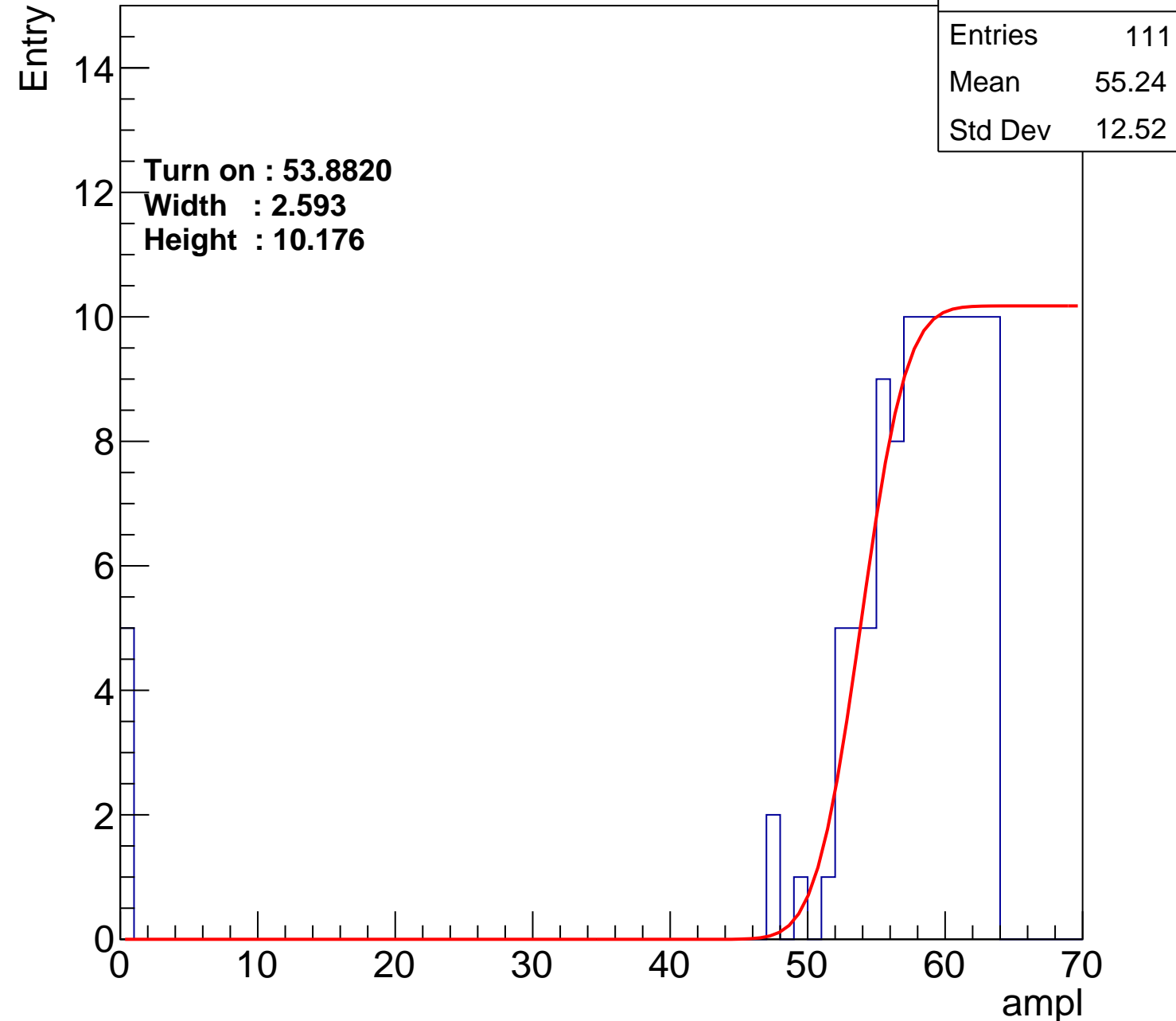
Width : 2.593

Height : 10.176

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch23

calib_packv5_040323_1717.root, FC#2, port C3

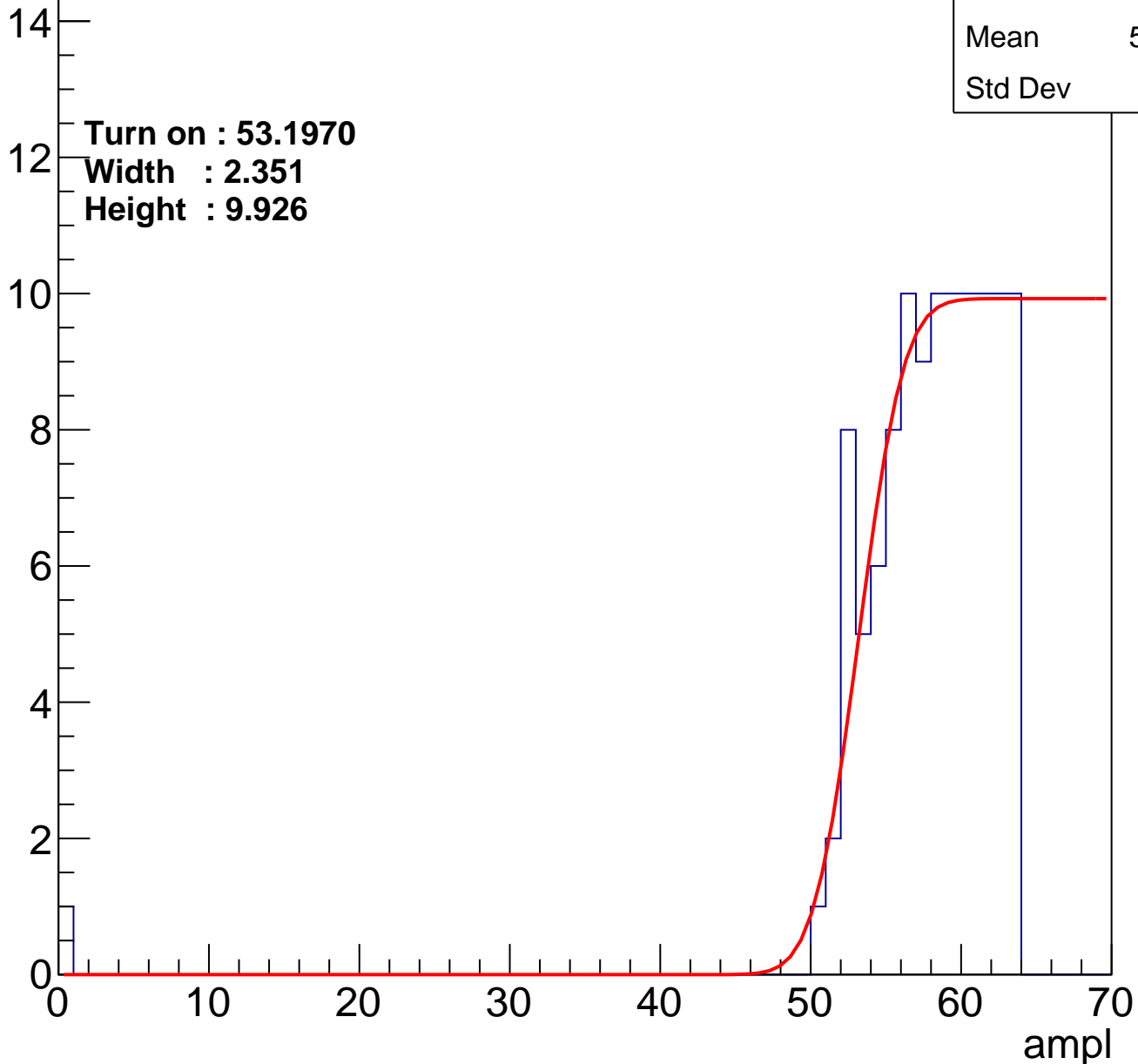
Entries	110
Mean	57.27
Std Dev	6.5

Turn on : 53.1970

Width : 2.351

Height : 9.926

Entry



B0L103S, U13-ch24

calib_packv5_040323_1717.root, FC#2, port C3

Entries	117
Mean	55.9
Std Dev	9.875

Turn on : 54.3272

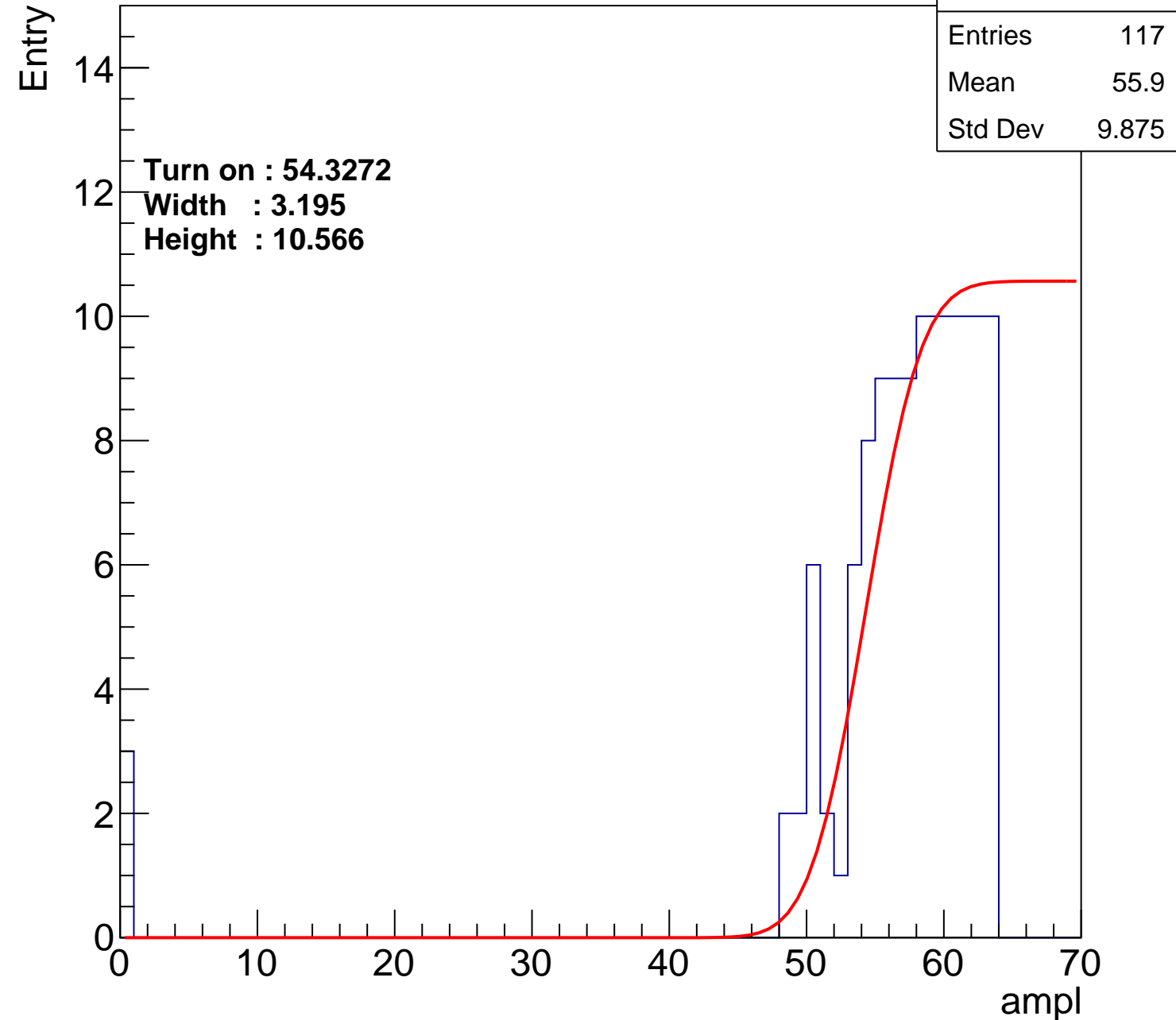
Width : 3.195

Height : 10.566

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch25

calib_packv5_040323_1717.root, FC#2, port C3

Entries	103
Mean	56.45
Std Dev	10.32

Turn on : 54.0142

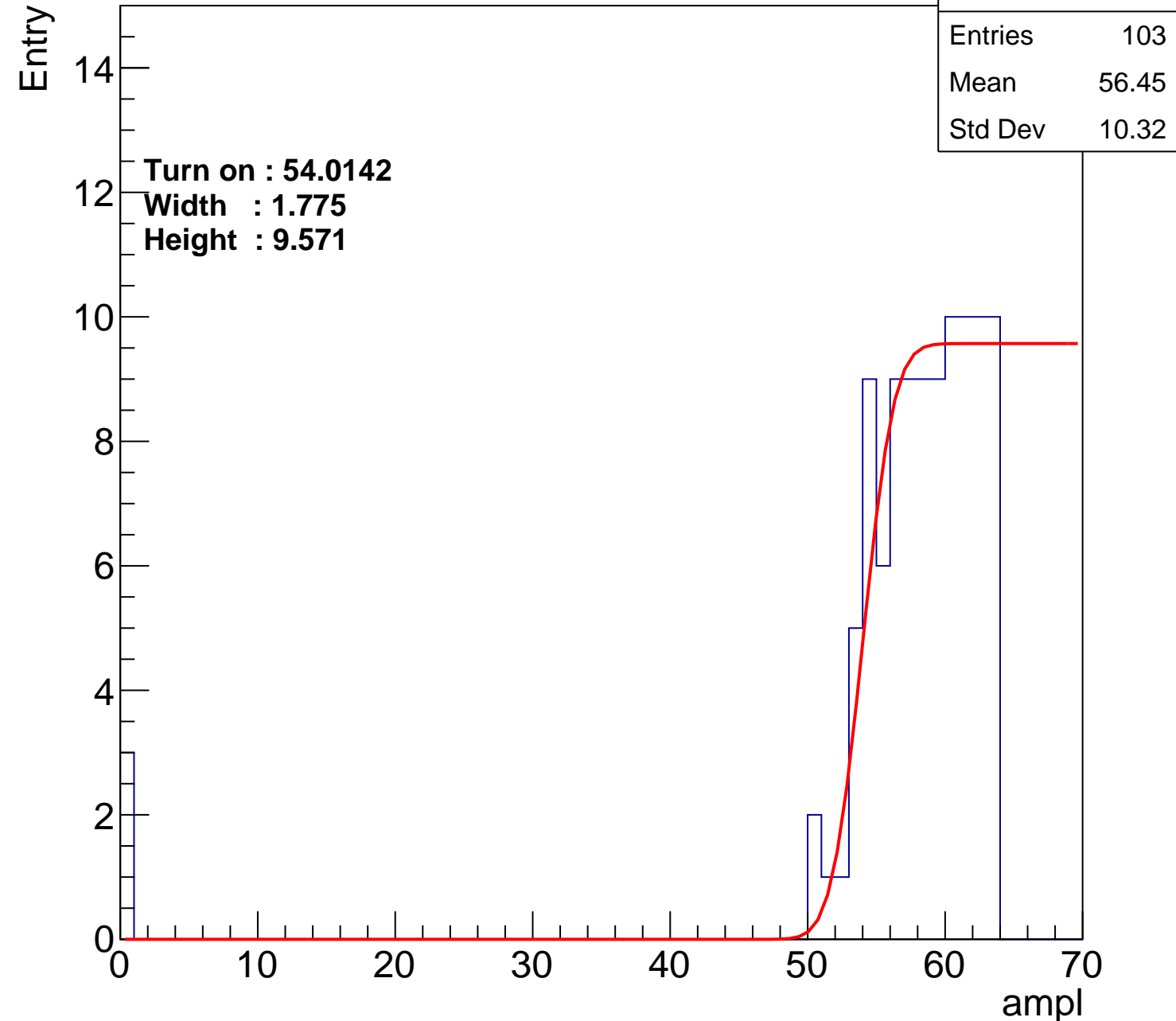
Width : 1.775

Height : 9.571

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch26

calib_packv5_040323_1717.root, FC#2, port C3

Entries	106
Mean	56.82
Std Dev	8.629

Turn on : 53.6531

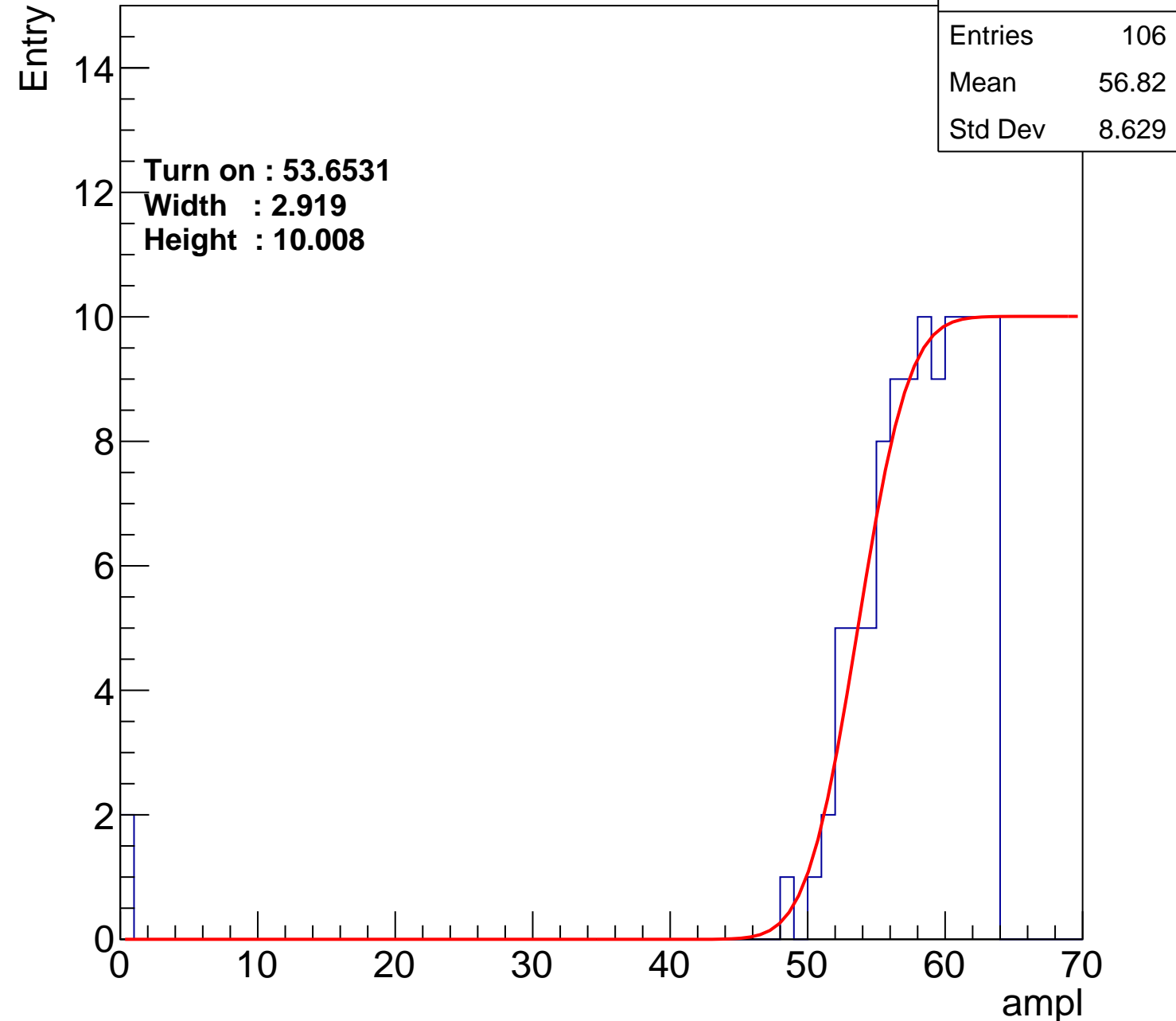
Width : 2.919

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch27

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.58
Std Dev	11.4

Turn on : 50.9820

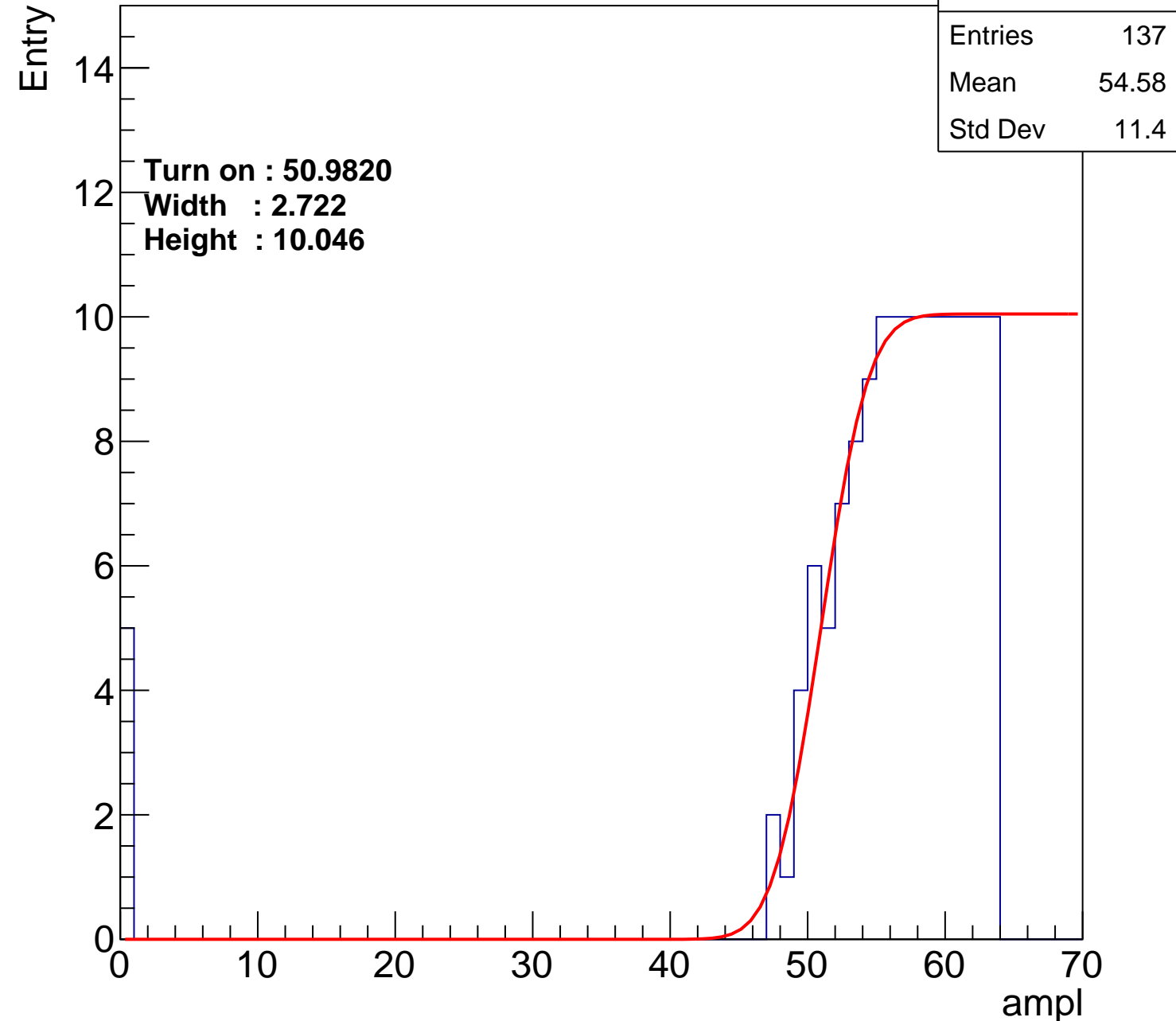
Width : 2.722

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch28

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.76
Std Dev	11.63

Turn on : 51.6640

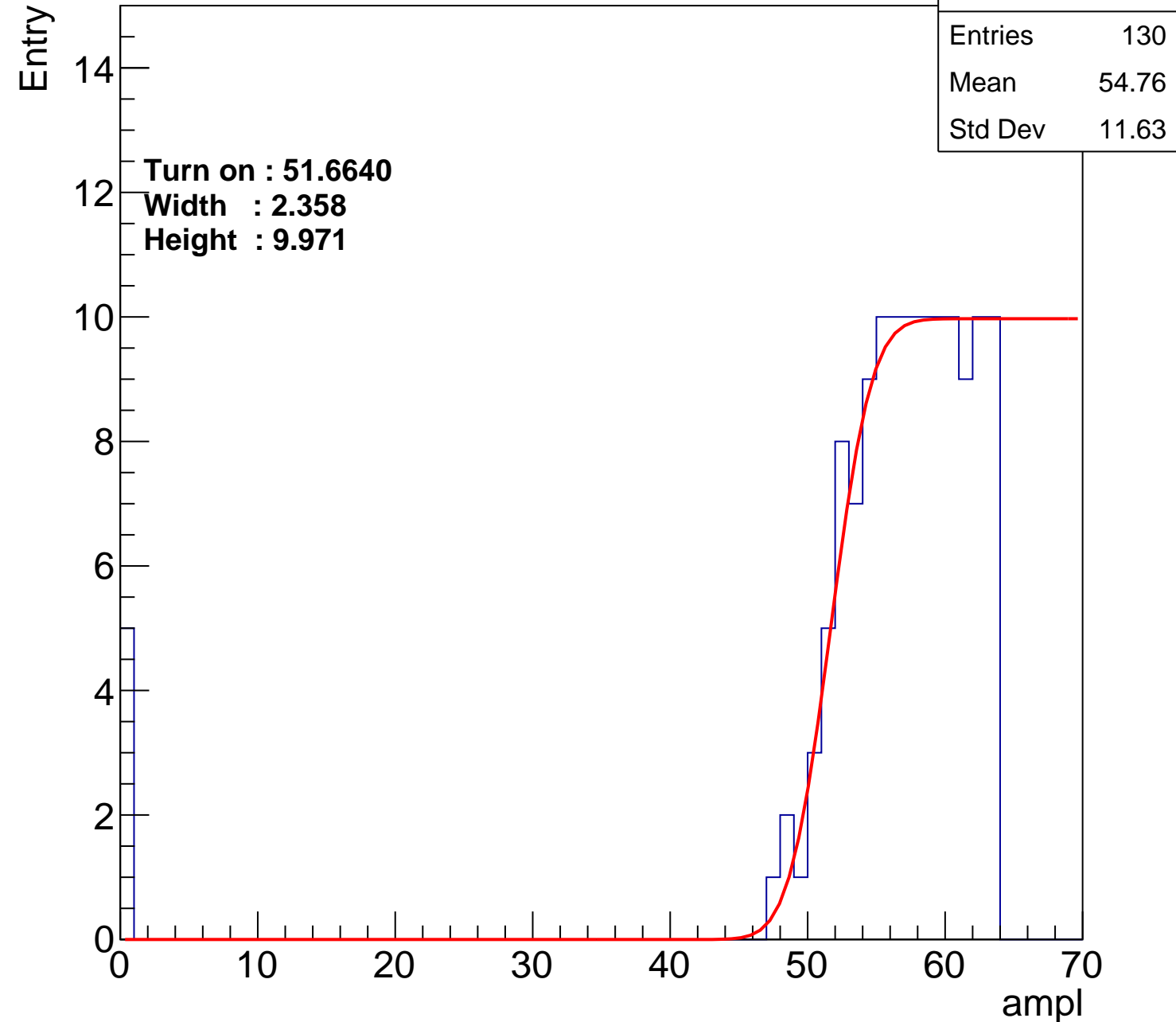
Width : 2.358

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch29

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	56.65
Std Dev	8.439

Turn on : 52.7069

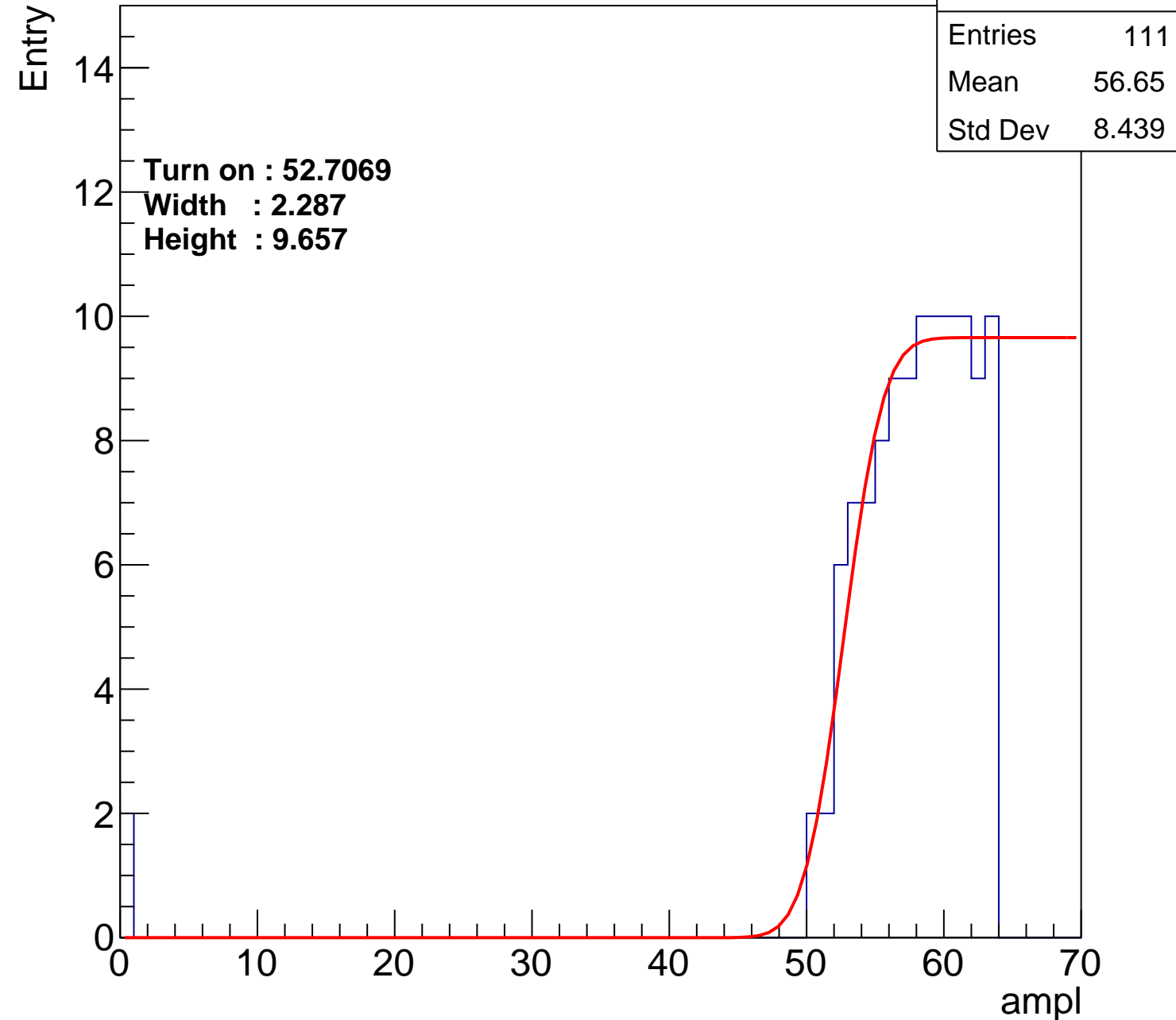
Width : 2.287

Height : 9.657

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch30

calib_packv5_040323_1717.root, FC#2, port C3

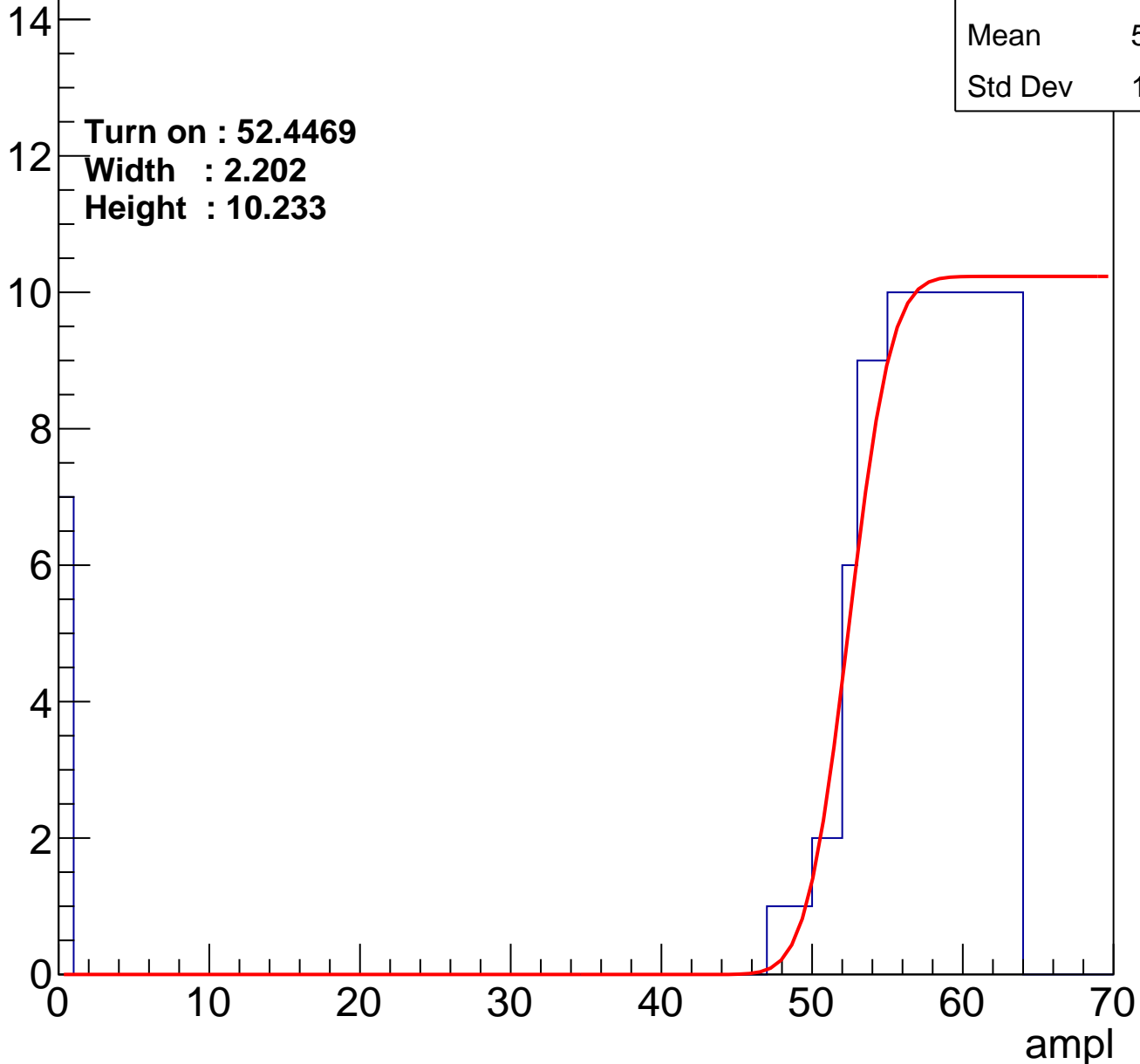
Entries	128
Mean	54.15
Std Dev	13.54

Turn on : 52.4469

Width : 2.202

Height : 10.233

Entry



B0L103S, U13-ch31

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	56.41
Std Dev	8.271

Turn on : 52.3975

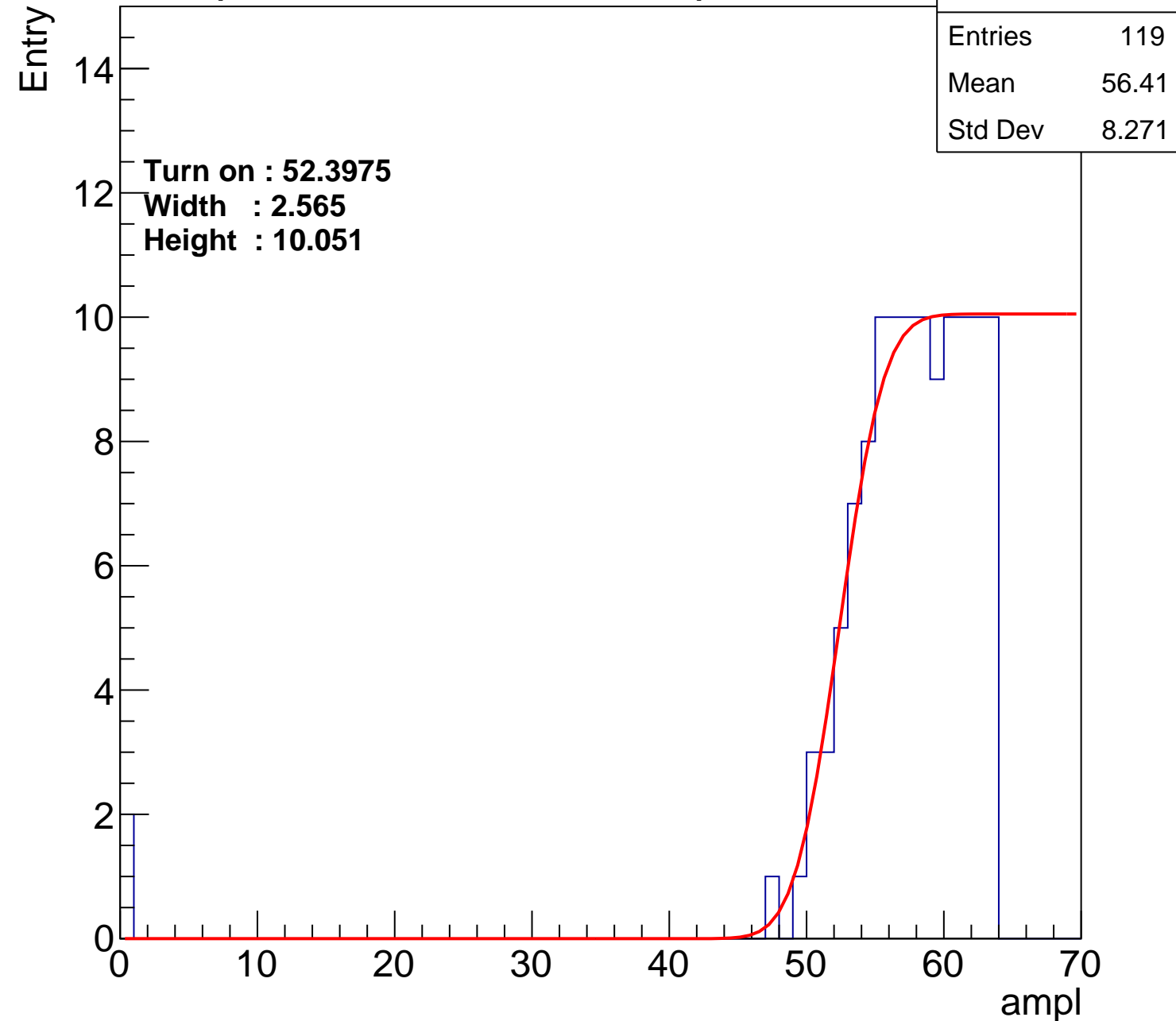
Width : 2.565

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch32

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	55.07
Std Dev	12.6

Turn on : 53.1470

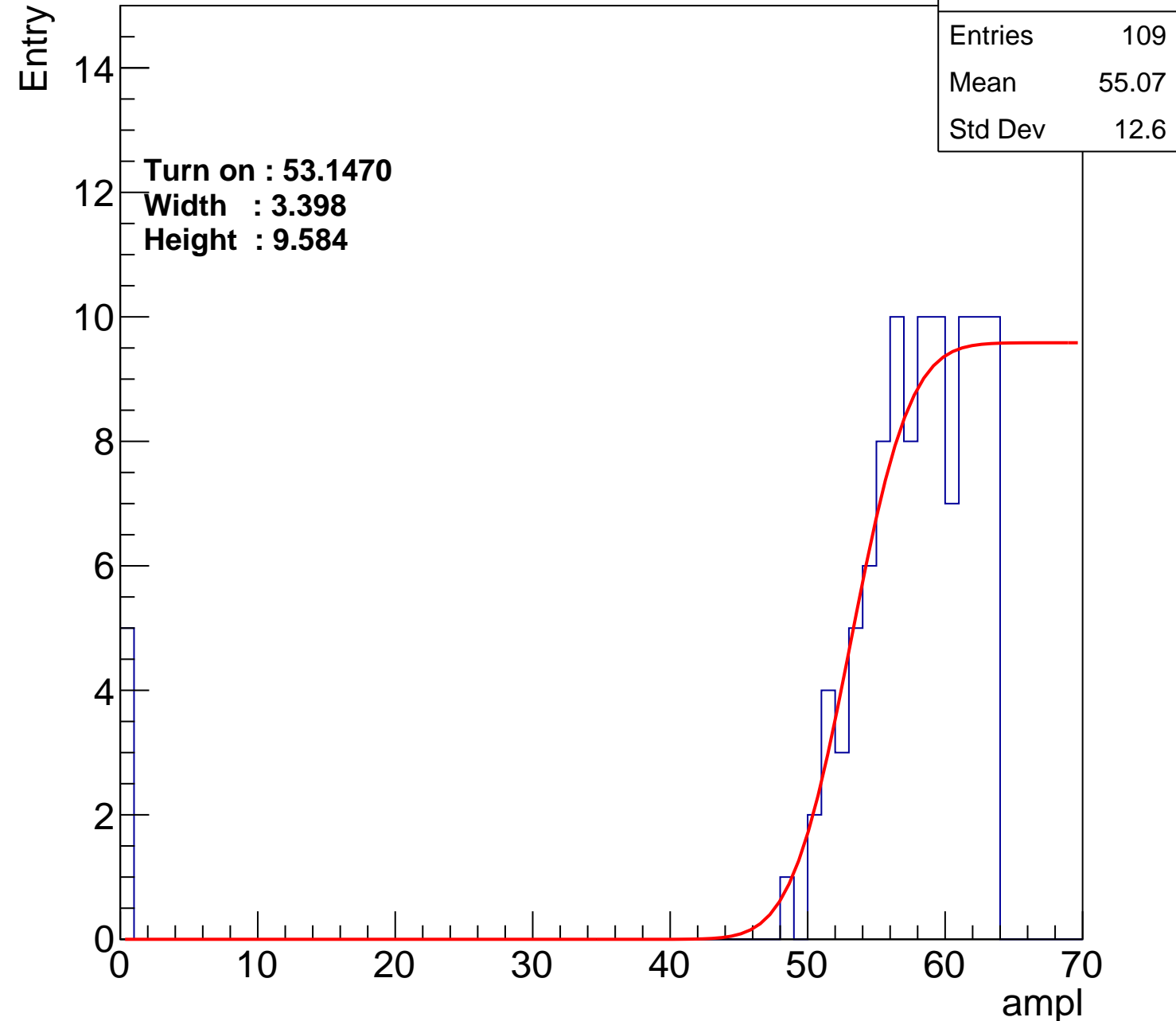
Width : 3.398

Height : 9.584

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch33

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.62
Std Dev	8.379

Turn on : 52.8616

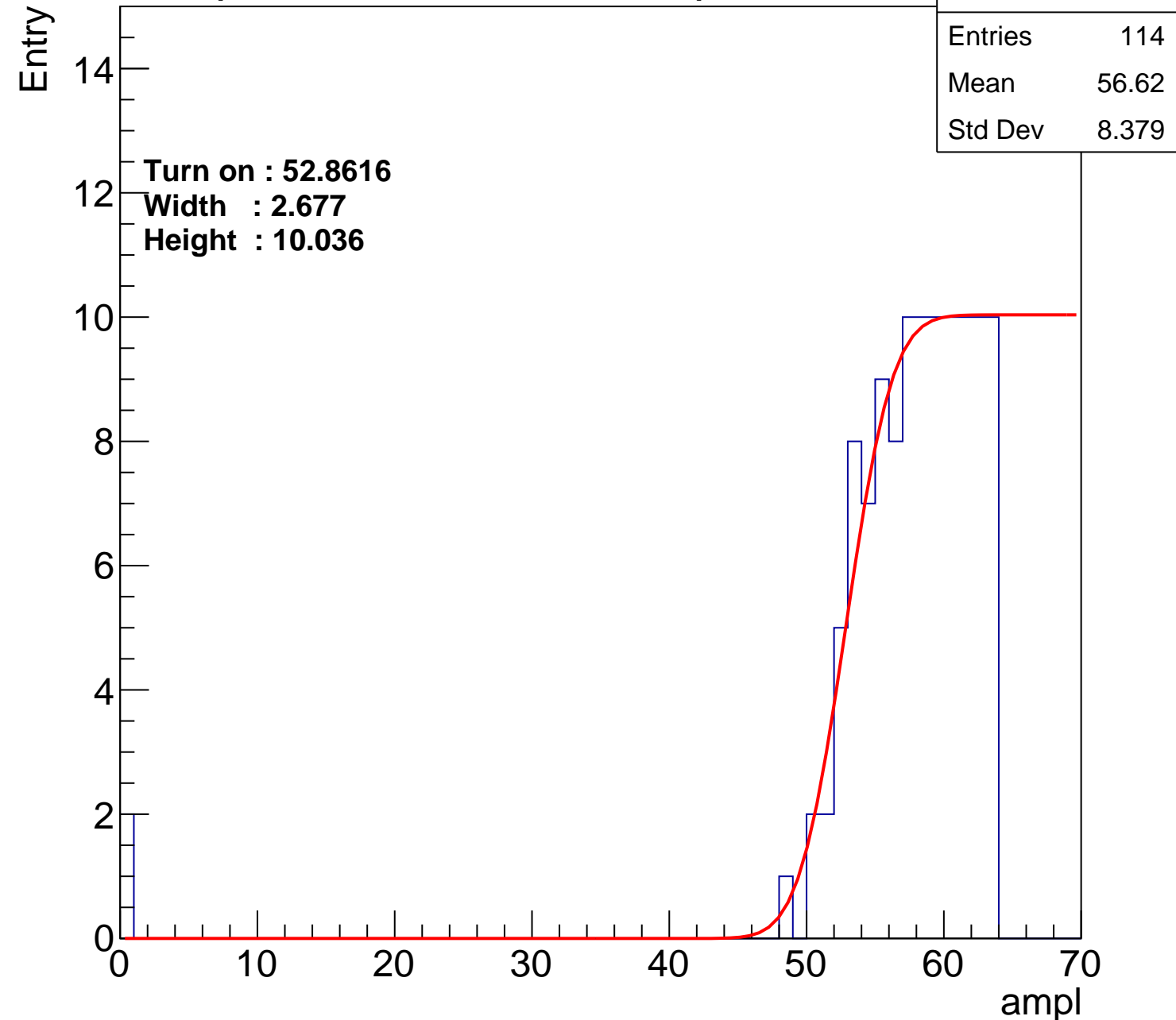
Width : 2.677

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch34

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.77
Std Dev	8.12

Turn on : 51.3856

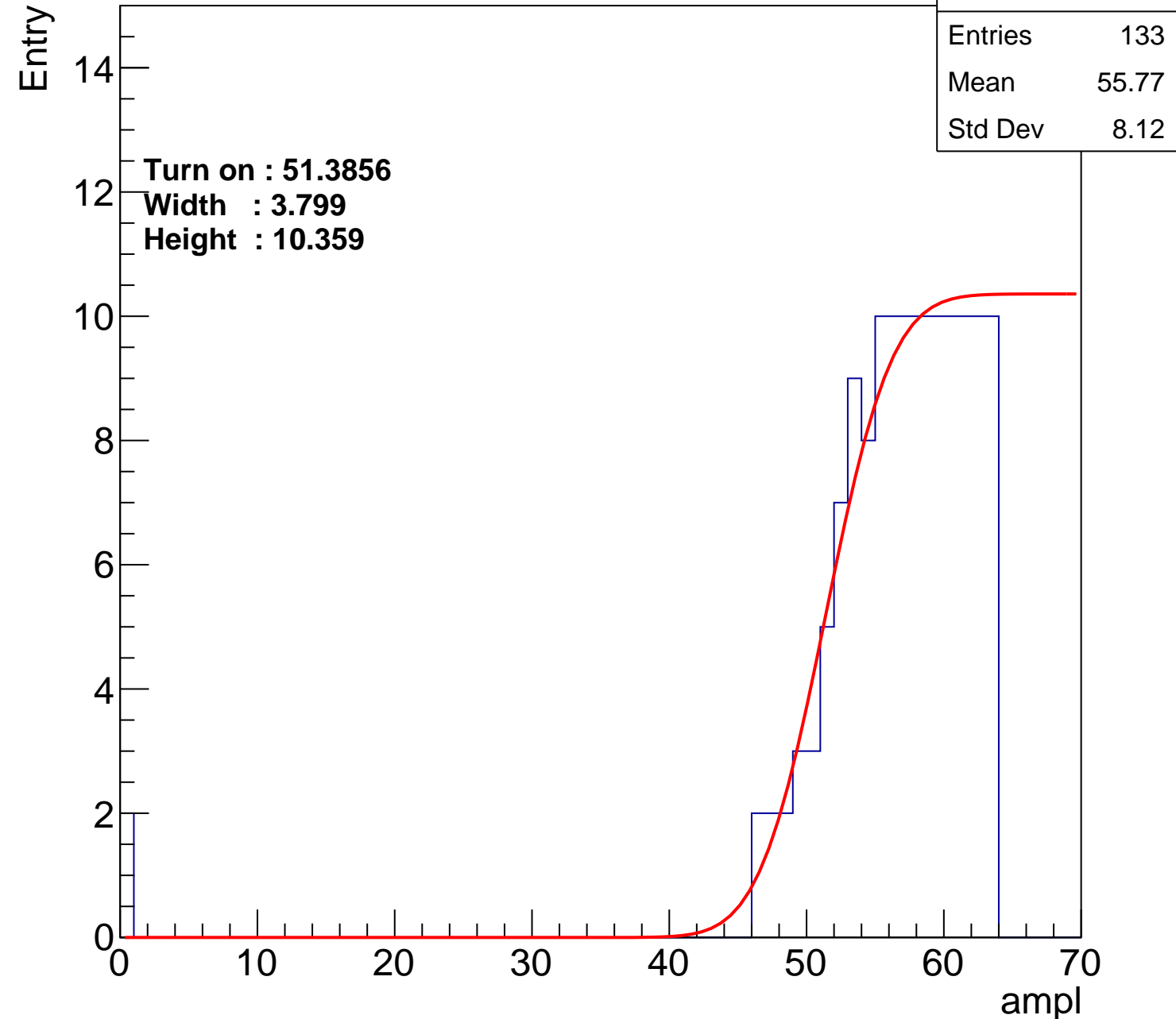
Width : 3.799

Height : 10.359

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch35

calib_packv5_040323_1717.root, FC#2, port C3

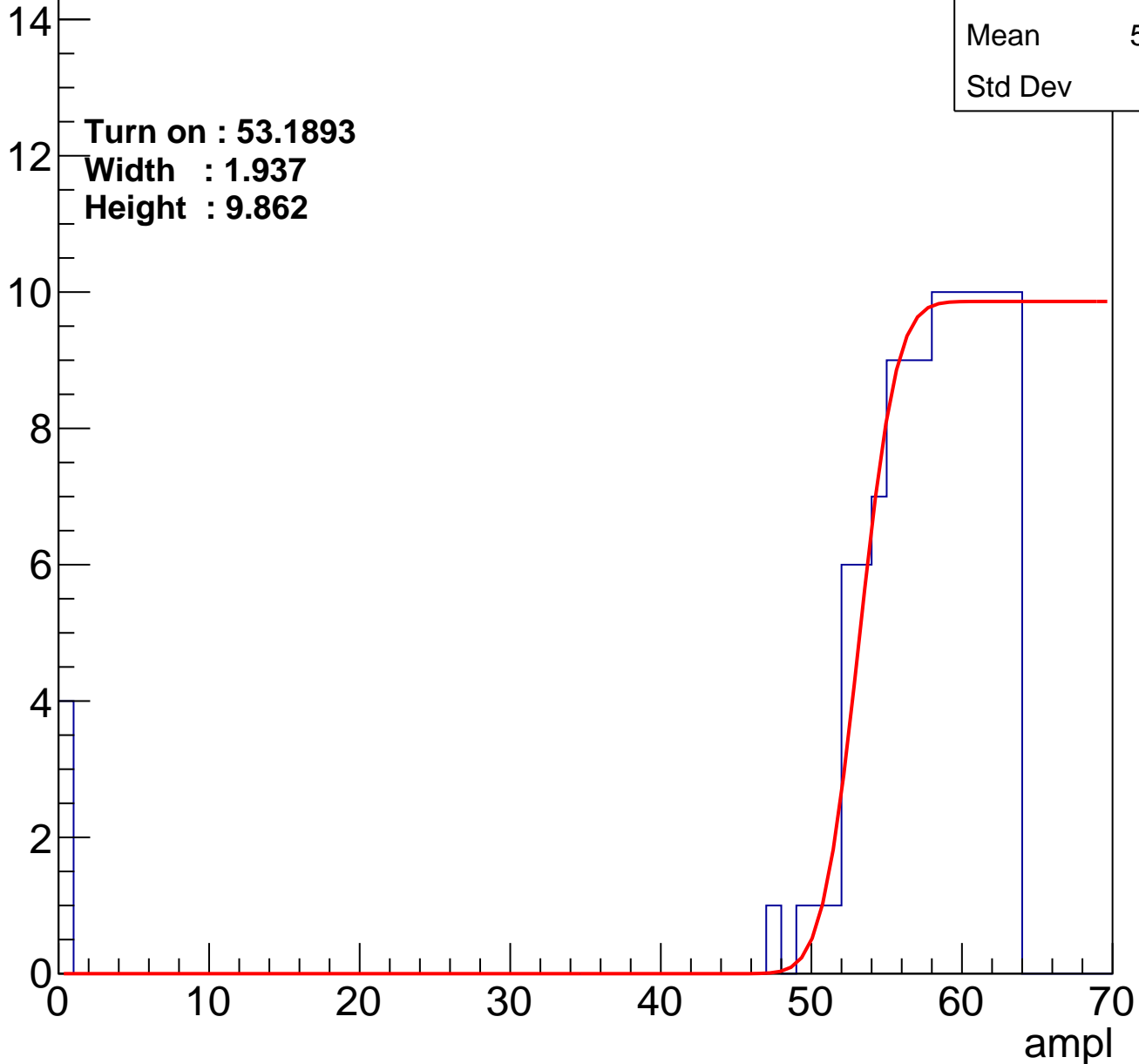
Entries	114
Mean	55.68
Std Dev	11.2

Turn on : 53.1893

Width : 1.937

Height : 9.862

Entry



B0L103S, U13-ch36

calib_packv5_040323_1717.root, FC#2, port C3

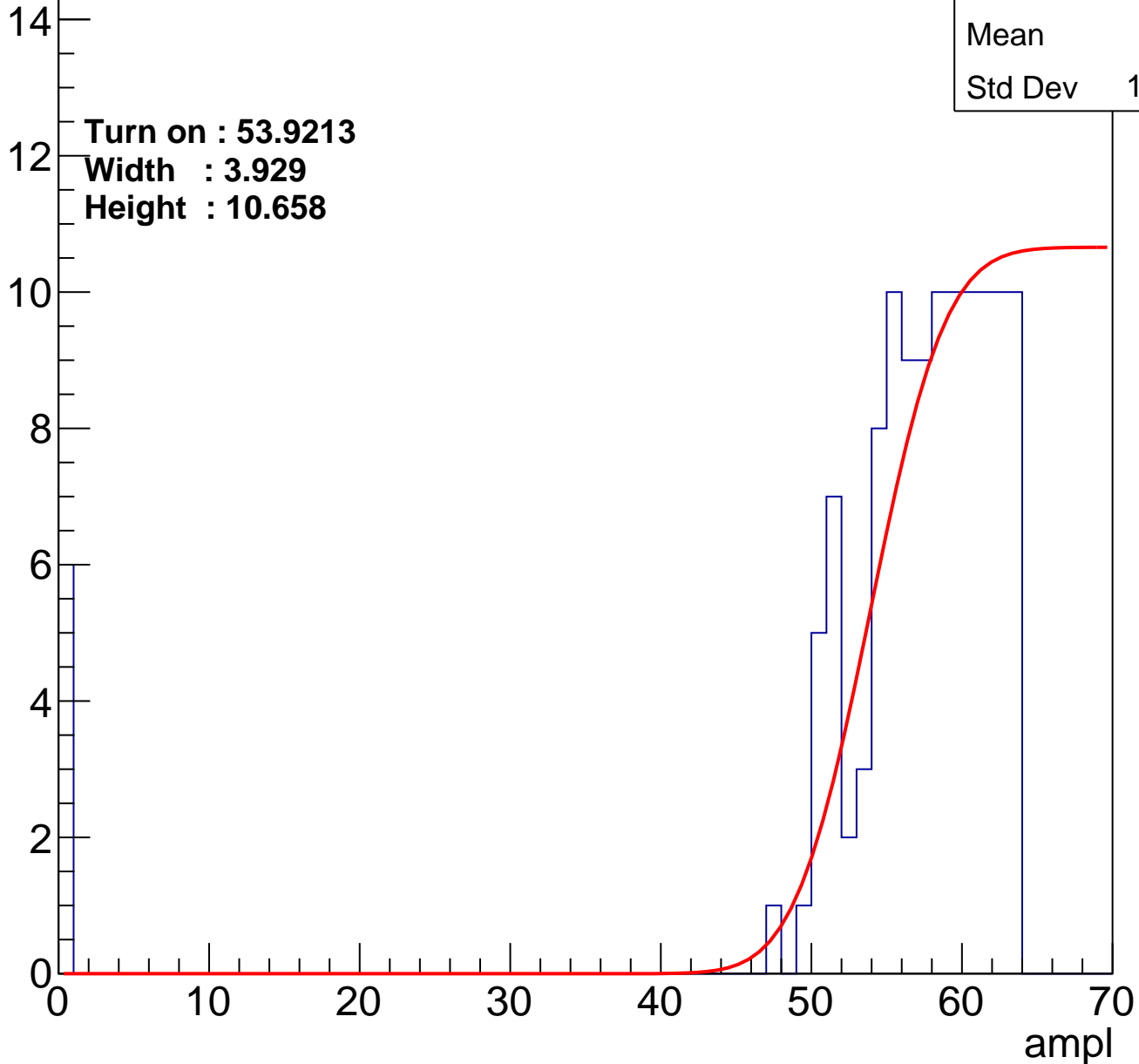
Entries	121
Mean	54.5
Std Dev	13.03

Turn on : 53.9213

Width : 3.929

Height : 10.658

Entry



B0L103S, U13-ch37

calib_packv5_040323_1717.root, FC#2, port C3

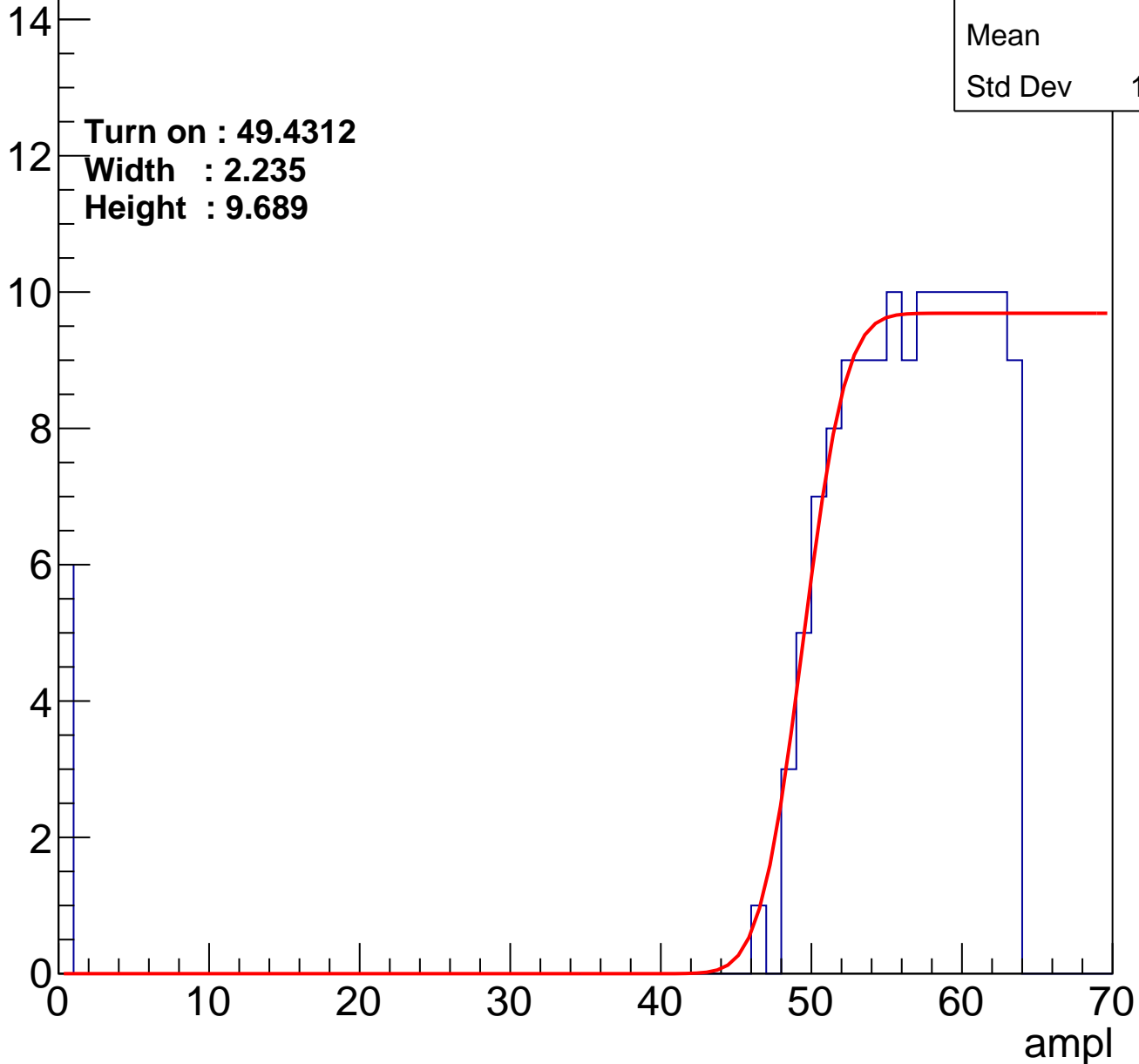
Entries	145
Mean	53.9
Std Dev	11.97

Turn on : 49.4312

Width : 2.235

Height : 9.689

Entry



B0L103S, U13-ch38

calib_packv5_040323_1717.root, FC#2, port C3

Entries	139
Mean	54.85
Std Dev	10.37

Turn on : 51.0929

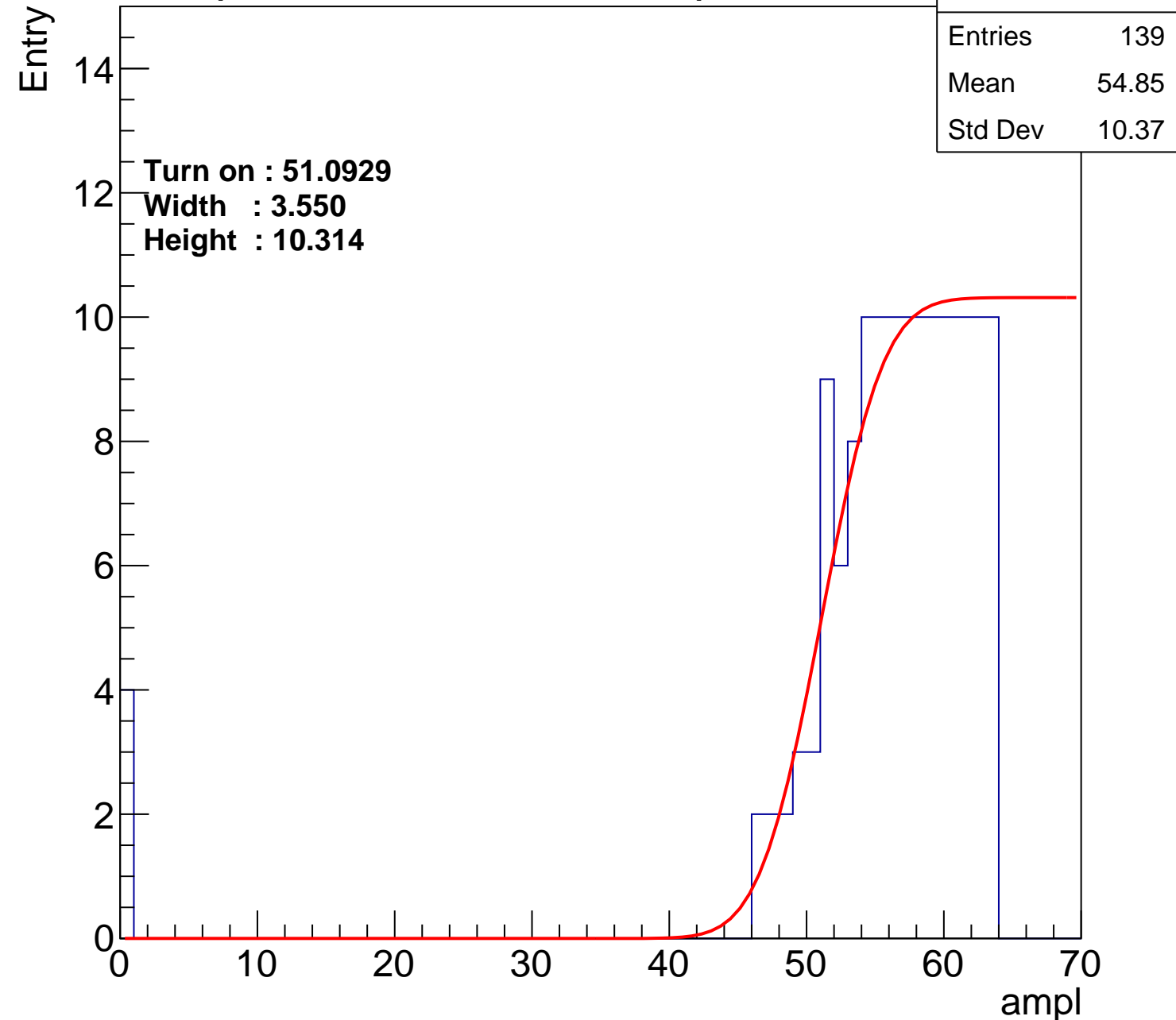
Width : 3.550

Height : 10.314

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch39

calib_packv5_040323_1717.root, FC#2, port C3

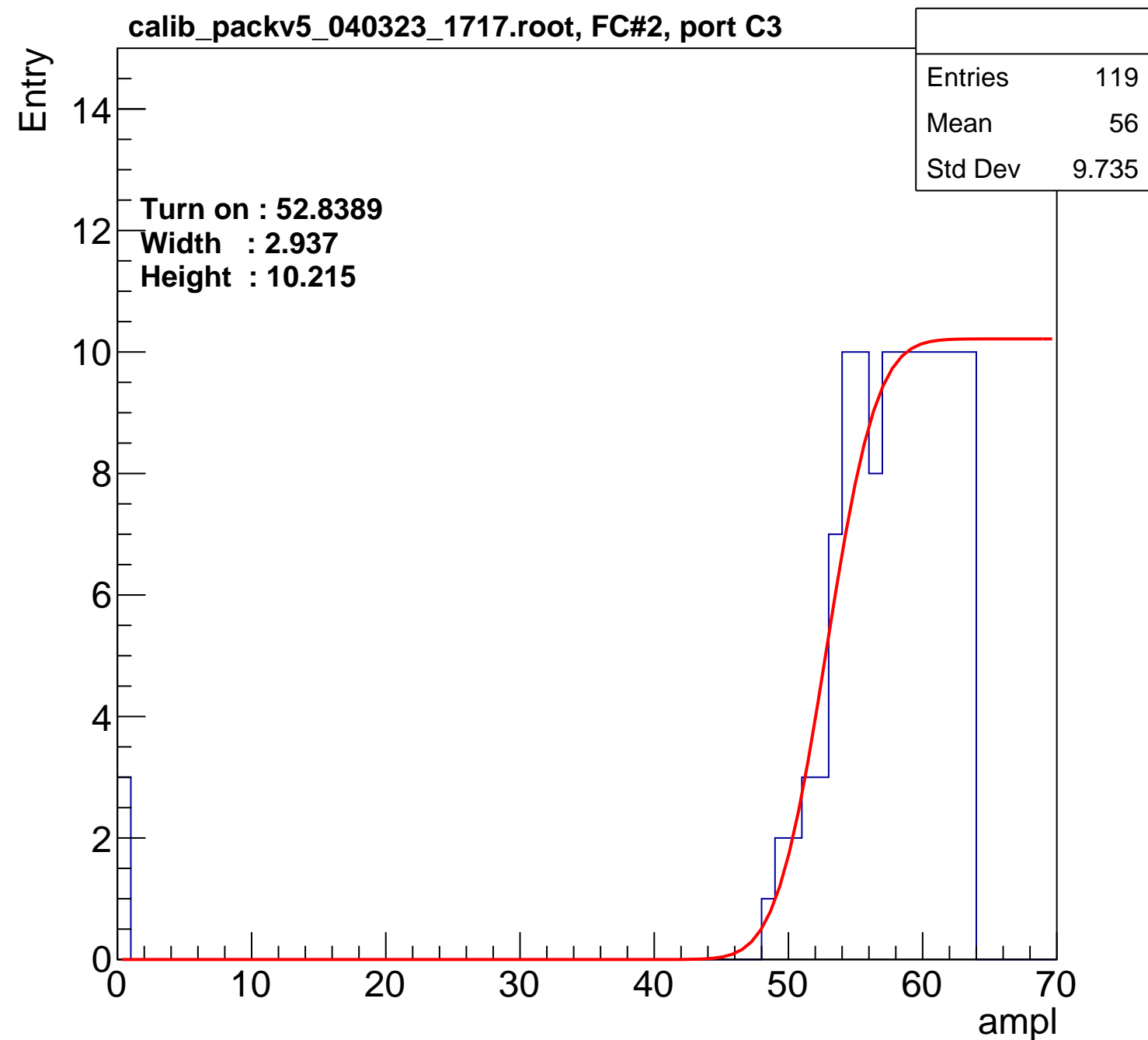
Entry

14
12
10
8
6
4
2
0

Turn on : 52.8389
Width : 2.937
Height : 10.215

Entries	119
Mean	56
Std Dev	9.735

ampl



B0L103S, U13-ch40

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	55.97
Std Dev	9.894

Turn on : 53.0869

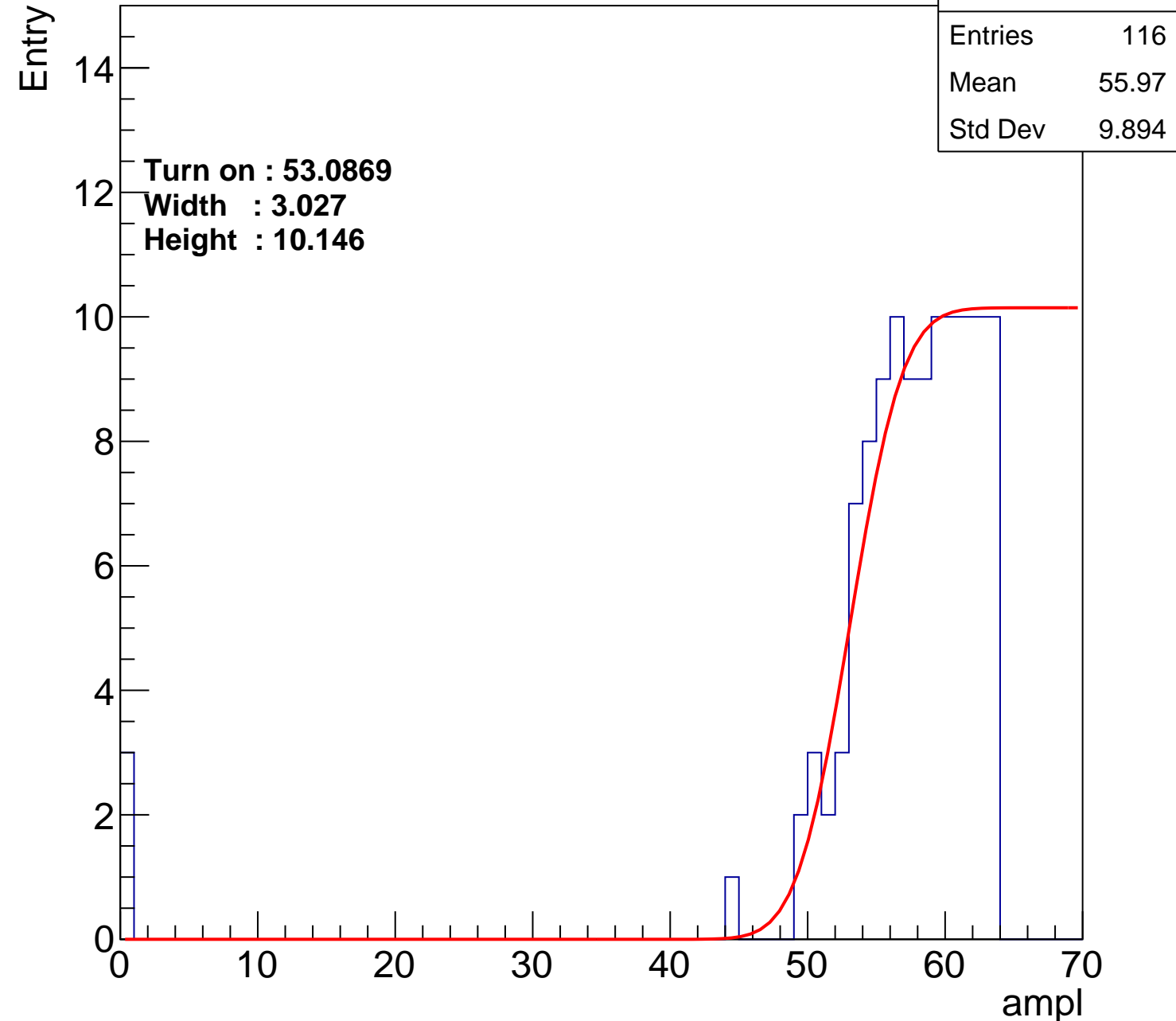
Width : 3.027

Height : 10.146

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch41

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.05
Std Dev	13.44

Turn on : 51.6072

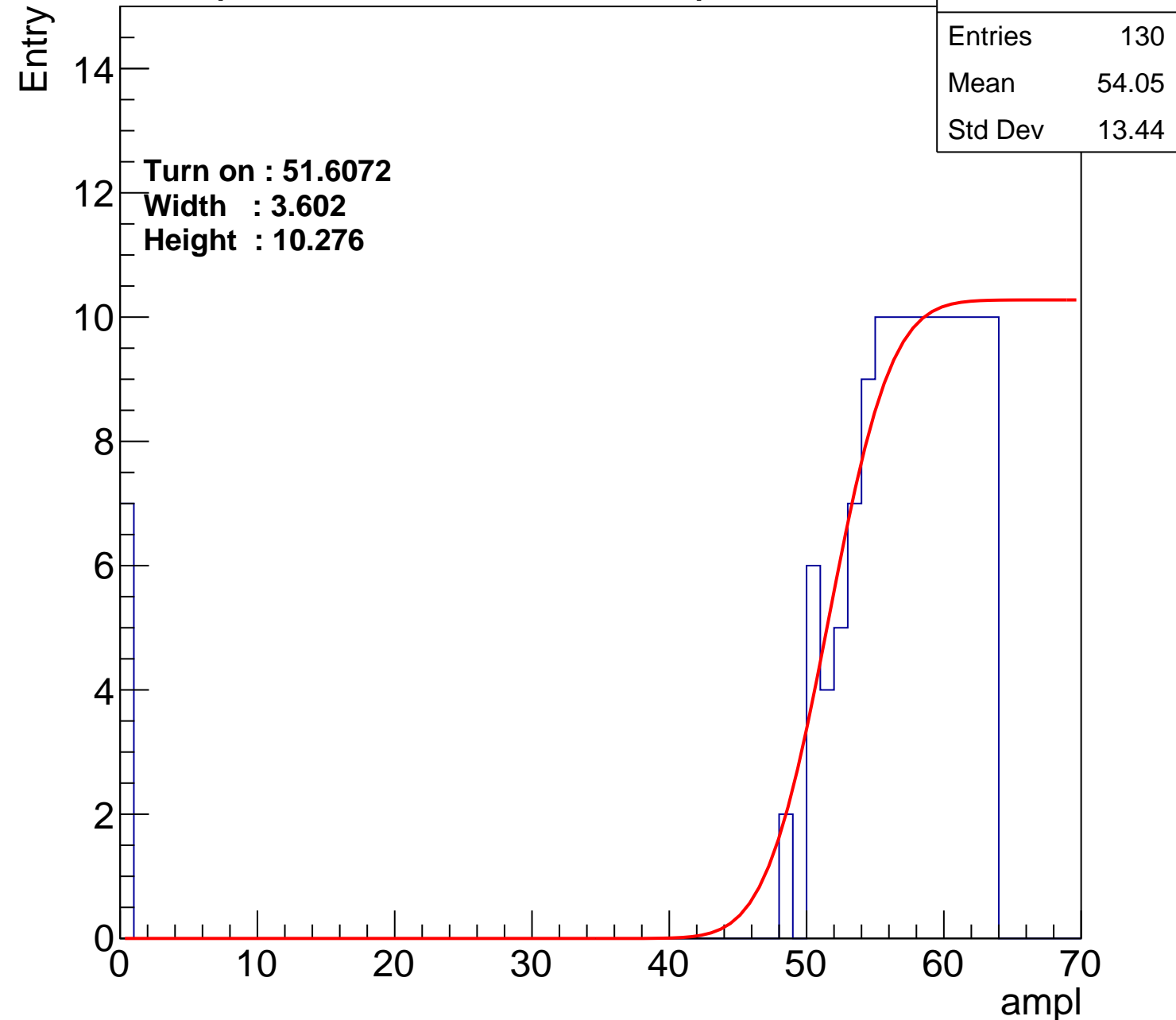
Width : 3.602

Height : 10.276

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch42

calib_packv5_040323_1717.root, FC#2, port C3

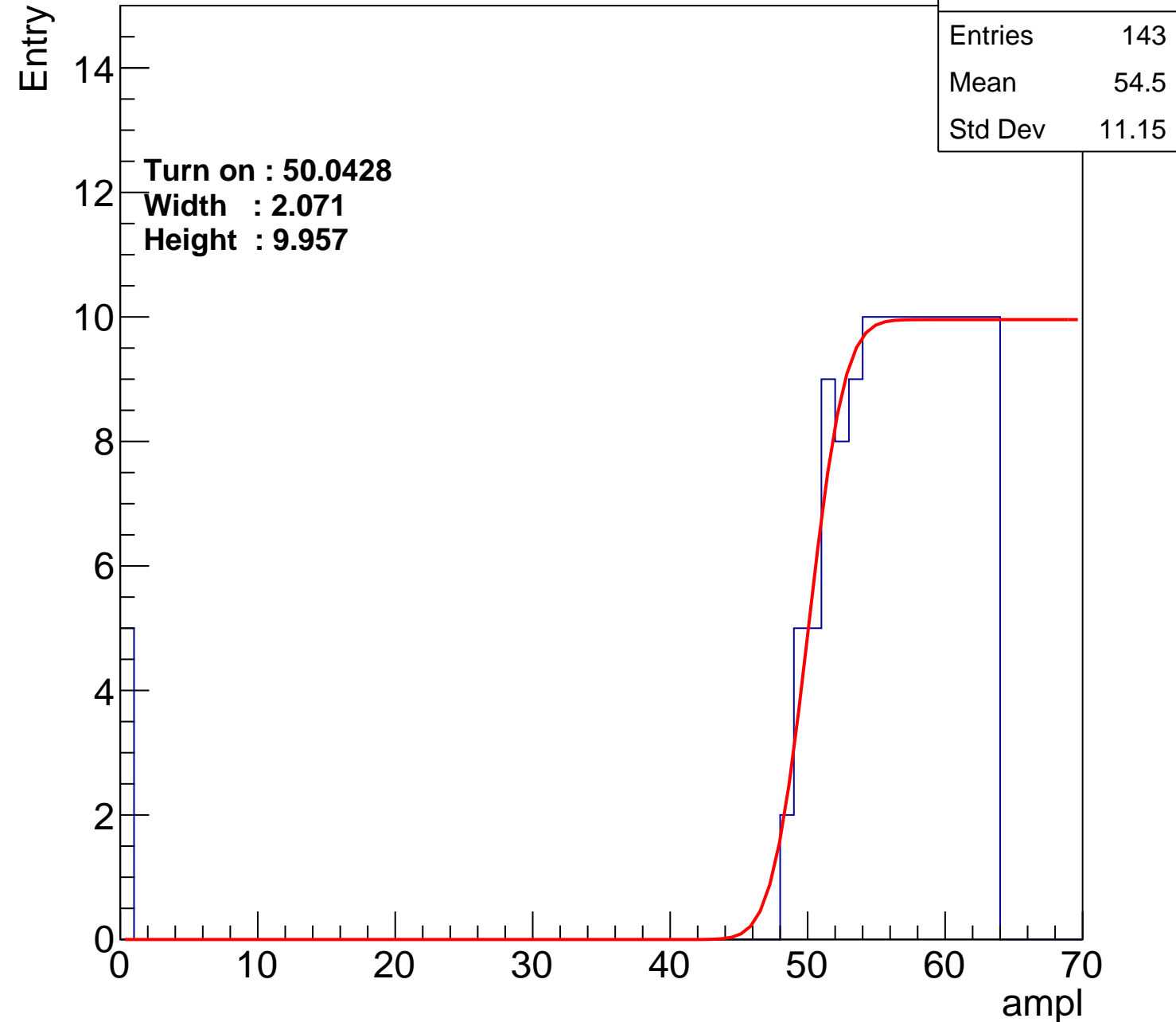
Entry

14
12
10
8
6
4
2
0

Turn on : 50.0428
Width : 2.071
Height : 9.957

Entries	143
Mean	54.5
Std Dev	11.15

ampl



B0L103S, U13-ch43

calib_packv5_040323_1717.root, FC#2, port C3

Entries	143
Mean	53.97
Std Dev	12.1

Turn on : 50.6348

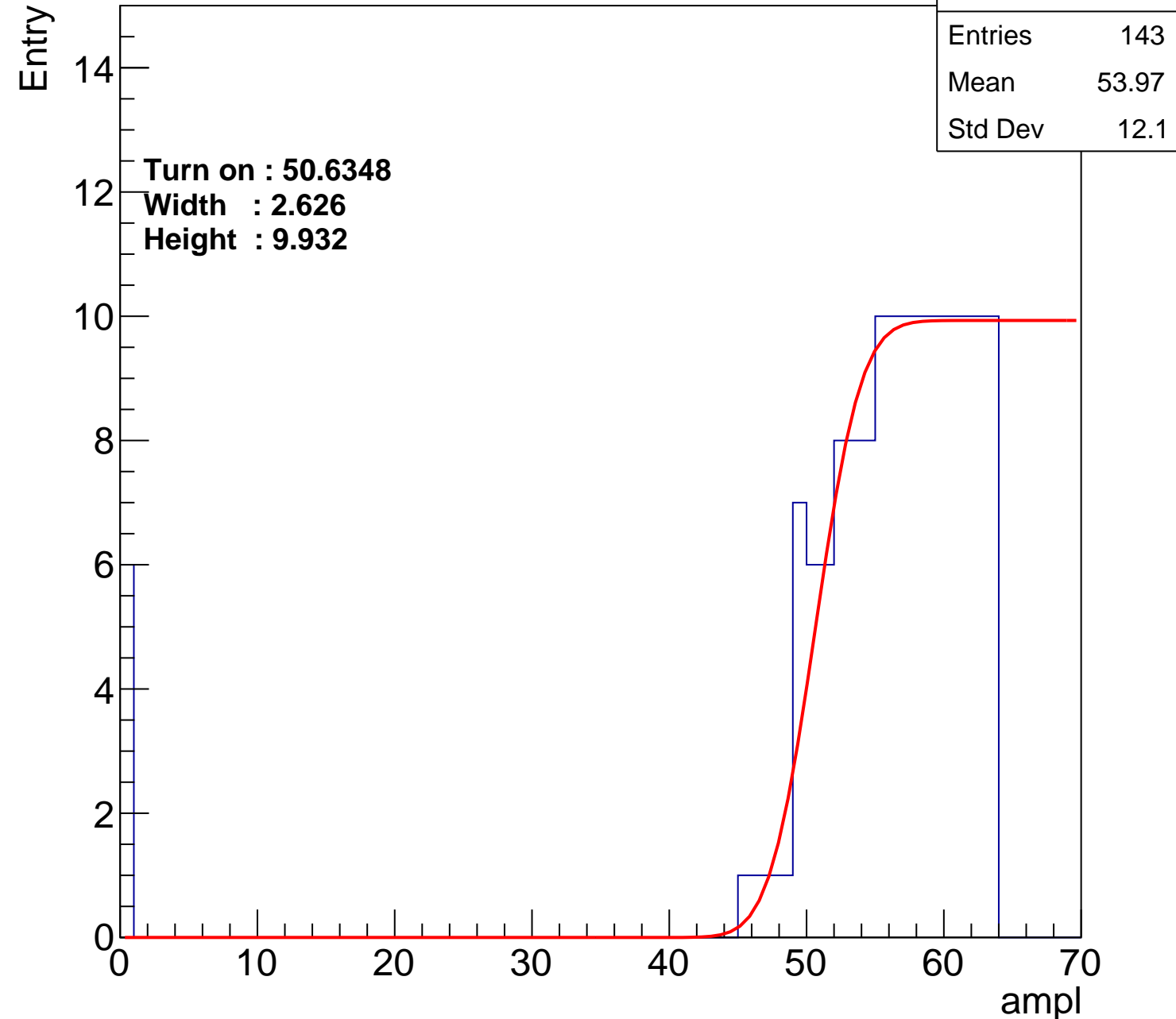
Width : 2.626

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch44

calib_packv5_040323_1717.root, FC#2, port C3

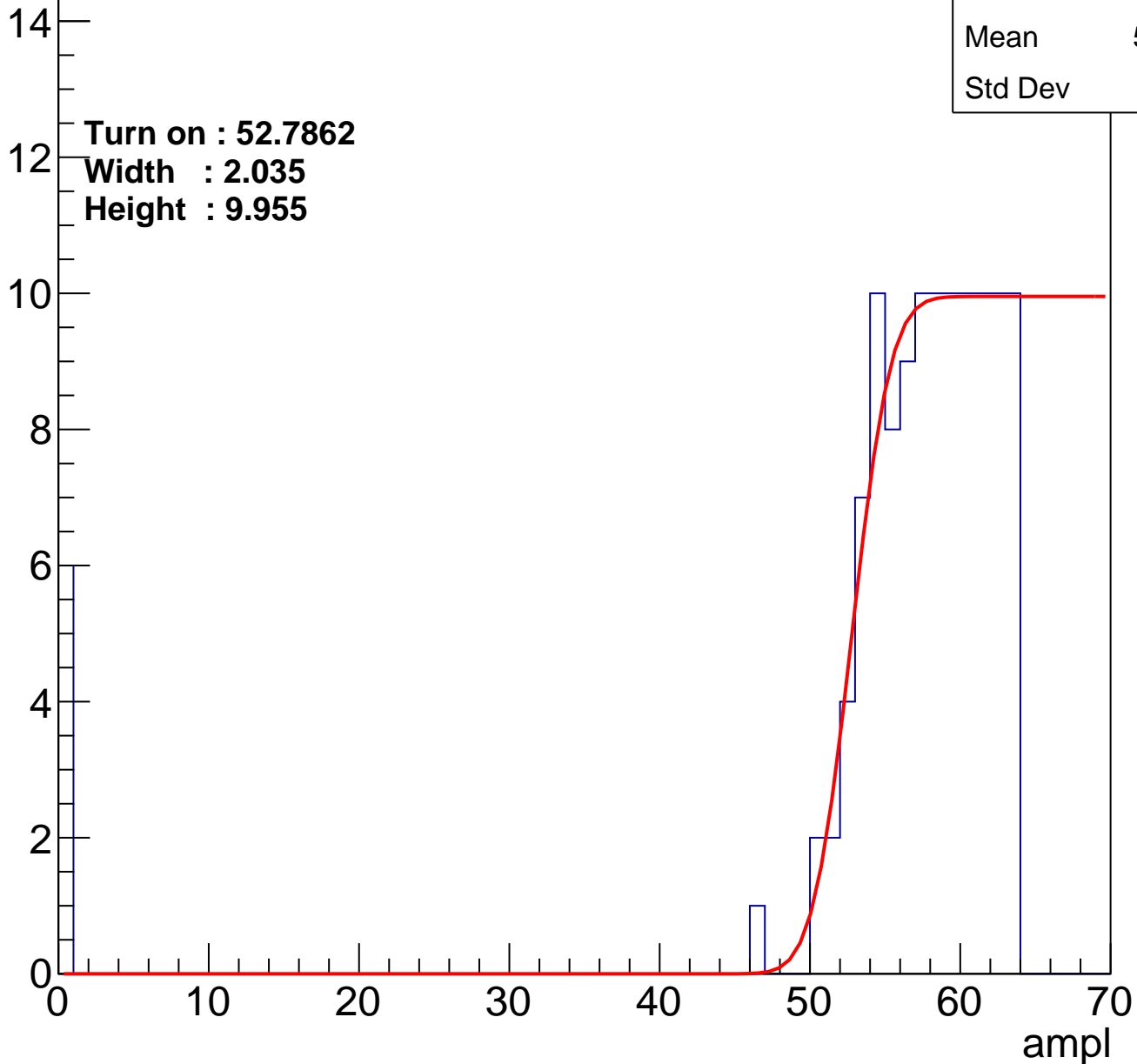
Entries	119
Mean	54.71
Std Dev	13.1

Turn on : 52.7862

Width : 2.035

Height : 9.955

Entry



B0L103S, U13-ch45

calib_packv5_040323_1717.root, FC#2, port C3

Entries	126
Mean	56.44
Std Dev	6.557

Turn on : 51.8874

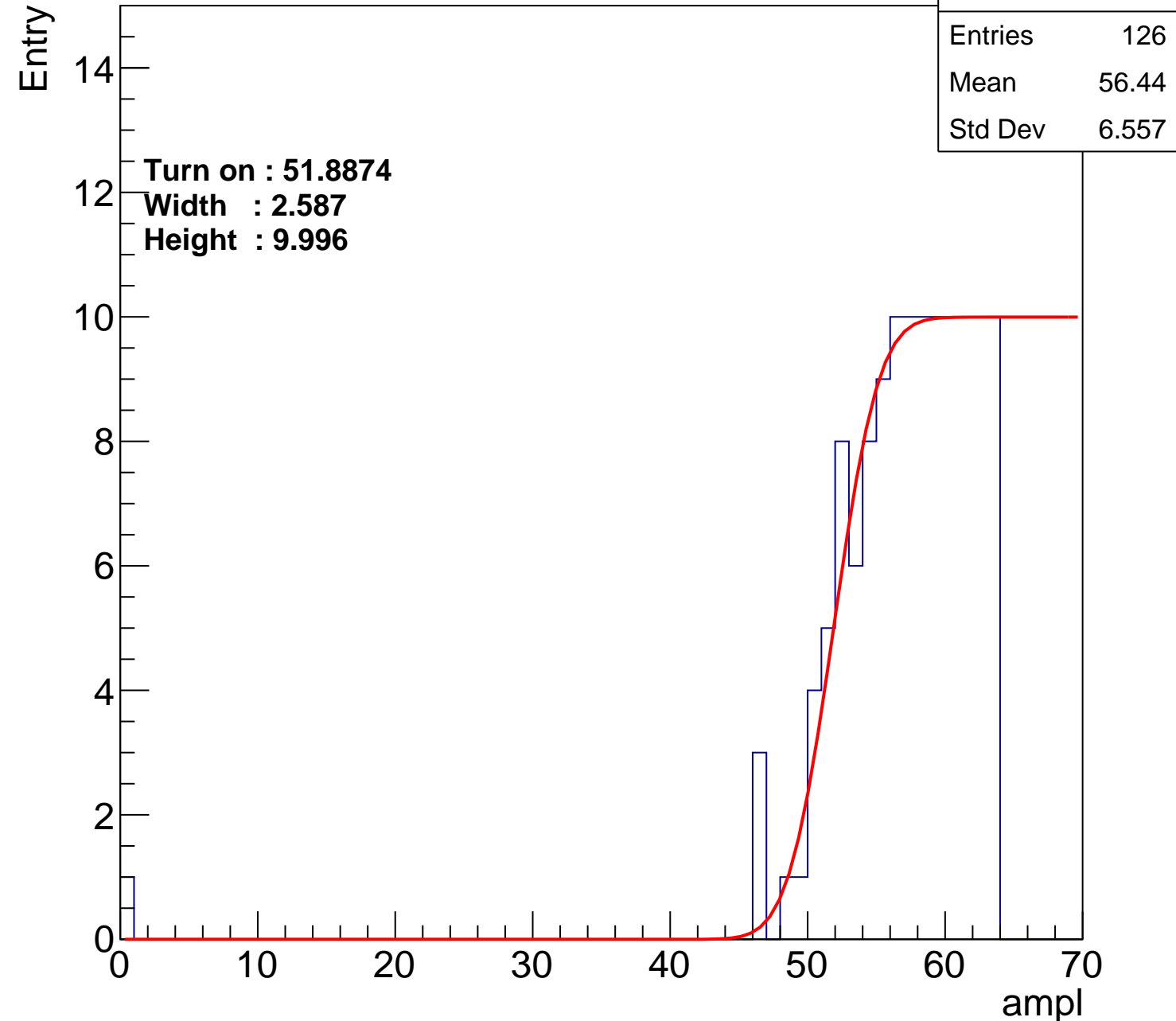
Width : 2.587

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch46

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.18
Std Dev	8.154

Turn on : 51.7683

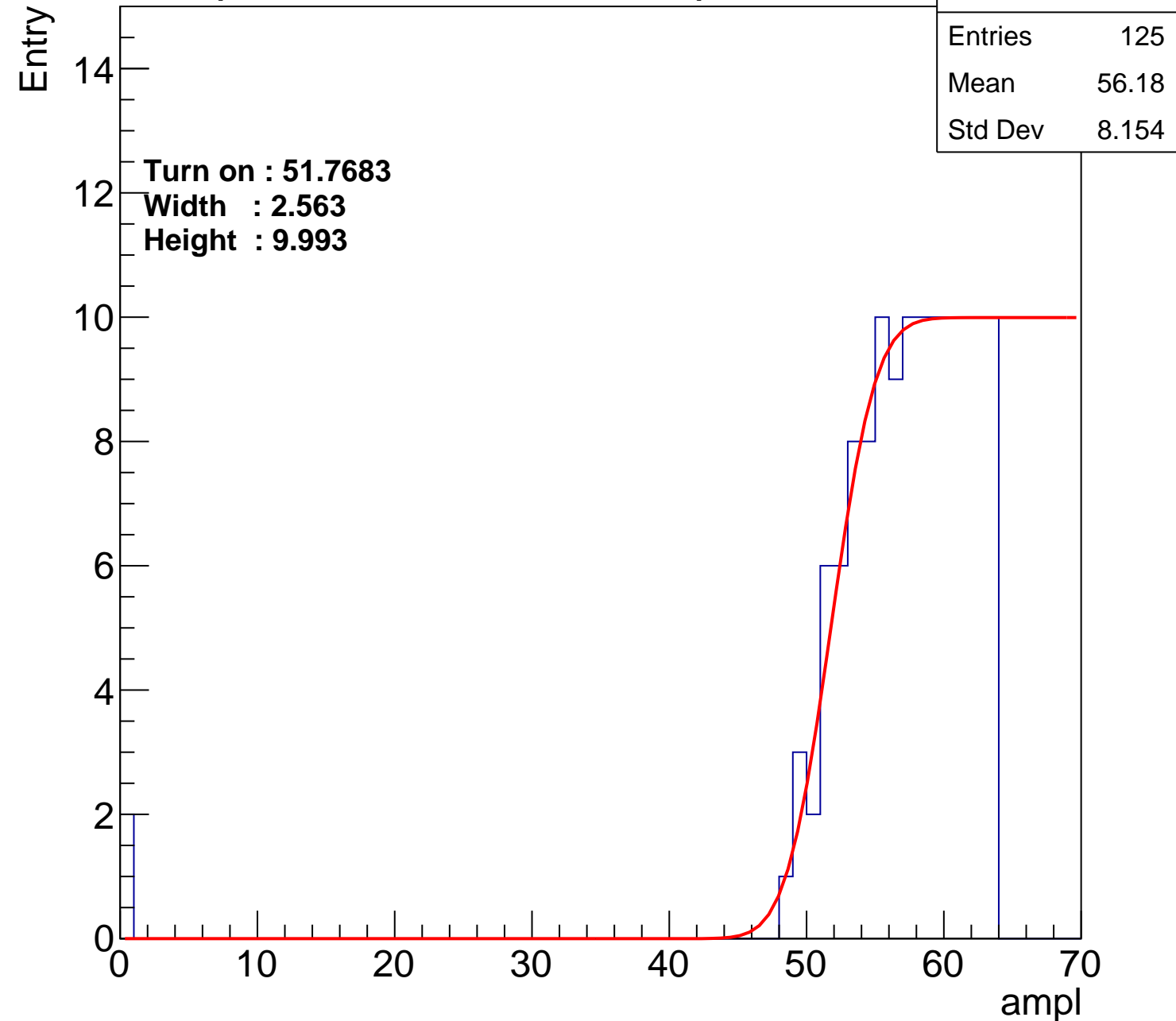
Width : 2.563

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch47

calib_packv5_040323_1717.root, FC#2, port C3

Entries	120
Mean	55.96
Std Dev	9.708

Turn on : 52.5893

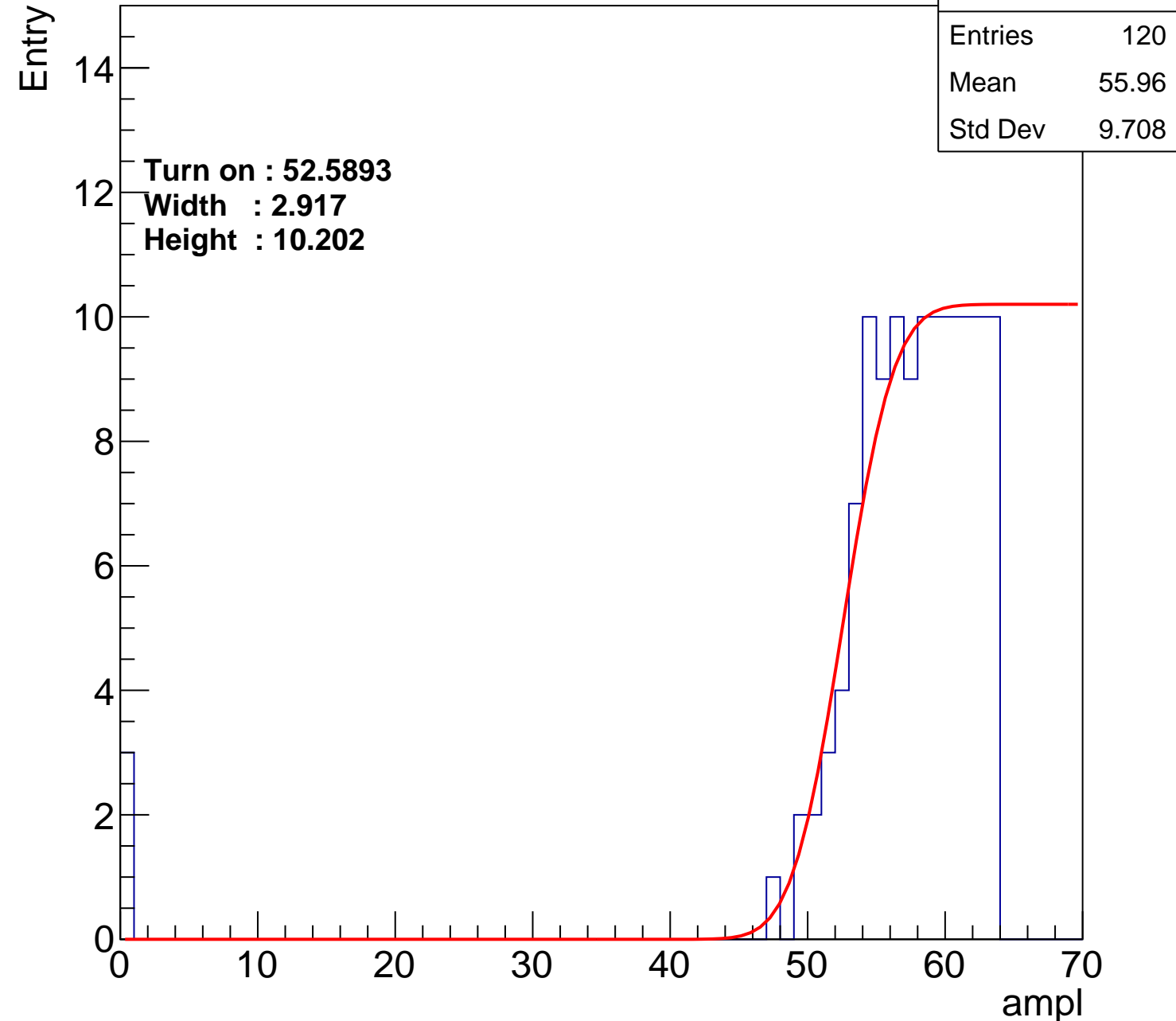
Width : 2.917

Height : 10.202

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch48

calib_packv5_040323_1717.root, FC#2, port C3

Entries	147
Mean	55.7
Std Dev	6.37

Turn on : 49.7468

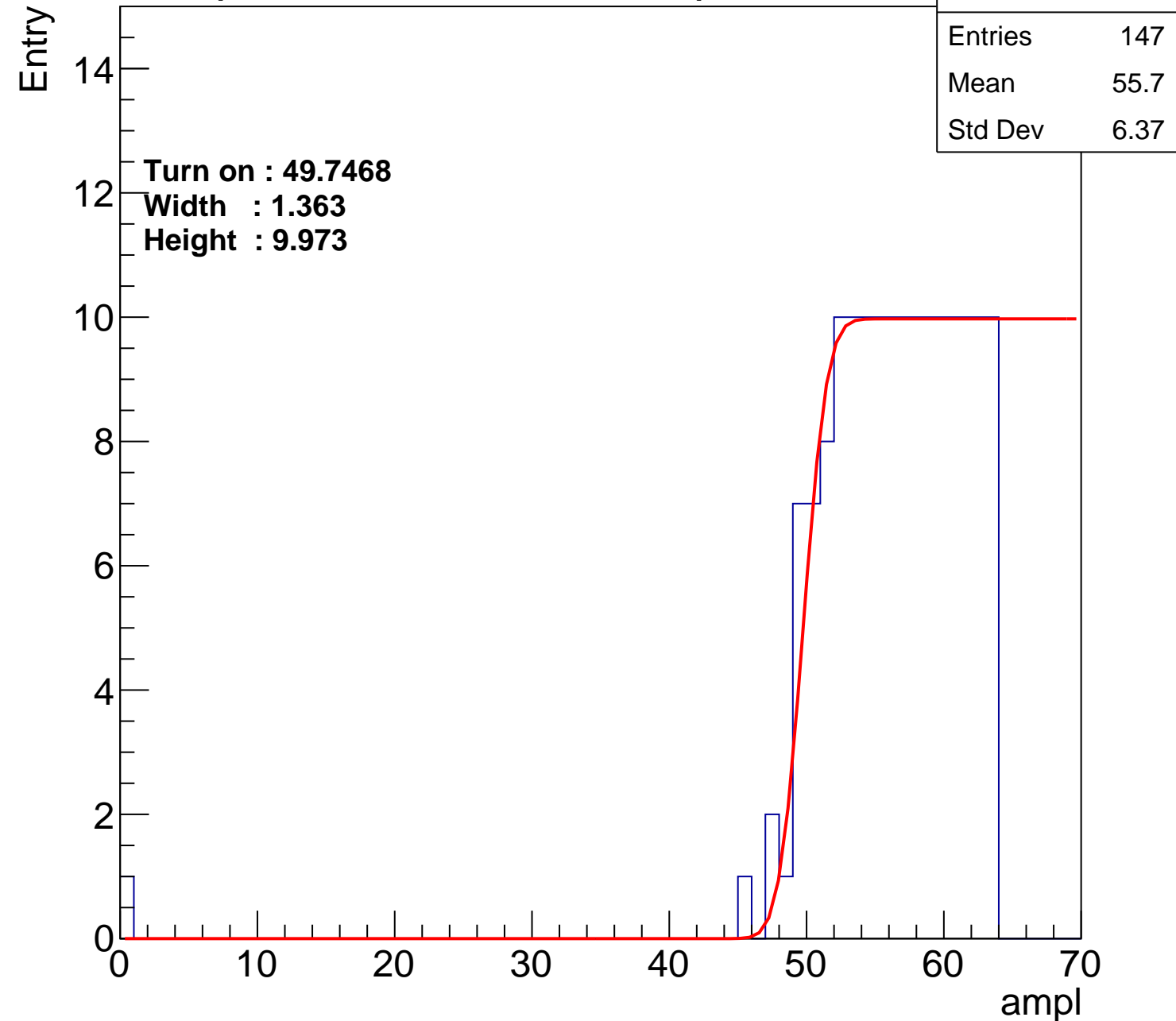
Width : 1.363

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch49

calib_packv5_040323_1717.root, FC#2, port C3

Entries	107
Mean	56.93
Std Dev	8.528

Turn on : 53.3780

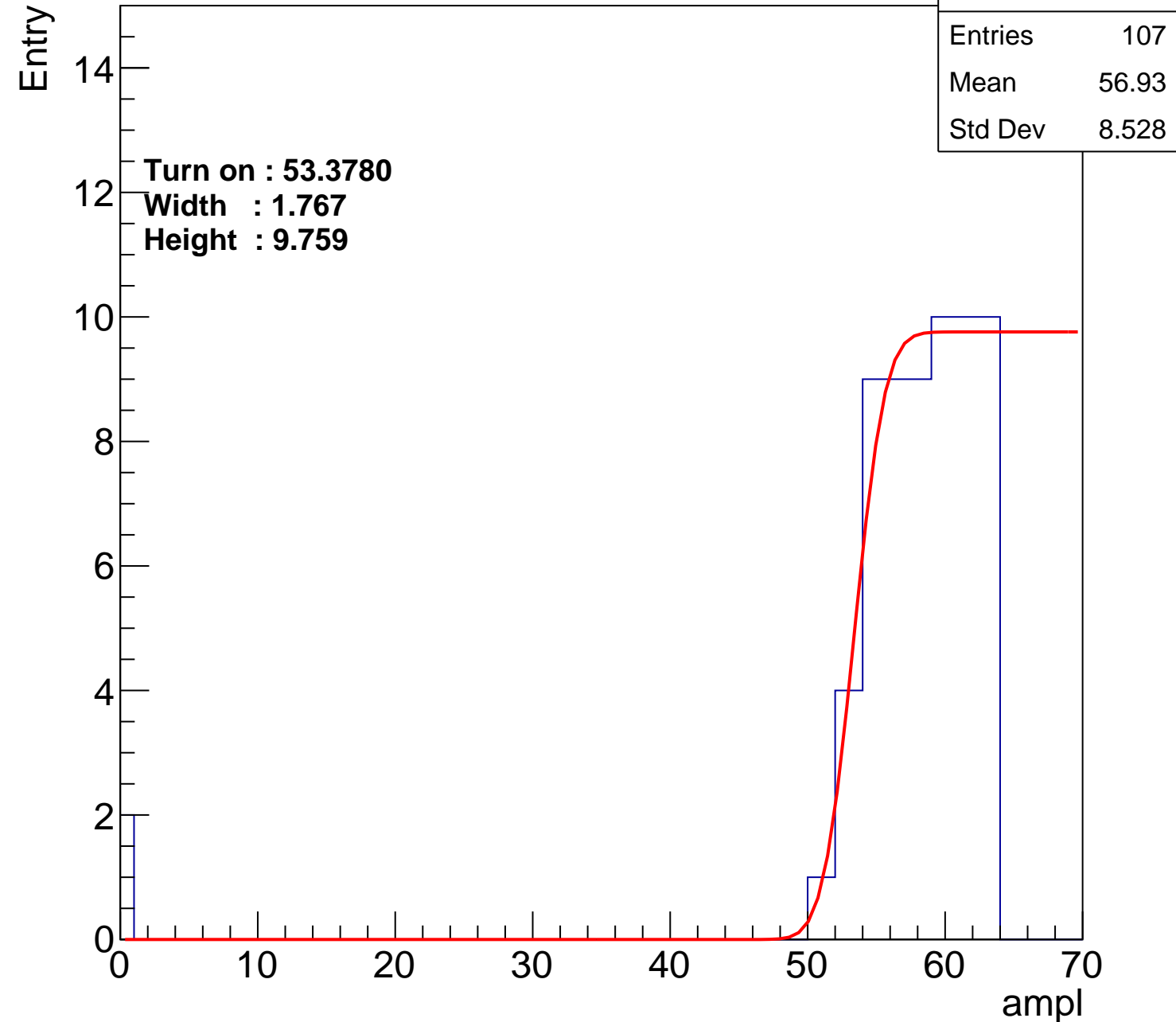
Width : 1.767

Height : 9.759

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch50

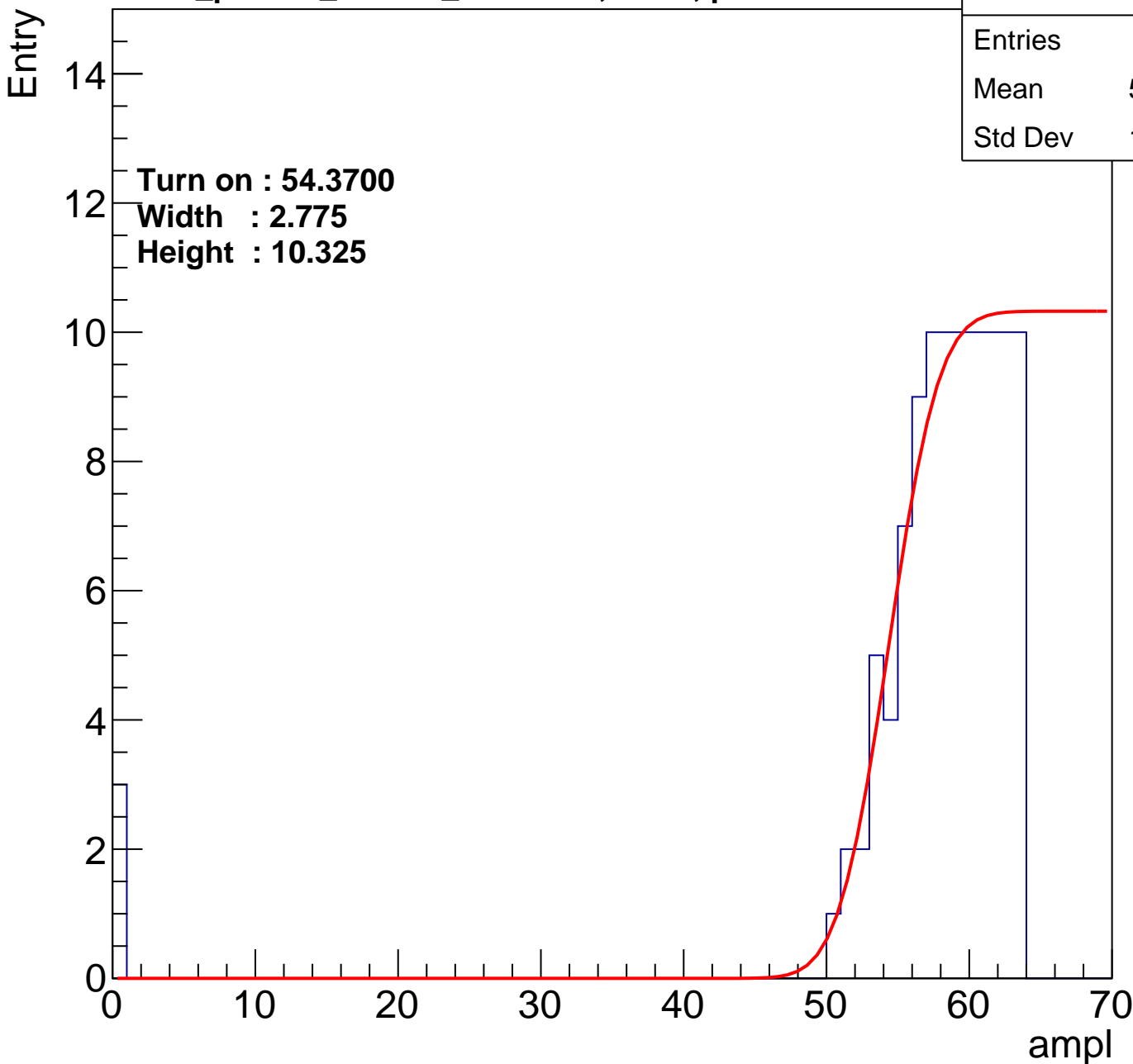
calib_packv5_040323_1717.root, FC#2, port C3

Entries	103
Mean	56.56
Std Dev	10.32

Turn on : 54.3700

Width : 2.775

Height : 10.325



B0L103S, U13-ch51

calib_packv5_040323_1717.root, FC#2, port C3

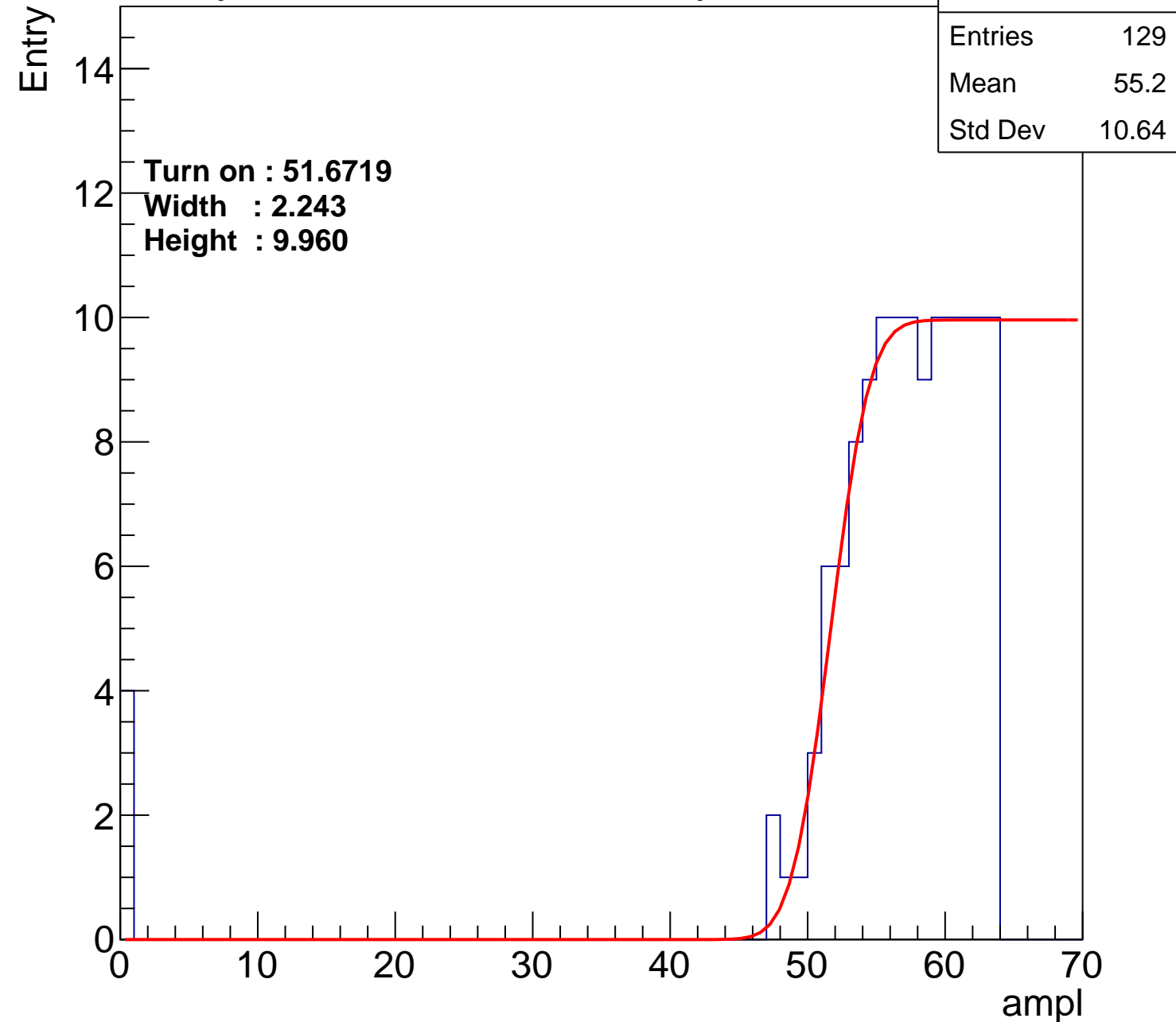
Entry

14
12
10
8
6
4
2
0

Turn on : 51.6719
Width : 2.243
Height : 9.960

Entries	129
Mean	55.2
Std Dev	10.64

ampl



B0L103S, U13-ch52

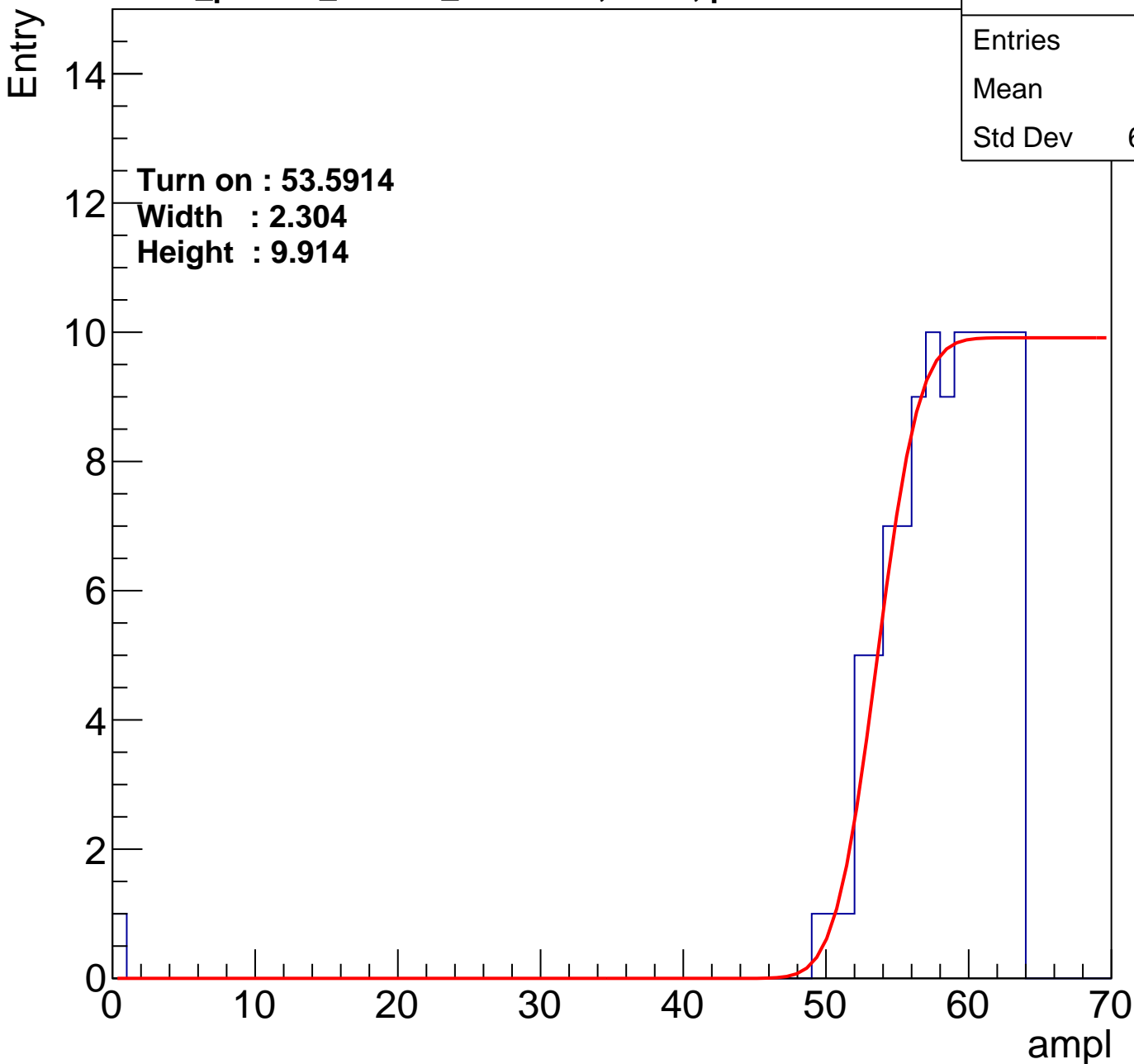
calib_packv5_040323_1717.root, FC#2, port C3

Entries	106
Mean	57.4
Std Dev	6.584

Turn on : 53.5914

Width : 2.304

Height : 9.914



B0L103S, U13-ch53

calib_packv5_040323_1717.root, FC#2, port C3

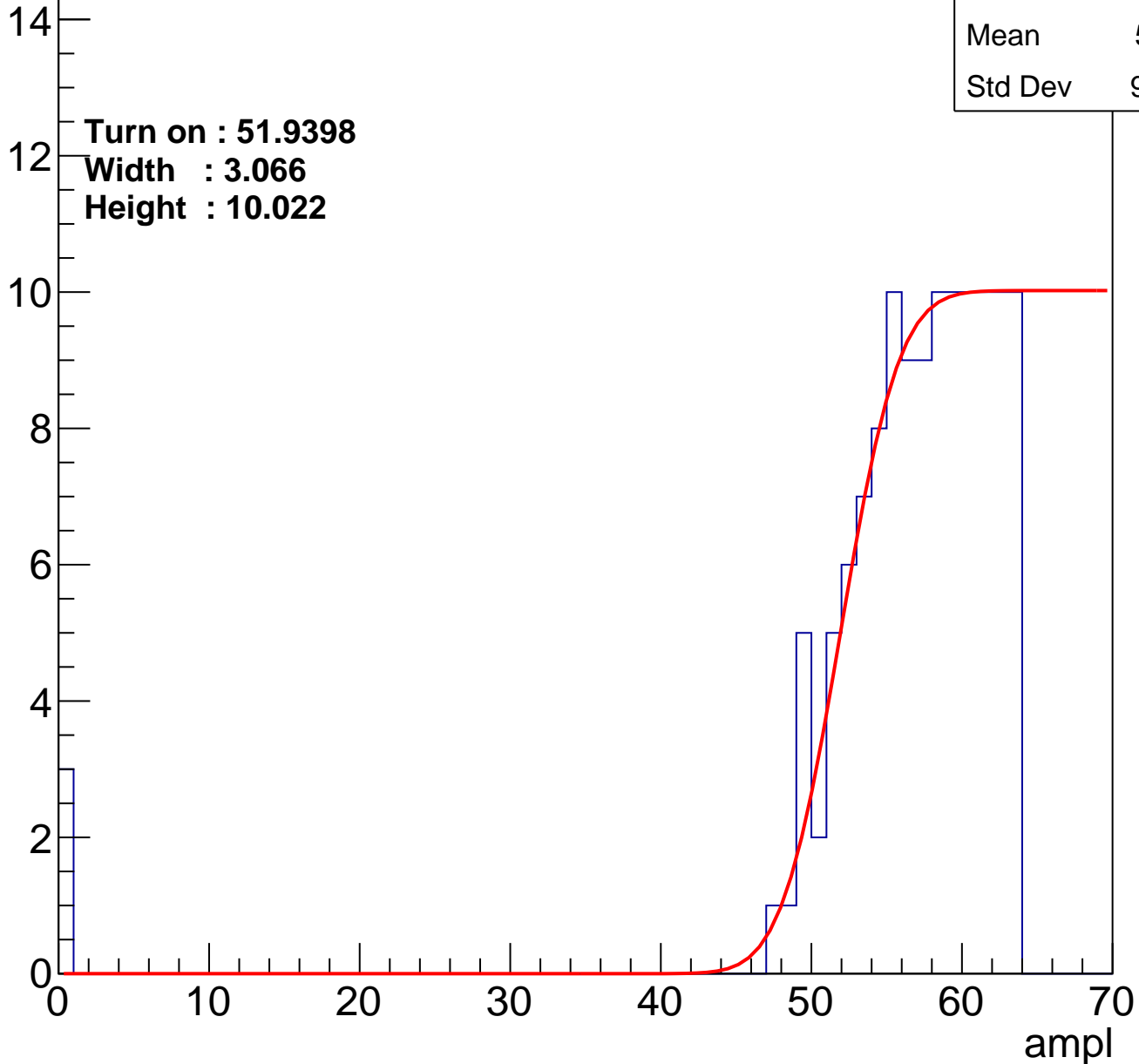
Entries	126
Mean	55.61
Std Dev	9.585

Turn on : 51.9398

Width : 3.066

Height : 10.022

Entry



B0L103S, U13-ch54

calib_packv5_040323_1717.root, FC#2, port C3

Entries	145
Mean	54.07
Std Dev	11.98

Turn on : 50.0997

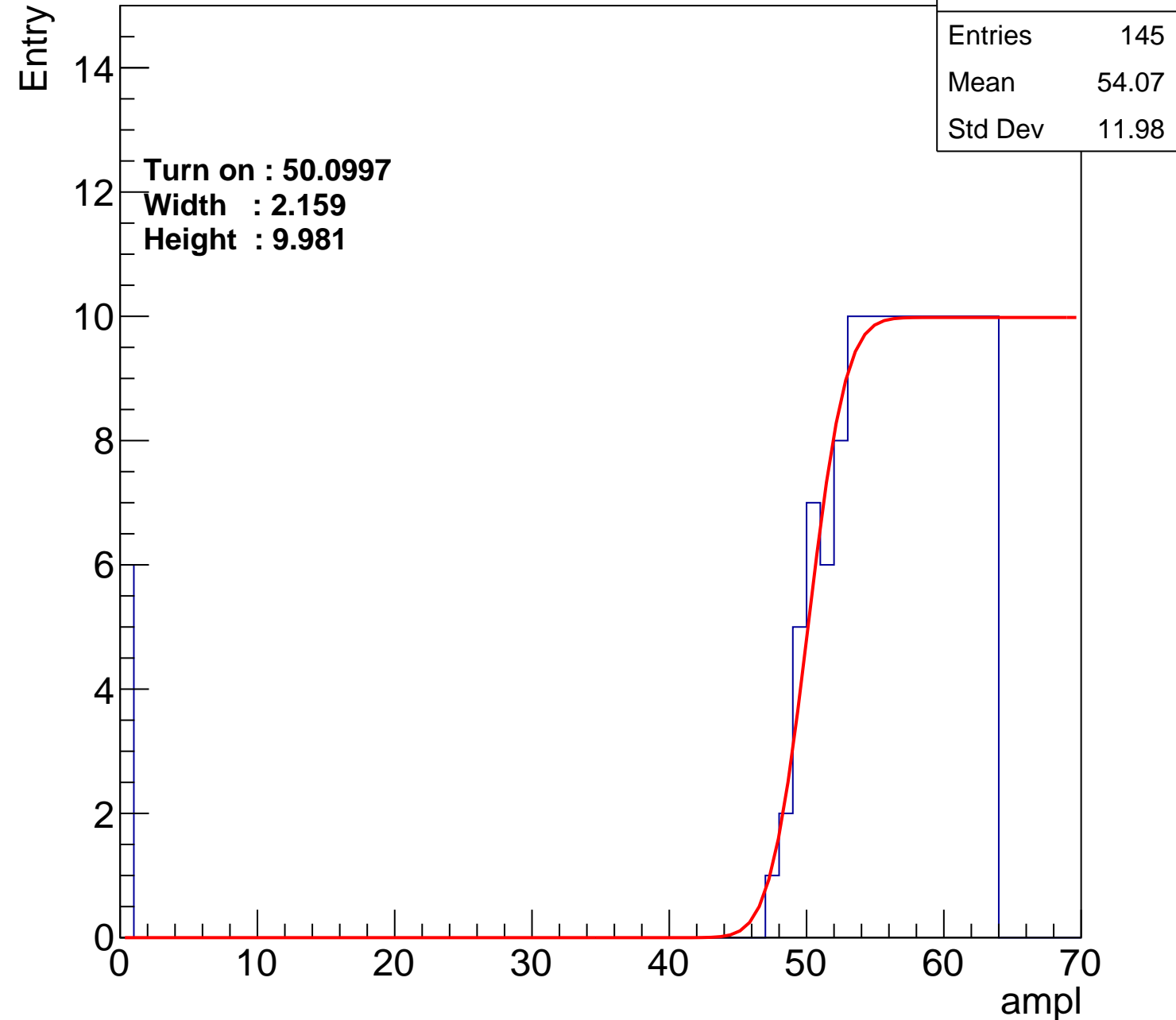
Width : 2.159

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch55

calib_packv5_040323_1717.root, FC#2, port C3

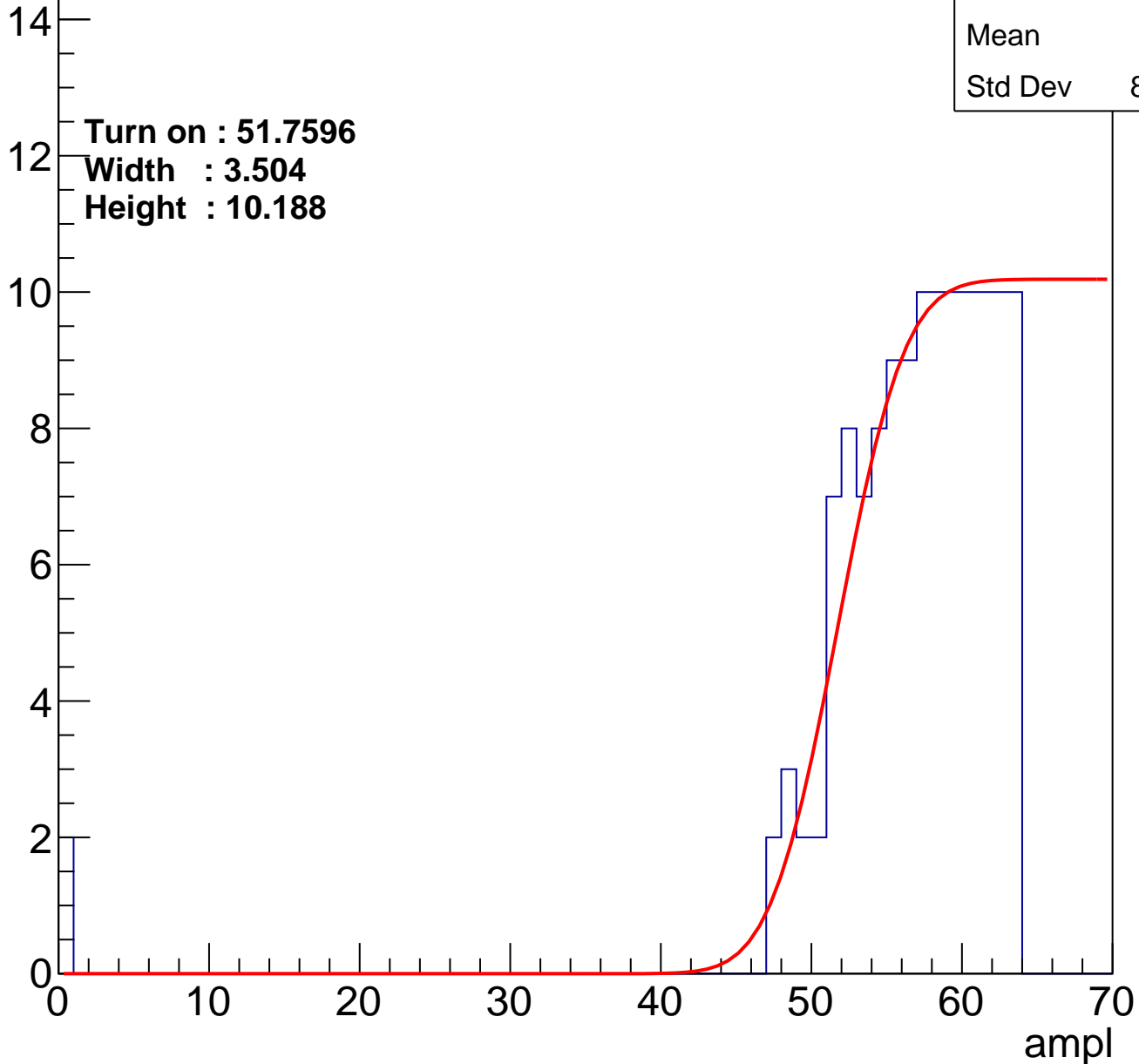
Entries	129
Mean	55.9
Std Dev	8.166

Turn on : 51.7596

Width : 3.504

Height : 10.188

Entry



B0L103S, U13-ch56

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.72
Std Dev	9.647

Turn on : 52.1708

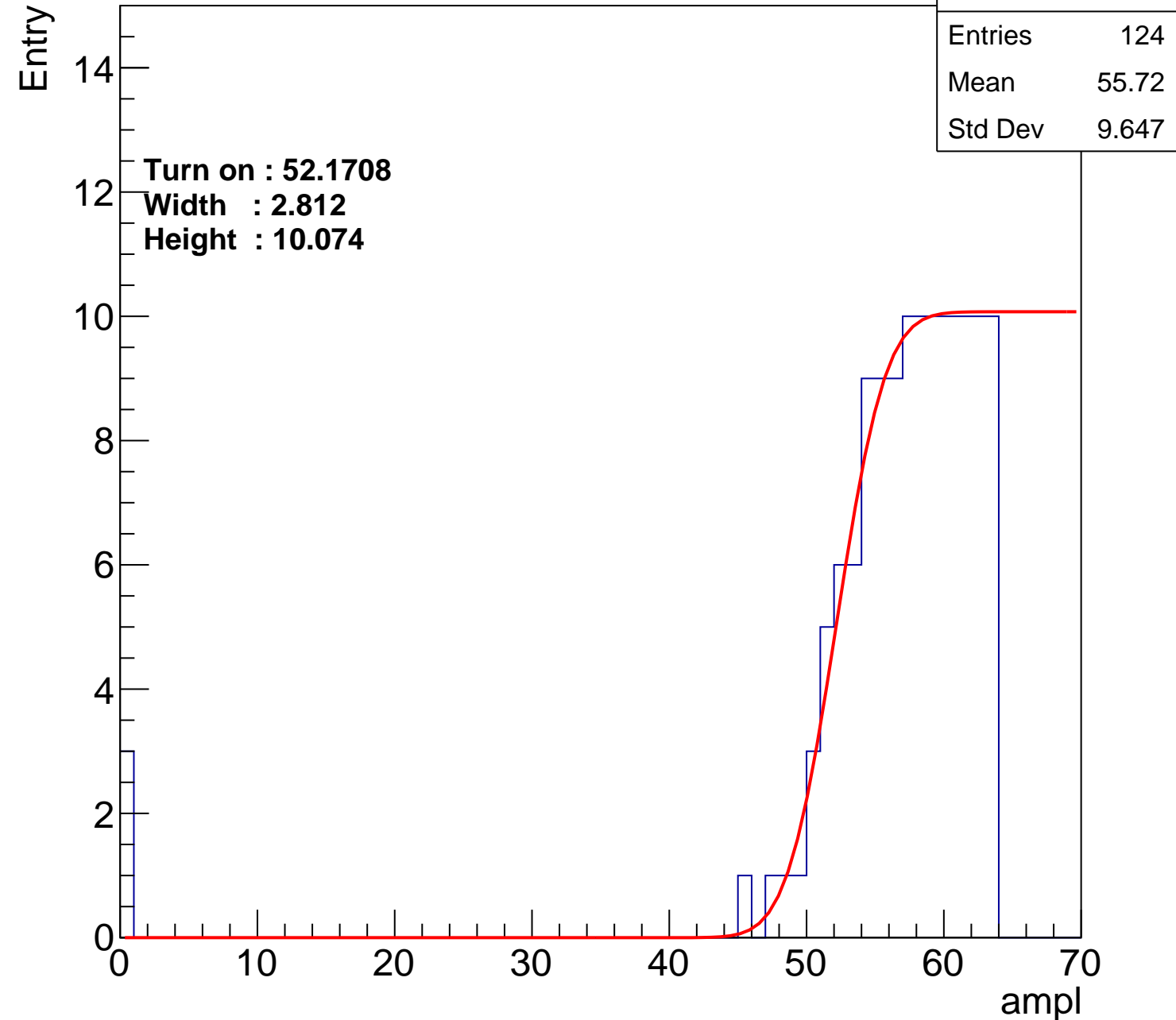
Width : 2.812

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch57

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.99
Std Dev	10.46

Turn on : 50.7335

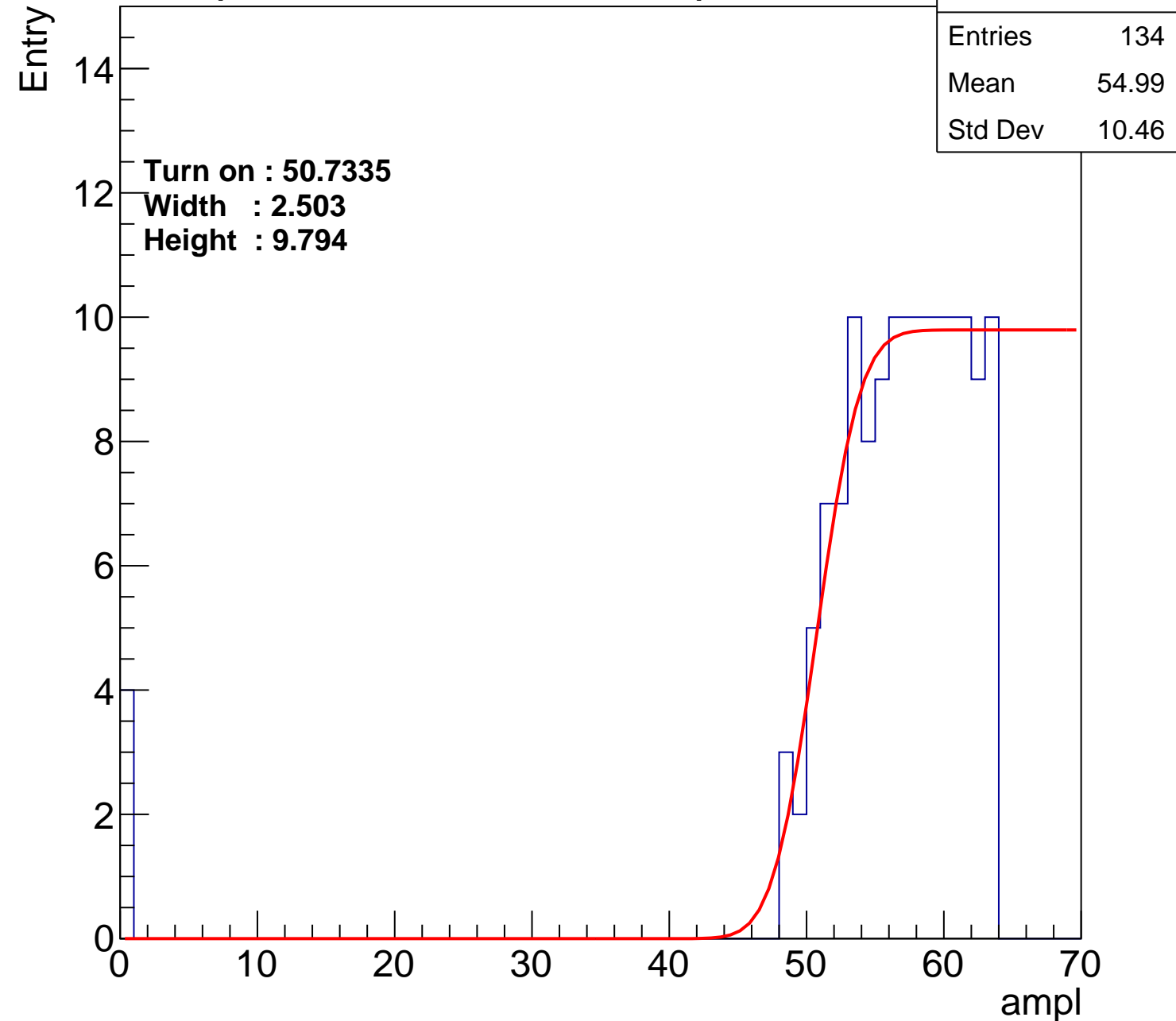
Width : 2.503

Height : 9.794

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch58

calib_packv5_040323_1717.root, FC#2, port C3

Entries	115
Mean	56.52
Std Dev	8.412

Turn on : 53.0641

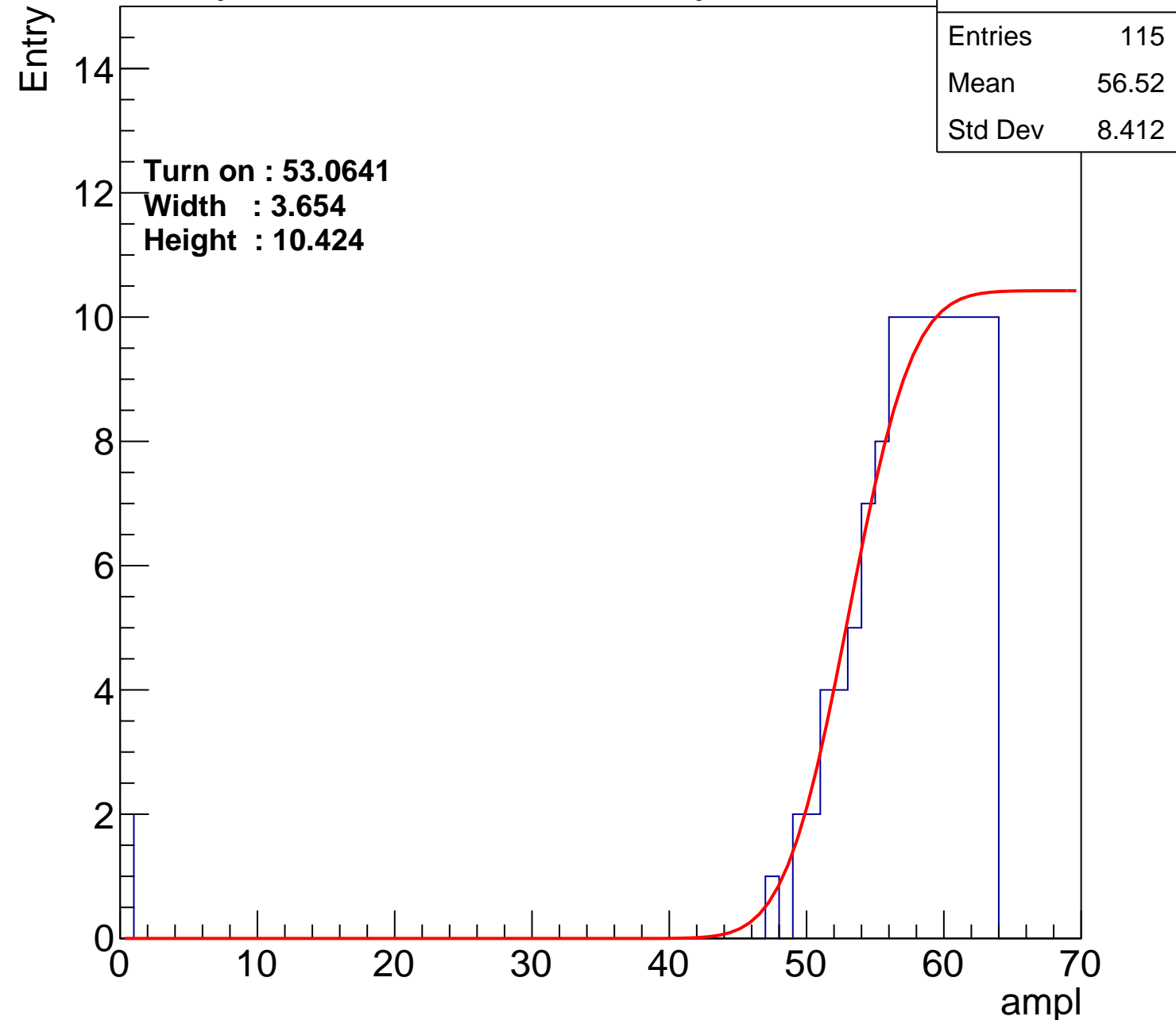
Width : 3.654

Height : 10.424

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch59

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.66
Std Dev	11.5

Turn on : 51.0925

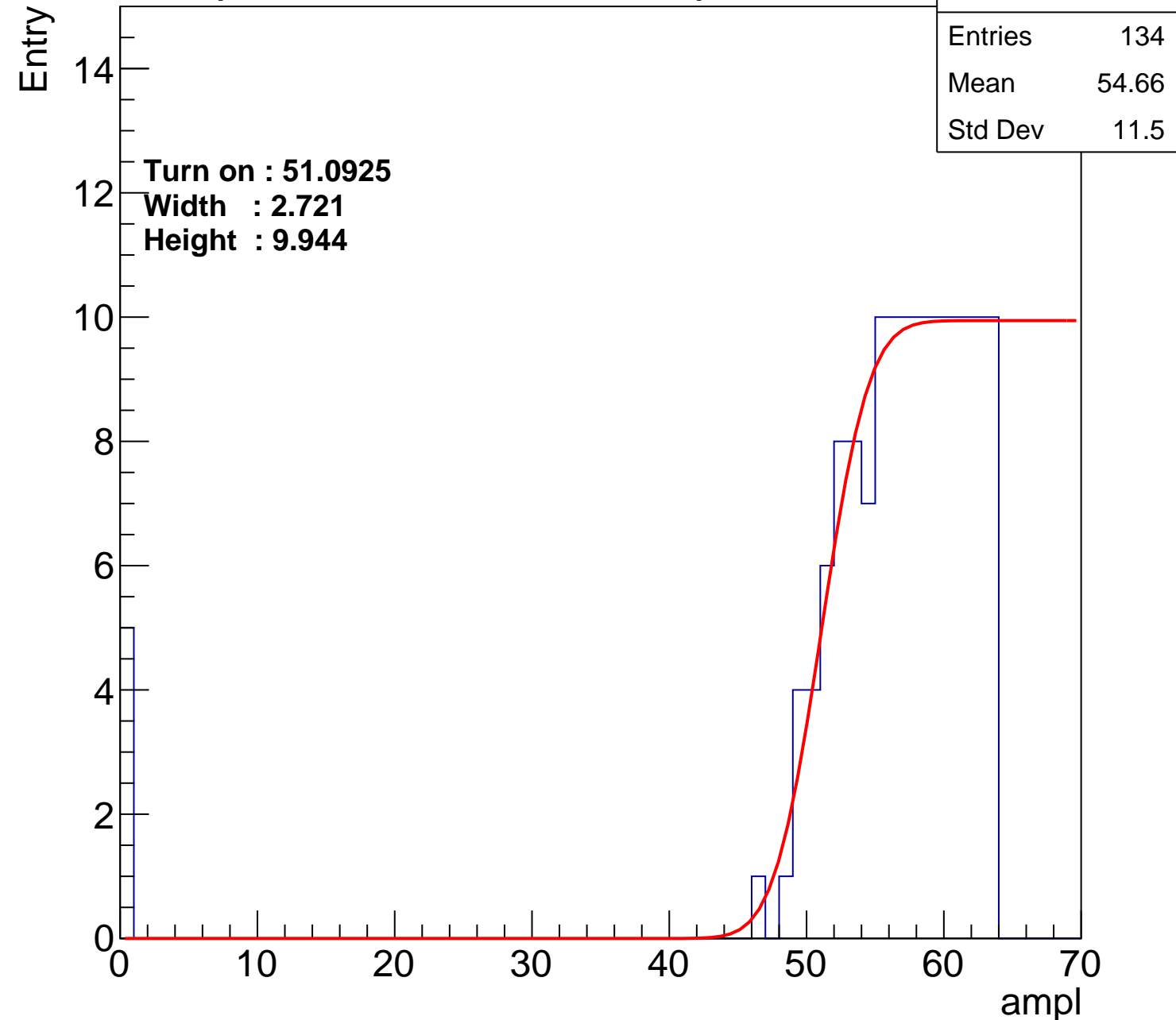
Width : 2.721

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch60

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	56.02
Std Dev	8.275

Turn on : 52.5394

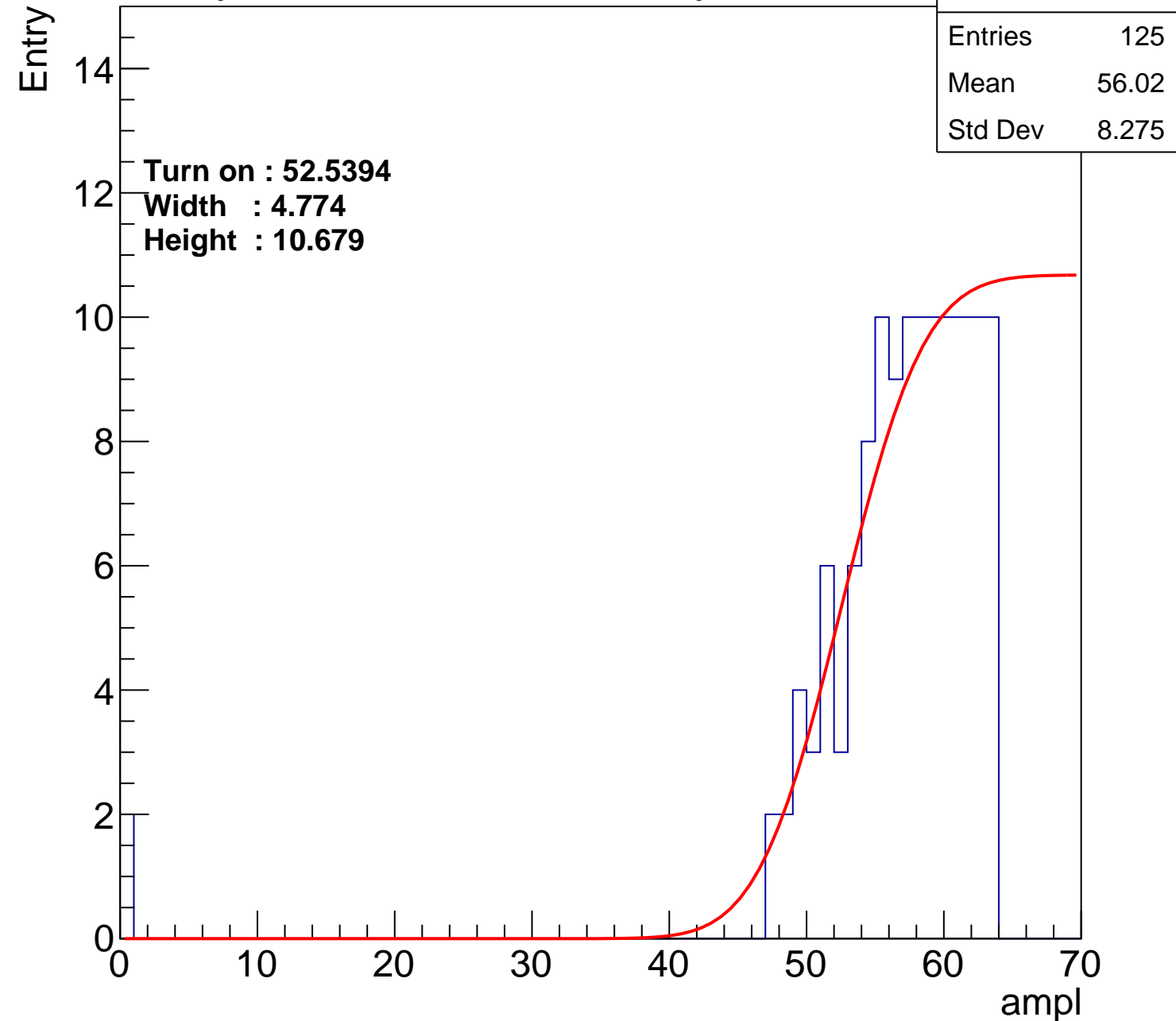
Width : 4.774

Height : 10.679

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch61

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.84
Std Dev	11.44

Turn on : 51.4718

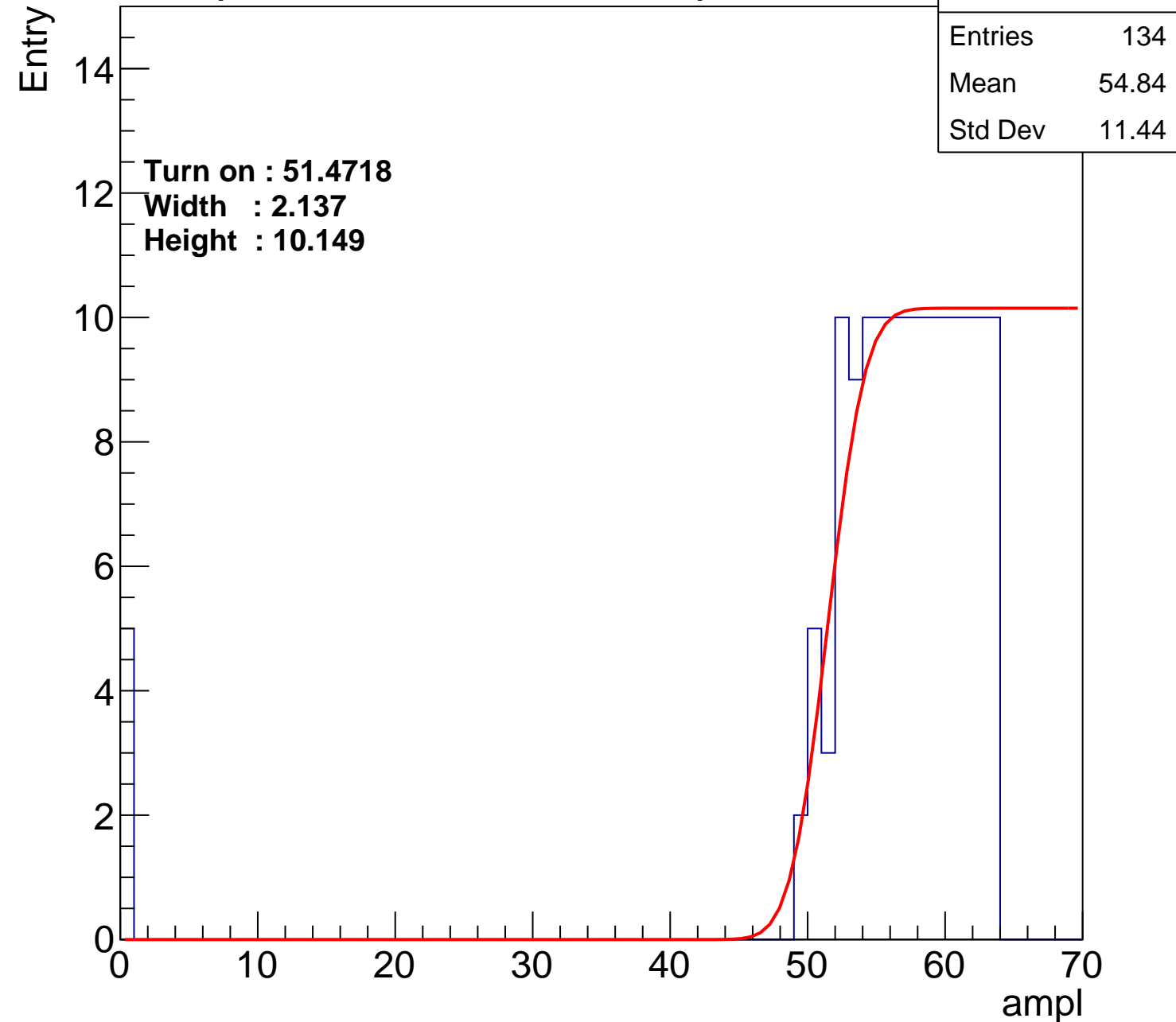
Width : 2.137

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch62

calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	55.34
Std Dev	9.313

Turn on : 50.9455

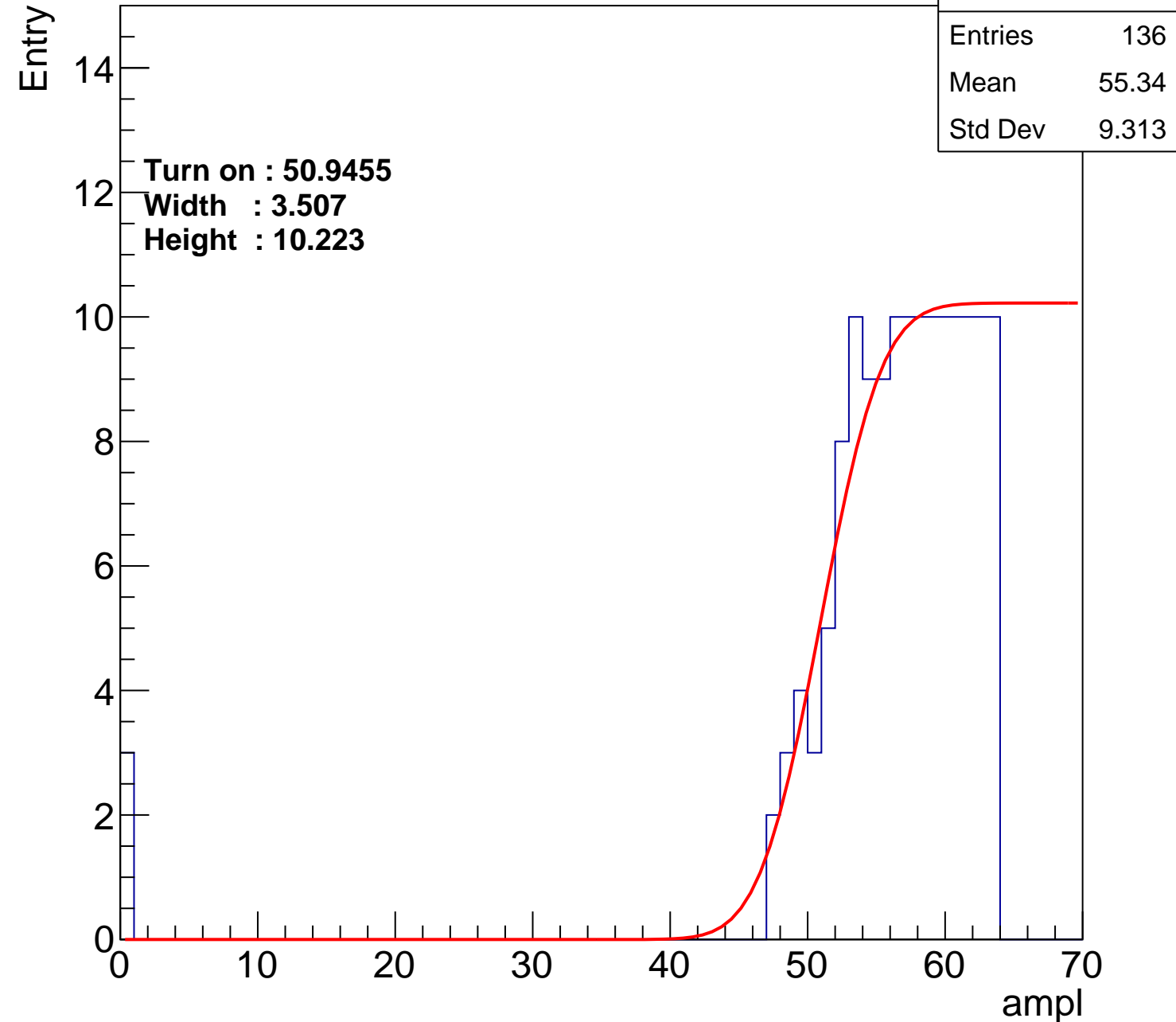
Width : 3.507

Height : 10.223

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch63

calib_packv5_040323_1717.root, FC#2, port C3

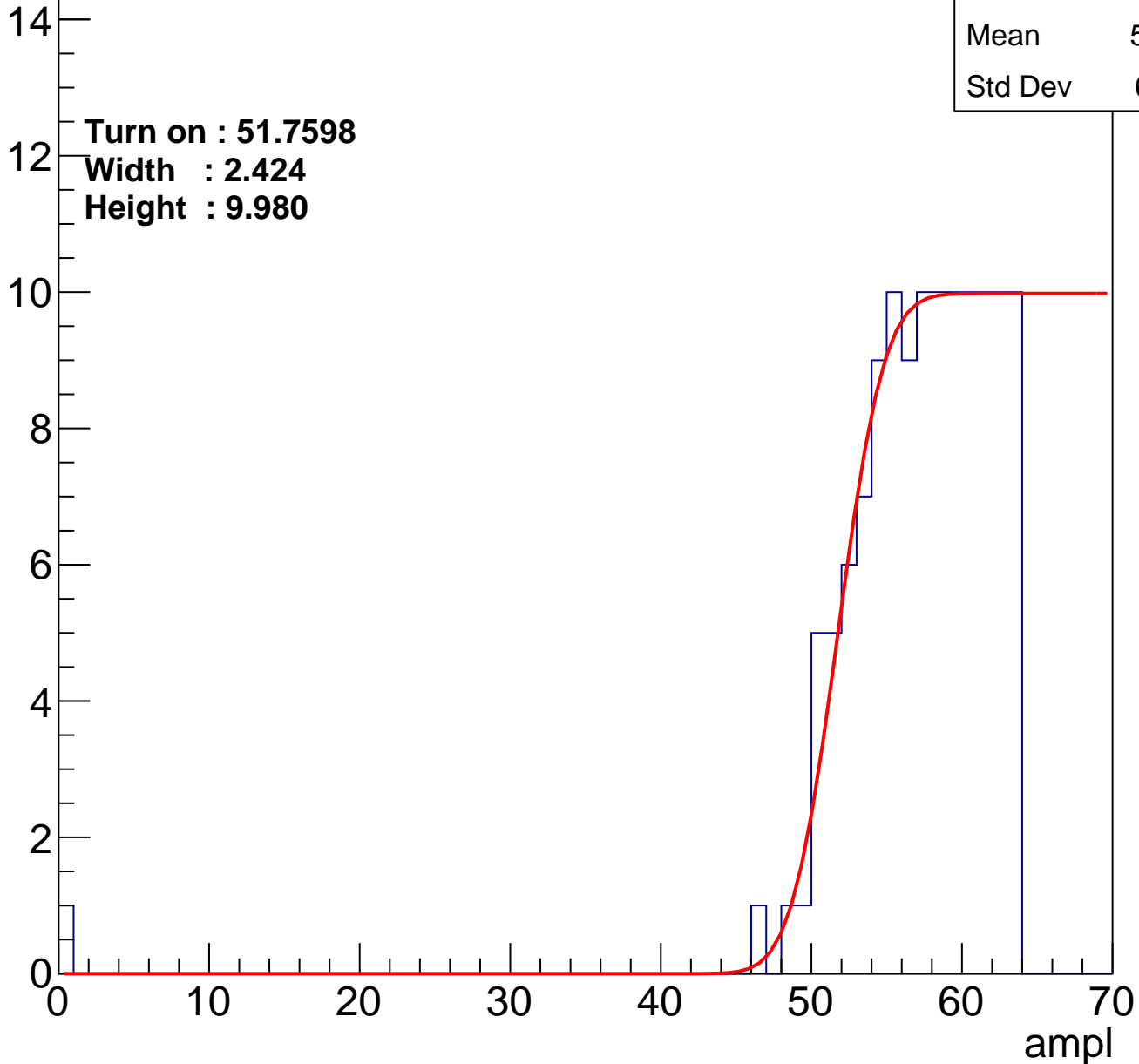
Entries	125
Mean	56.57
Std Dev	6.461

Turn on : 51.7598

Width : 2.424

Height : 9.980

Entry



B0L103S, U13-ch64

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	55.56
Std Dev	7.937

Turn on : 50.2995

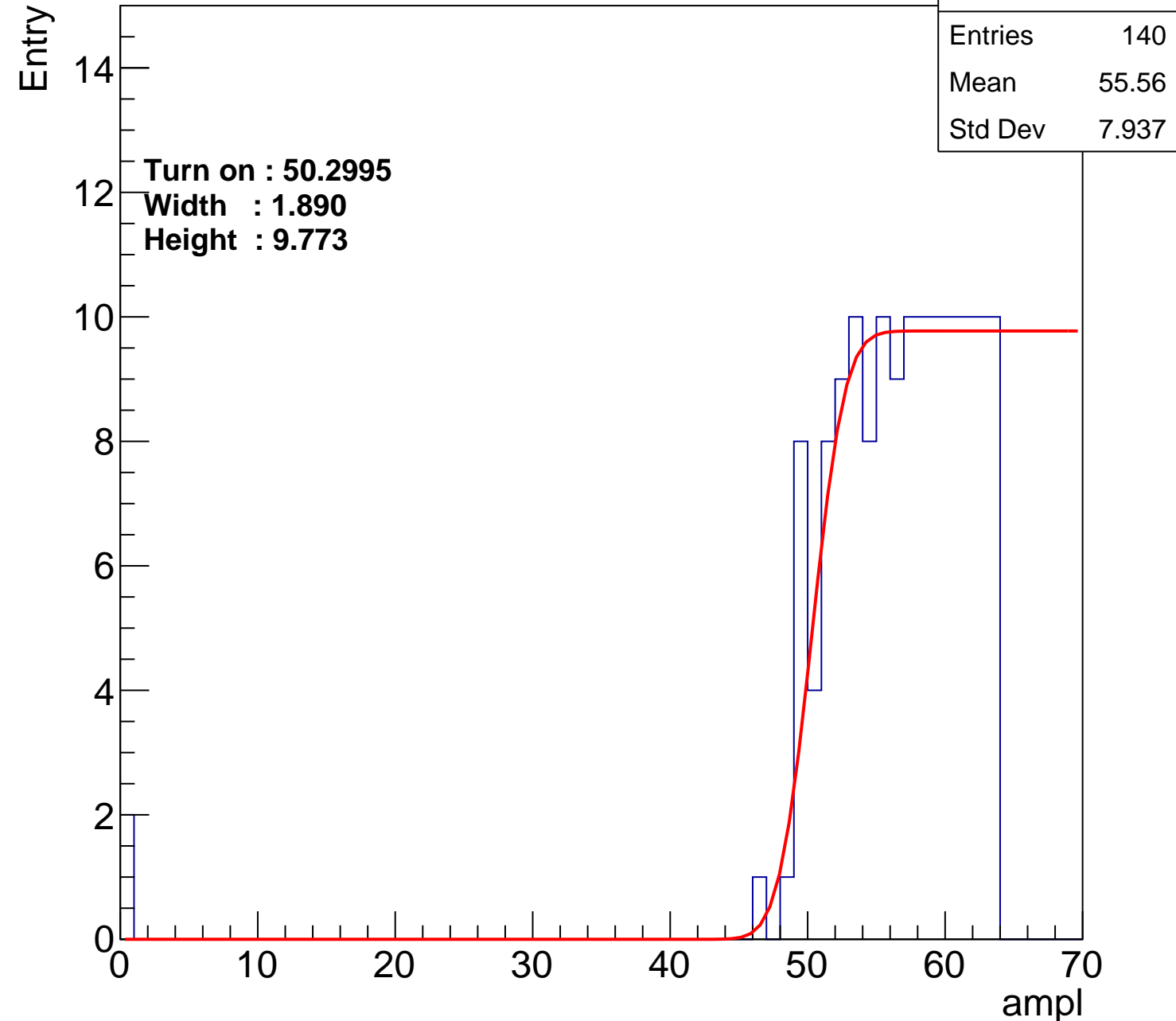
Width : 1.890

Height : 9.773

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch65

calib_packv5_040323_1717.root, FC#2, port C3

Entries	108
Mean	55.42
Std Dev	12.64

Turn on : 54.0332

Width : 2.526

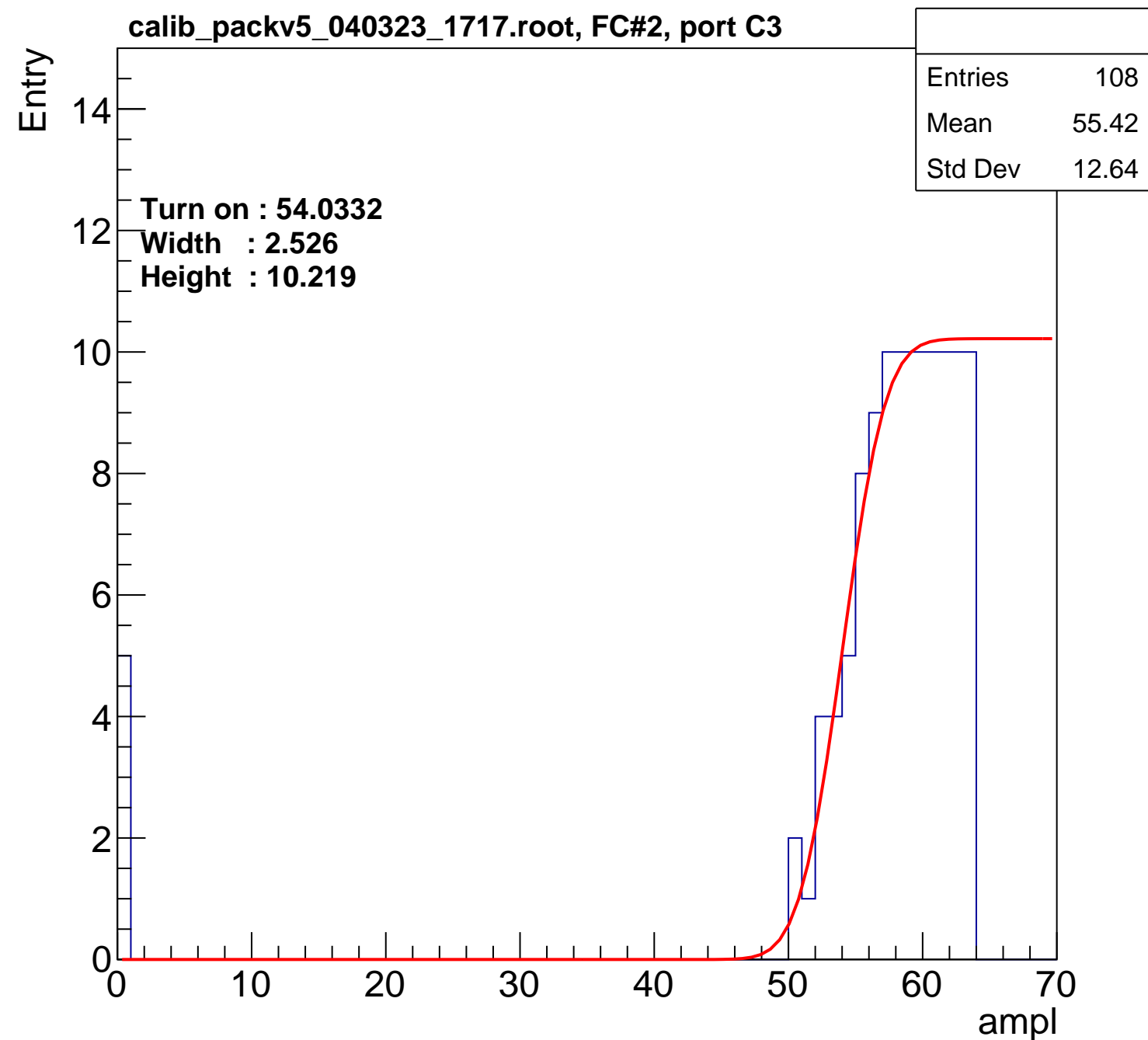
Height : 10.219

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U13-ch66

calib_packv5_040323_1717.root, FC#2, port C3

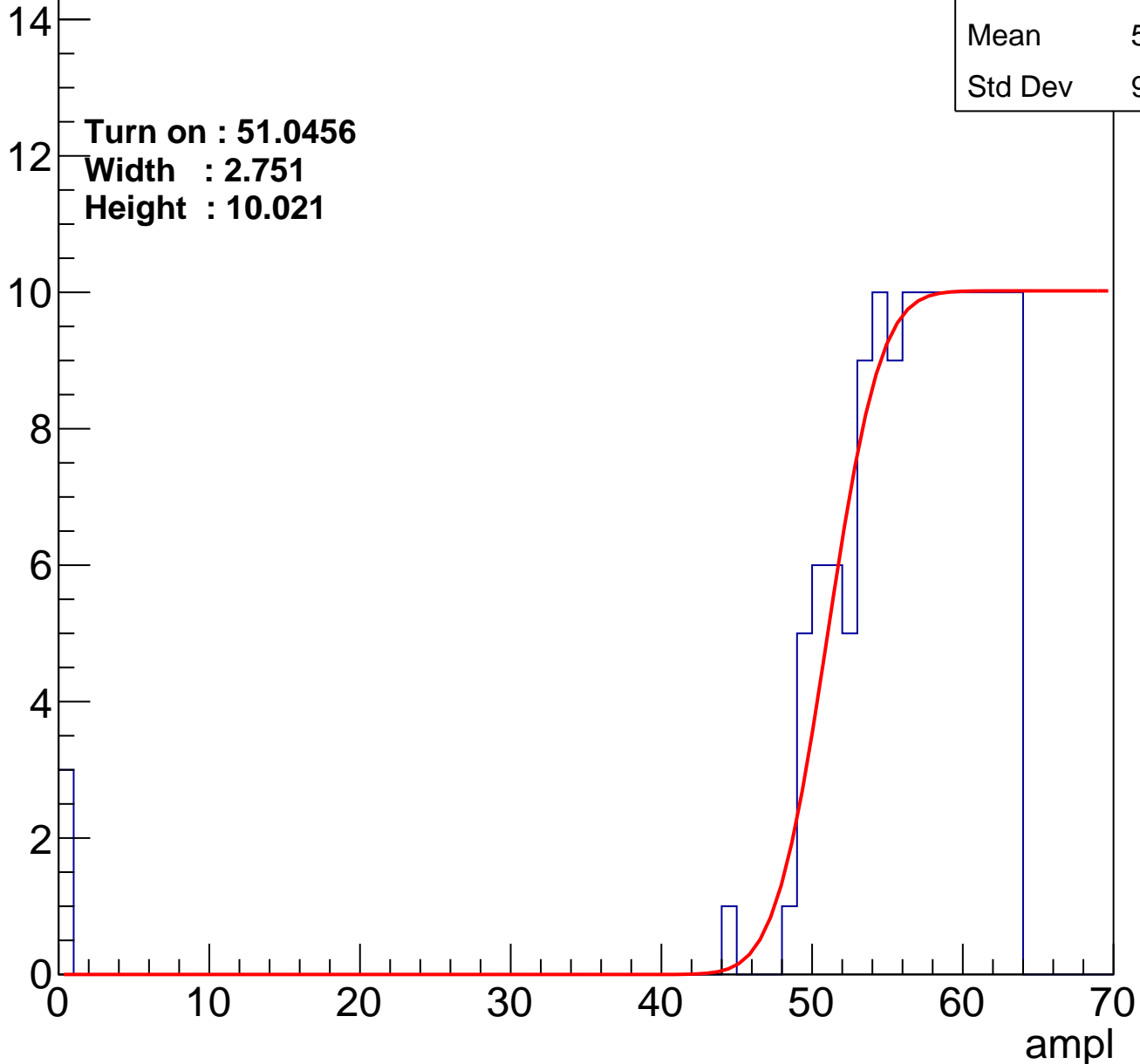
Entries	135
Mean	55.37
Std Dev	9.343

Turn on : 51.0456

Width : 2.751

Height : 10.021

Entry



B0L103S, U13-ch67

calib_packv5_040323_1717.root, FC#2, port C3

Entries	129
Mean	54.83
Std Dev	11.68

Turn on : 51.7878

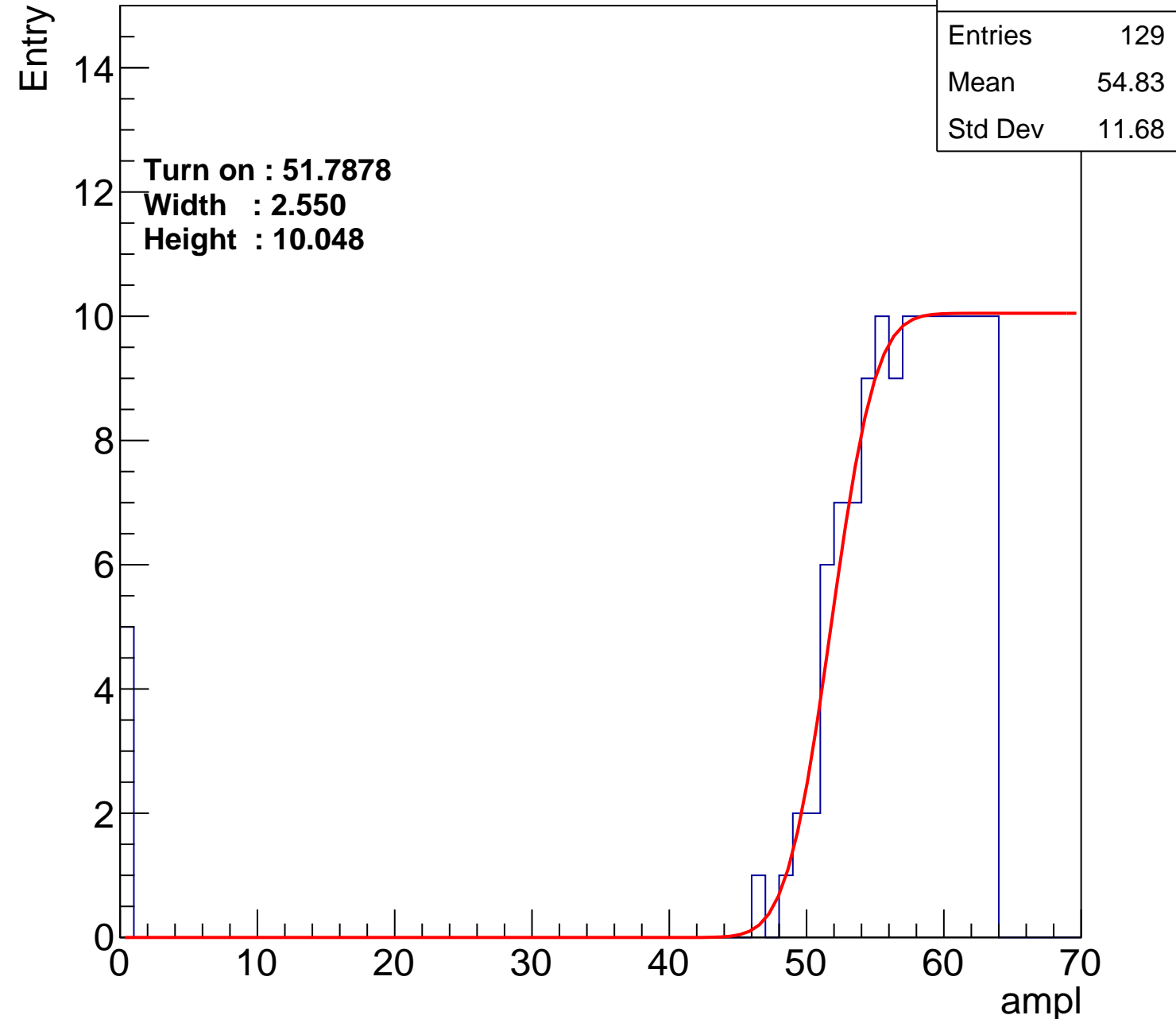
Width : 2.550

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch68

calib_packv5_040323_1717.root, FC#2, port C3

Entries	134
Mean	54.58
Std Dev	11.52

Turn on : 50.7354

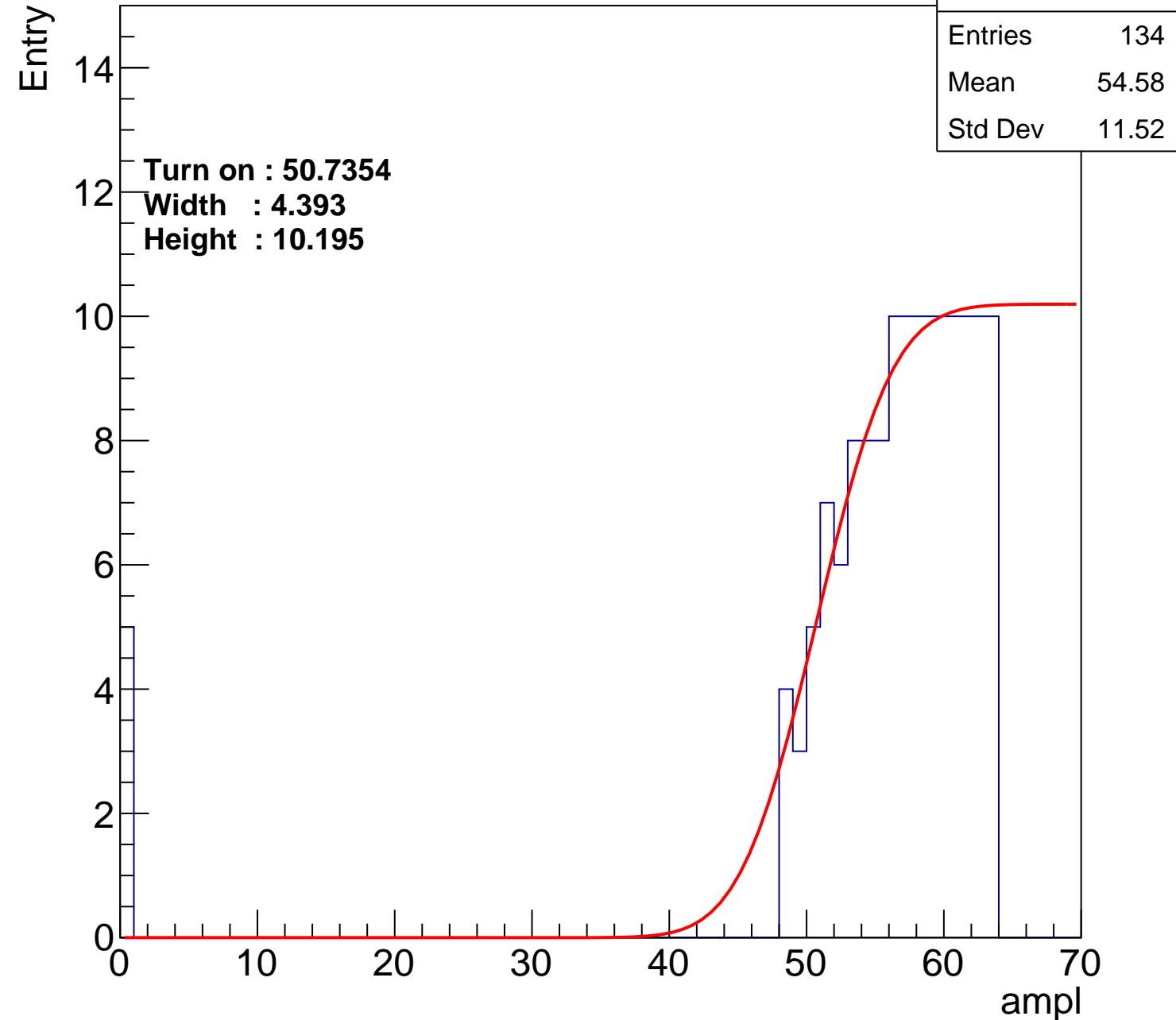
Width : 4.393

Height : 10.195

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch69

calib_packv5_040323_1717.root, FC#2, port C3

Entries	110
Mean	56.65
Std Dev	8.556

Turn on : 53.7630

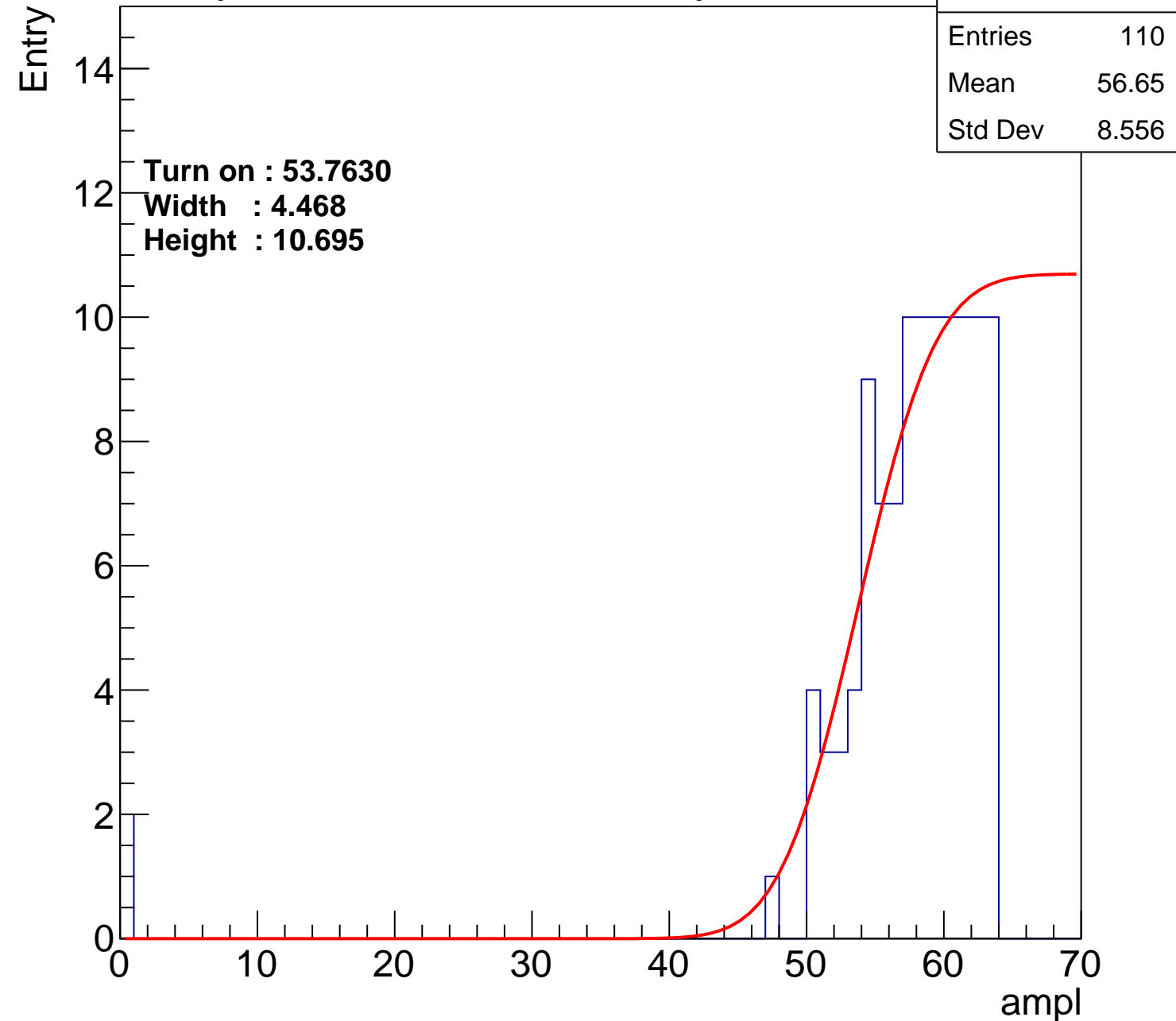
Width : 4.468

Height : 10.695

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch70

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.89
Std Dev	10.54

Turn on : 50.8799

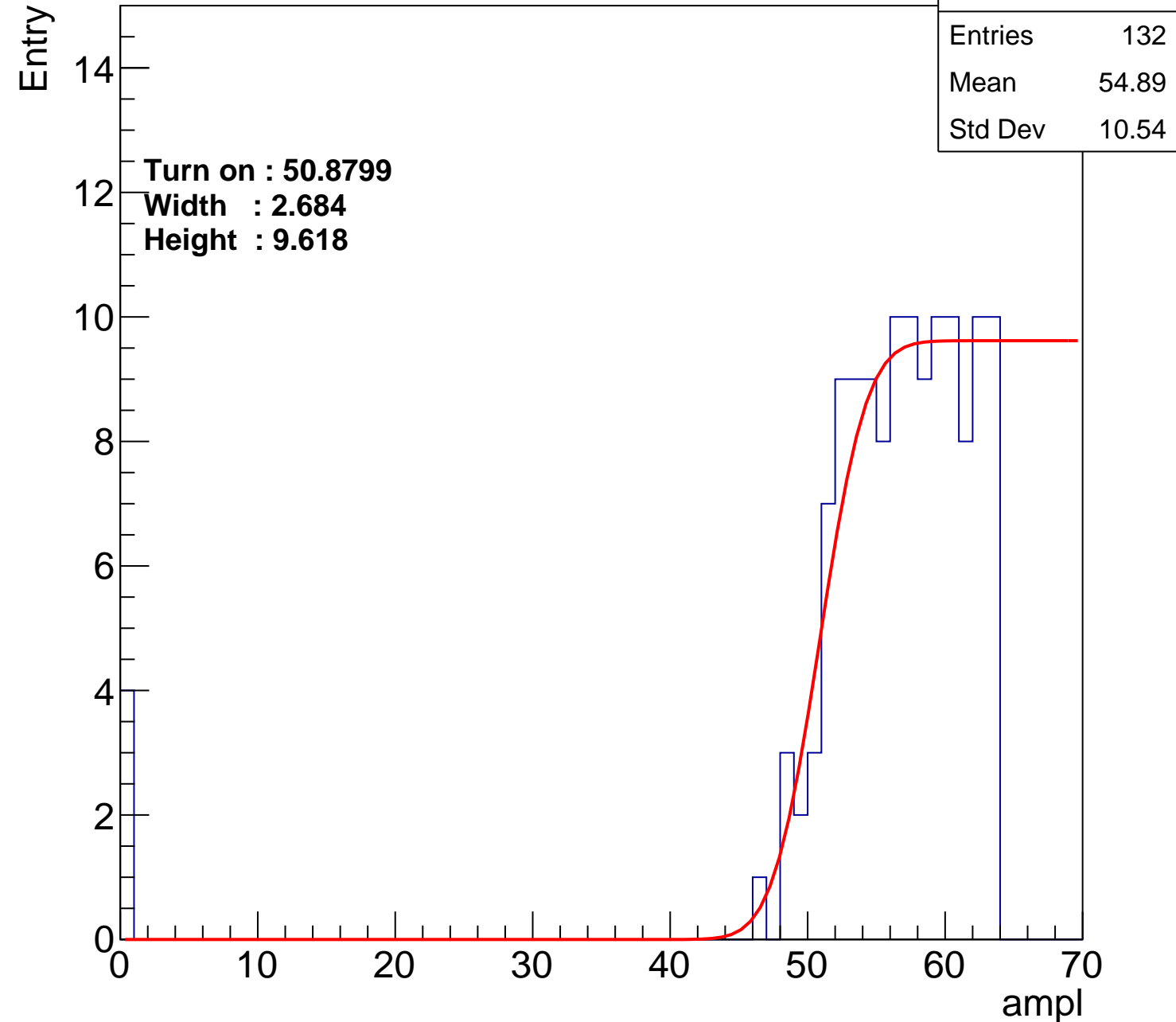
Width : 2.684

Height : 9.618

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch71

calib_packv5_040323_1717.root, FC#2, port C3

Entries	114
Mean	56.04
Std Dev	9.953

Turn on : 53.3127

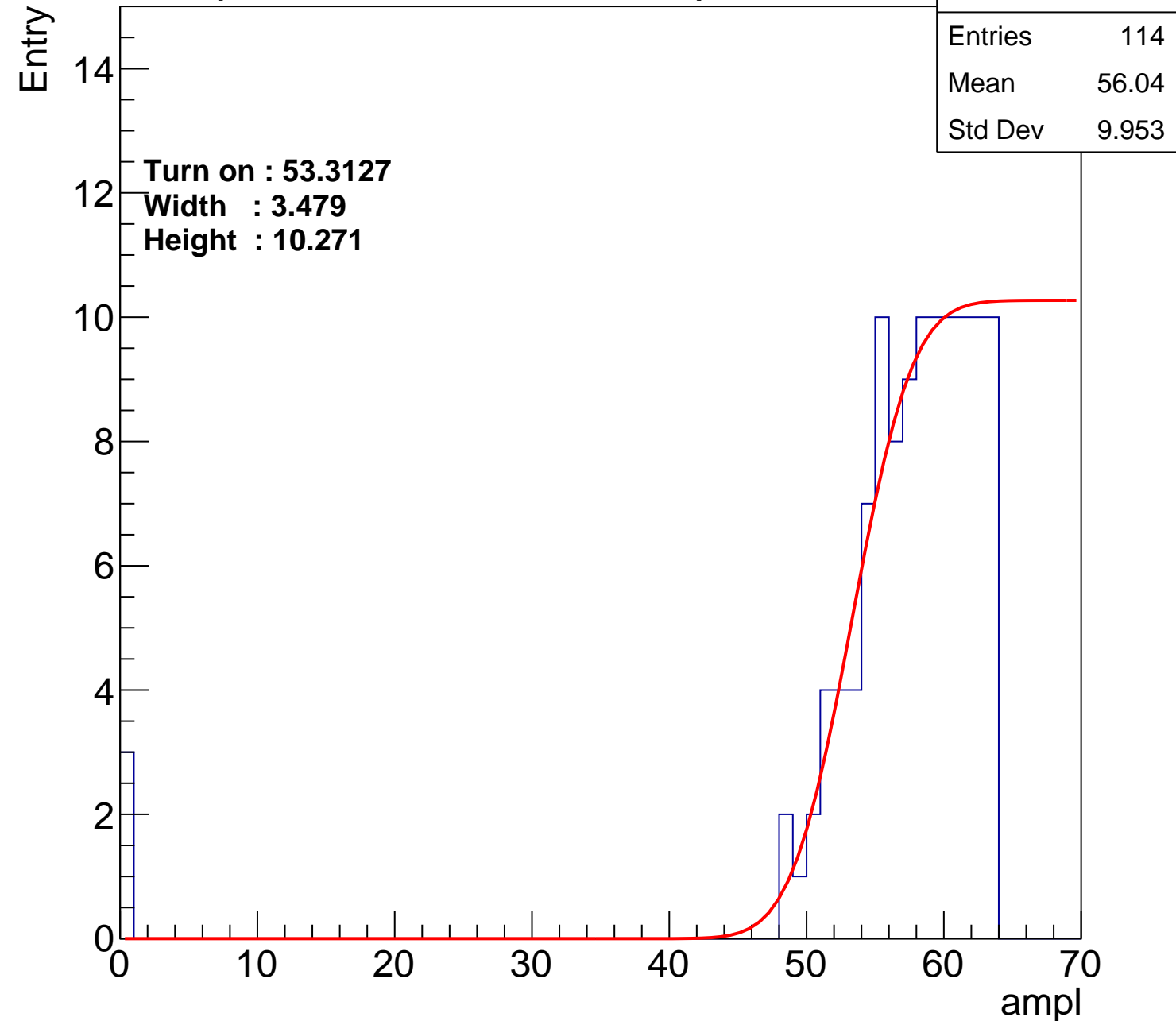
Width : 3.479

Height : 10.271

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch72

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	55.18
Std Dev	12.45

Turn on : 53.4396

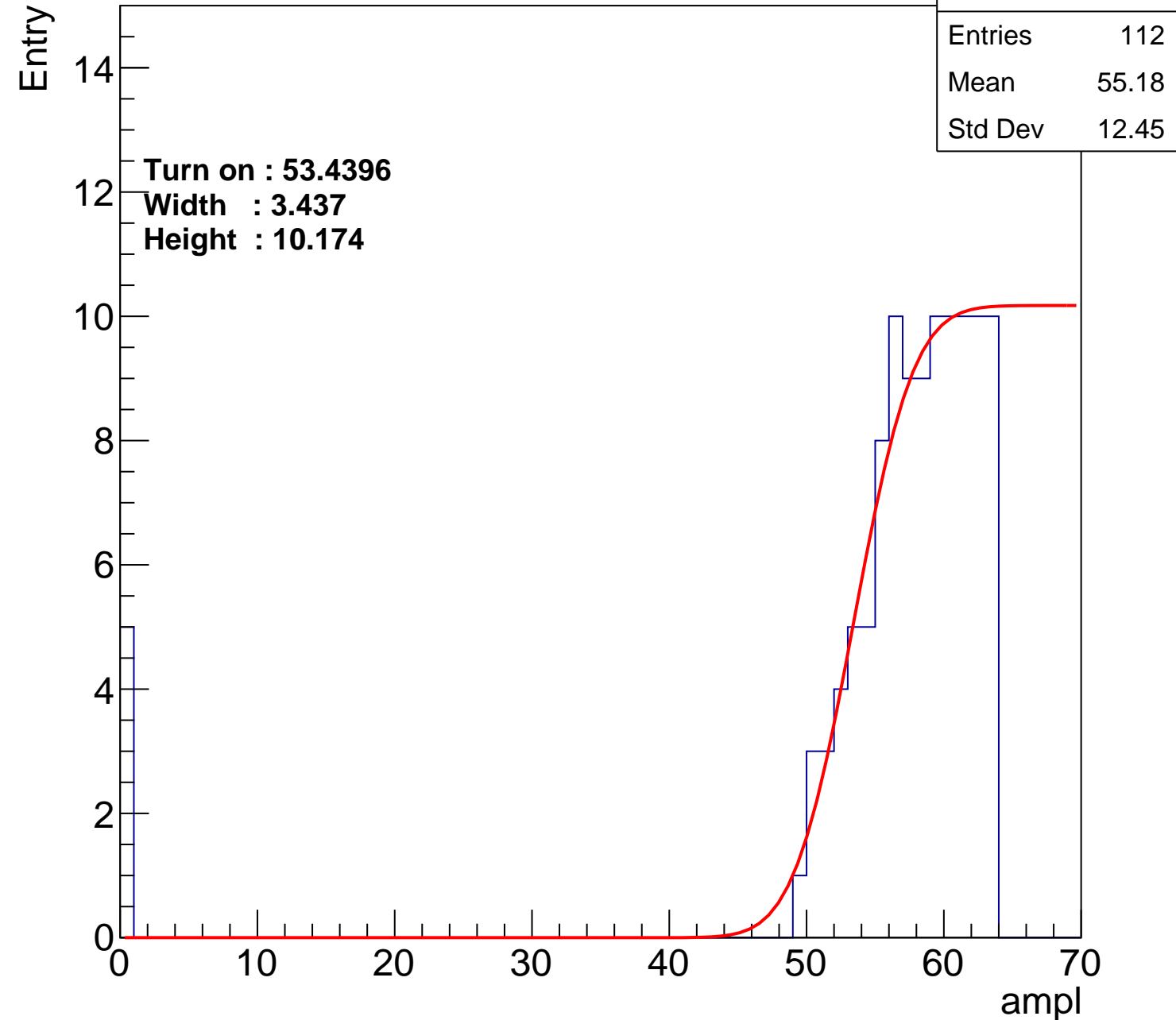
Width : 3.437

Height : 10.174

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch73

calib_packv5_040323_1717.root, FC#2, port C3

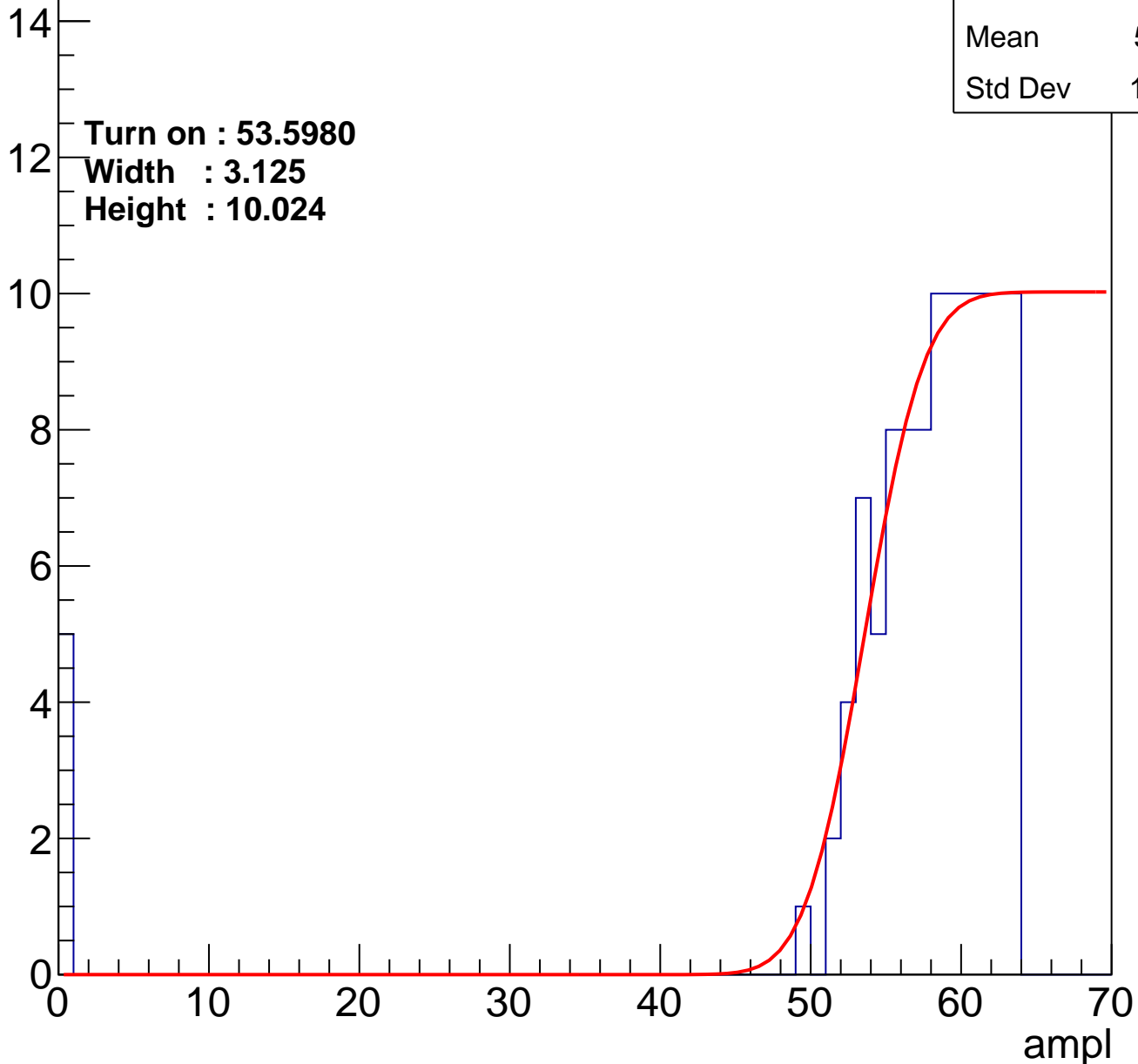
Entries	108
Mean	55.31
Std Dev	12.65

Turn on : 53.5980

Width : 3.125

Height : 10.024

Entry



B0L103S, U13-ch74

calib_packv5_040323_1717.root, FC#2, port C3

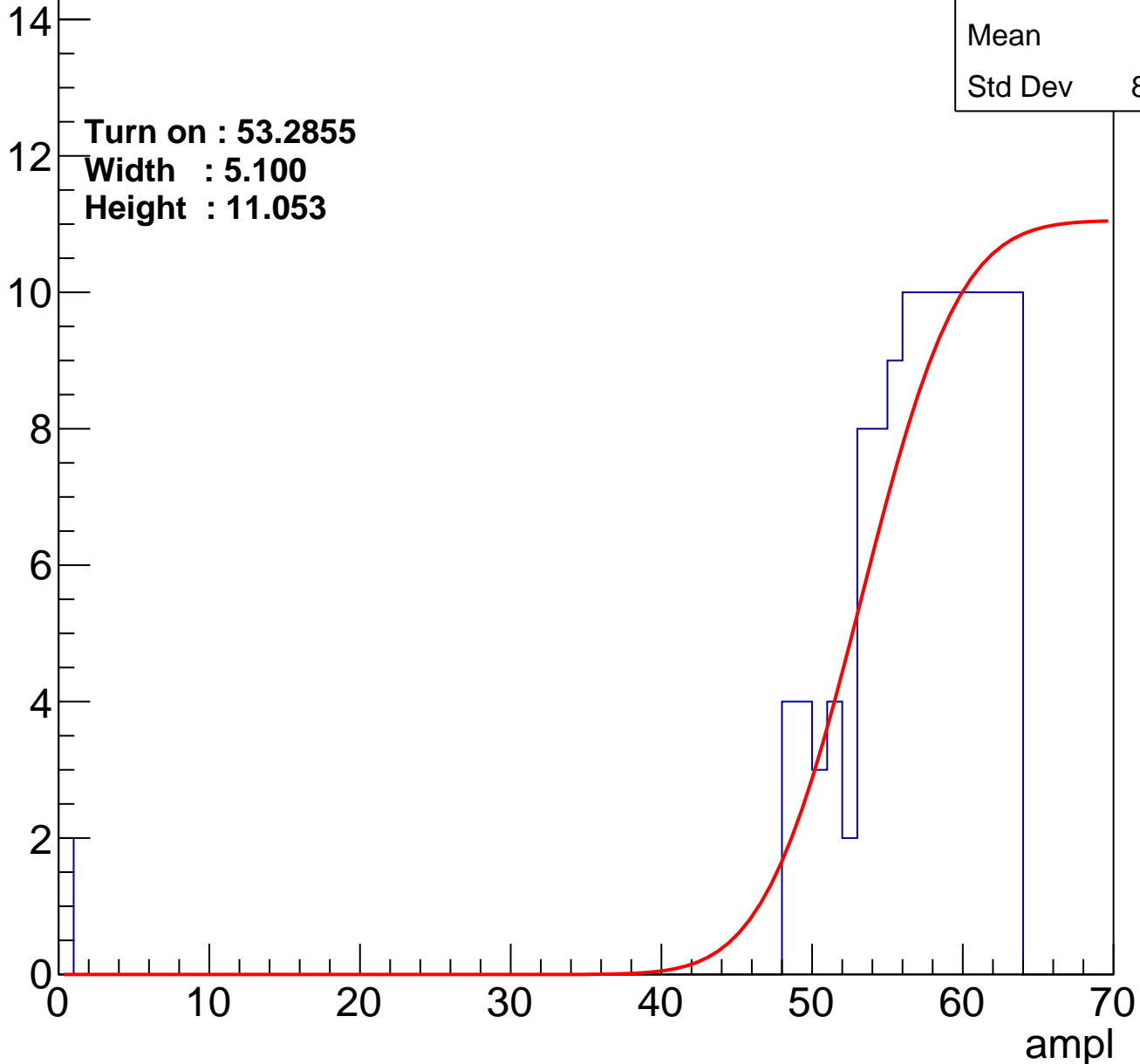
Entries	124
Mean	56.1
Std Dev	8.268

Turn on : 53.2855

Width : 5.100

Height : 11.053

Entry



B0L103S, U13-ch75

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.77
Std Dev	8.094

Turn on : 51.0048

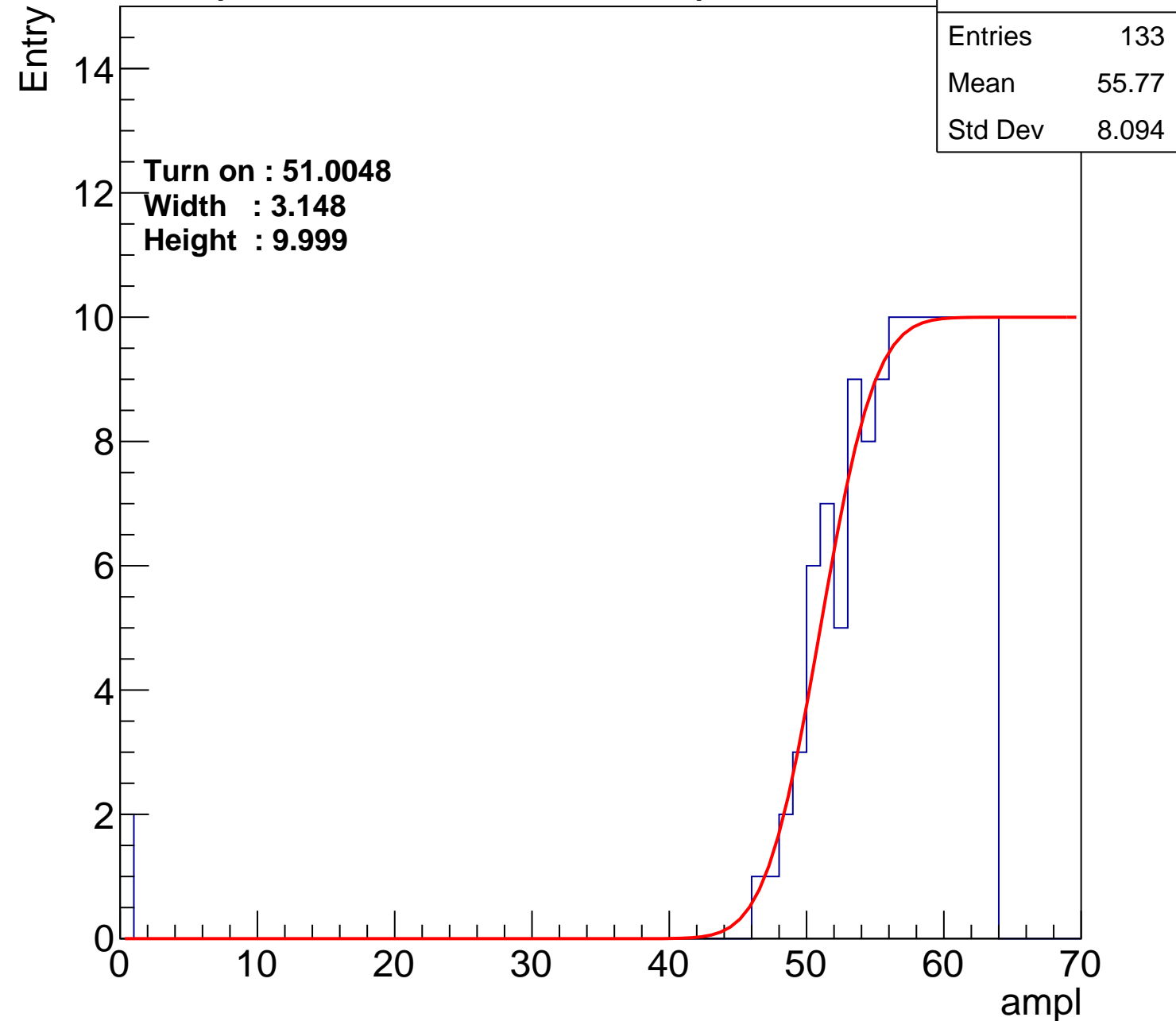
Width : 3.148

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch76

calib_packv5_040323_1717.root, FC#2, port C3

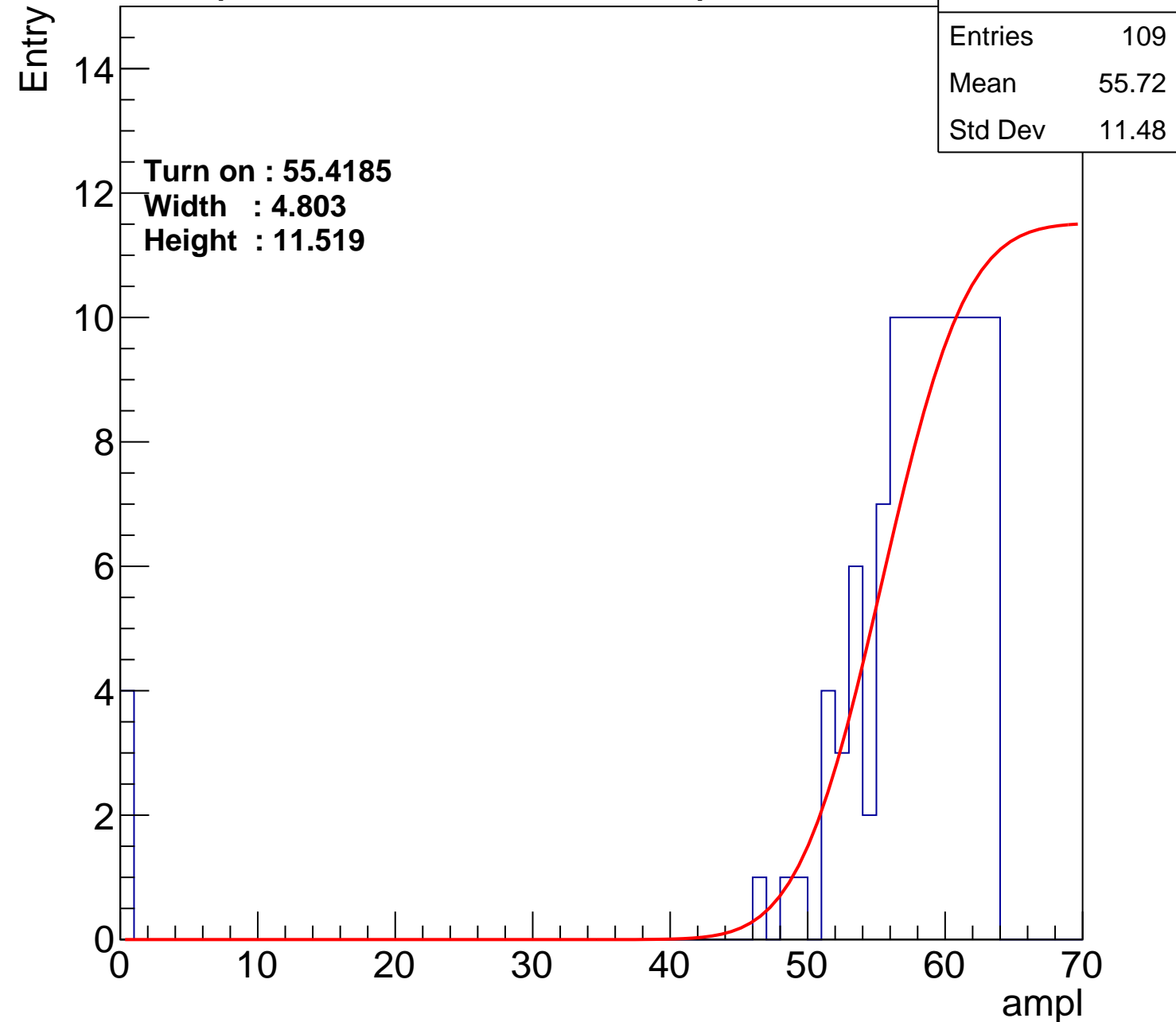
Entry

14
12
10
8
6
4
2
0

Turn on : 55.4185
Width : 4.803
Height : 11.519

Entries	109
Mean	55.72
Std Dev	11.48

ampl



B0L103S, U13-ch77

calib_packv5_040323_1717.root, FC#2, port C3

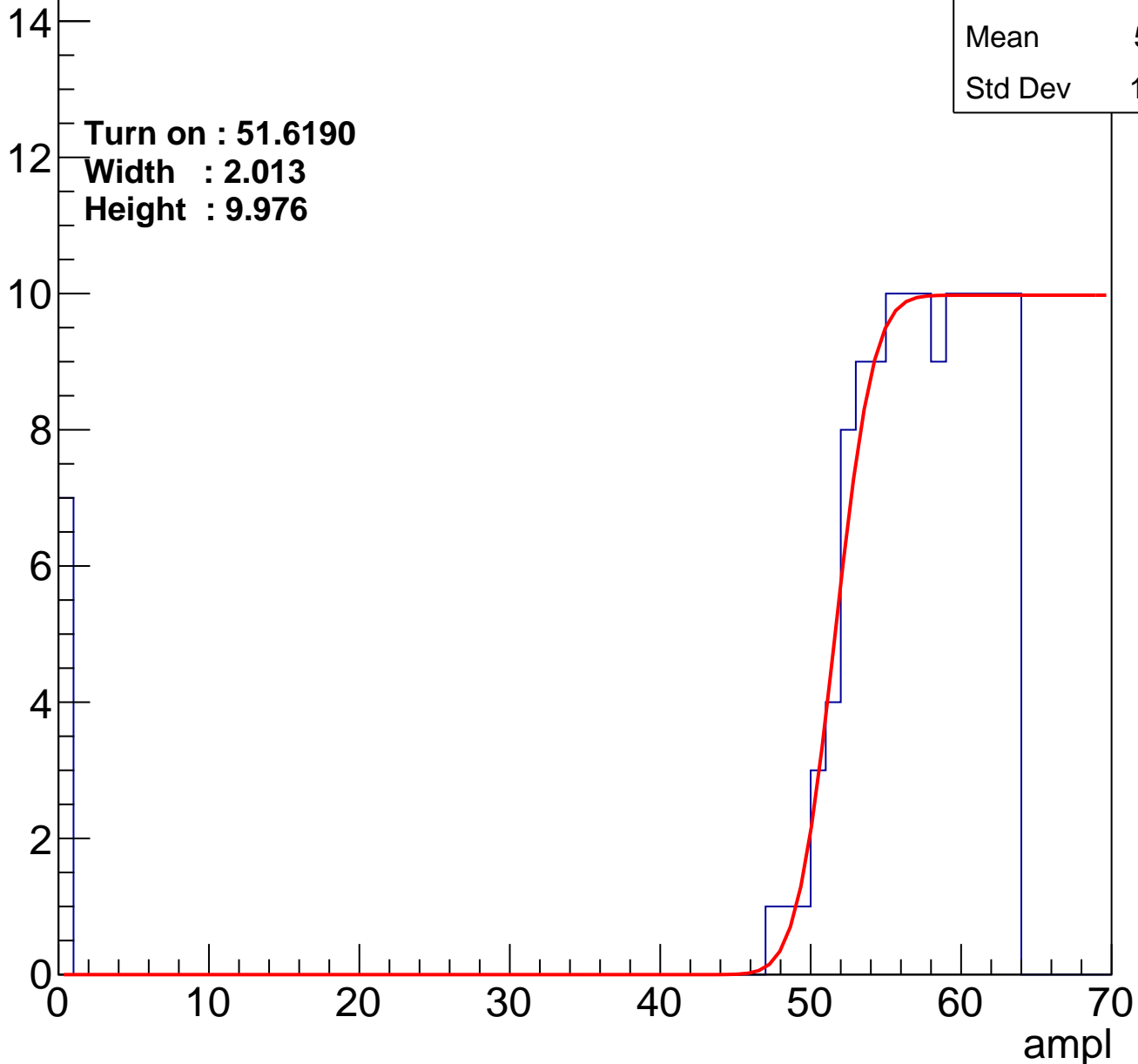
Entries	132
Mean	54.01
Std Dev	13.34

Turn on : 51.6190

Width : 2.013

Height : 9.976

Entry



B0L103S, U13-ch78

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	54.24
Std Dev	14.01

Turn on : 53.2564

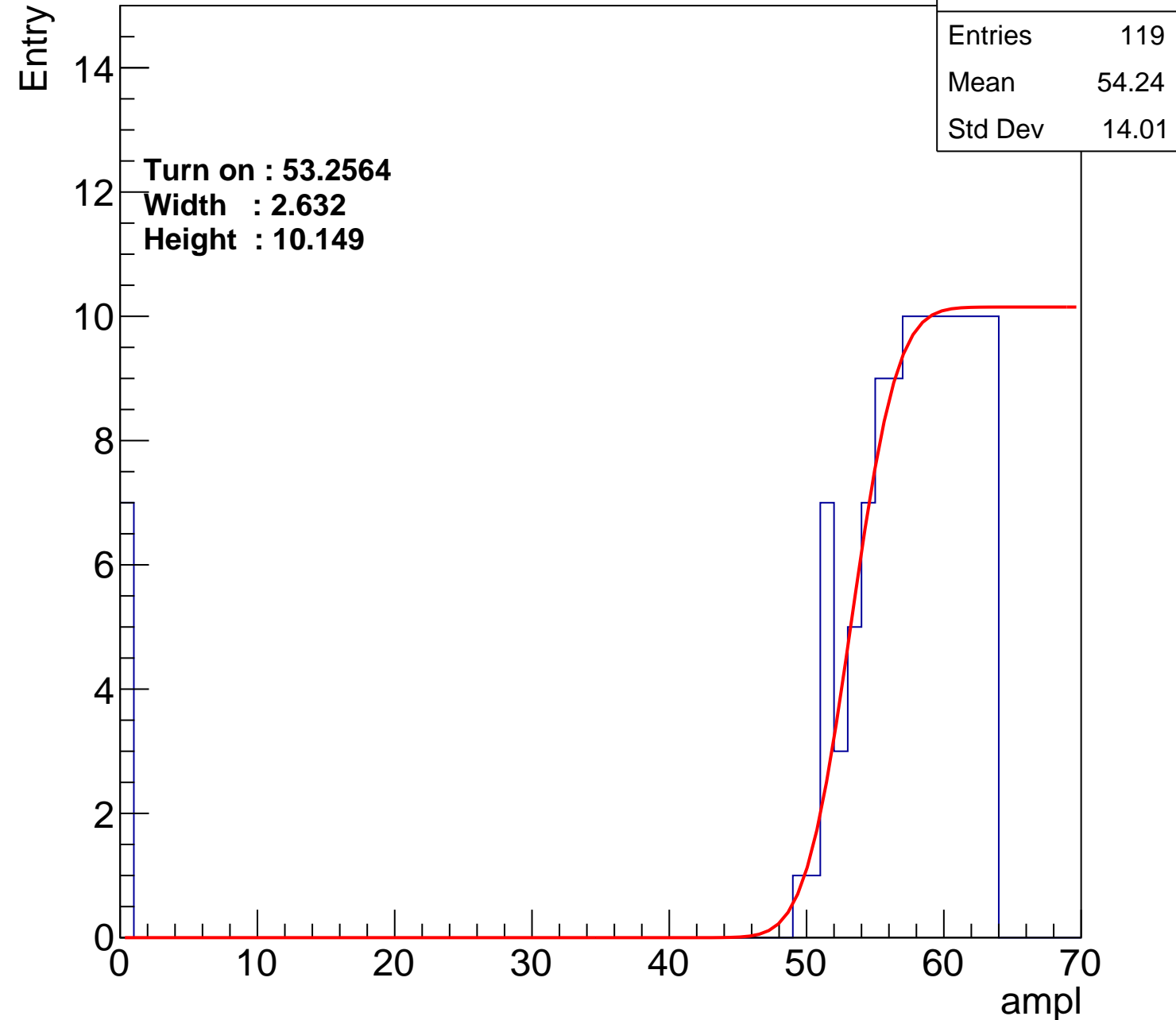
Width : 2.632

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch79

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.87
Std Dev	9.644

Turn on : 52.4261

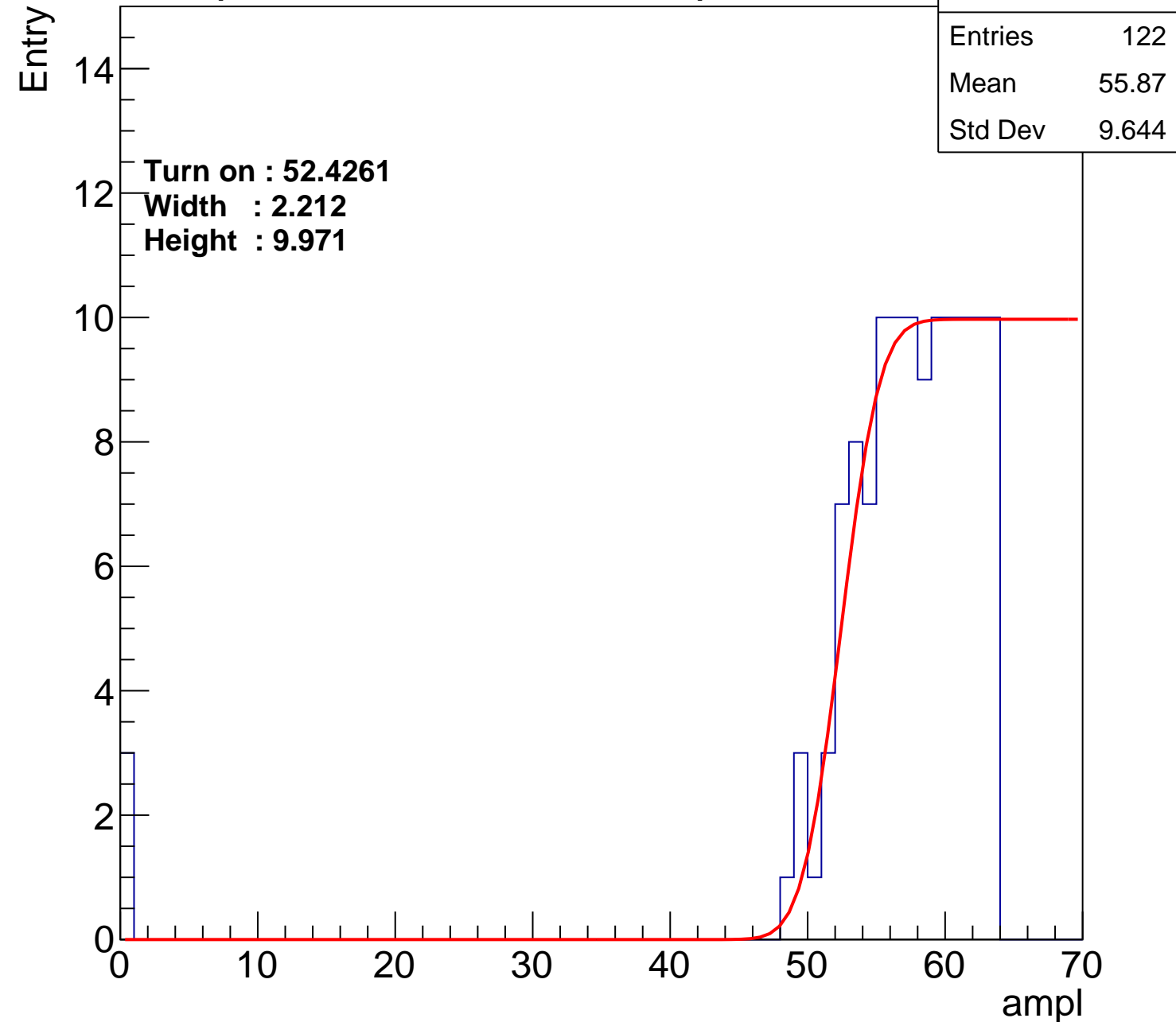
Width : 2.212

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch80

calib_packv5_040323_1717.root, FC#2, port C3

Entries	108
Mean	56.6
Std Dev	8.678

Turn on : 54.5944

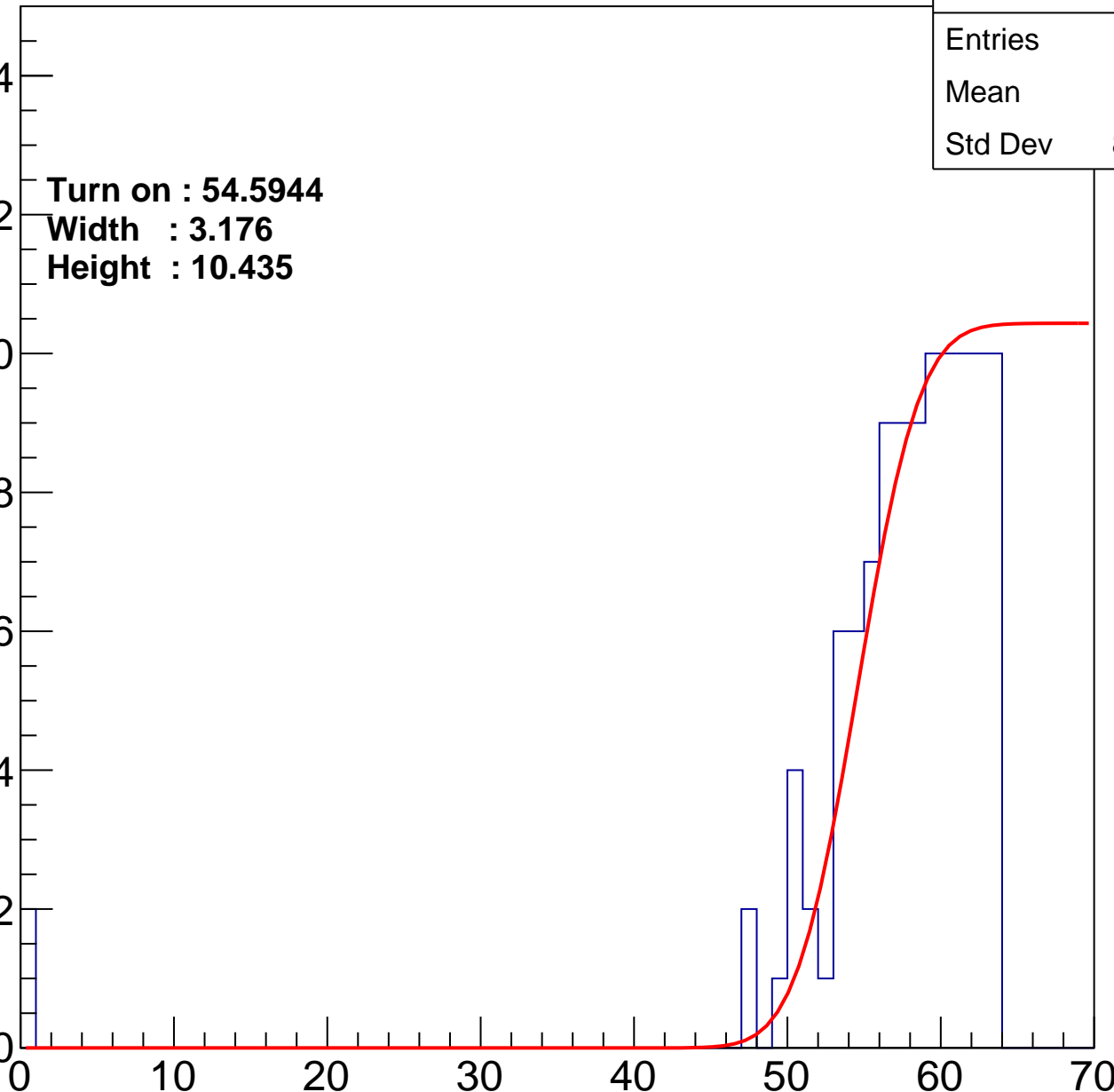
Width : 3.176

Height : 10.435

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch81

calib_packv5_040323_1717.root, FC#2, port C3

Entries	111
Mean	54.77
Std Dev	13.55

Turn on : 53.9704

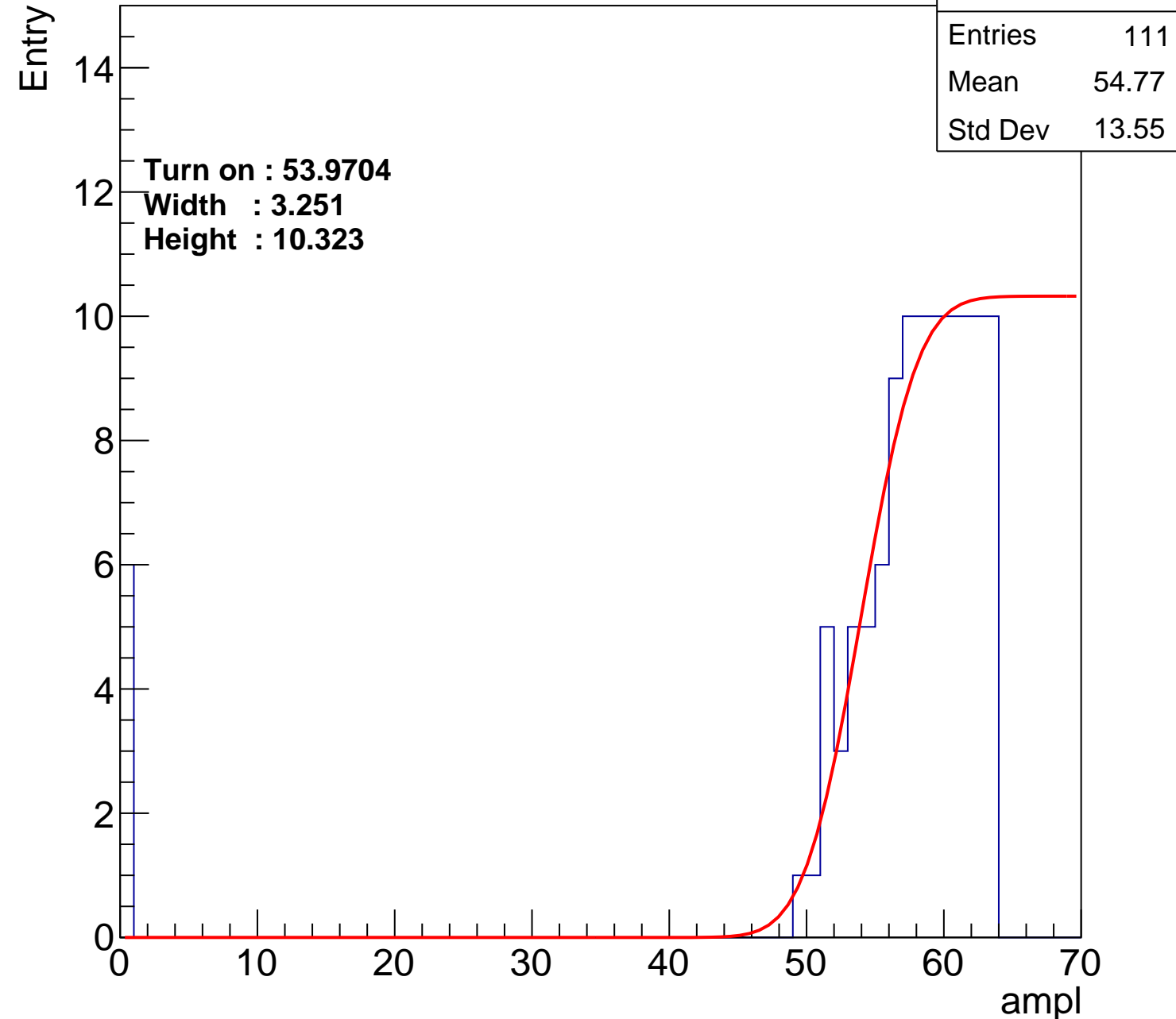
Width : 3.251

Height : 10.323

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch82

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.82
Std Dev	9.545

Turn on : 51.9987

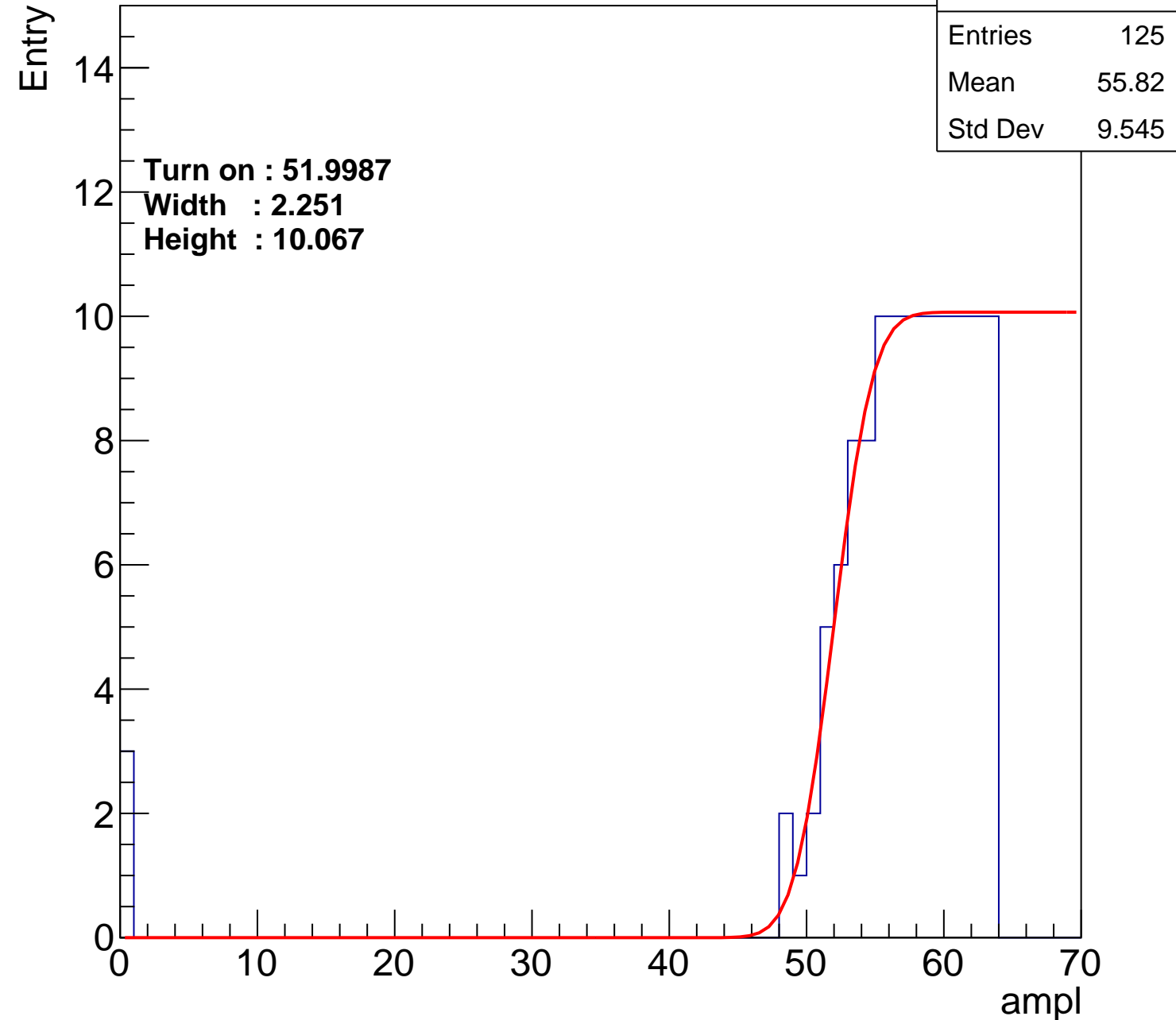
Width : 2.251

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch83

calib_packv5_040323_1717.root, FC#2, port C3

Entries	122
Mean	55.81
Std Dev	9.669

Turn on : 52.6359

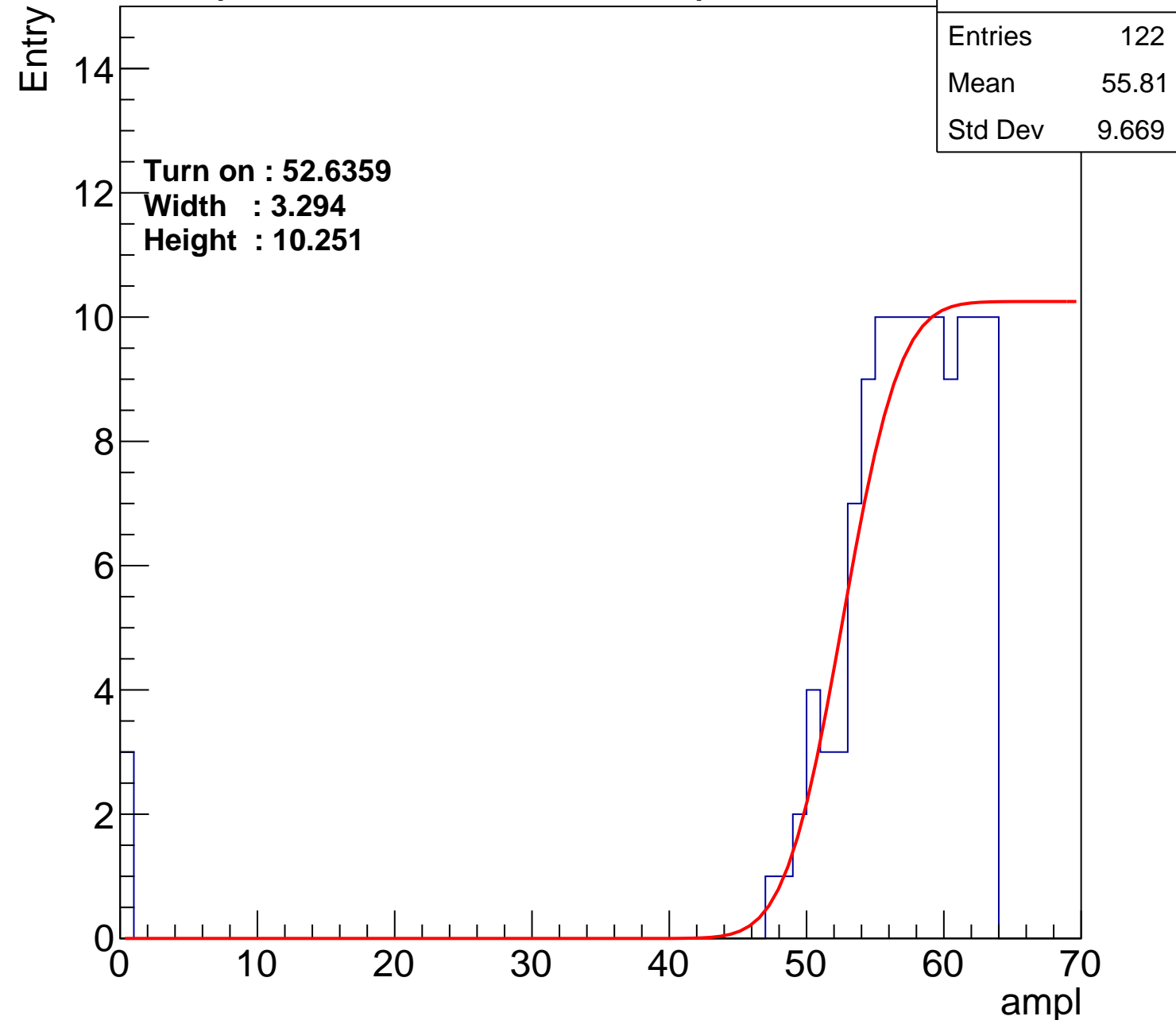
Width : 3.294

Height : 10.251

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch84

calib_packv5_040323_1717.root, FC#2, port C3

Entries	119
Mean	55
Std Dev	12.15

Turn on : 53.5315

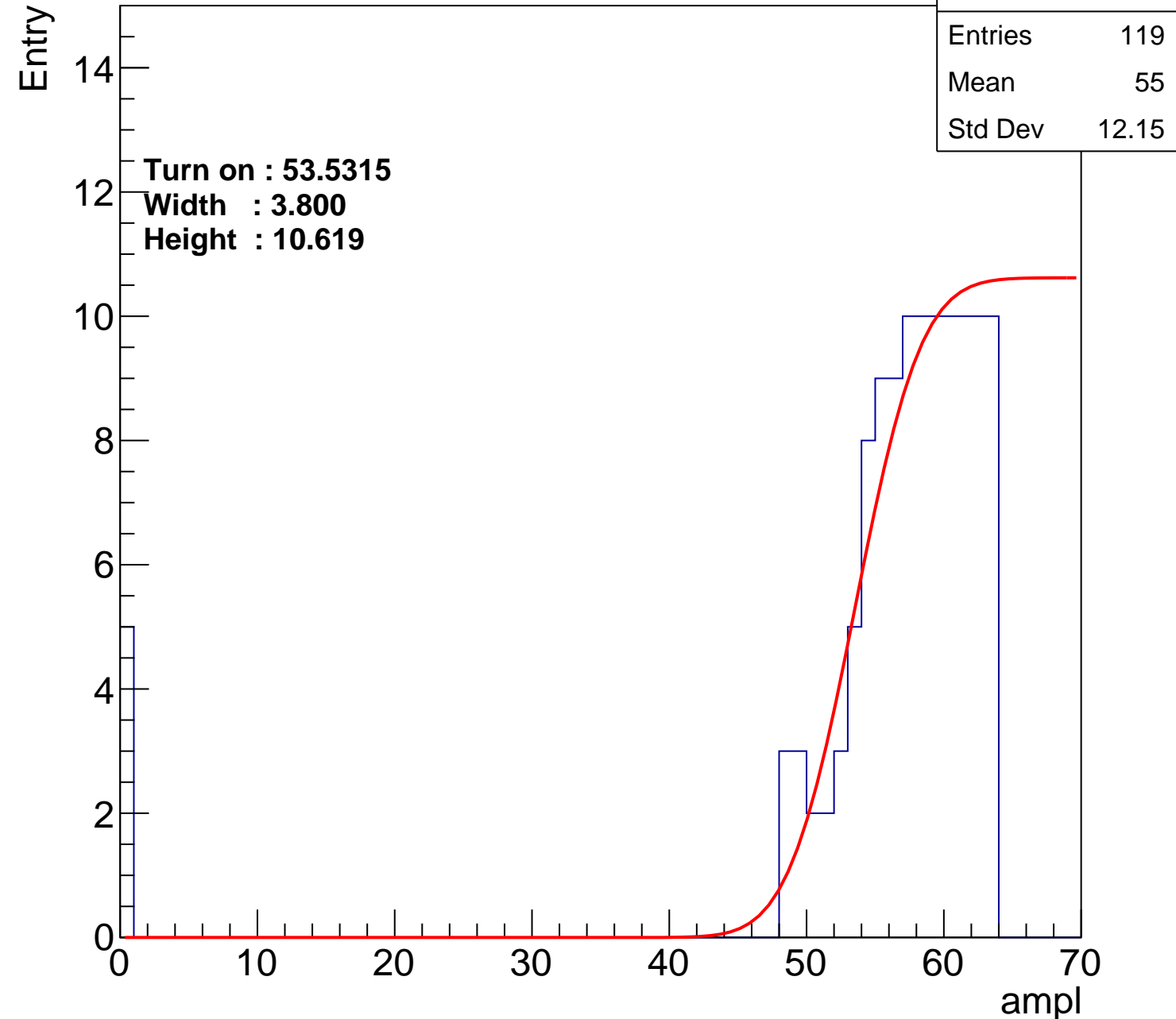
Width : 3.800

Height : 10.619

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch85

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.14
Std Dev	10.1

Turn on : 53.5559

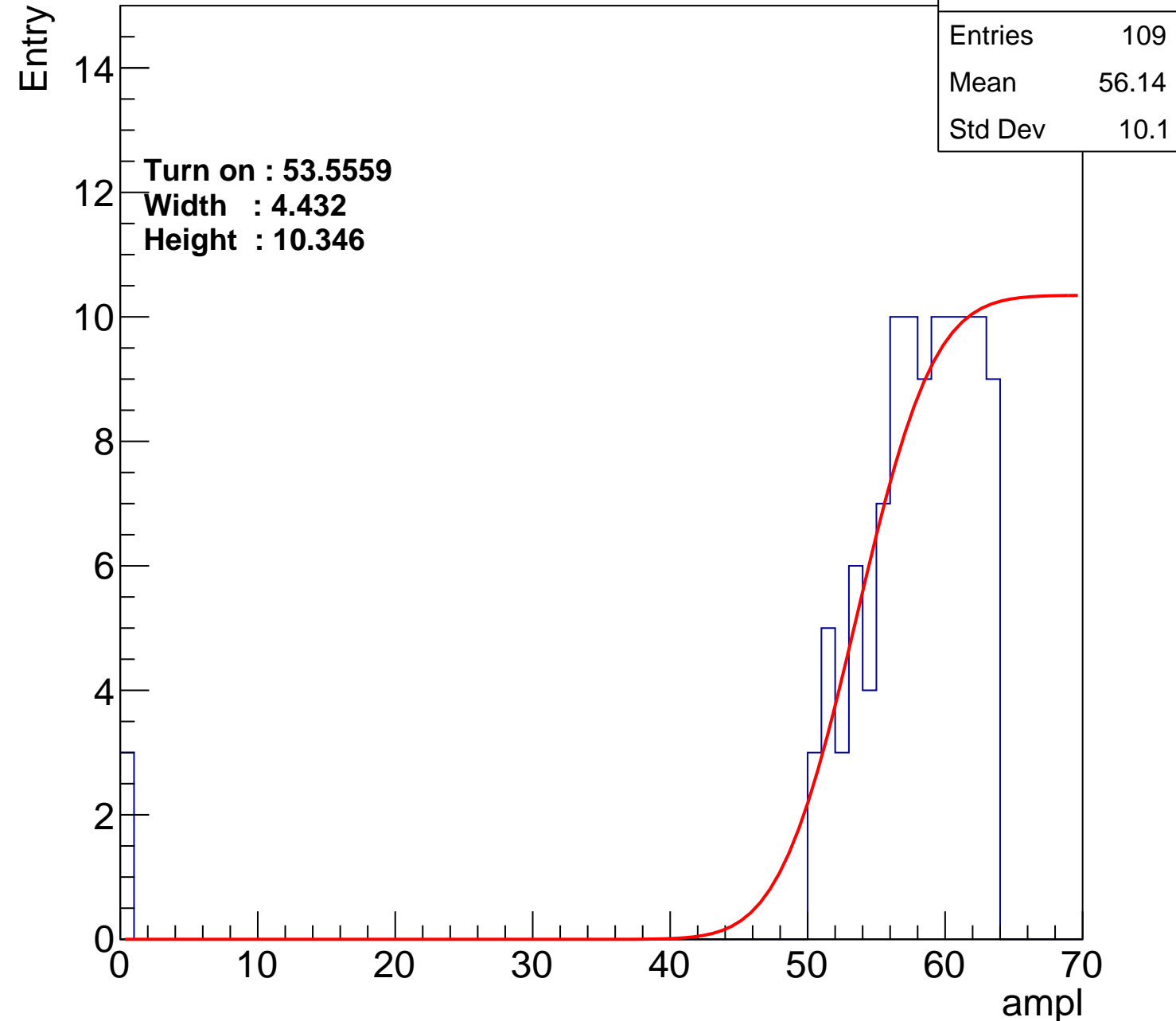
Width : 4.432

Height : 10.346

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch86

calib_packv5_040323_1717.root, FC#2, port C3

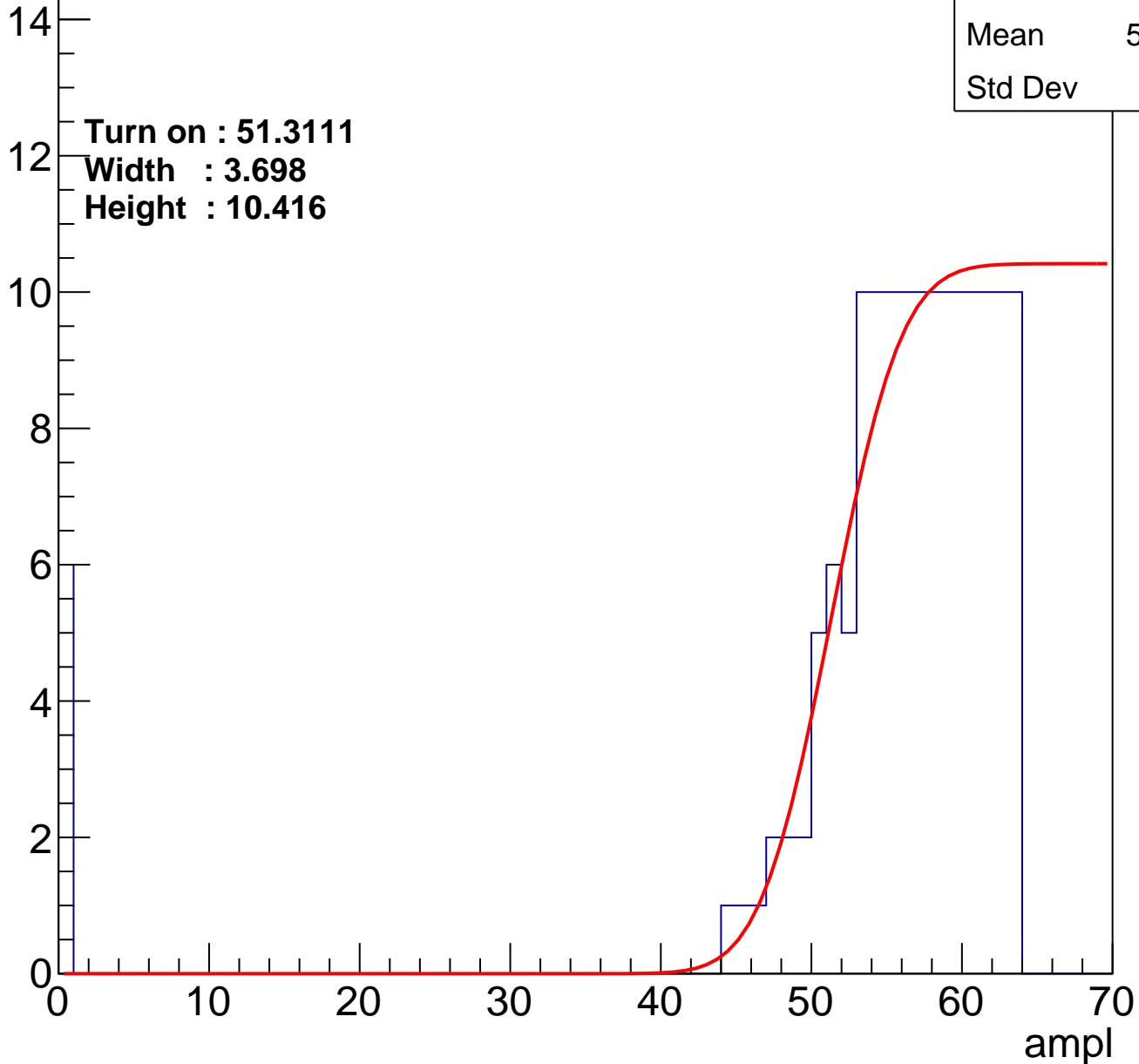
Entries	141
Mean	54.04
Std Dev	12.2

Turn on : 51.3111

Width : 3.698

Height : 10.416

Entry



B0L103S, U13-ch87

calib_packv5_040323_1717.root, FC#2, port C3

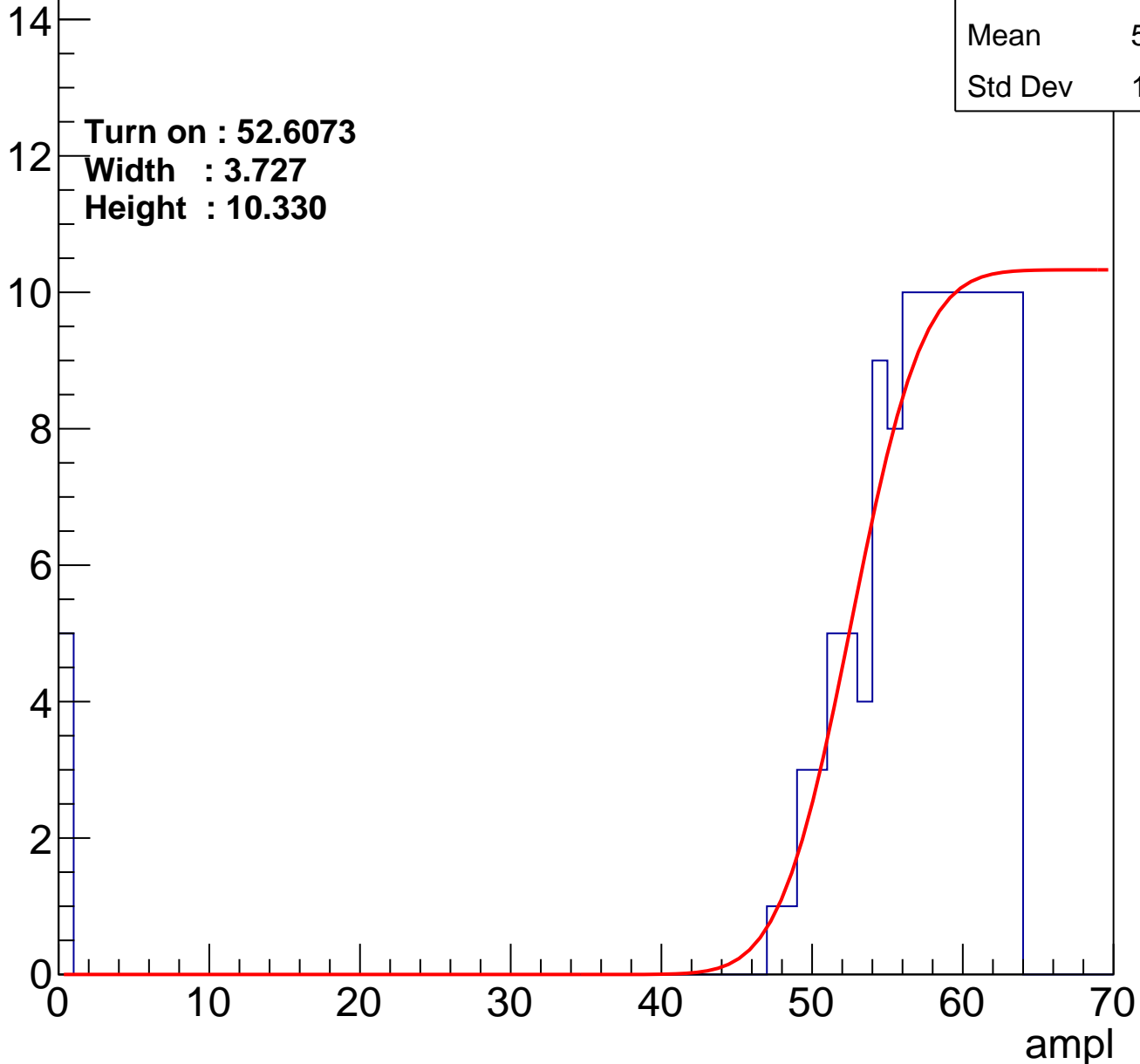
Entries	124
Mean	54.88
Std Dev	11.92

Turn on : 52.6073

Width : 3.727

Height : 10.330

Entry



B0L103S, U13-ch88

calib_packv5_040323_1717.root, FC#2, port C3

Entries	105
Mean	55.47
Std Dev	12.81

Turn on : 54.3640

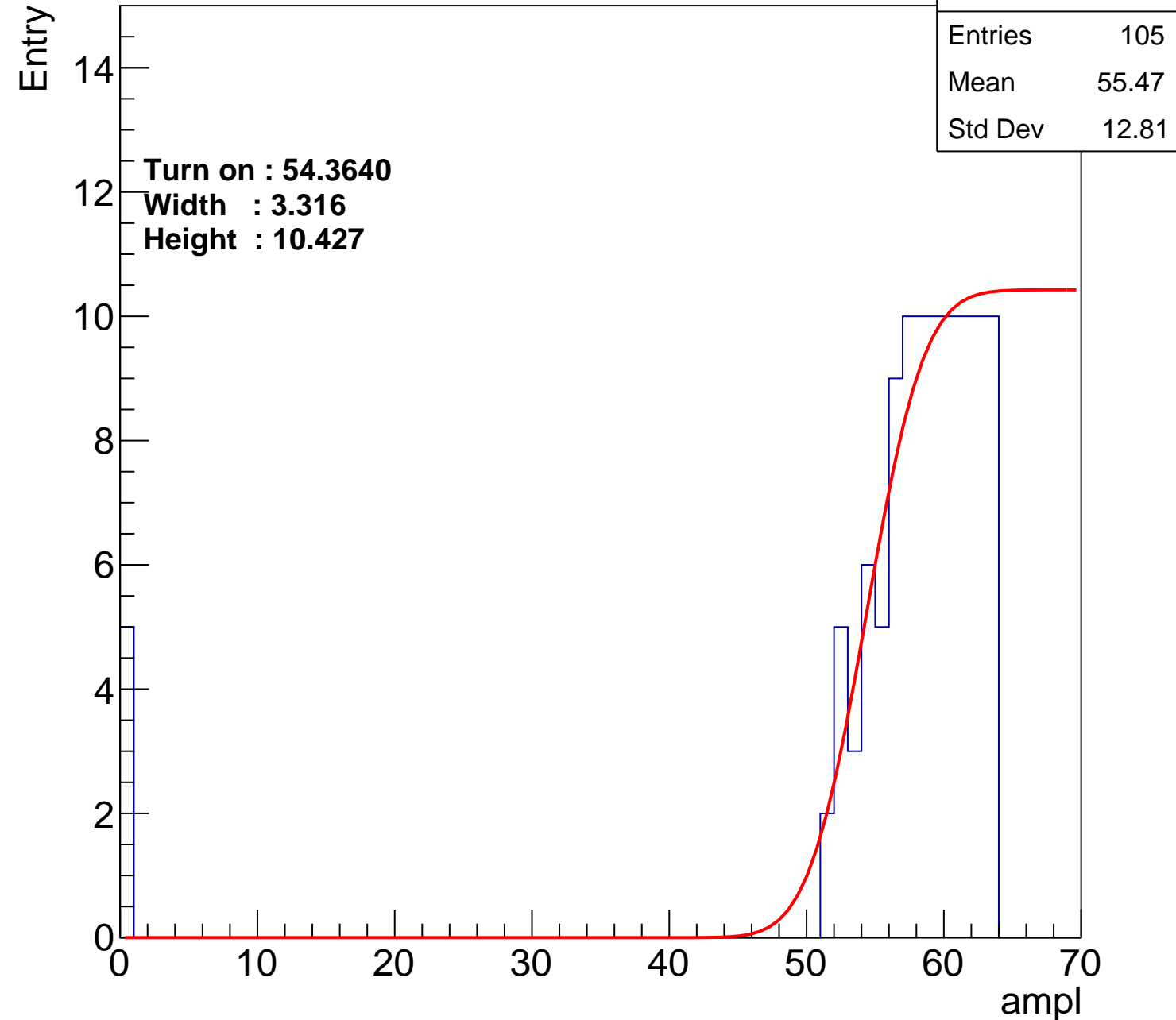
Width : 3.316

Height : 10.427

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch89

calib_packv5_040323_1717.root, FC#2, port C3

Entries	123
Mean	55.42
Std Dev	10.83

Turn on : 51.9578

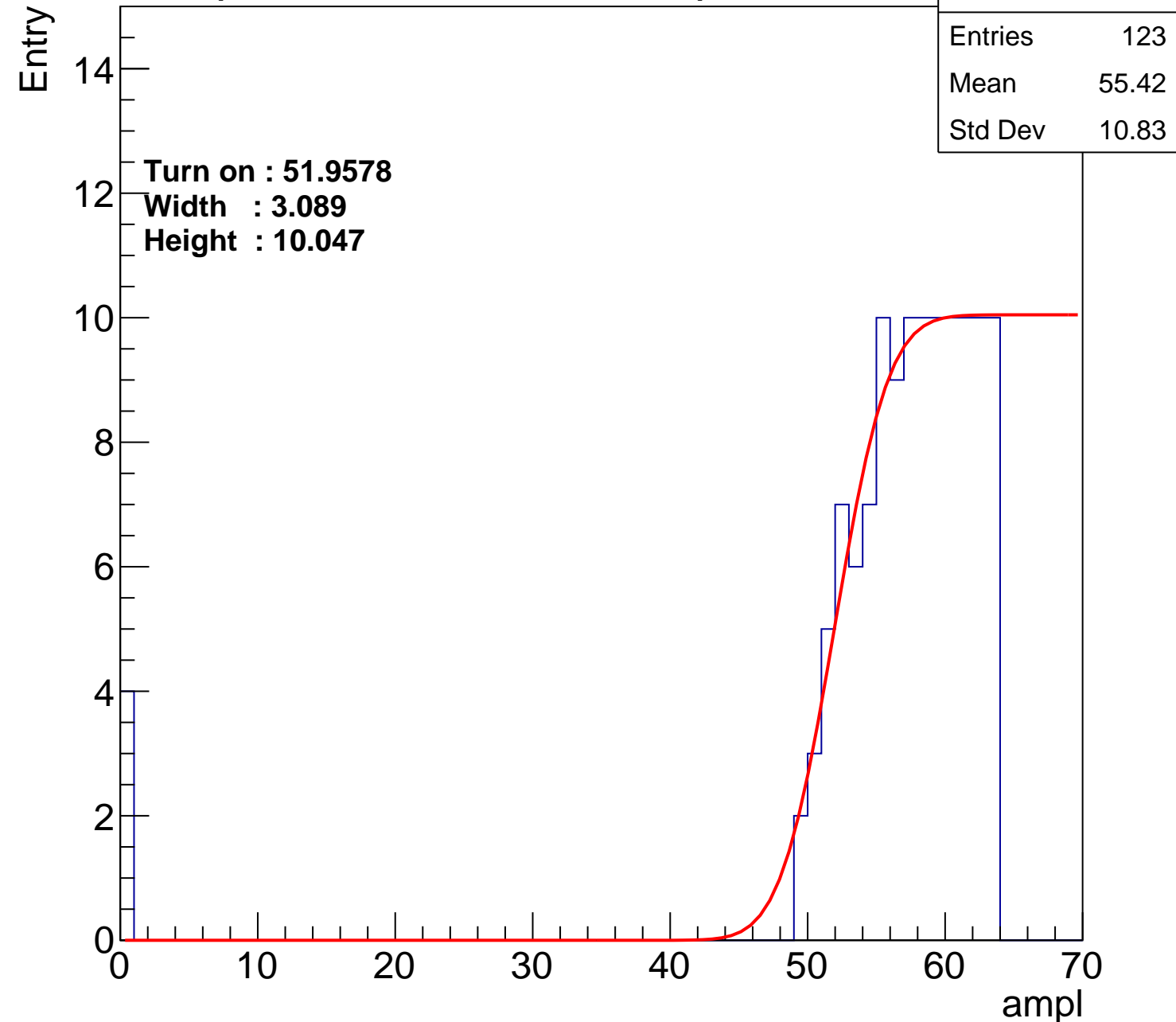
Width : 3.089

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch90

calib_packv5_040323_1717.root, FC#2, port C3

Entries	116
Mean	55.49
Std Dev	11.14

Turn on : 52.7038

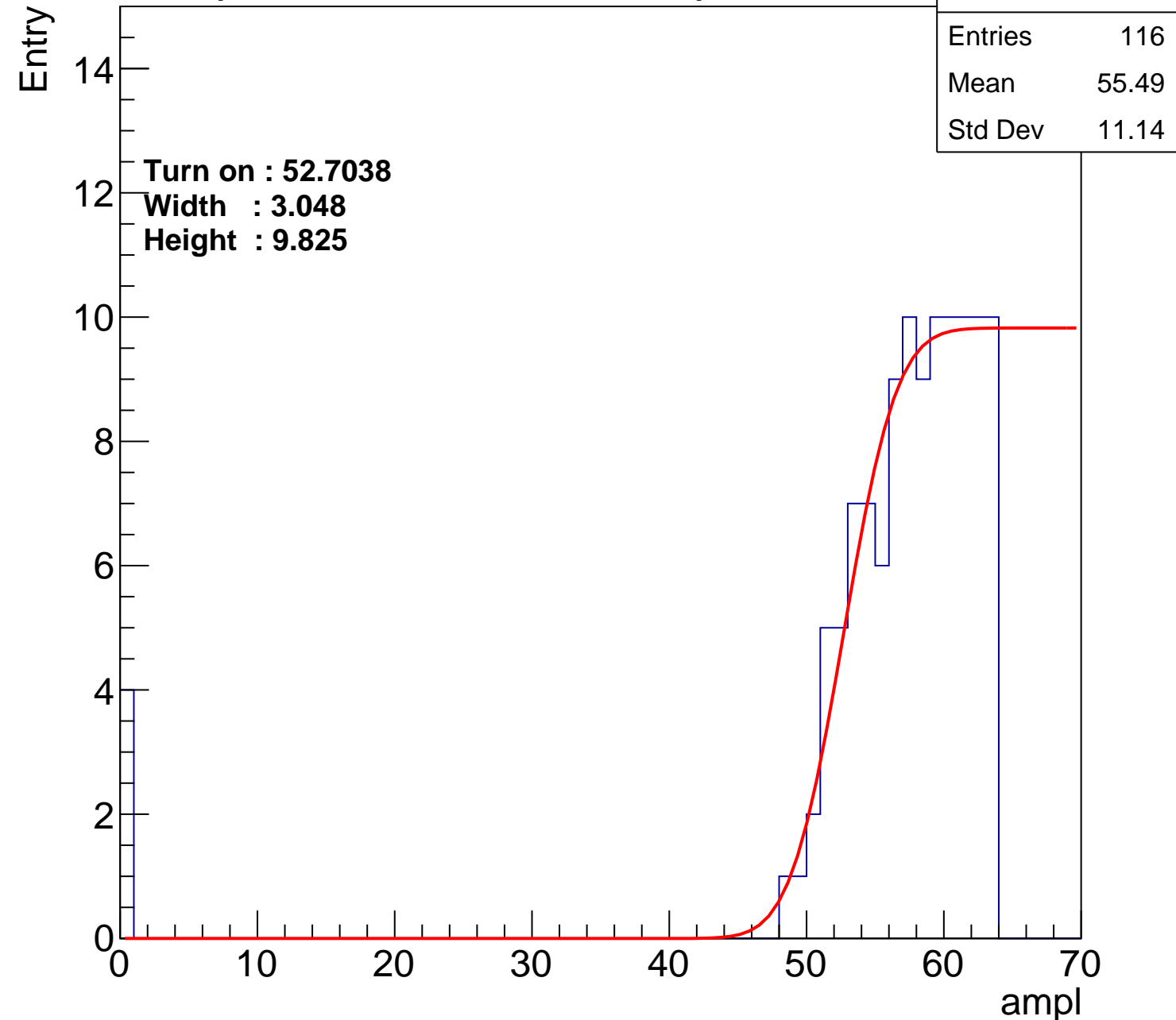
Width : 3.048

Height : 9.825

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch91

calib_packv5_040323_1717.root, FC#2, port C3

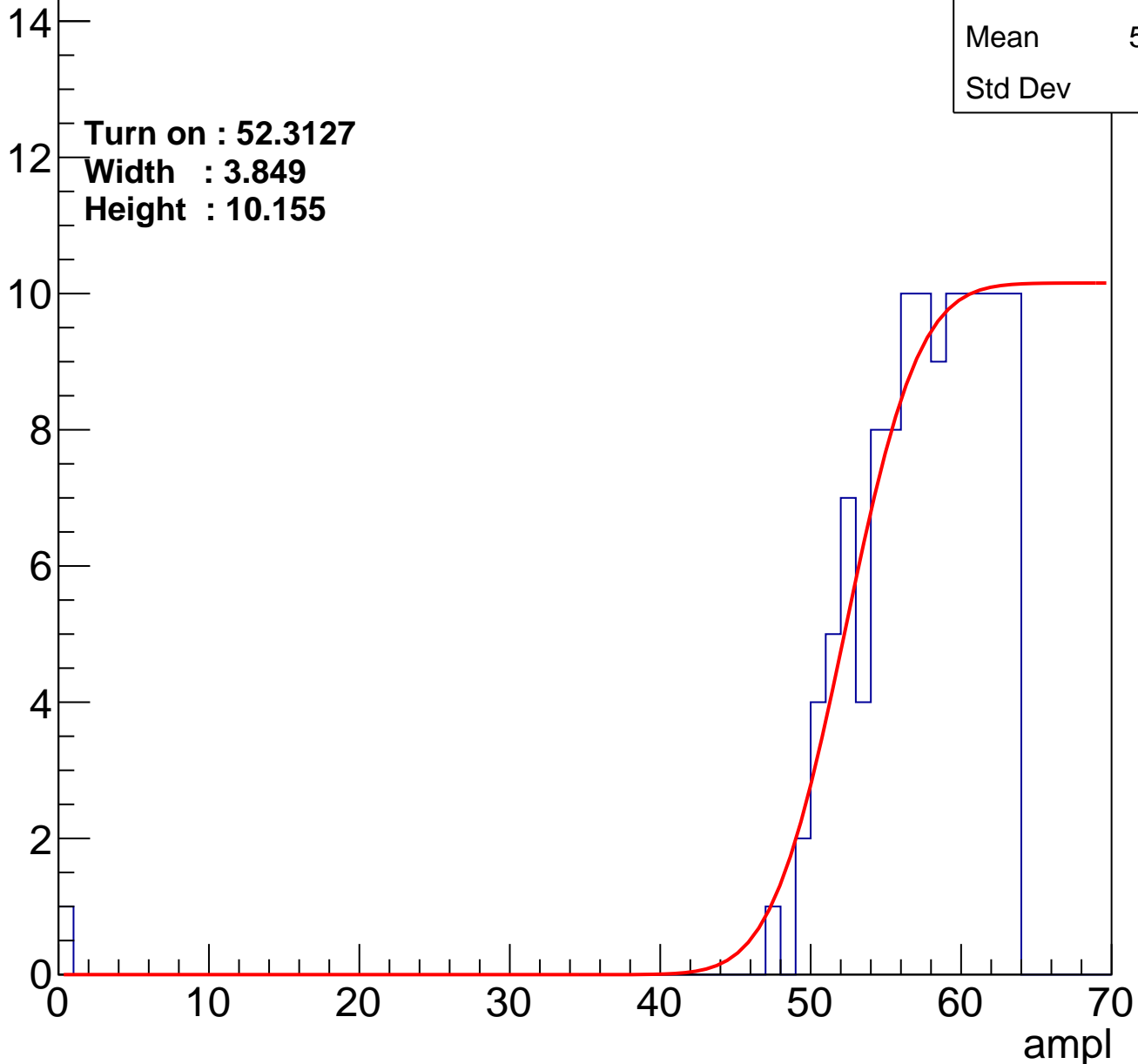
Entries	119
Mean	56.72
Std Dev	6.55

Turn on : 52.3127

Width : 3.849

Height : 10.155

Entry



B0L103S, U13-ch92

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.95
Std Dev	10.5

Turn on : 51.2376

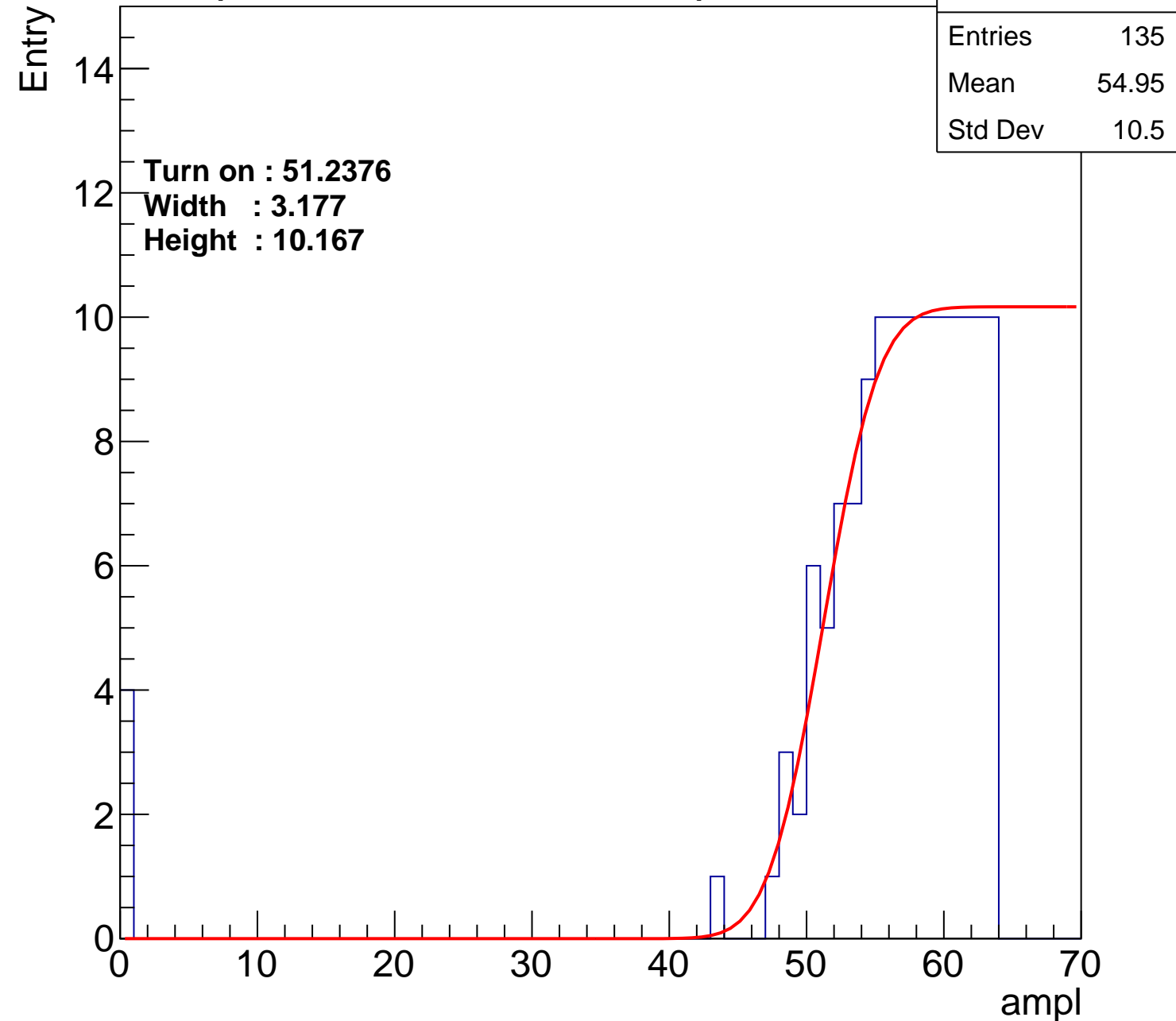
Width : 3.177

Height : 10.167

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch93

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	55.55
Std Dev	9.558

Turn on : 51.8903

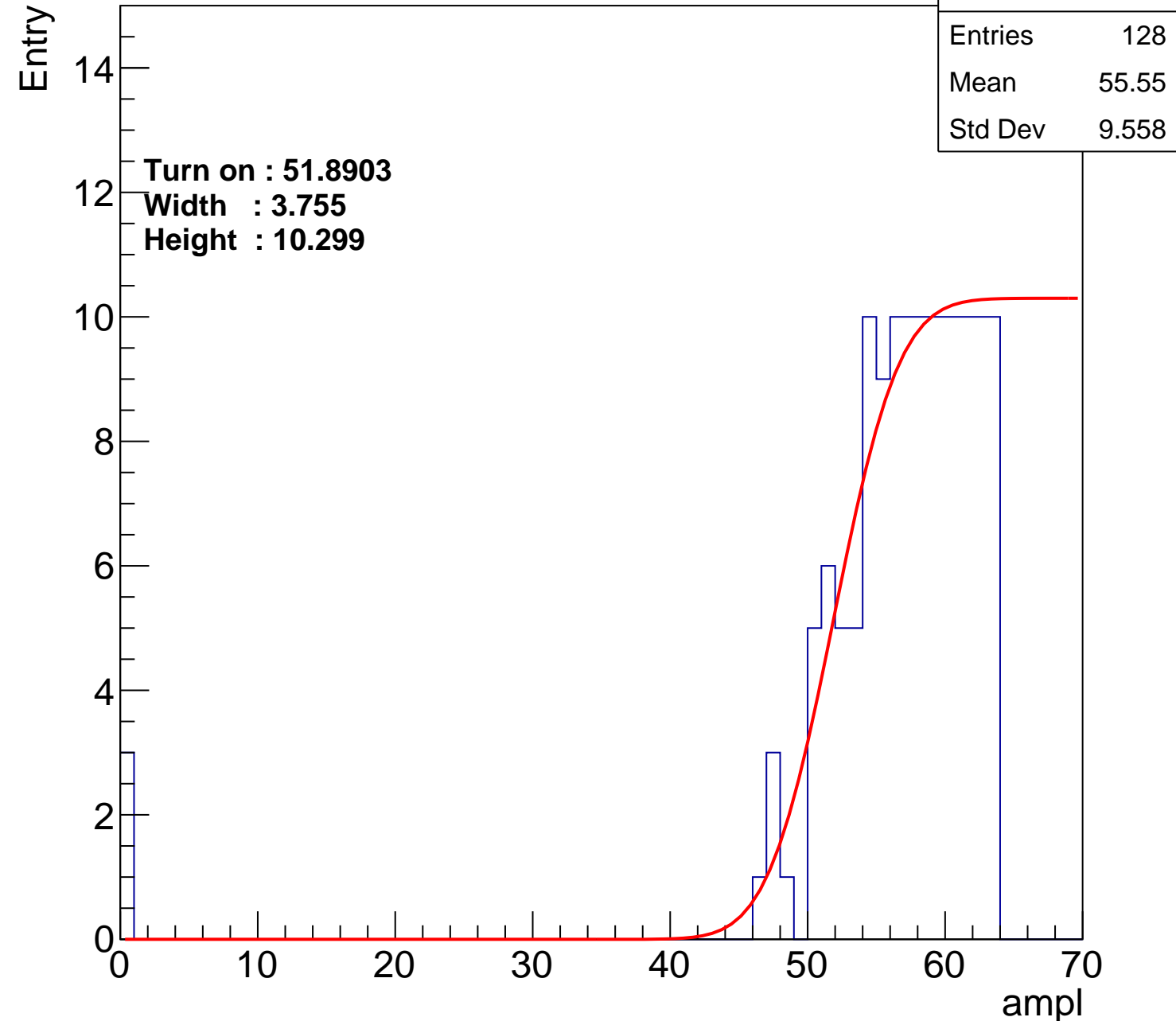
Width : 3.755

Height : 10.299

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch94

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.22
Std Dev	10.83

Turn on : 52.3837

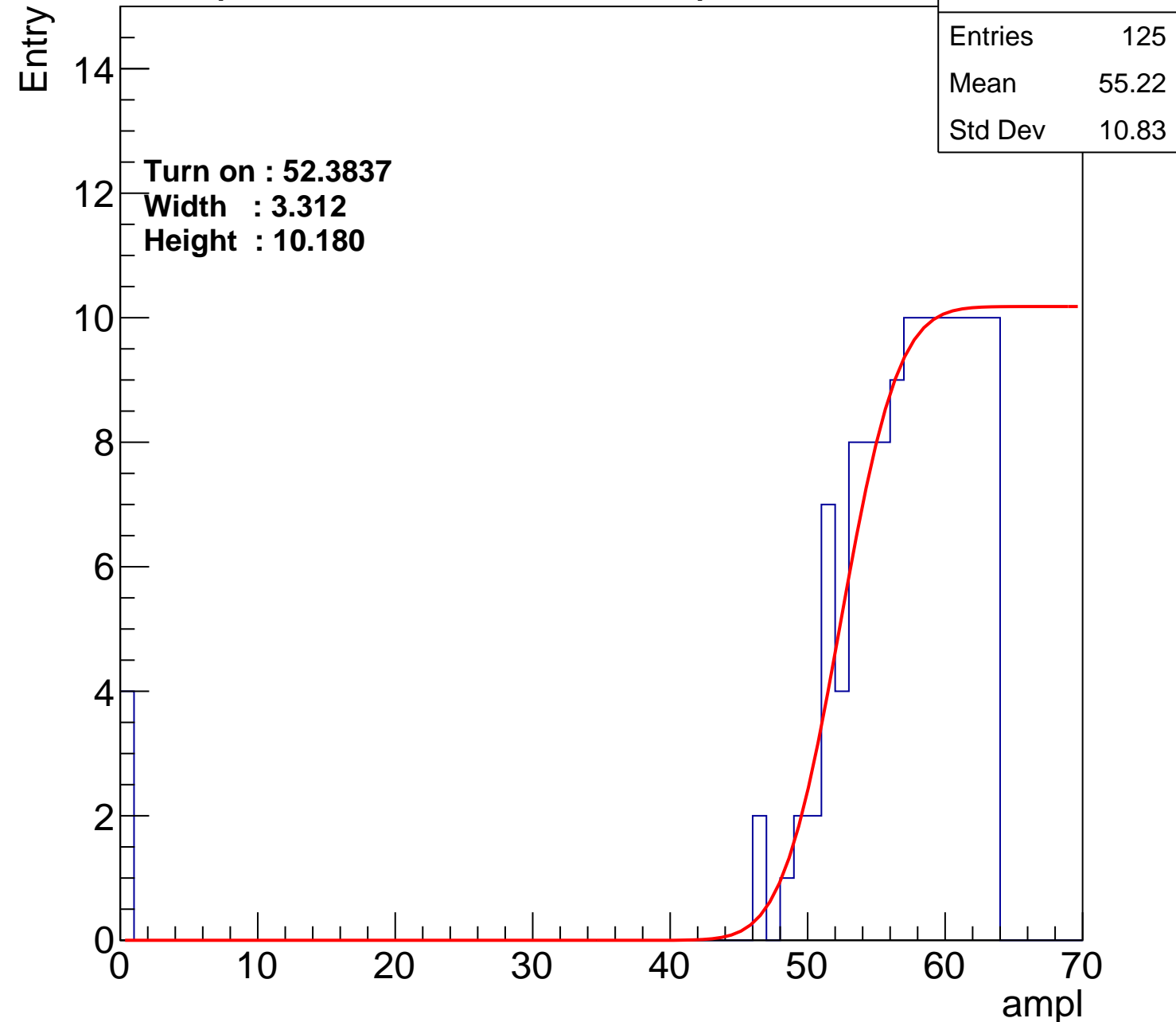
Width : 3.312

Height : 10.180

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch95

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.98
Std Dev	8.25

Turn on : 50.7553

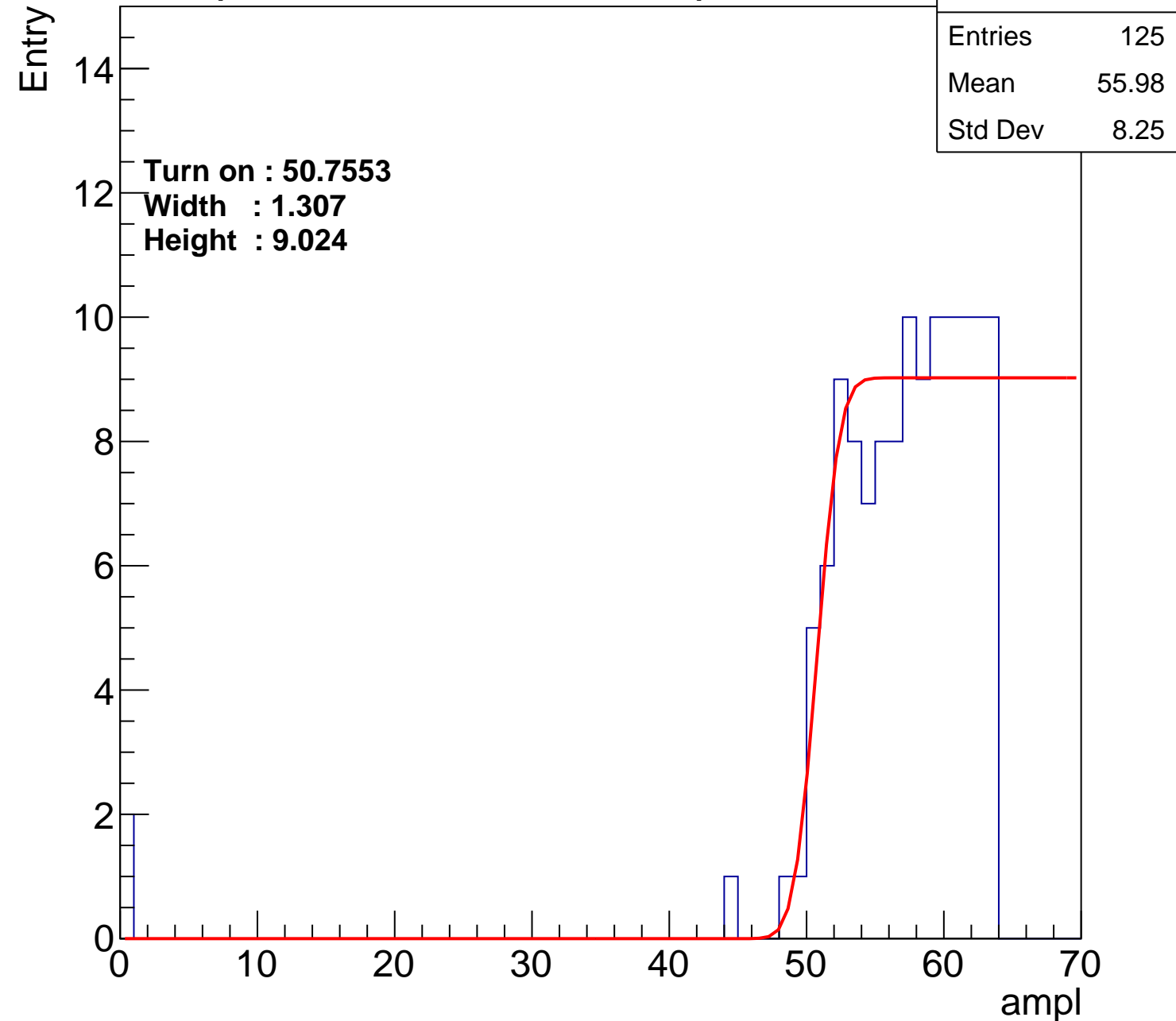
Width : 1.307

Height : 9.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch96

calib_packv5_040323_1717.root, FC#2, port C3

Entries	98
Mean	57.03
Std Dev	8.919

Turn on : 54.7487

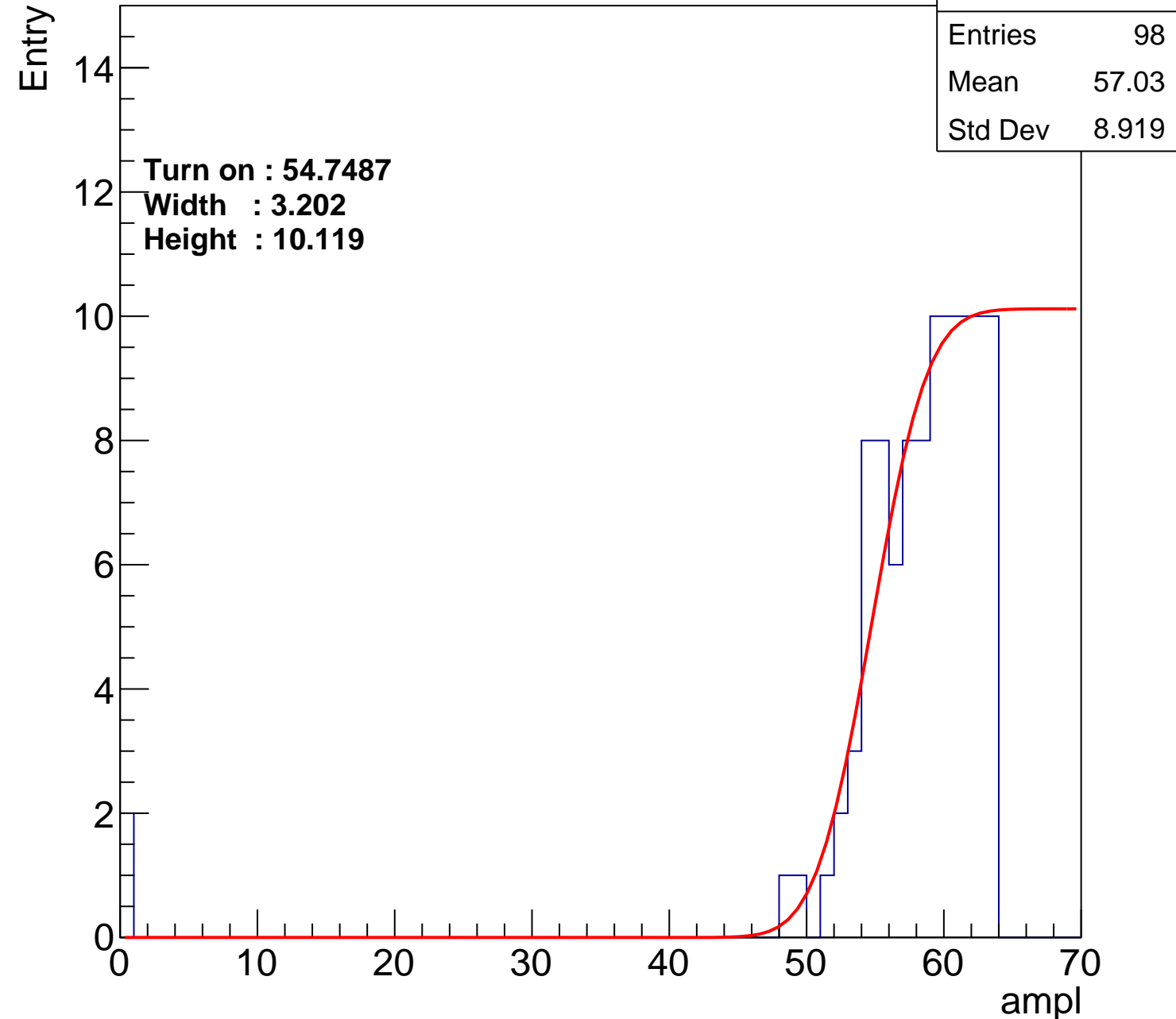
Width : 3.202

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch97

calib_packv5_040323_1717.root, FC#2, port C3

Entries	100
Mean	56.59
Std Dev	10.47

Turn on : 54.3806

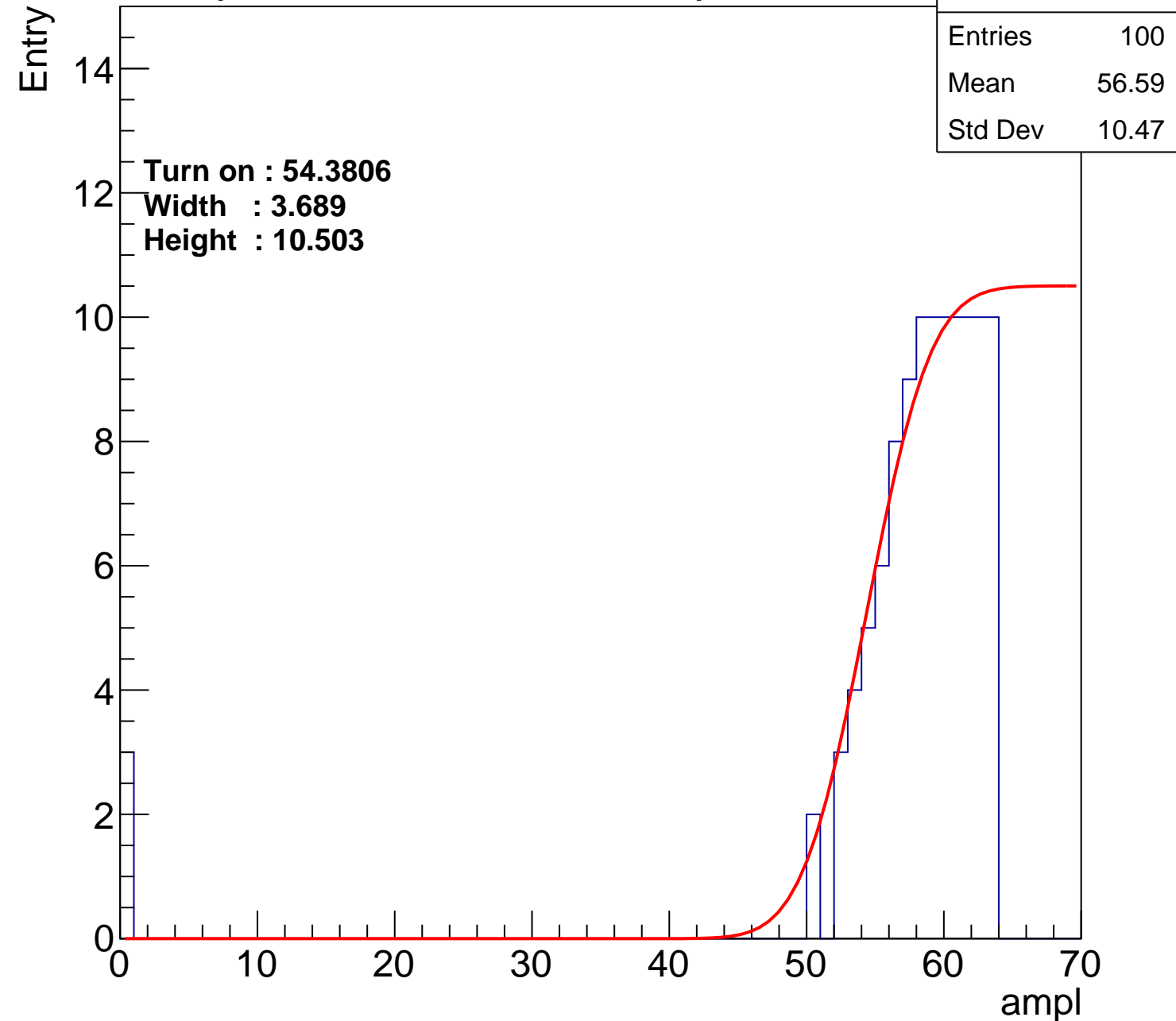
Width : 3.689

Height : 10.503

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch98

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	54.57
Std Dev	10.35

Turn on : 50.1412

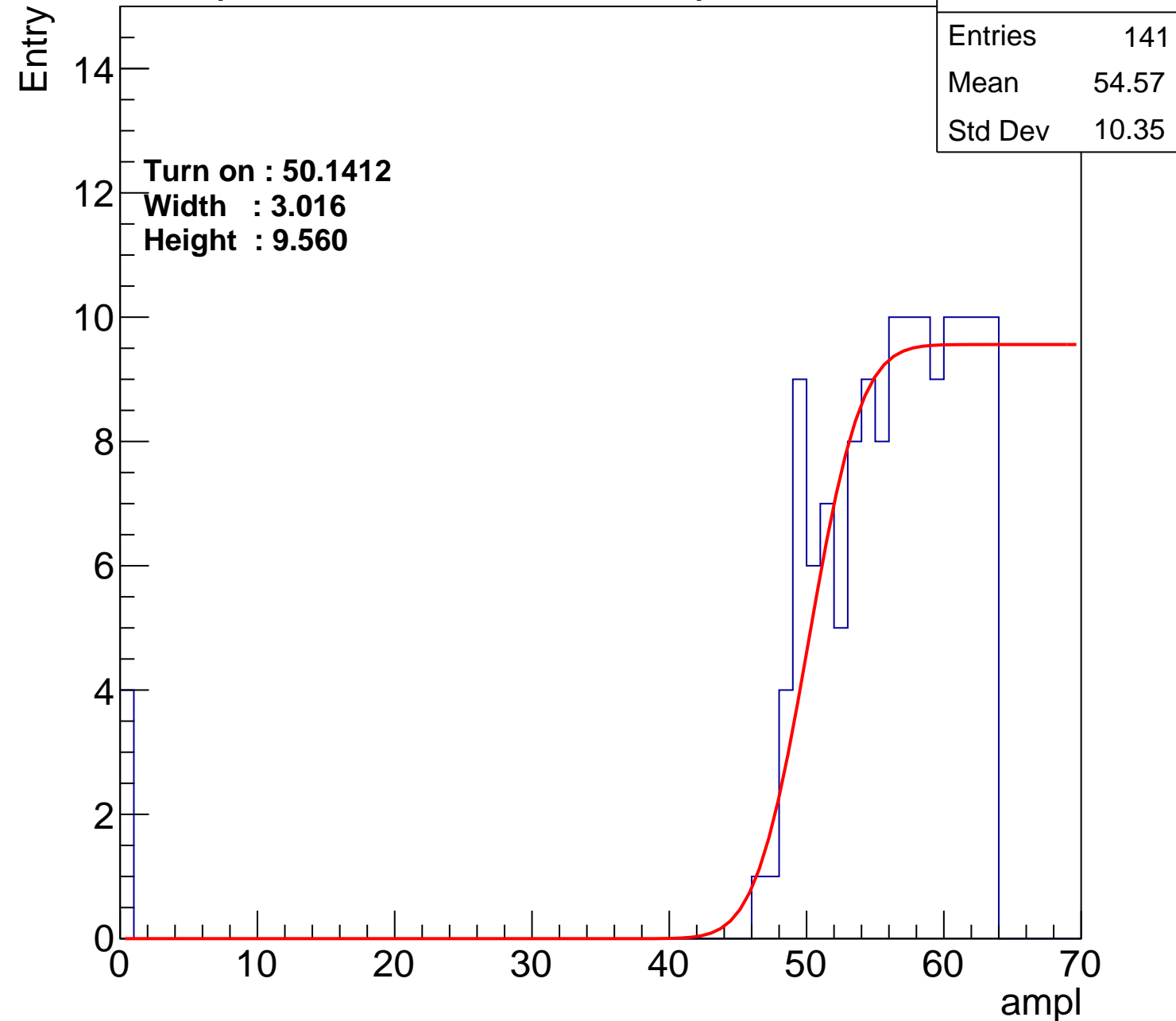
Width : 3.016

Height : 9.560

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch99

calib_packv5_040323_1717.root, FC#2, port C3

Entries	124
Mean	55.74
Std Dev	9.632

Turn on : 52.2071

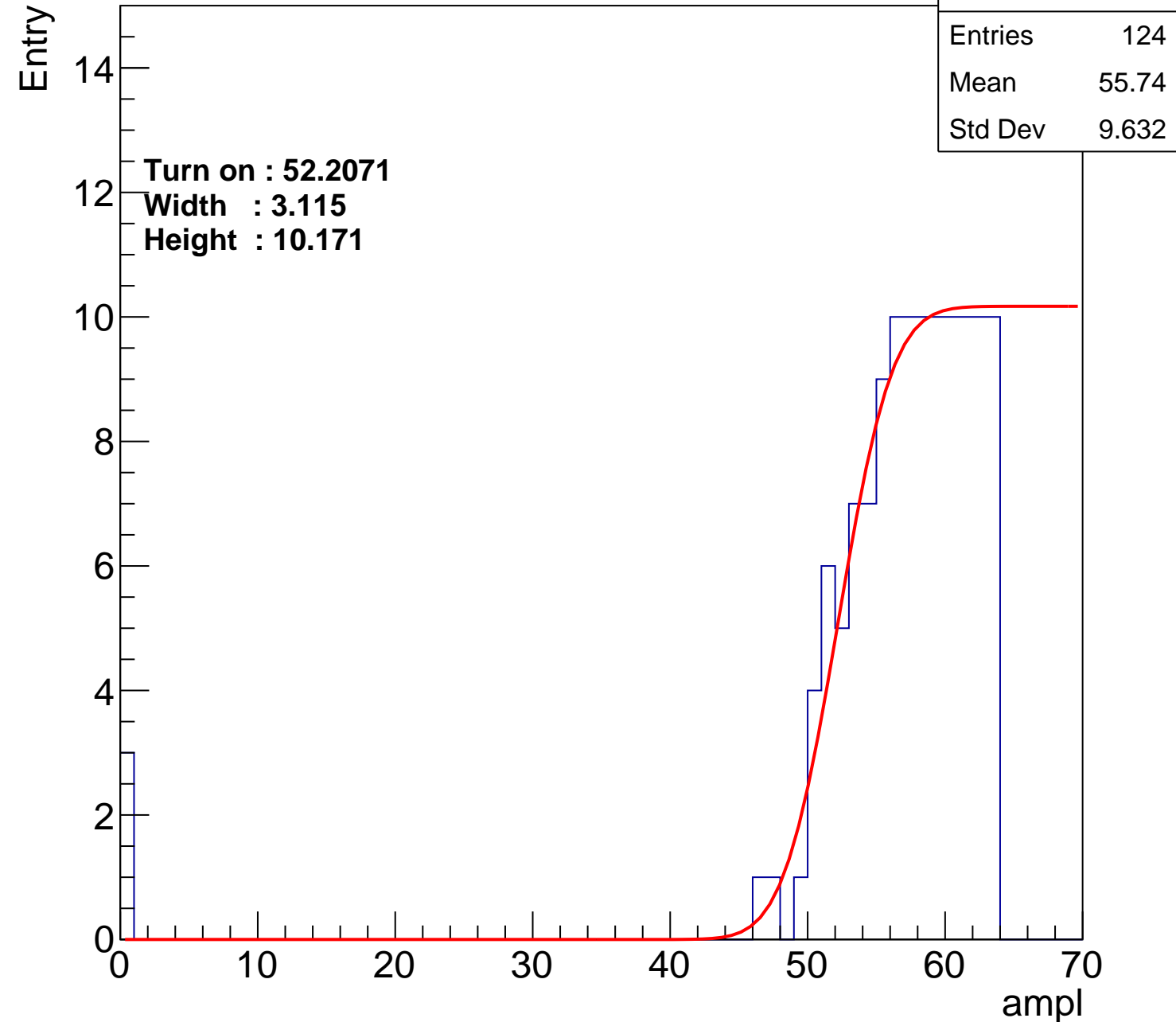
Width : 3.115

Height : 10.171

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch100

calib_packv5_040323_1717.root, FC#2, port C3

Entries	137
Mean	54.49
Std Dev	11.41

Turn on : 51.3085

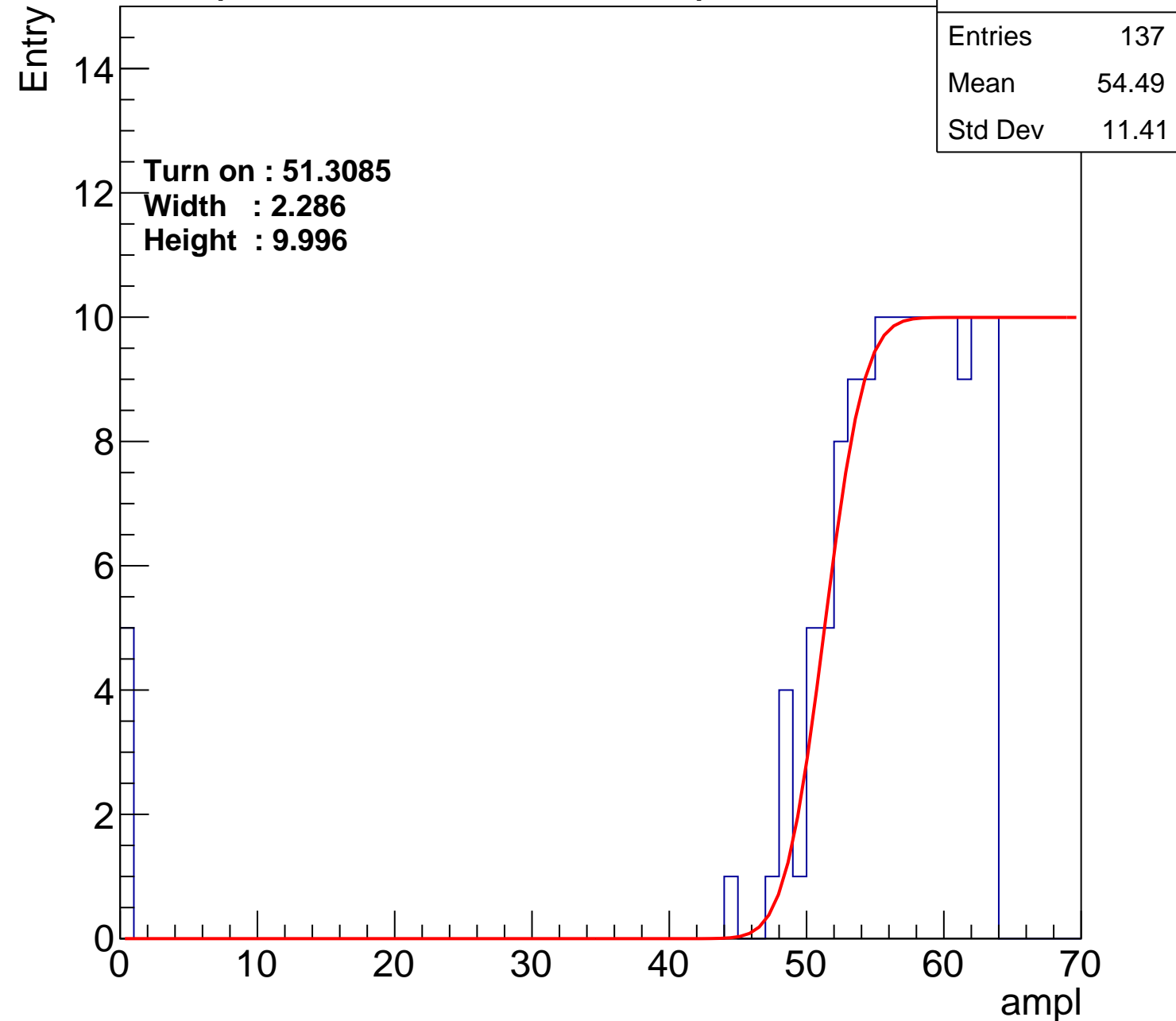
Width : 2.286

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch101

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.21
Std Dev	10.83

Turn on : 52.8751

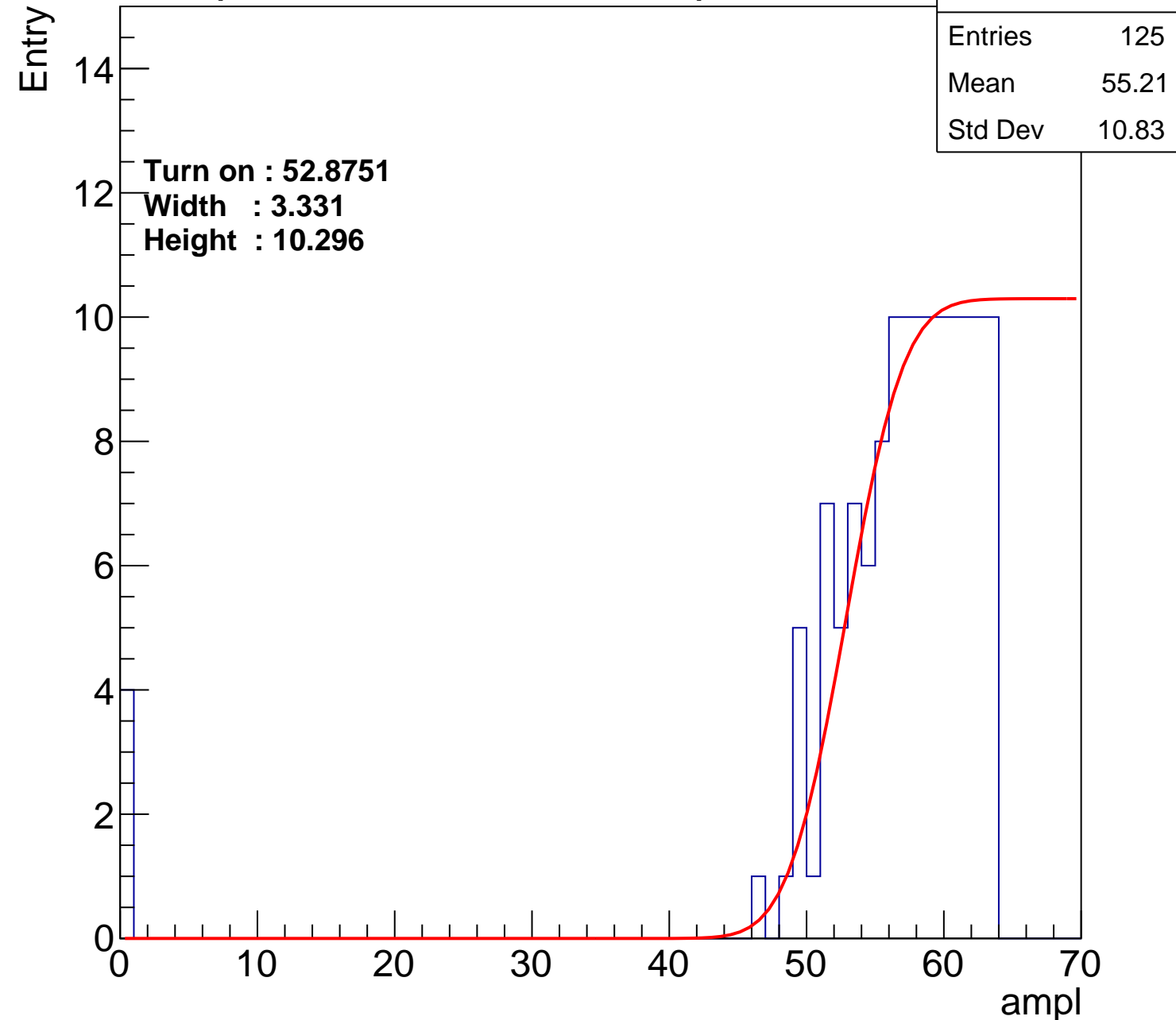
Width : 3.331

Height : 10.296

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch102

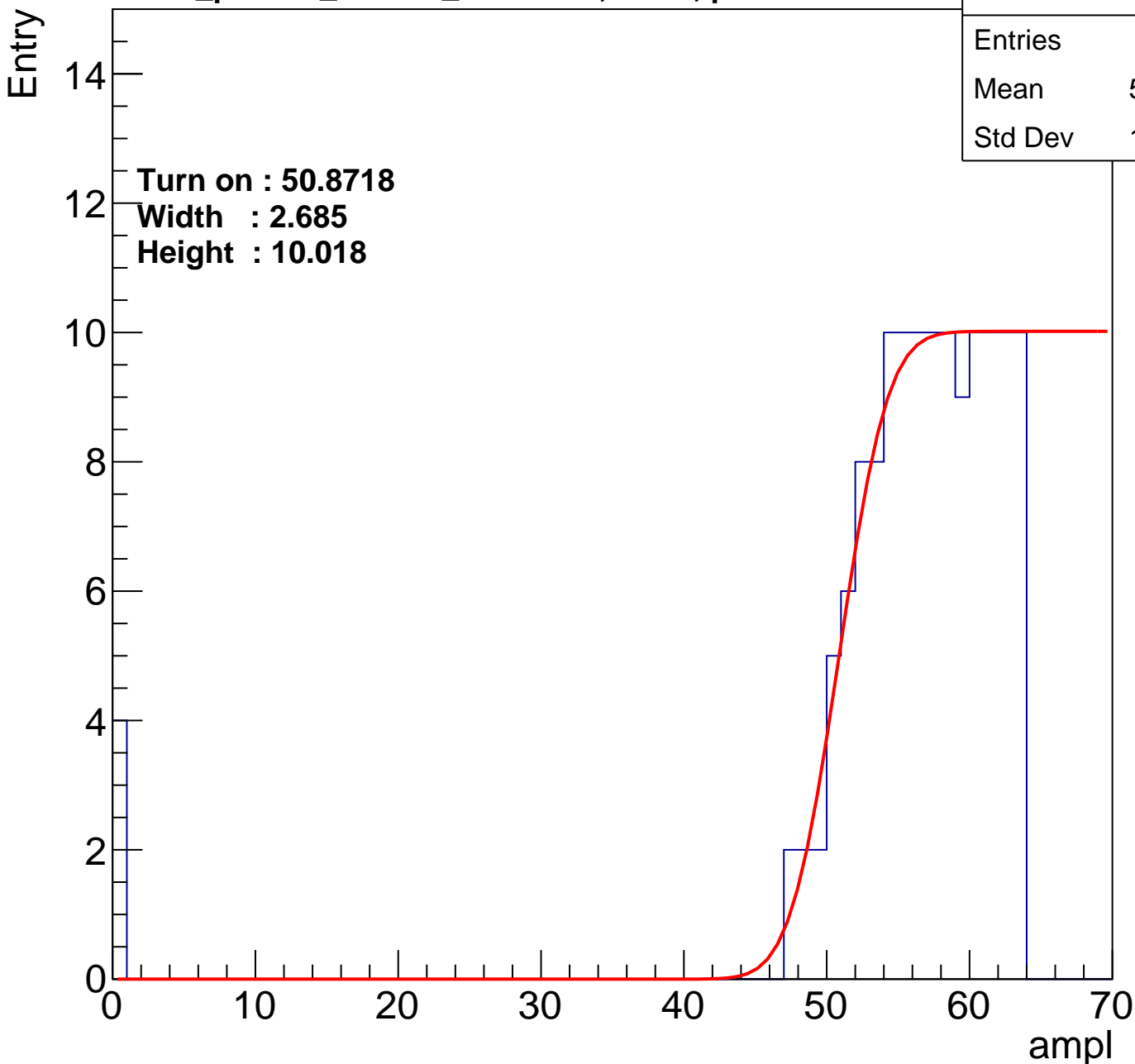
calib_packv5_040323_1717.root, FC#2, port C3

Entries	136
Mean	54.96
Std Dev	10.42

Turn on : 50.8718

Width : 2.685

Height : 10.018



B0L103S, U13-ch103

calib_packv5_040323_1717.root, FC#2, port C3

Entries	121
Mean	55.93
Std Dev	9.676

Turn on : 52.4254

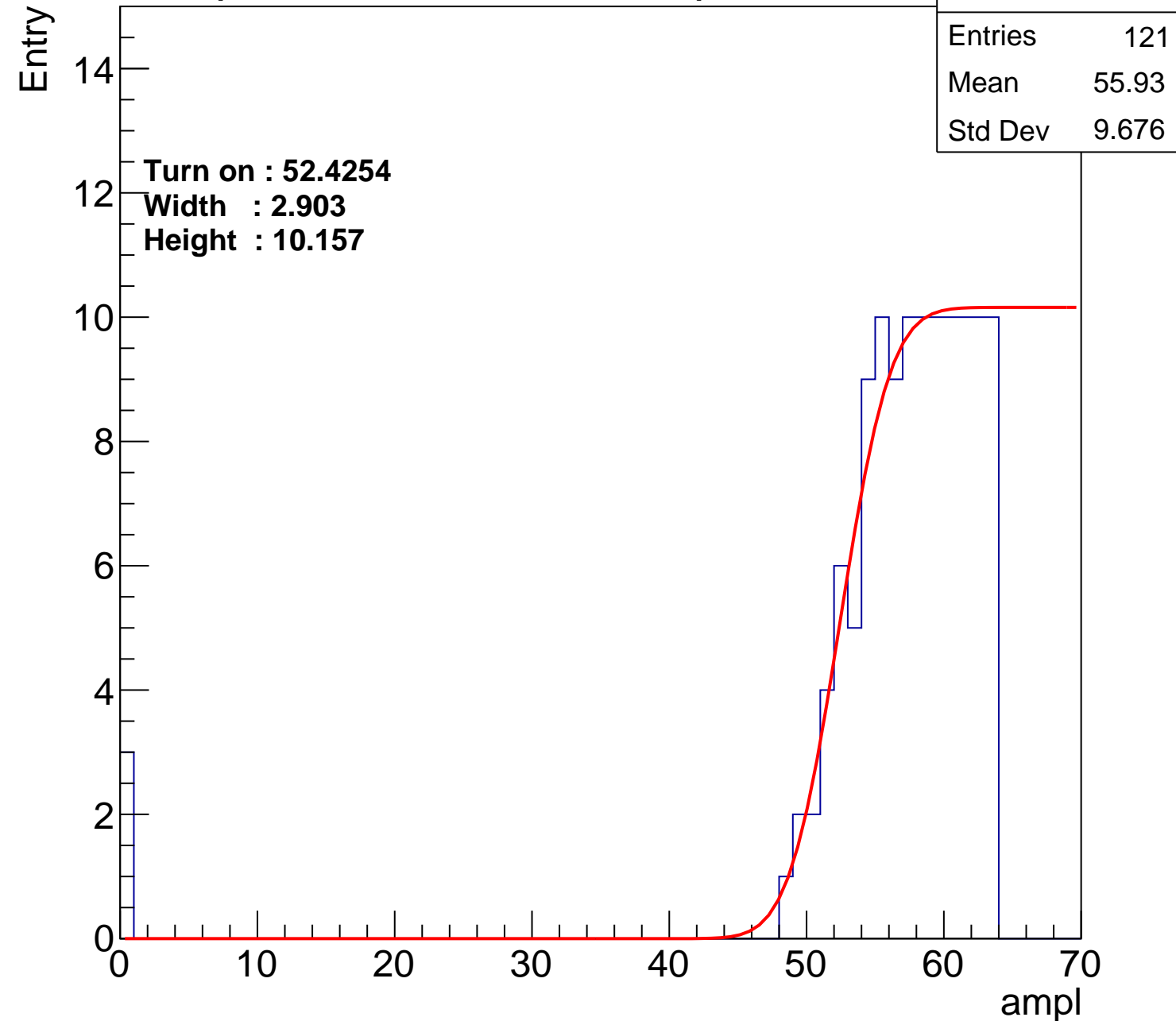
Width : 2.903

Height : 10.157

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch104

calib_packv5_040323_1717.root, FC#2, port C3

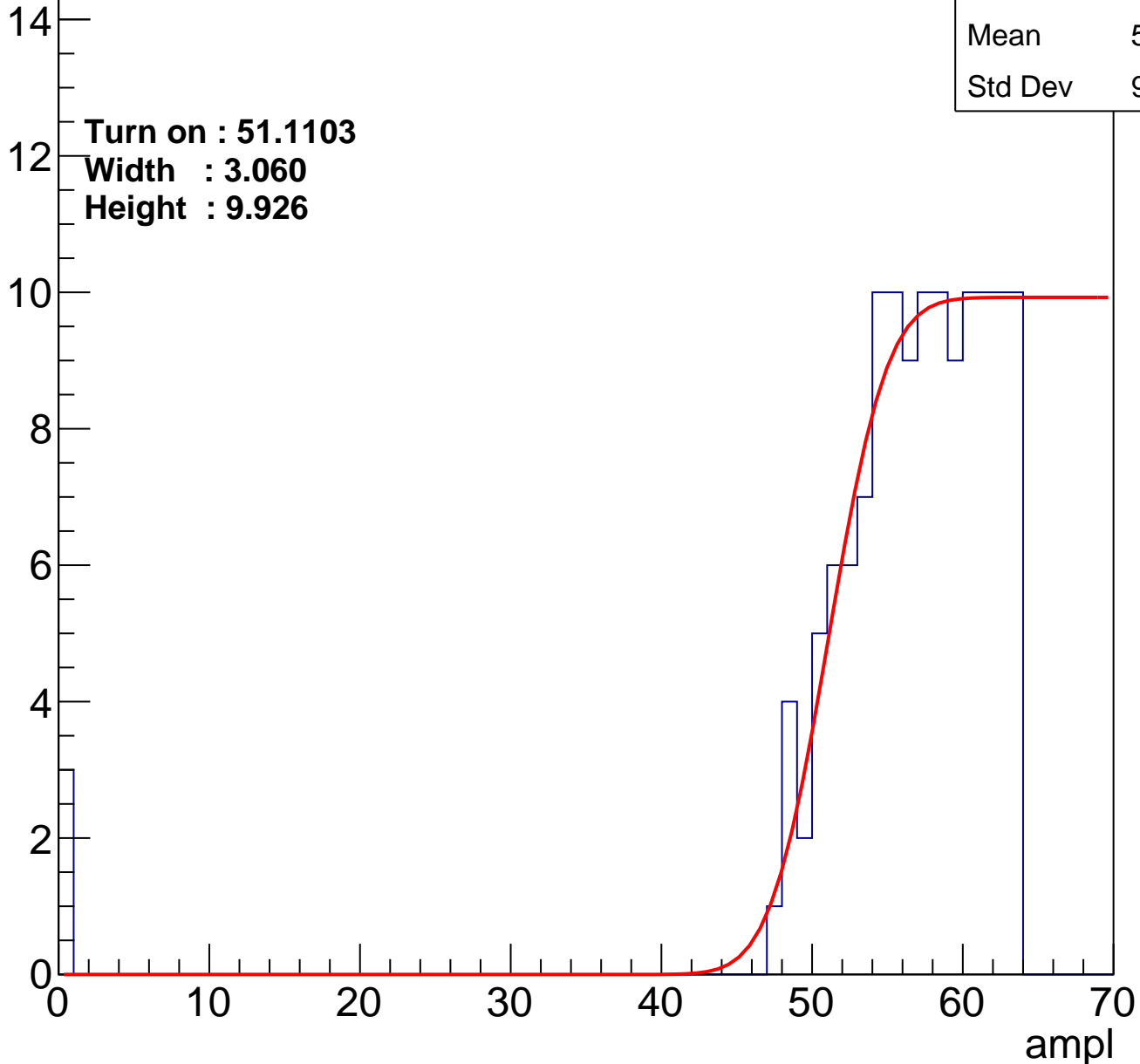
Entries	132
Mean	55.39
Std Dev	9.424

Turn on : 51.1103

Width : 3.060

Height : 9.926

Entry



B0L103S, U13-ch105

calib_packv5_040323_1717.root, FC#2, port C3

Entries	135
Mean	54.85
Std Dev	10.52

Turn on : 50.9627

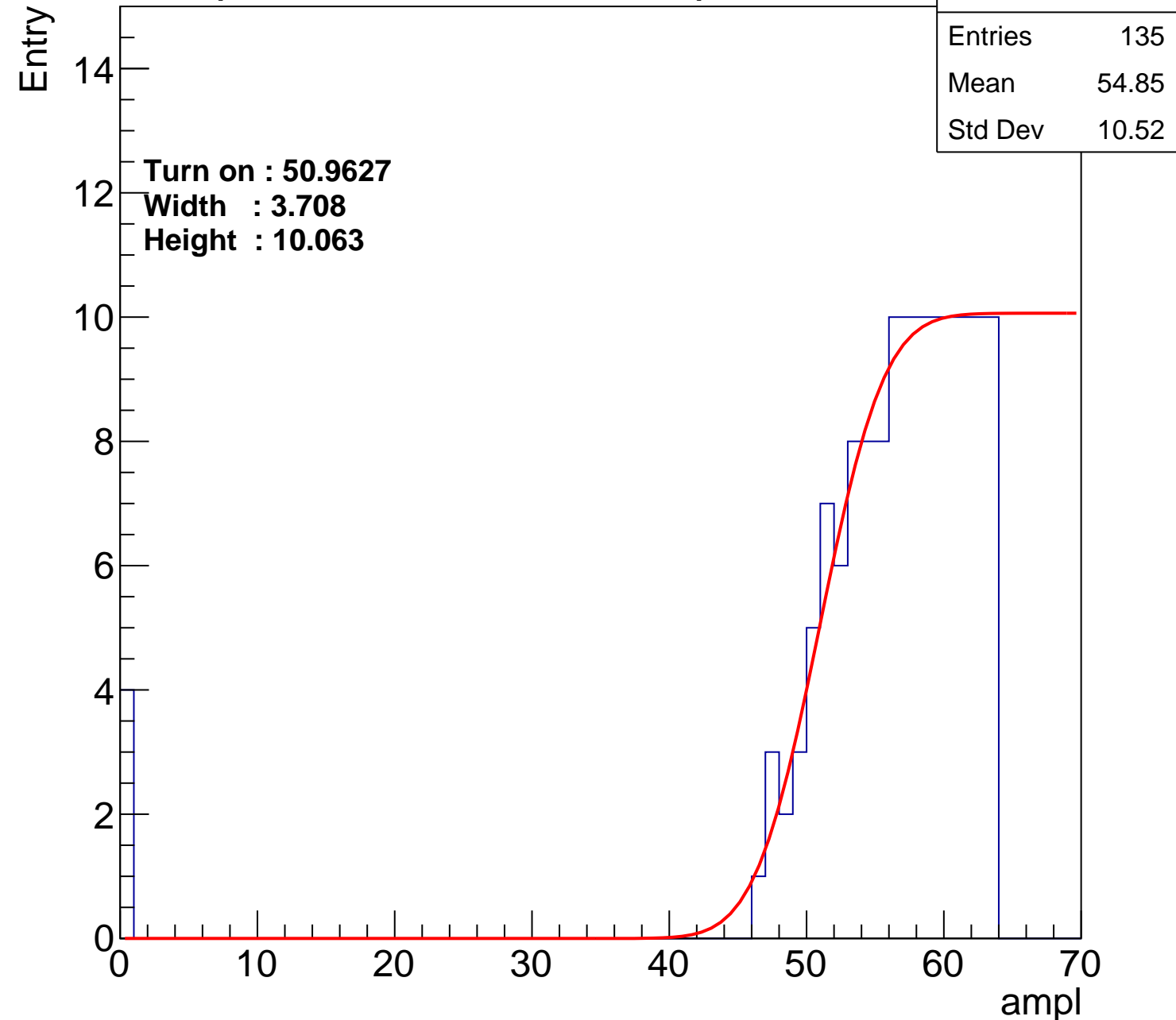
Width : 3.708

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch106

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.16
Std Dev	10.83

Turn on : 51.7717

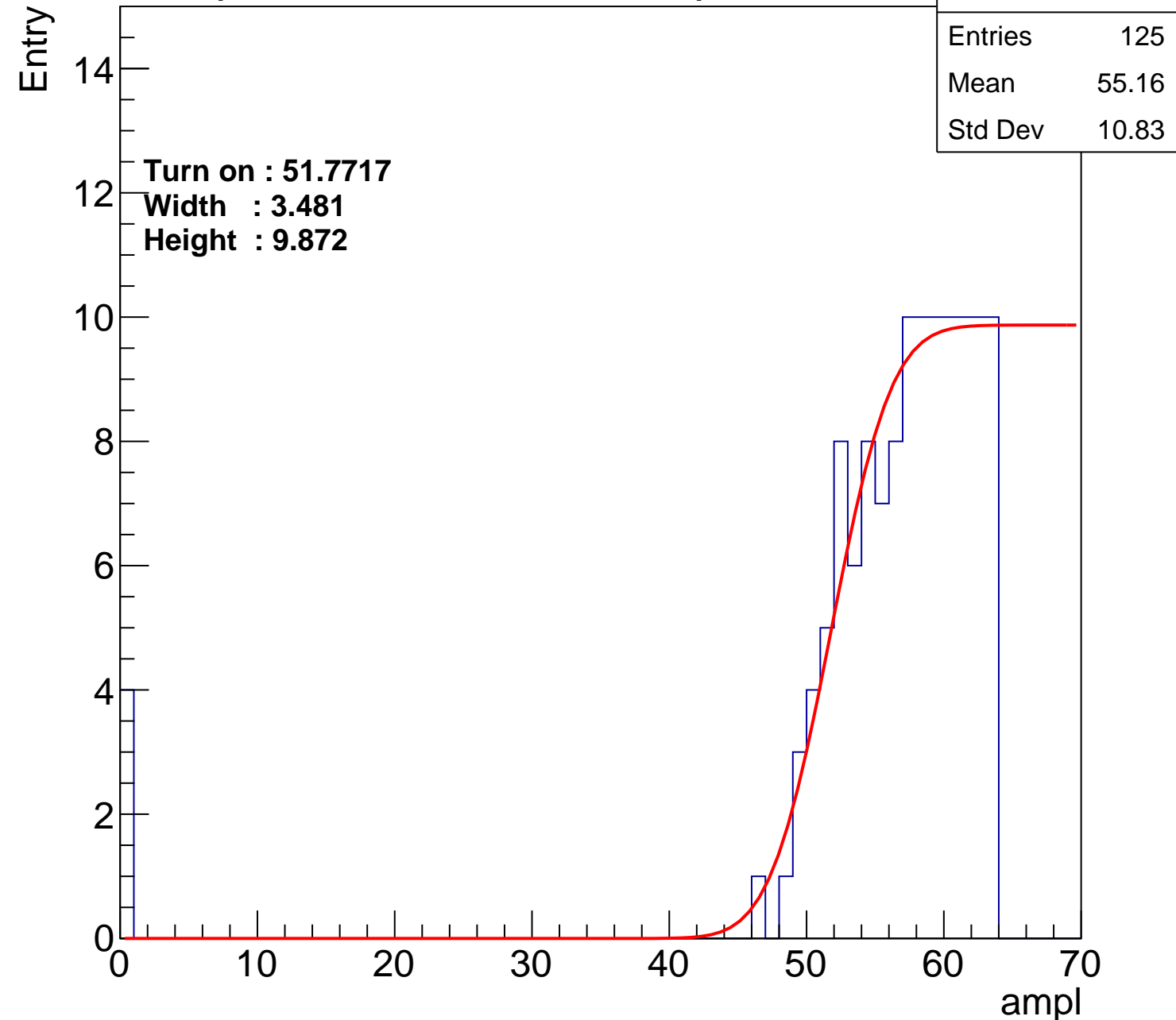
Width : 3.481

Height : 9.872

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch107

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.98
Std Dev	8.344

Turn on : 52.7582

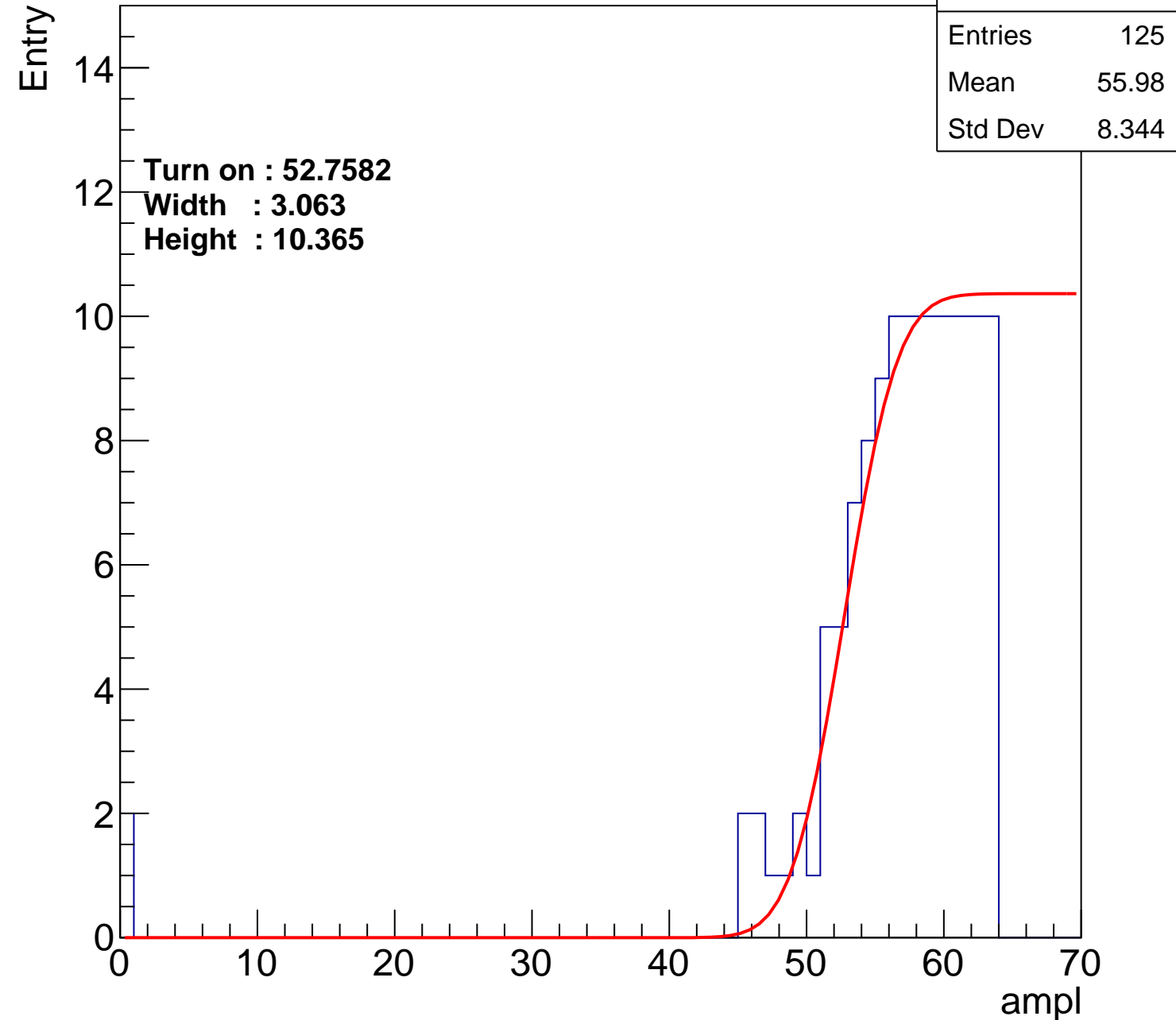
Width : 3.063

Height : 10.365

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch108

calib_packv5_040323_1717.root, FC#2, port C3

Entries	140
Mean	54.69
Std Dev	10.38

Turn on : 51.1828

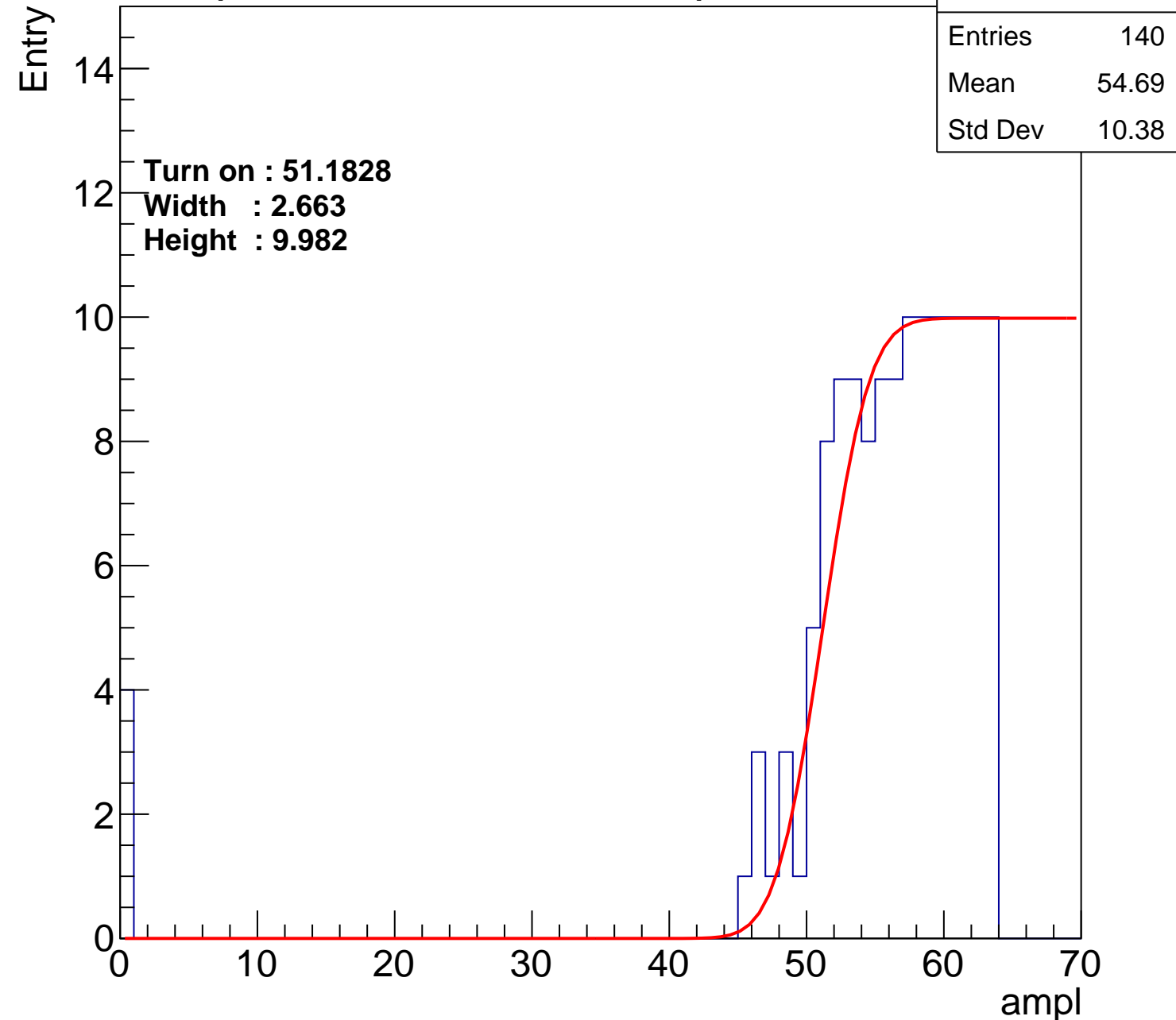
Width : 2.663

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch109

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.71
Std Dev	11.61

Turn on : 51.6694

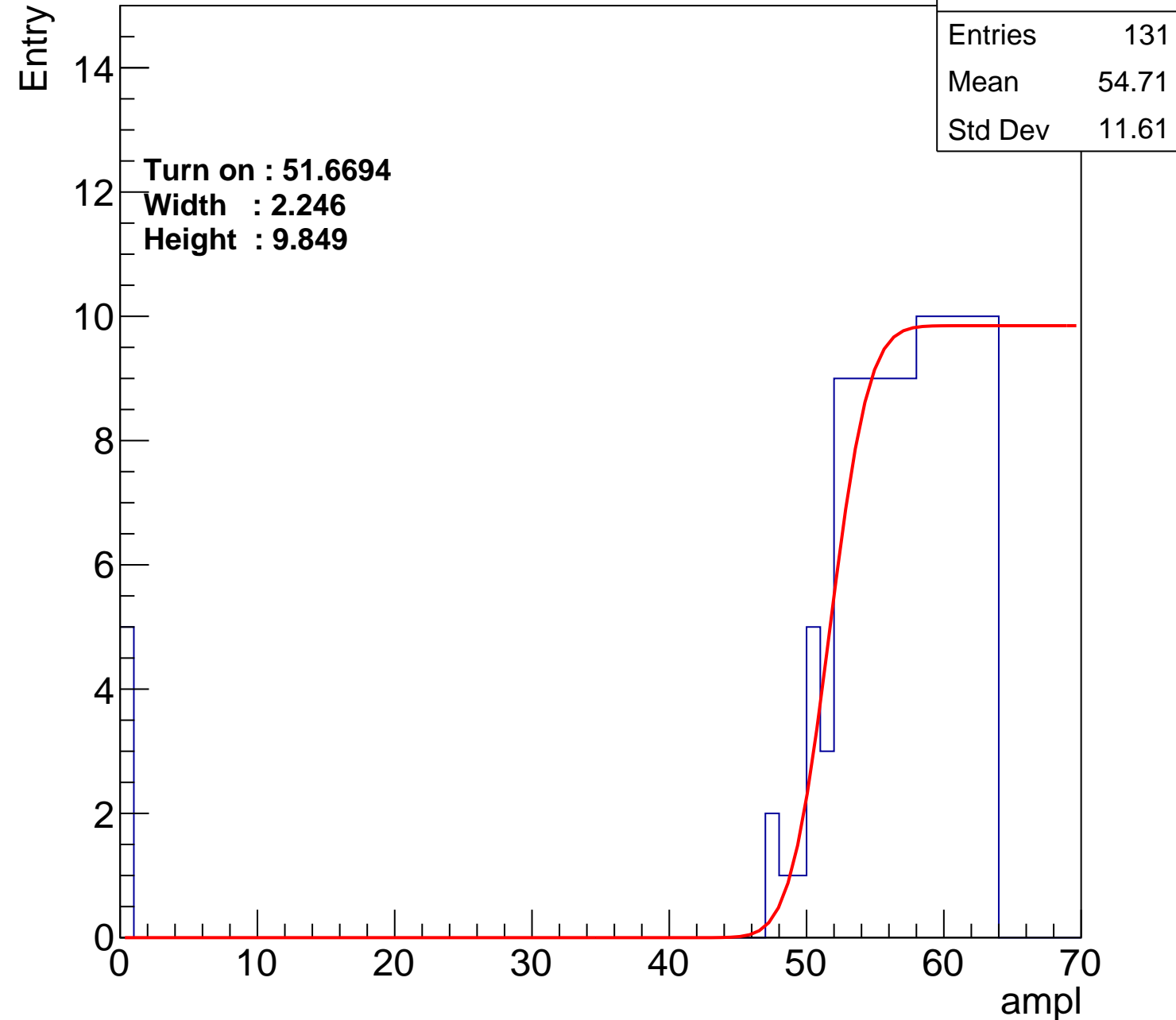
Width : 2.246

Height : 9.849

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch110

calib_packv5_040323_1717.root, FC#2, port C3

Entries	128
Mean	56.05
Std Dev	8.132

Turn on : 51.8476

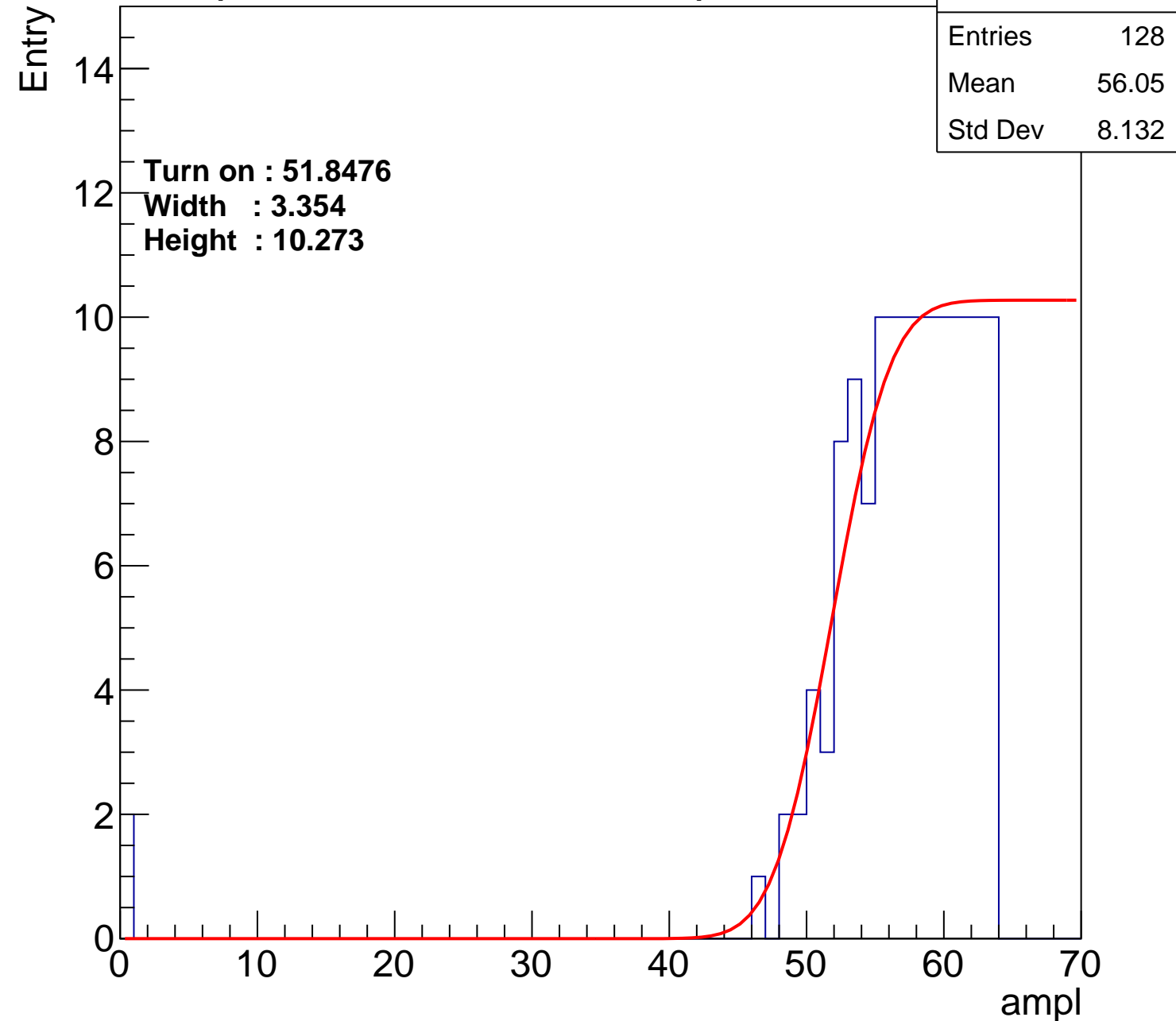
Width : 3.354

Height : 10.273

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch111

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	55.38
Std Dev	9.508

Turn on : 51.7900

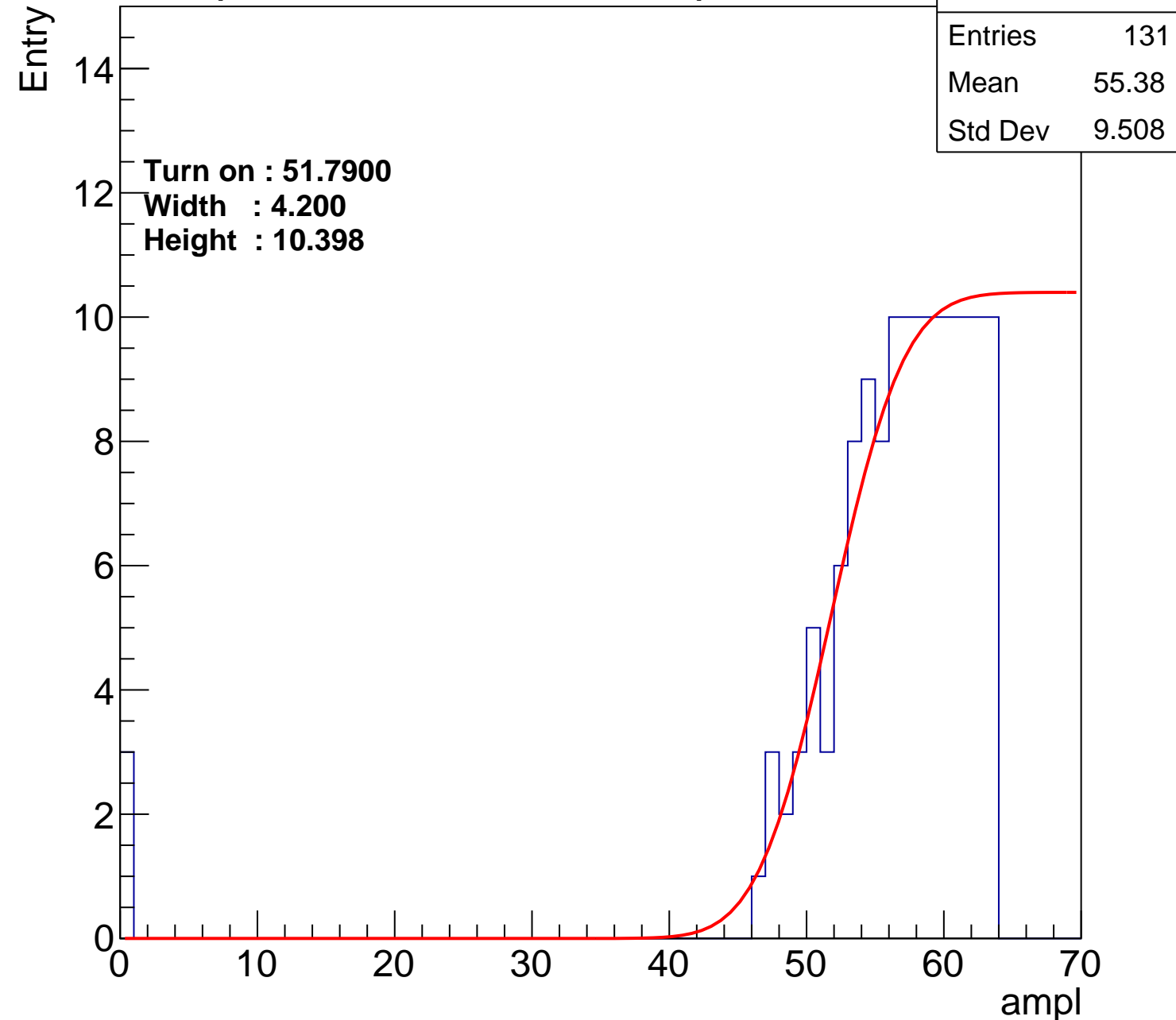
Width : 4.200

Height : 10.398

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch112

calib_packv5_040323_1717.root, FC#2, port C3

Entries	131
Mean	54.95
Std Dev	10.66

Turn on : 52.0835

Width : 4.480

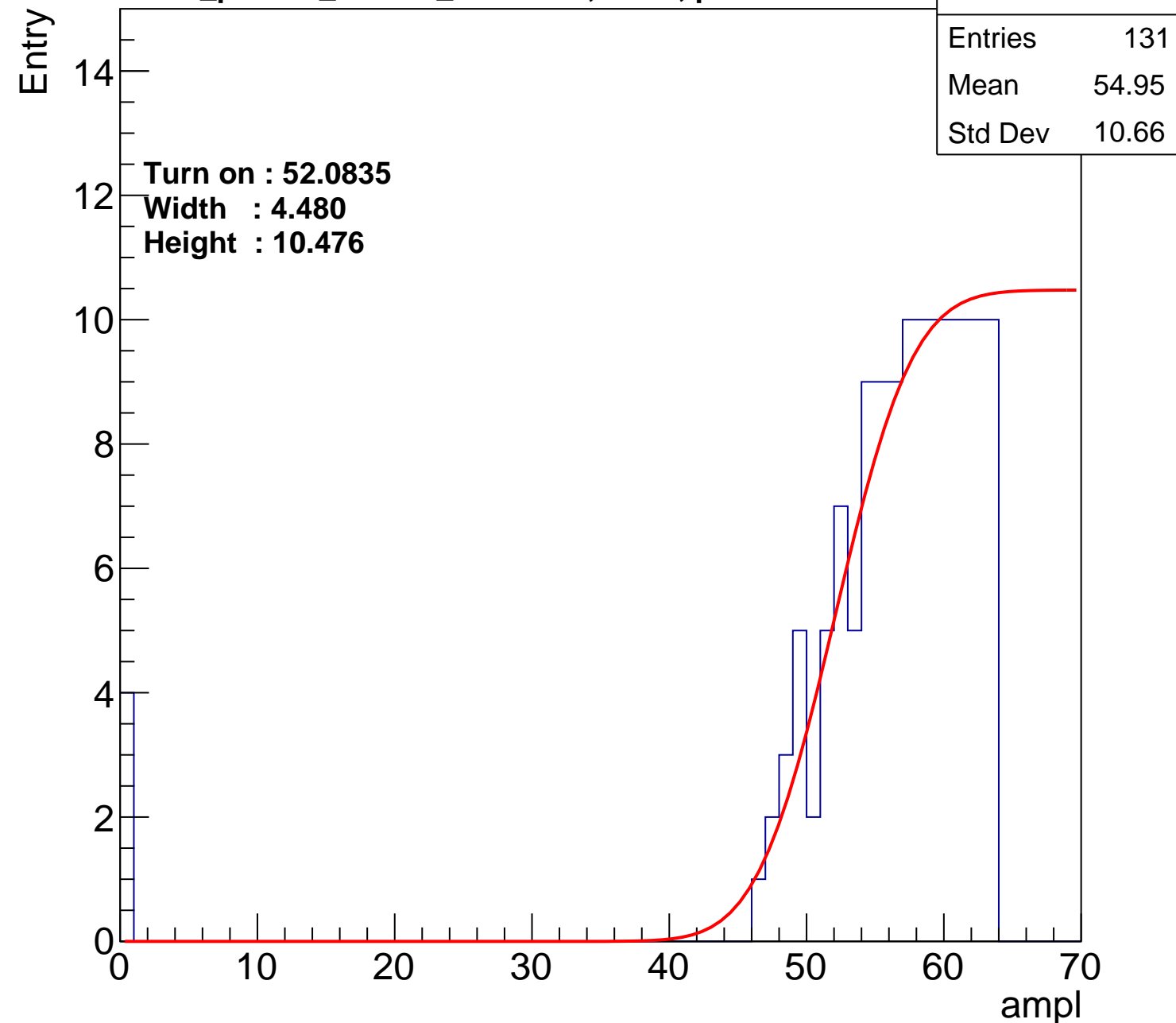
Height : 10.476

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L103S, U13-ch113

calib_packv5_040323_1717.root, FC#2, port C3

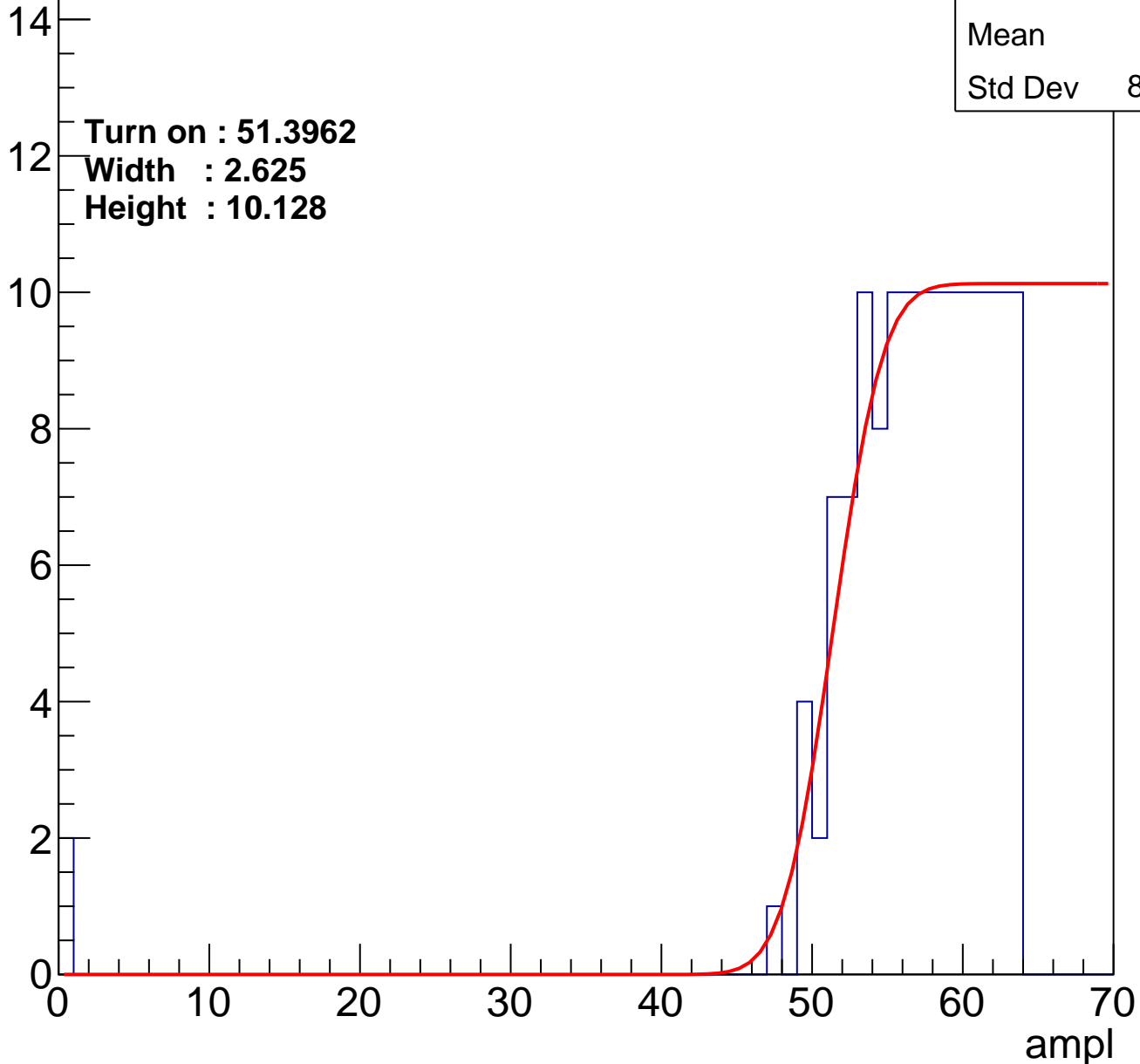
Entries	131
Mean	56
Std Dev	8.027

Turn on : 51.3962

Width : 2.625

Height : 10.128

Entry



B0L103S, U13-ch114

calib_packv5_040323_1717.root, FC#2, port C3

Entries	125
Mean	55.34
Std Dev	10.76

Turn on : 51.6118

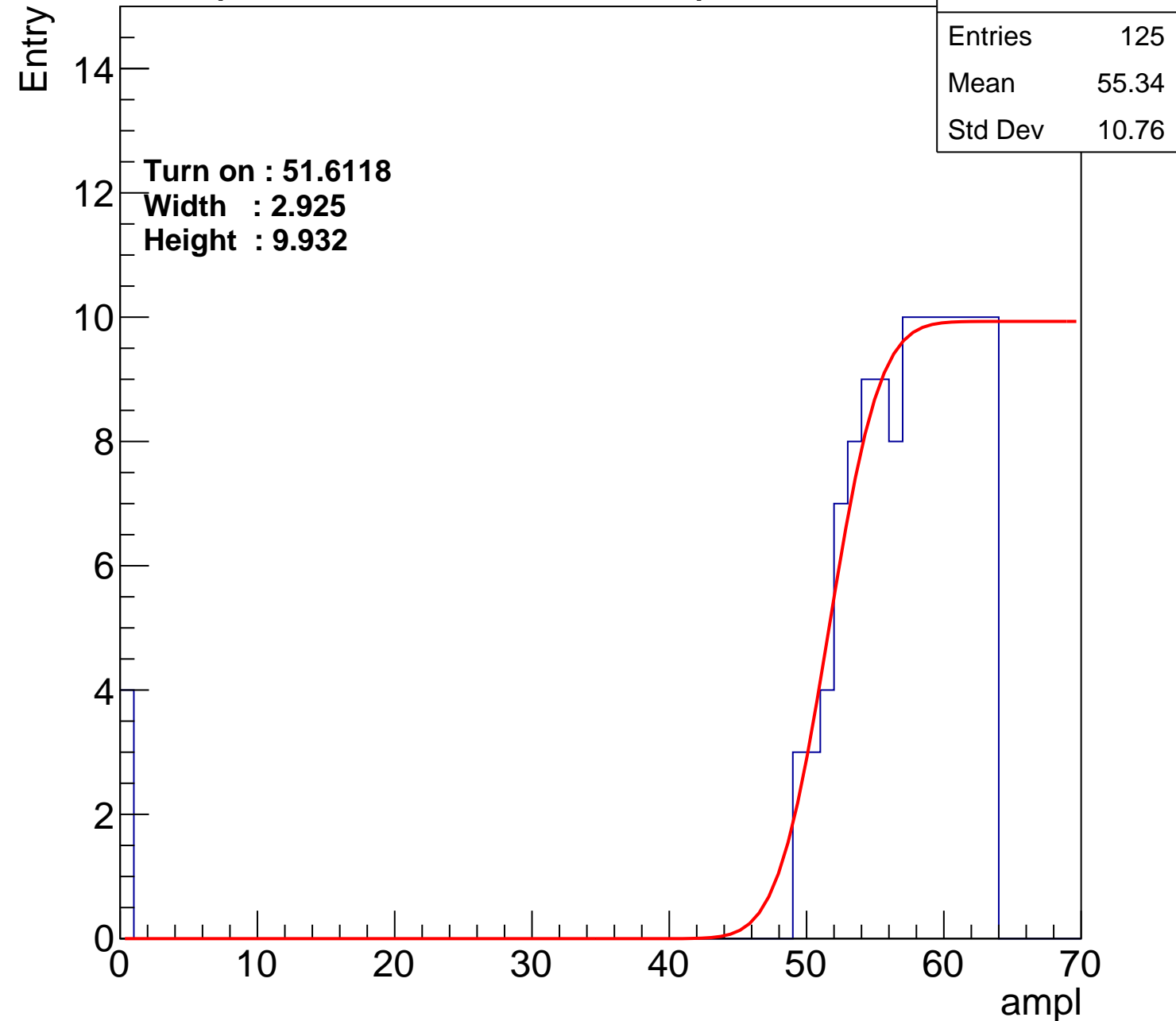
Width : 2.925

Height : 9.932

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch115

calib_packv5_040323_1717.root, FC#2, port C3

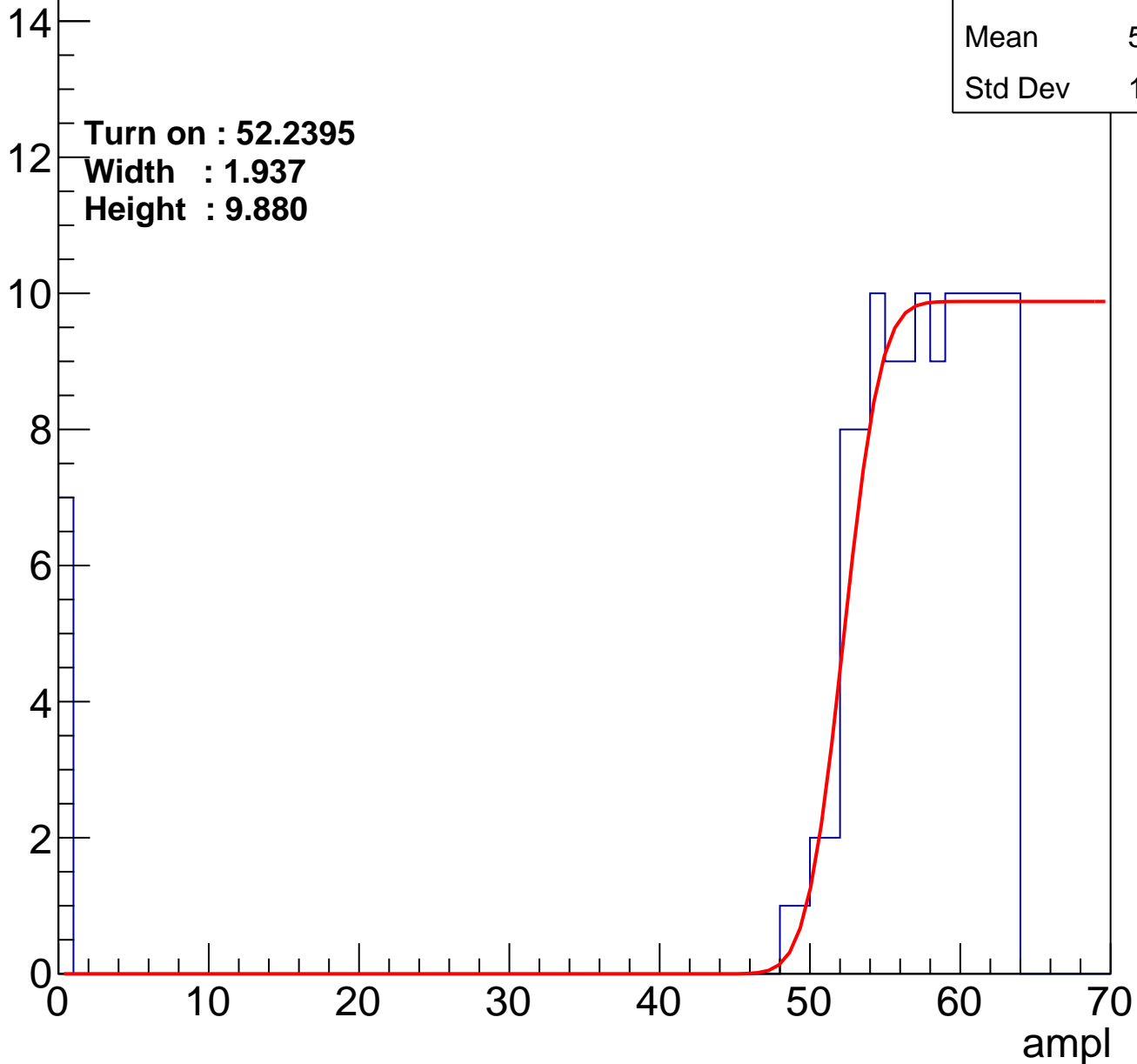
Entries	126
Mean	54.13
Std Dev	13.62

Turn on : 52.2395

Width : 1.937

Height : 9.880

Entry



B0L103S, U13-ch116

calib_packv5_040323_1717.root, FC#2, port C3

Entries	112
Mean	56.63
Std Dev	8.467

Turn on : 53.2867

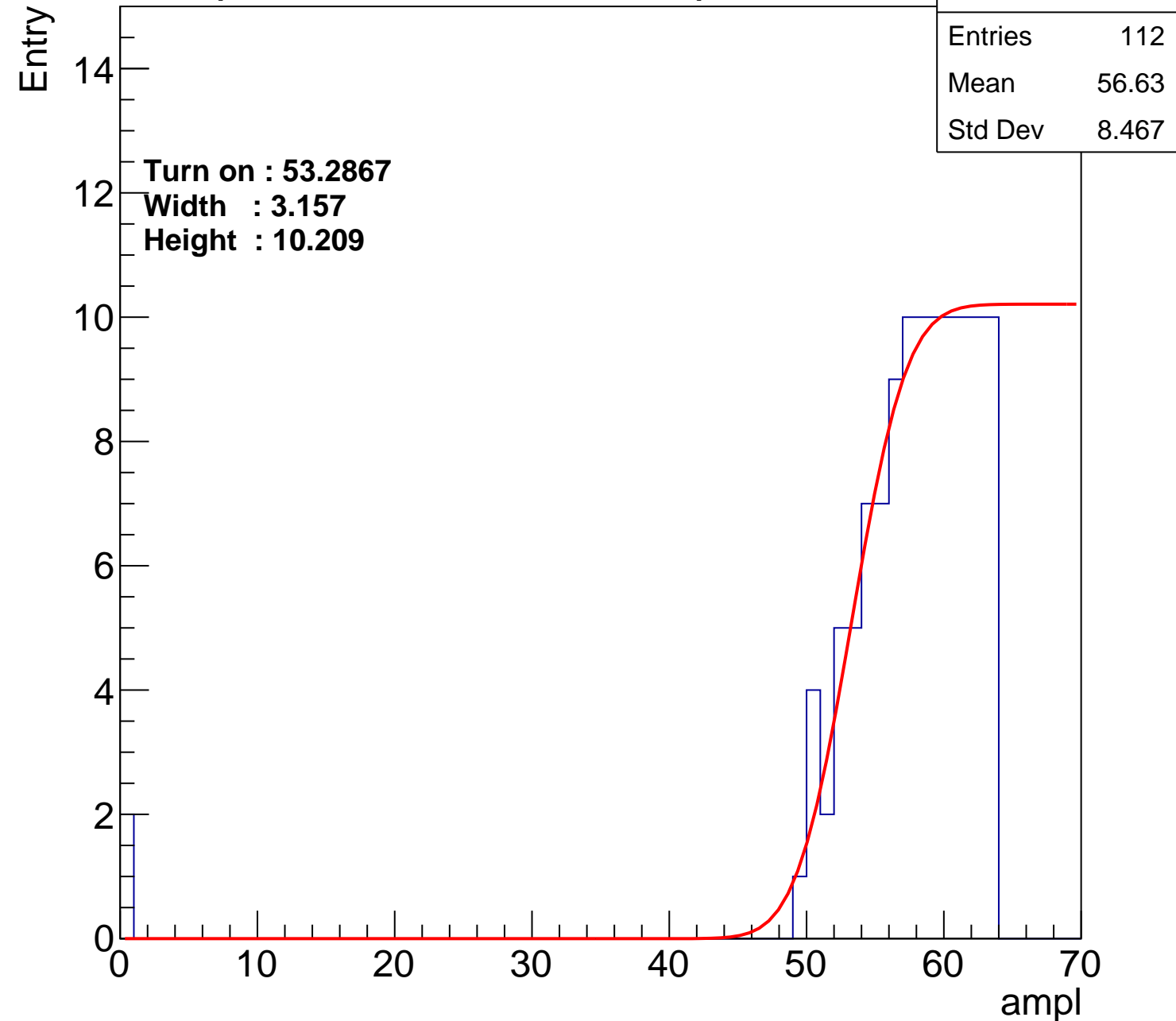
Width : 3.157

Height : 10.209

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch117

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	55.68
Std Dev	8.13

Turn on : 50.7830

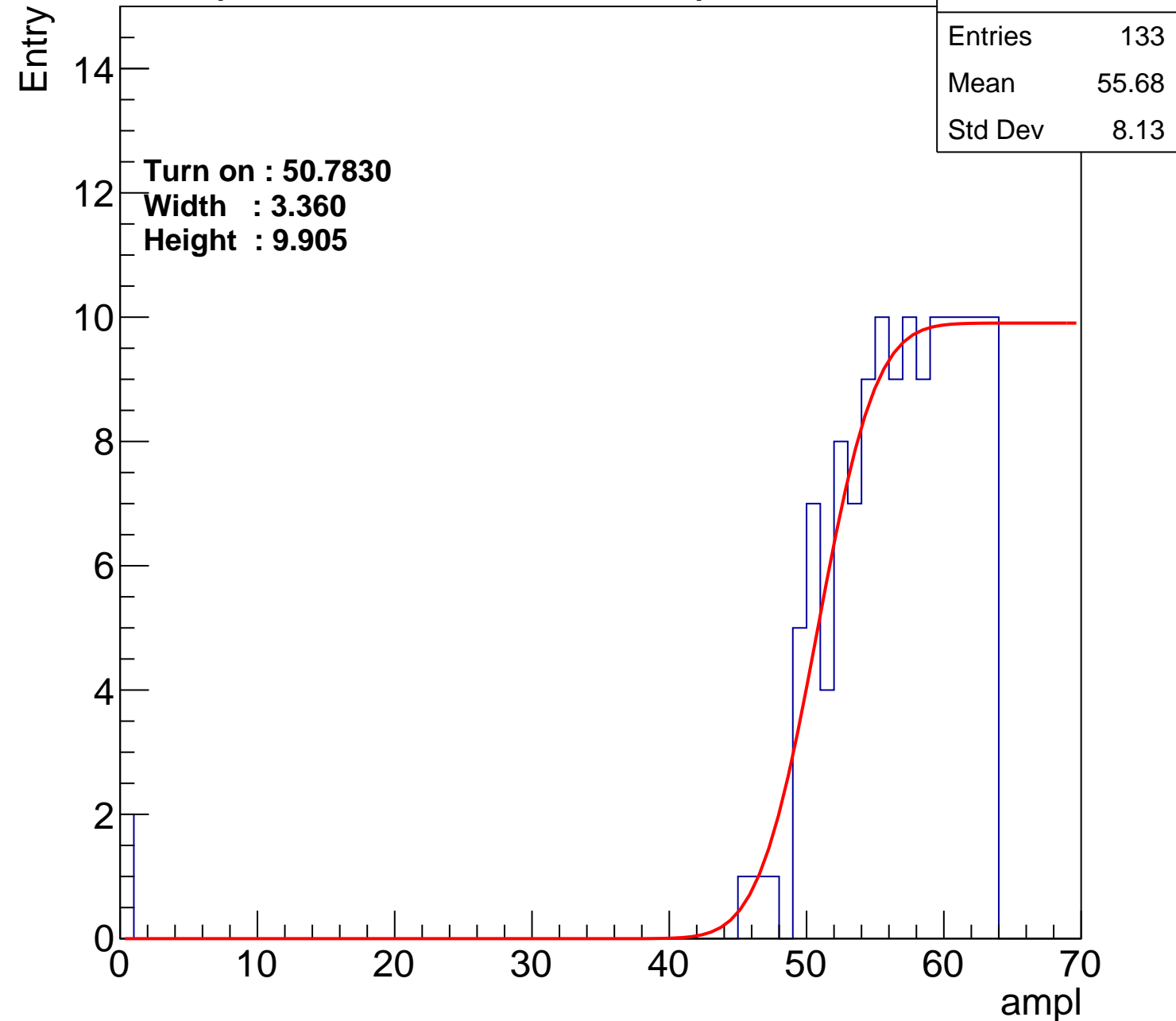
Width : 3.360

Height : 9.905

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch118

calib_packv5_040323_1717.root, FC#2, port C3

Entries	141
Mean	55.2
Std Dev	9.176

Turn on : 50.0574

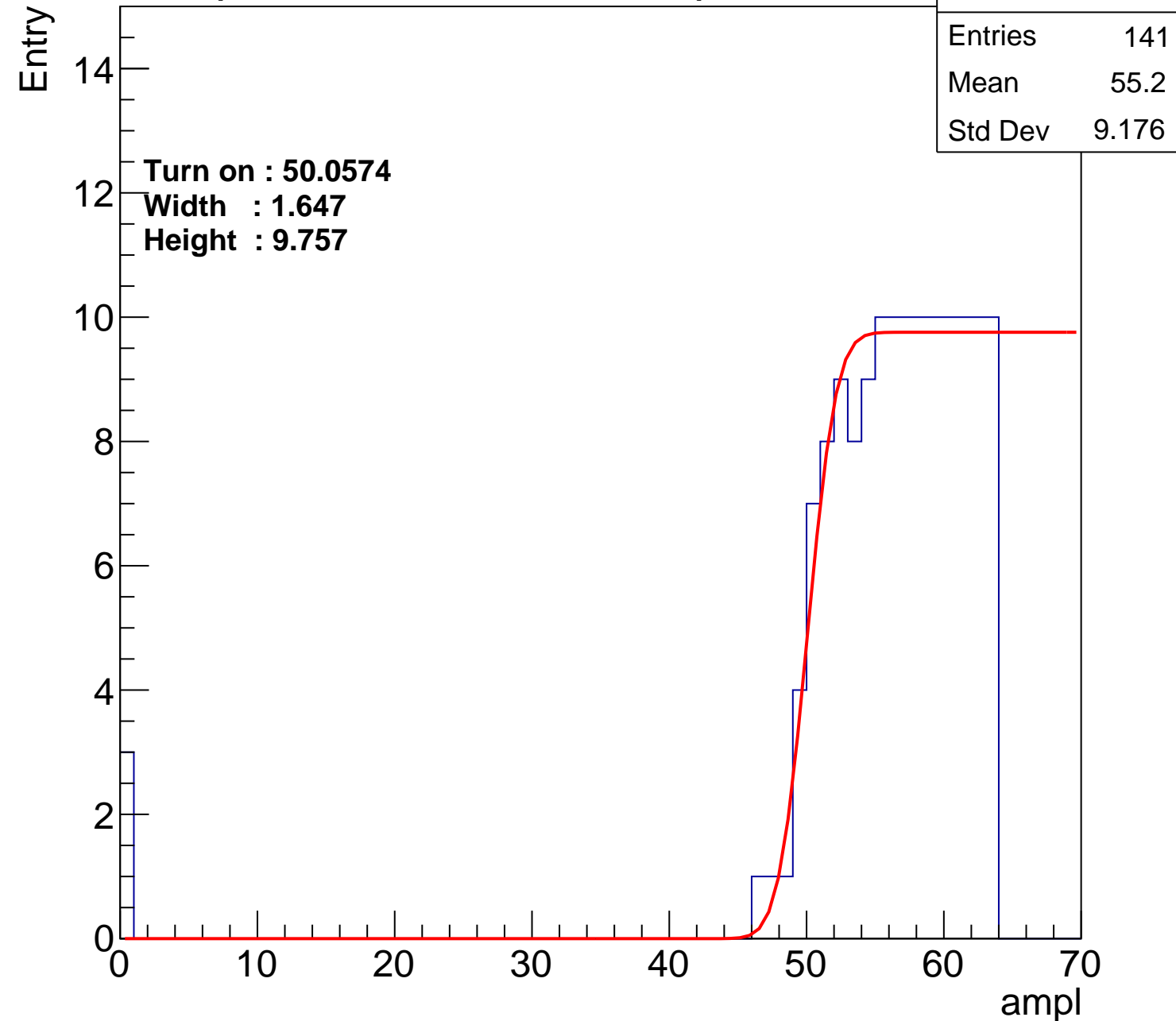
Width : 1.647

Height : 9.757

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch119

calib_packv5_040323_1717.root, FC#2, port C3

Entries	127
Mean	54.87
Std Dev	11.77

Turn on : 51.8051

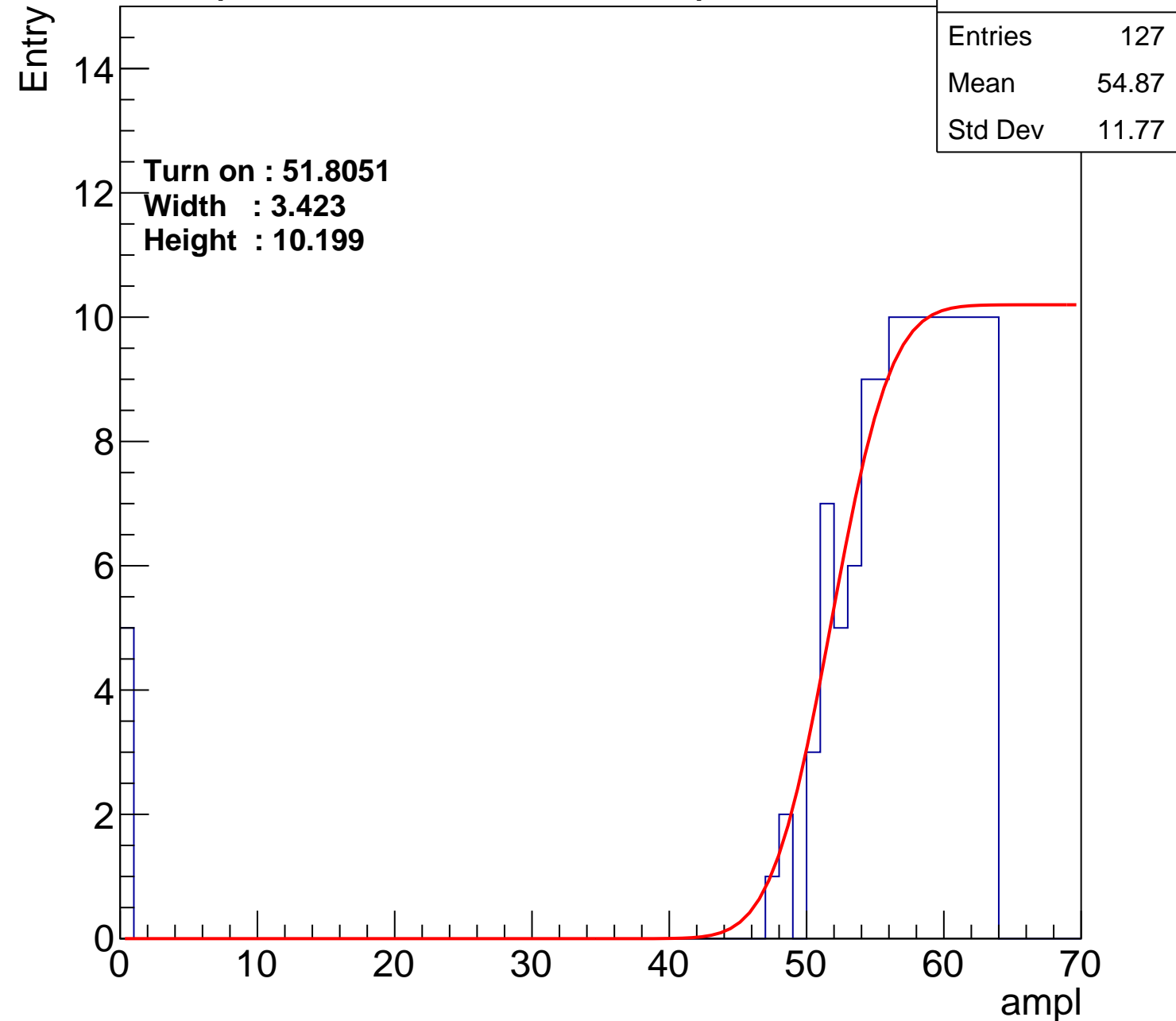
Width : 3.423

Height : 10.199

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch120

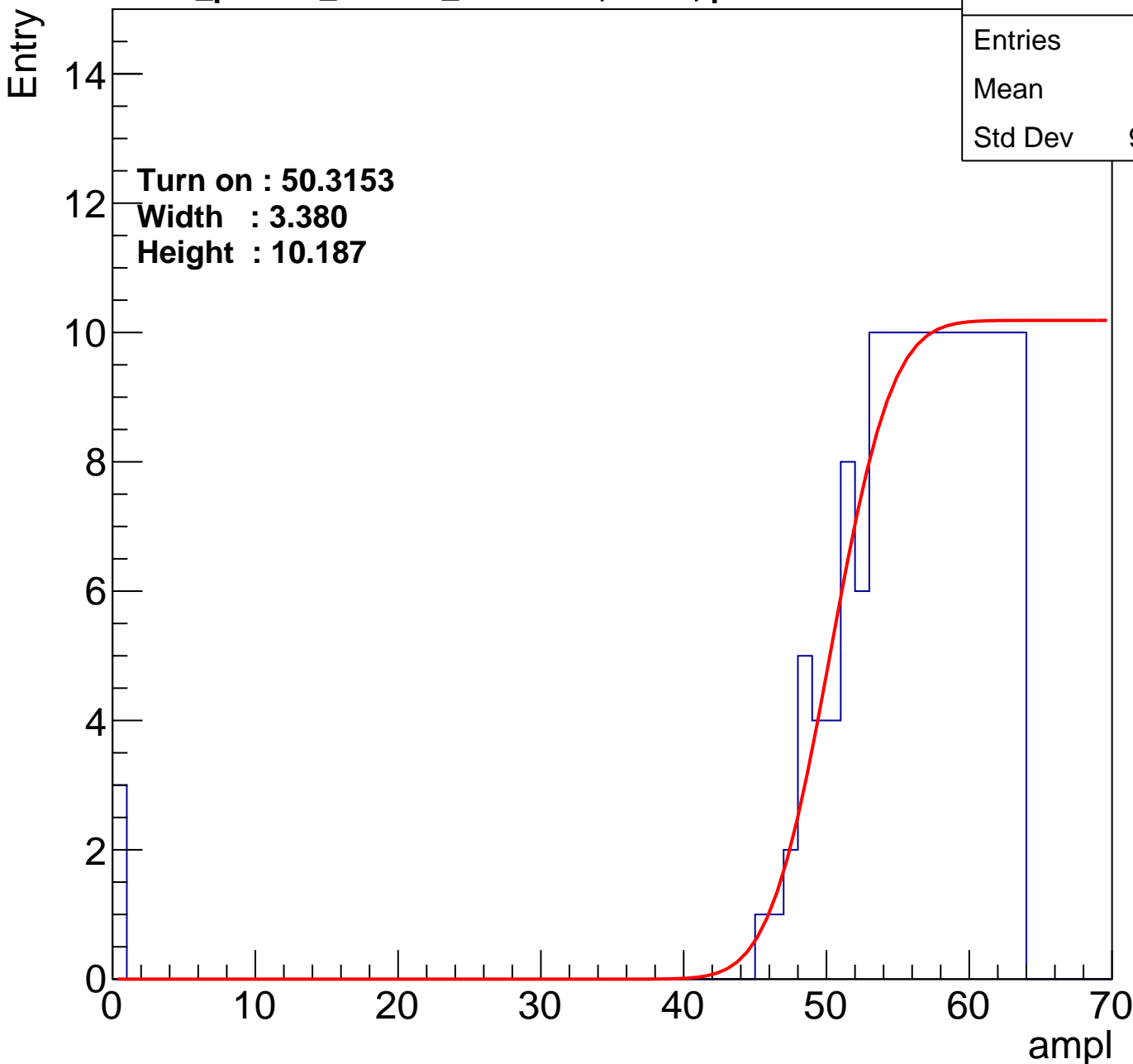
calib_packv5_040323_1717.root, FC#2, port C3

Entries	144
Mean	55.01
Std Dev	9.184

Turn on : 50.3153

Width : 3.380

Height : 10.187



B0L103S, U13-ch121

calib_packv5_040323_1717.root, FC#2, port C3

Entries	132
Mean	54.21
Std Dev	12.49

Turn on : 51.2840

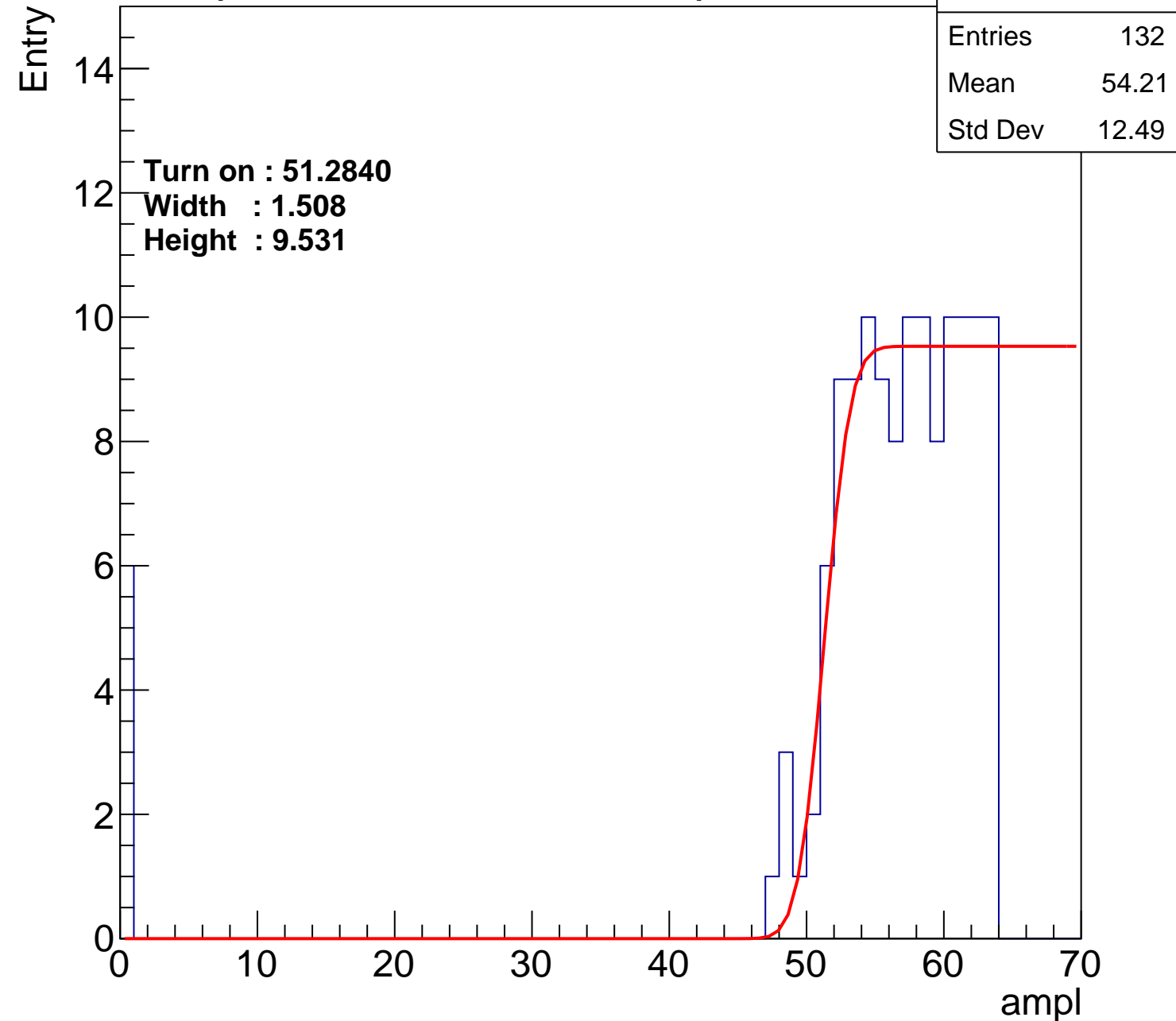
Width : 1.508

Height : 9.531

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch122

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	54.95
Std Dev	10.71

Turn on : 51.9323

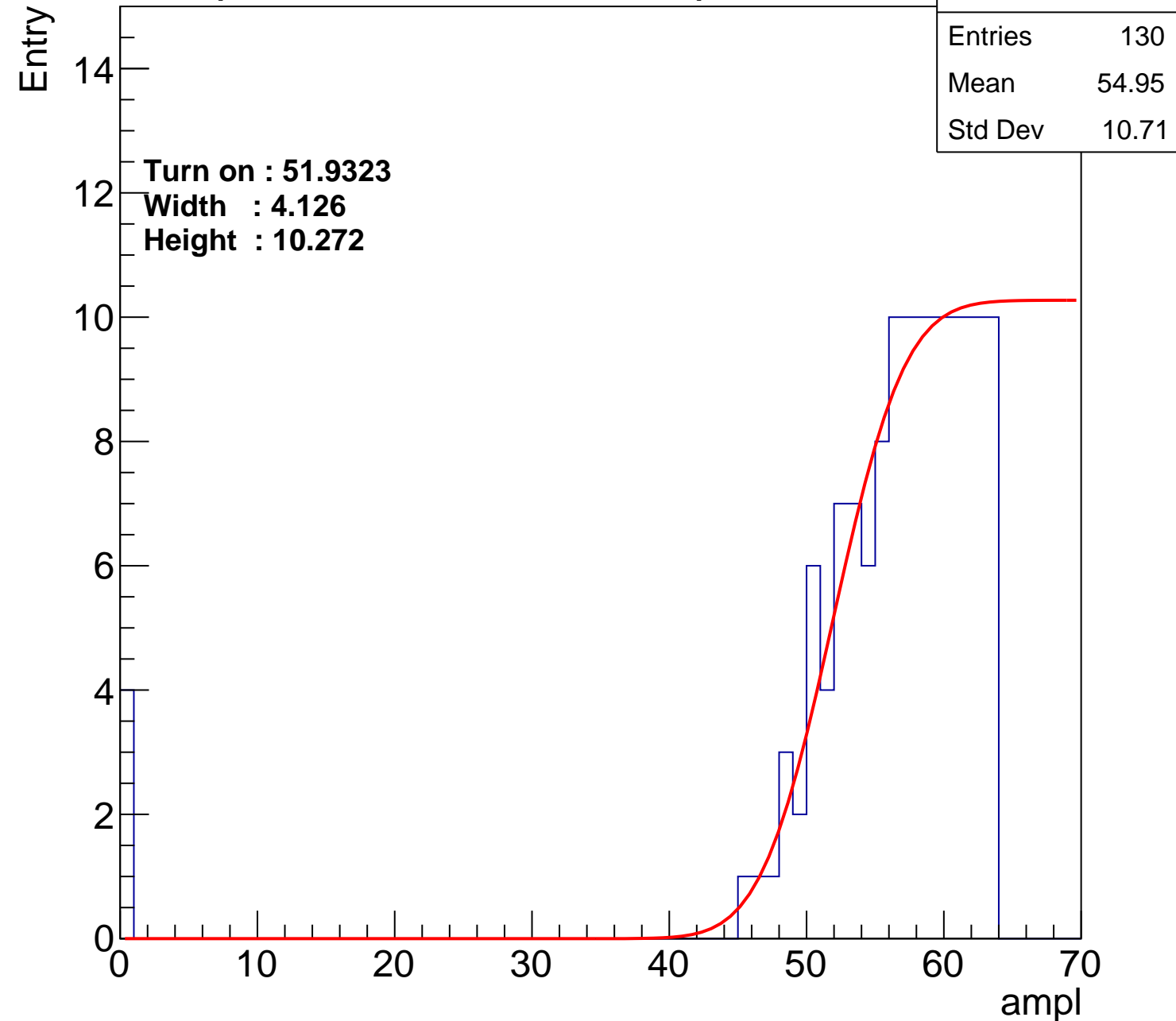
Width : 4.126

Height : 10.272

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch123

calib_packv5_040323_1717.root, FC#2, port C3

Entries	90
Mean	57.33
Std Dev	9.157

Turn on : 55.2136

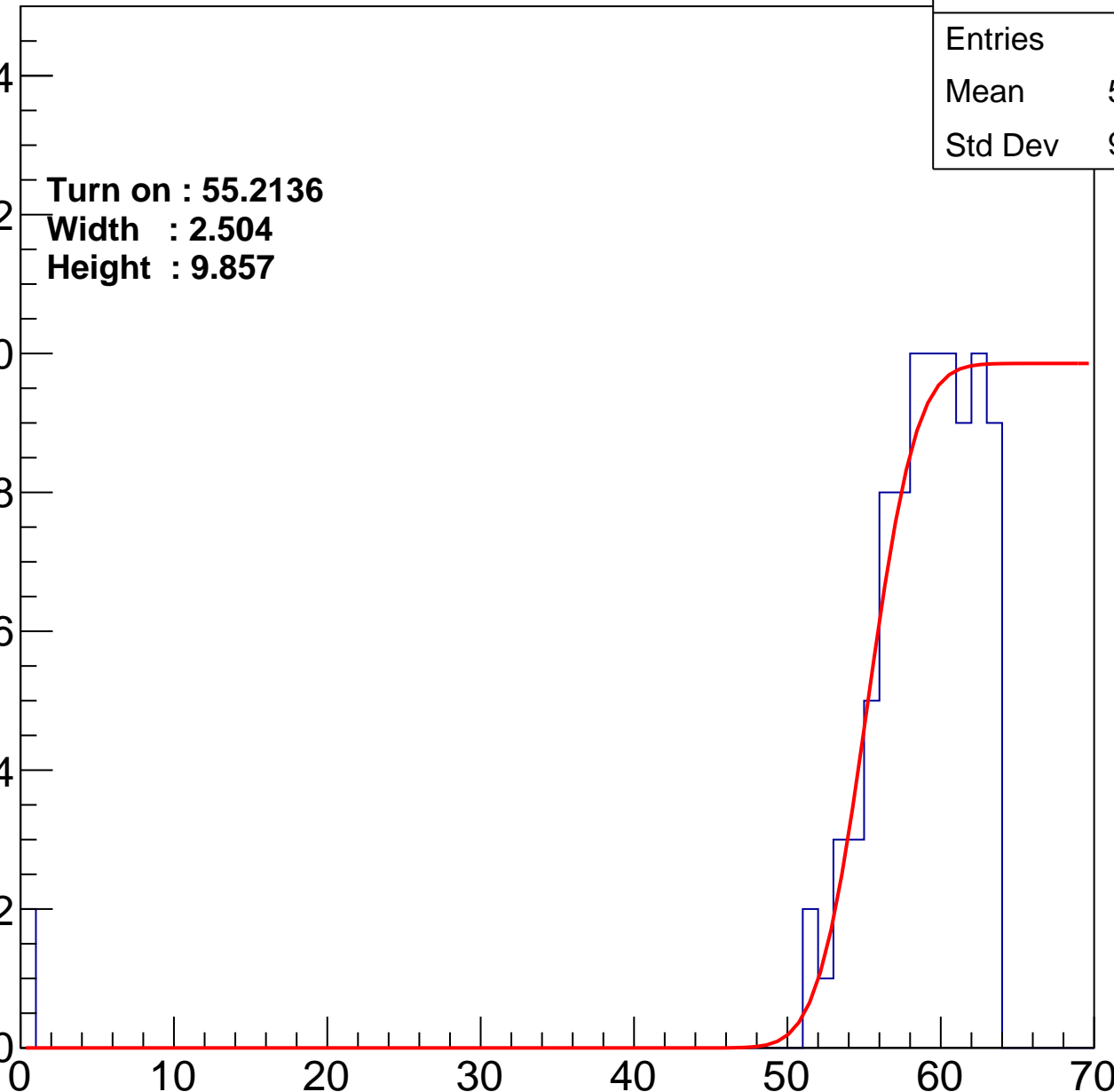
Width : 2.504

Height : 9.857

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch124

calib_packv5_040323_1717.root, FC#2, port C3

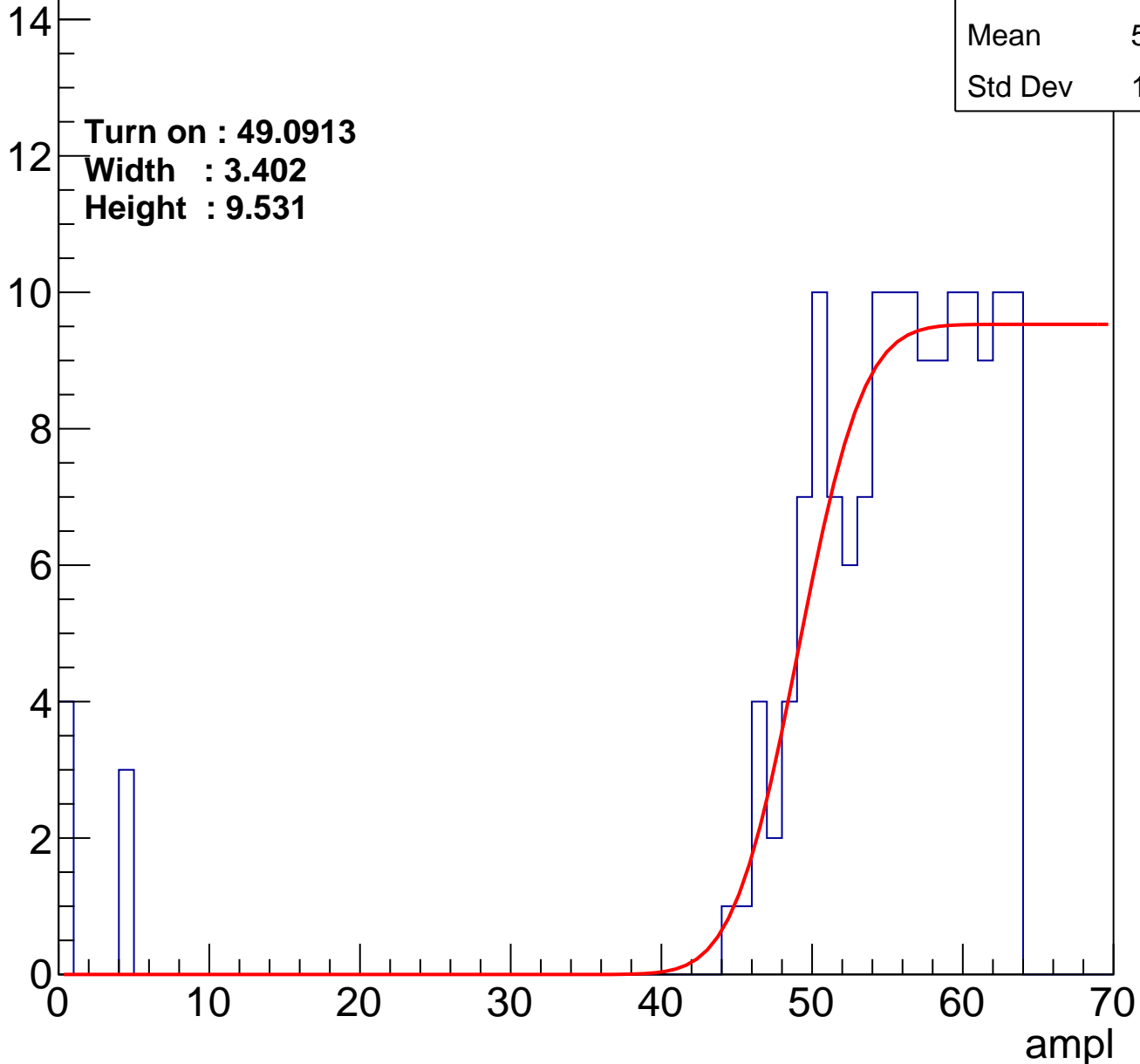
Entries	153
Mean	53.12
Std Dev	12.24

Turn on : 49.0913

Width : 3.402

Height : 9.531

Entry



B0L103S, U13-ch125

calib_packv5_040323_1717.root, FC#2, port C3

Entries	130
Mean	56.23
Std Dev	6.558

Turn on : 50.9916

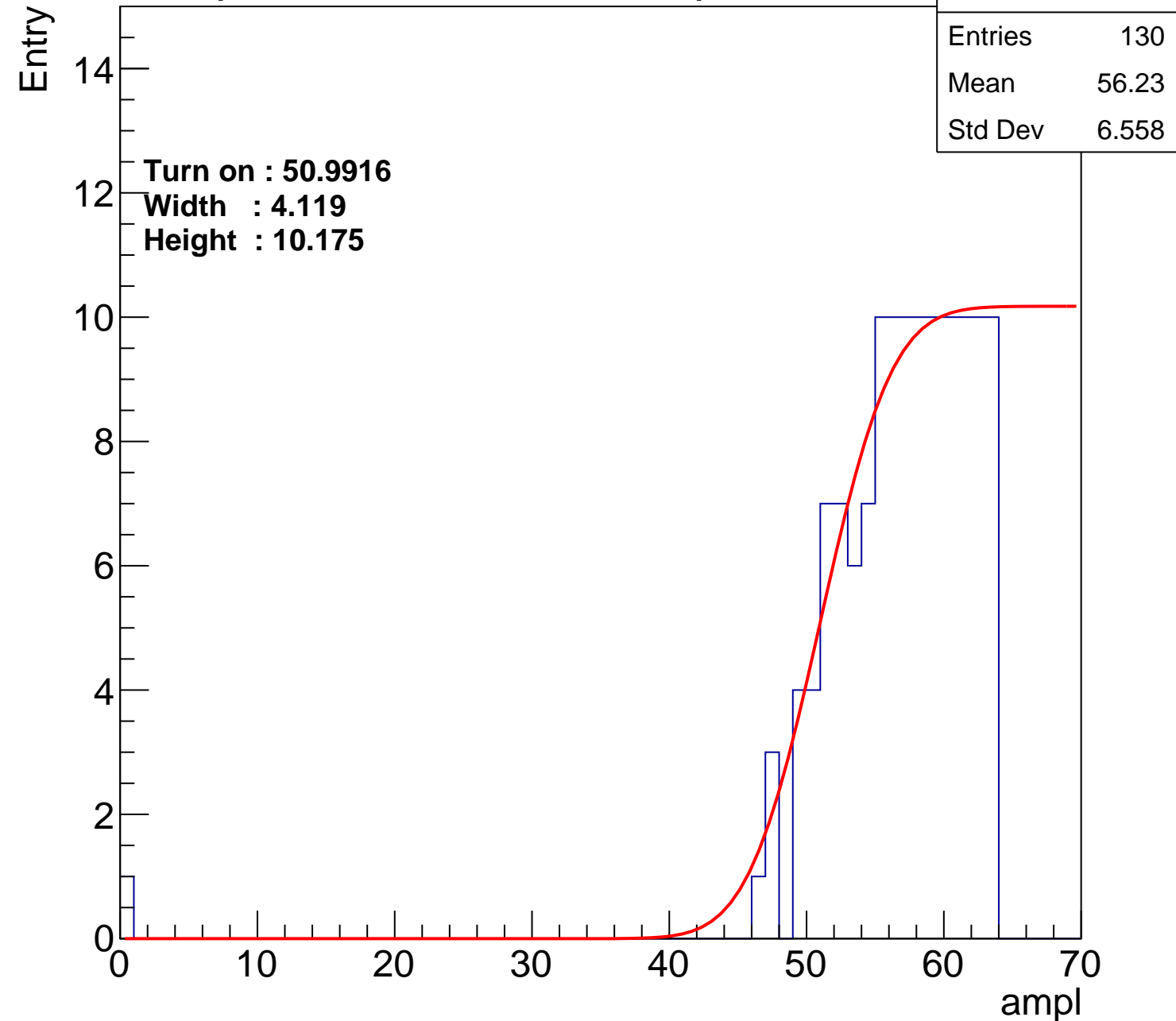
Width : 4.119

Height : 10.175

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch126

calib_packv5_040323_1717.root, FC#2, port C3

Entries	133
Mean	54.7
Std Dev	11.53

Turn on : 51.5373

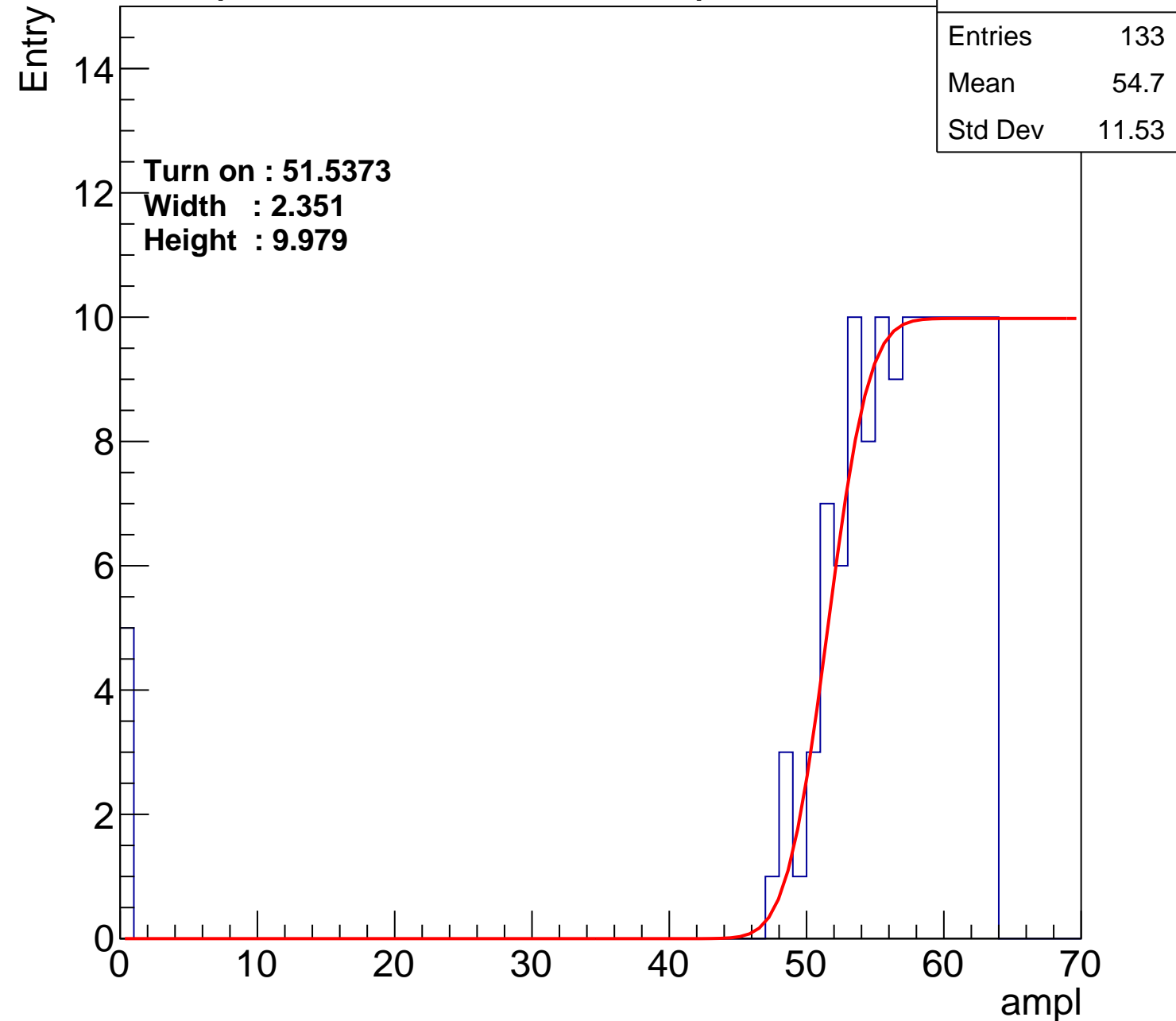
Width : 2.351

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.62
Std Dev	8.601

Turn on : 53.3490

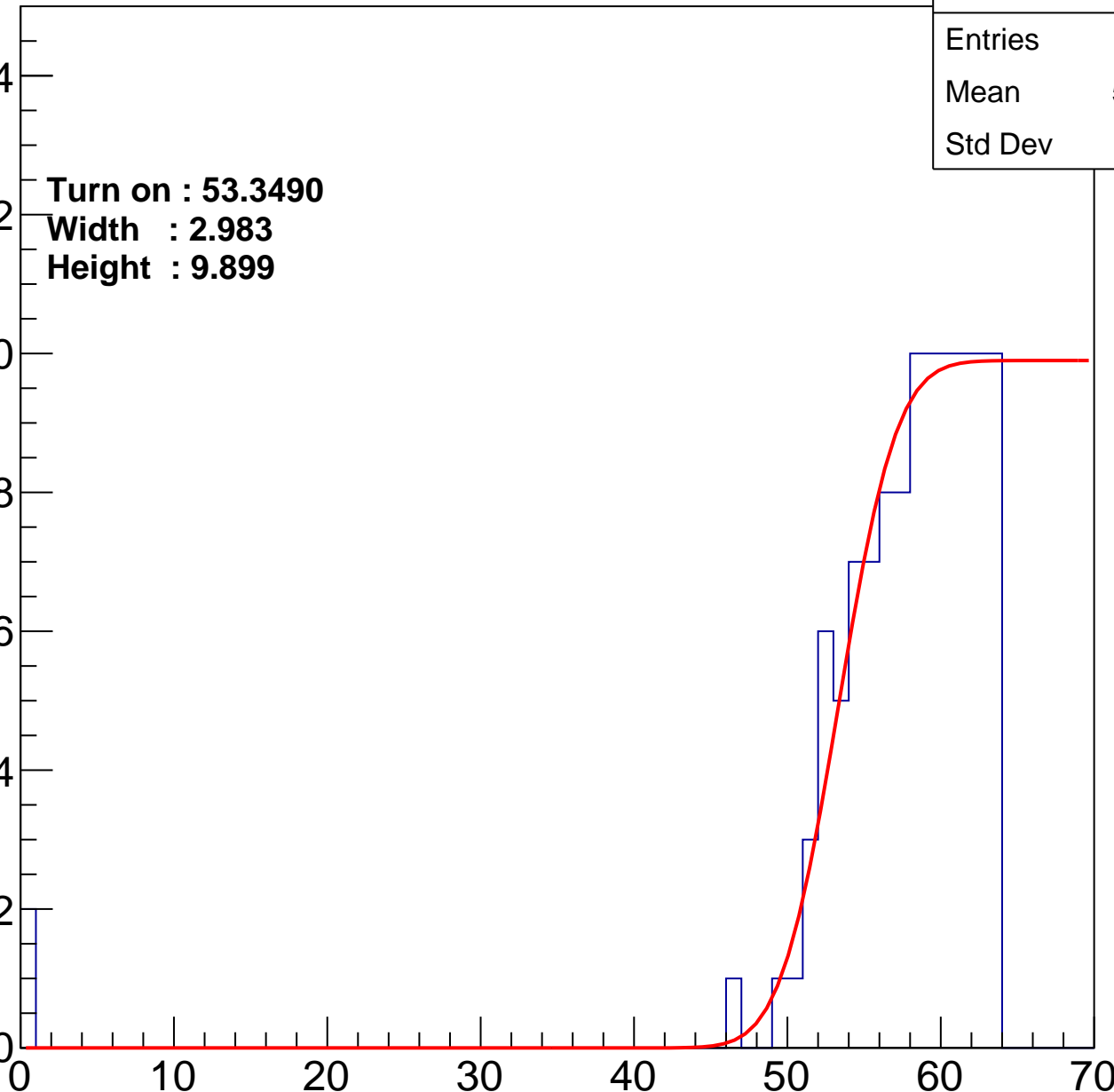
Width : 2.983

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B0L103S, U13-ch127

calib_packv5_040323_1717.root, FC#2, port C3

Entries	109
Mean	56.62
Std Dev	8.601

Turn on : 53.3490

Width : 2.983

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl

