



# B0L000S, U18-ch0, adc0

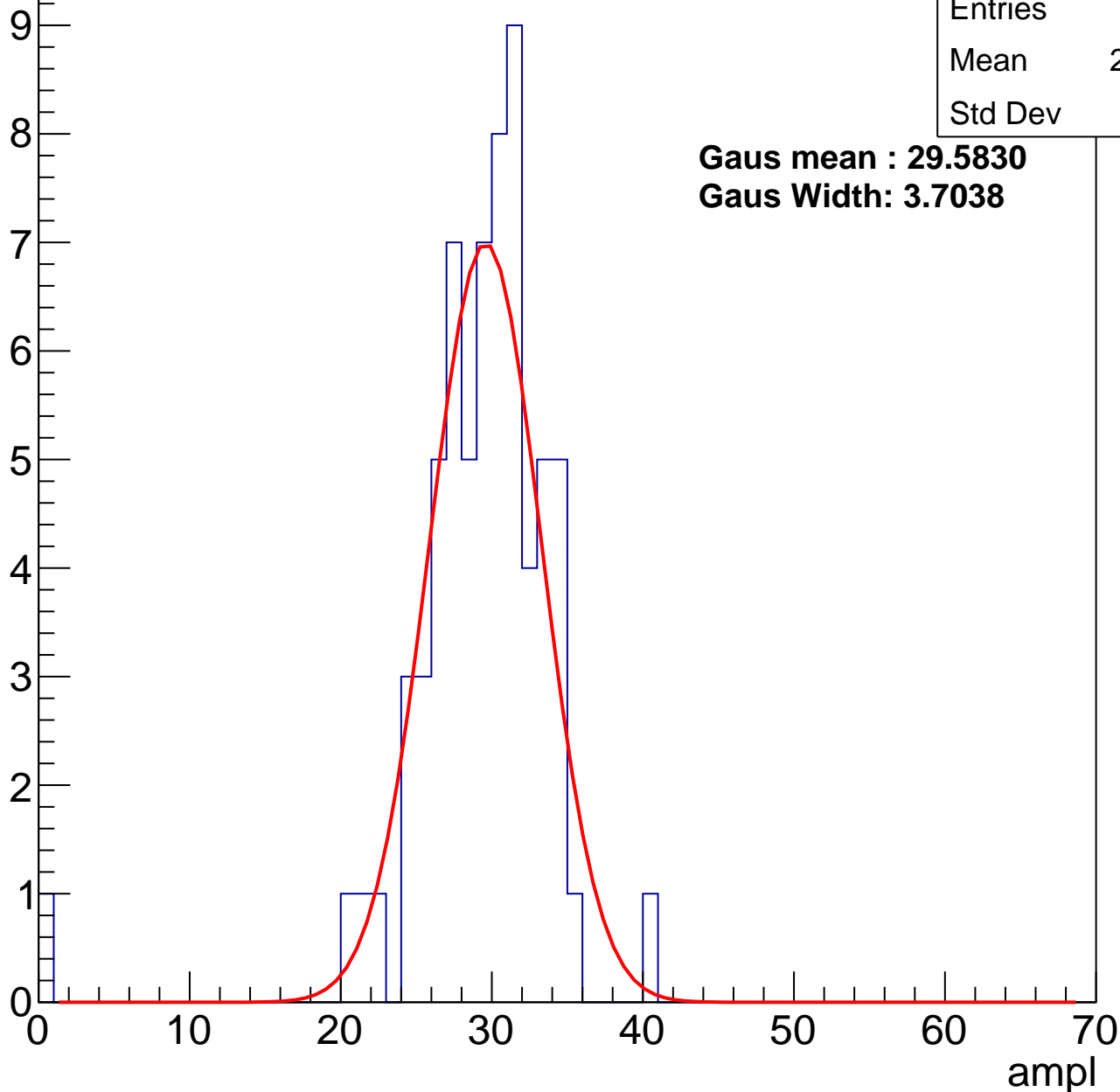
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.79
Std Dev	5

**Gaus mean : 29.5830**

**Gaus Width: 3.7038**



# B0L000S, U18-ch0, adc1

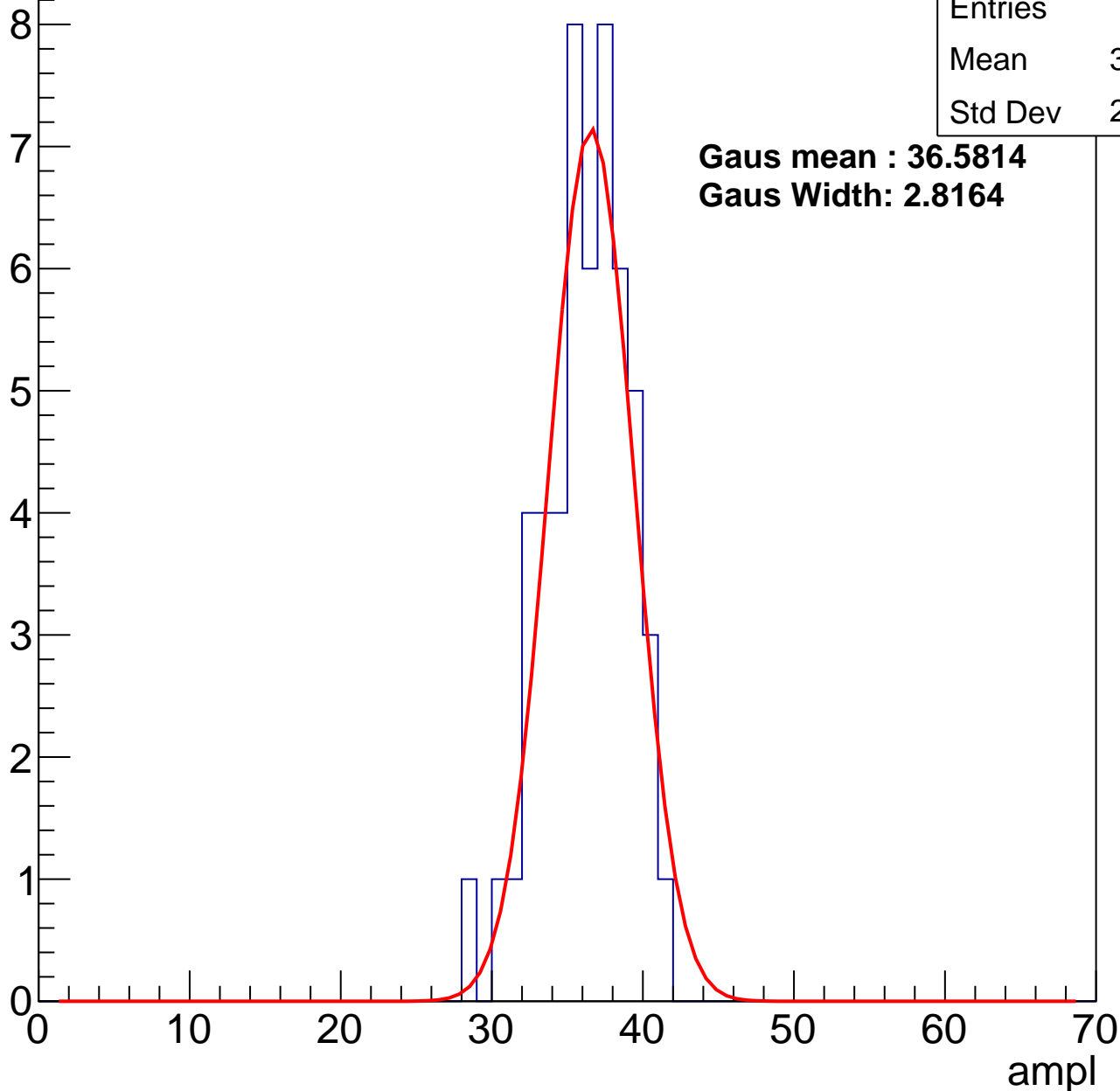
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	35.79
Std Dev	2.762

**Gaus mean : 36.5814**

**Gaus Width: 2.8164**



# B0L000S, U18-ch0, adc2

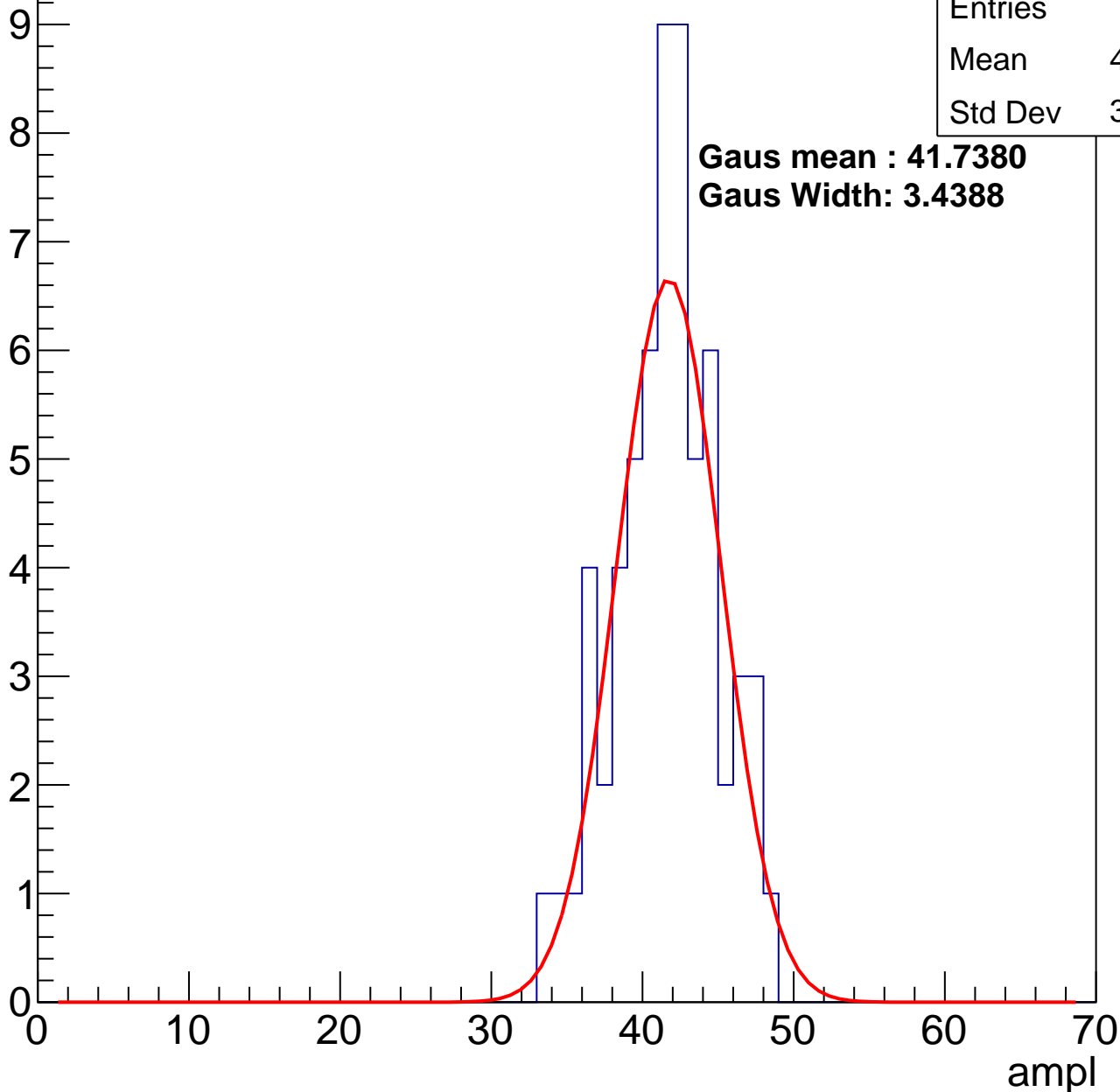
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	41.13
Std Dev	3.334

**Gaus mean : 41.7380**

**Gaus Width: 3.4388**

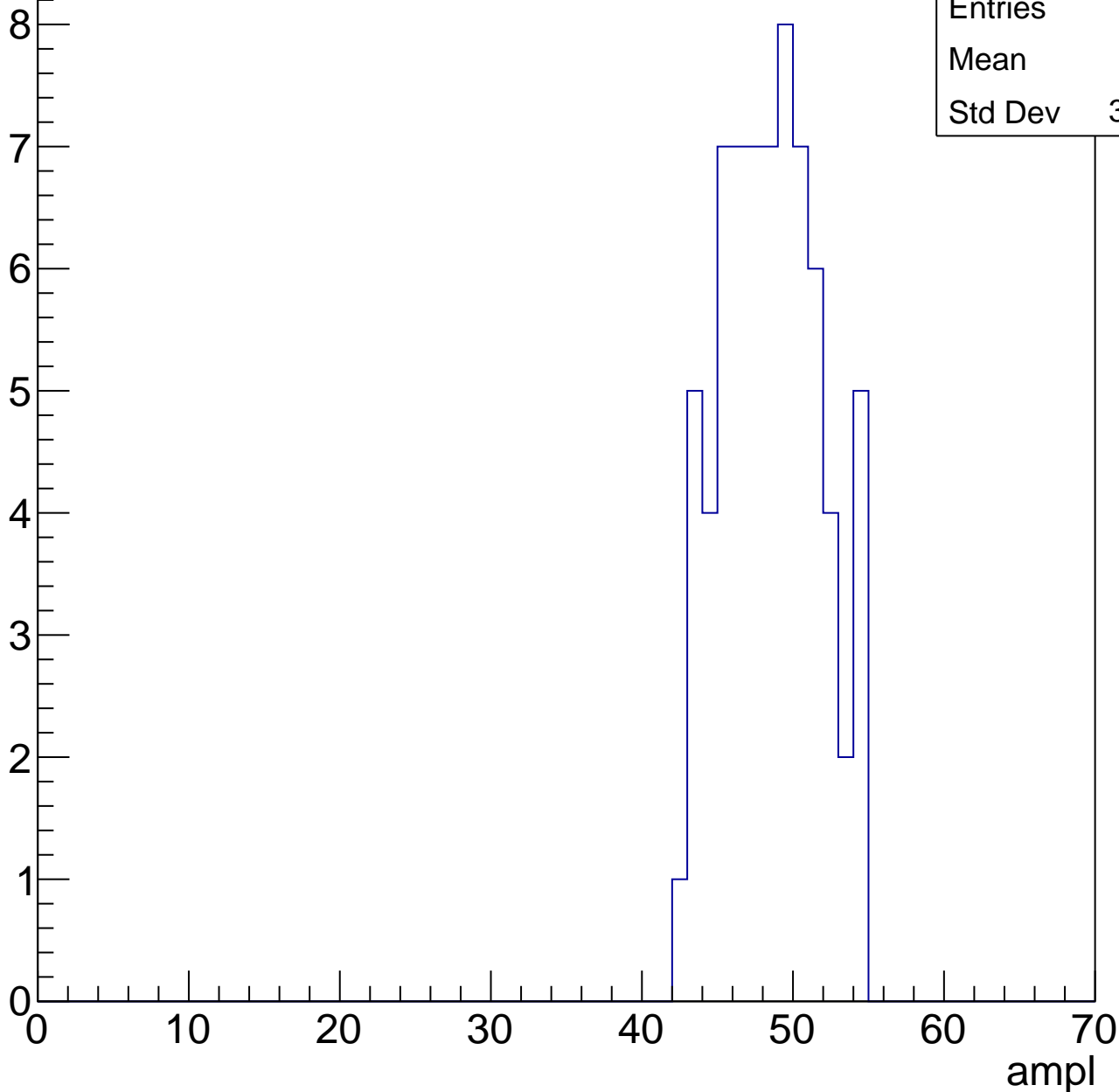


# B0L000S, U18-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.1
Std Dev	3.176

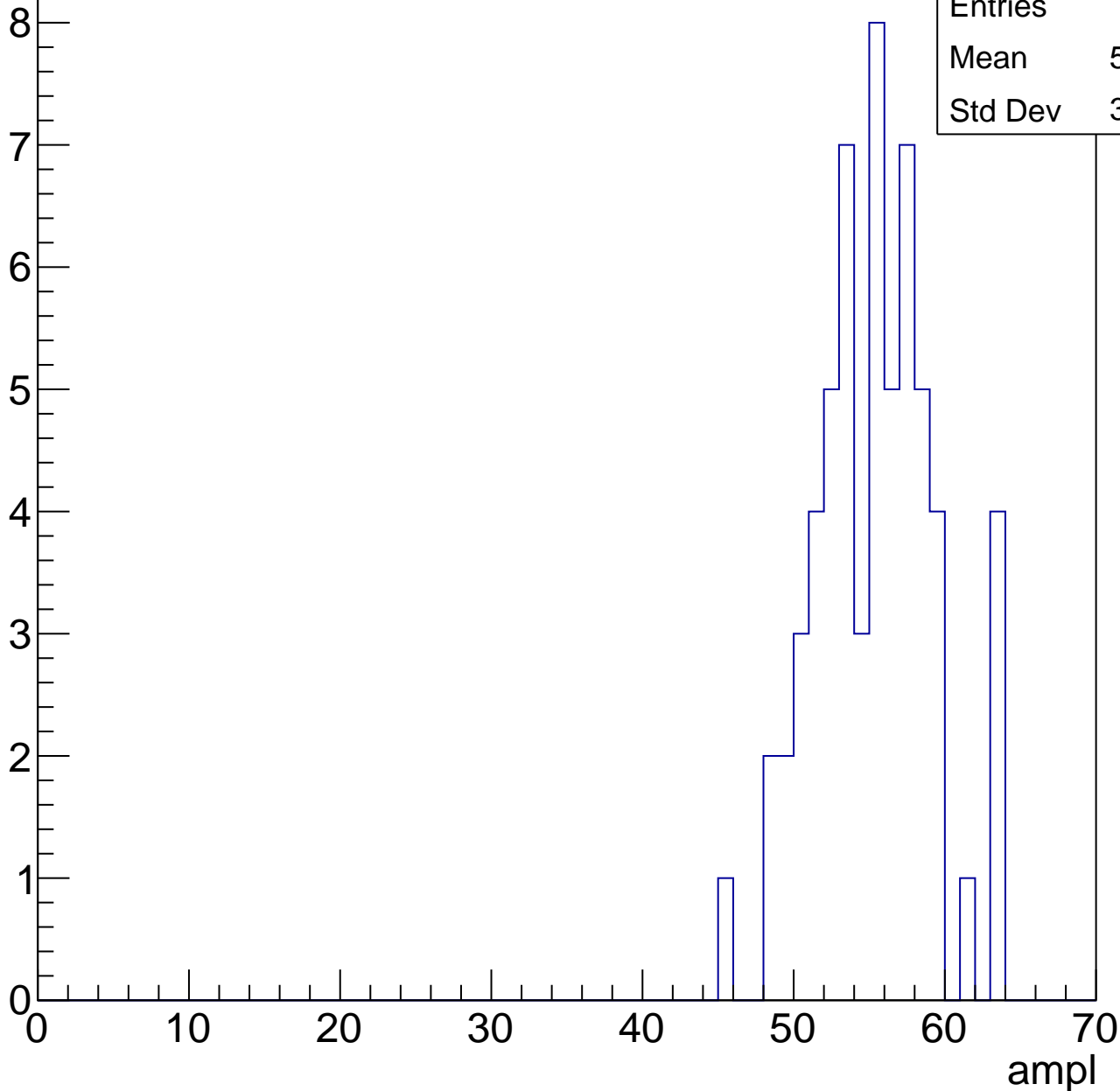


# B0L000S, U18-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	54.82
Std Dev	3.877

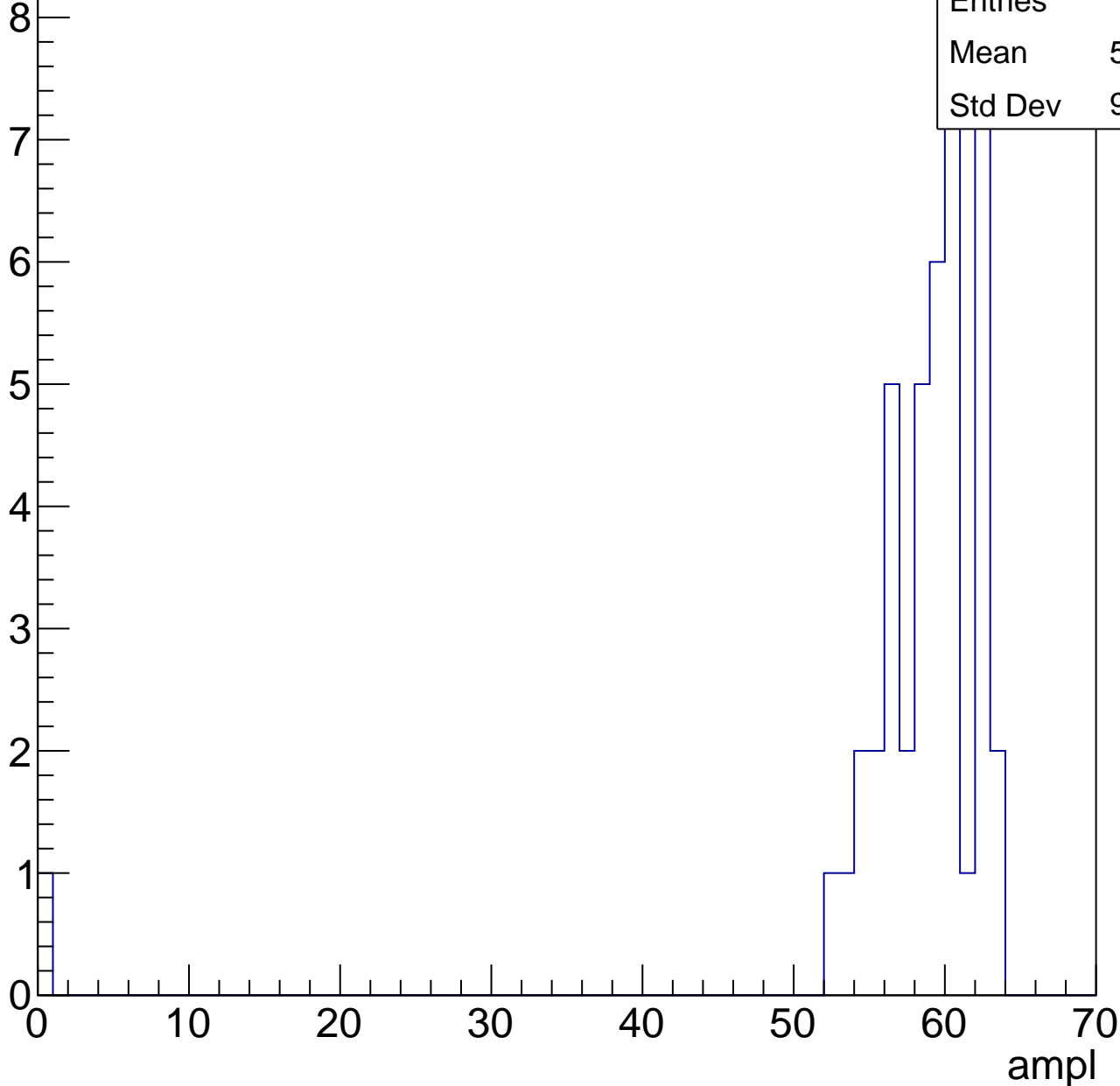


# B0L000S, U18-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	57.36
Std Dev	9.175

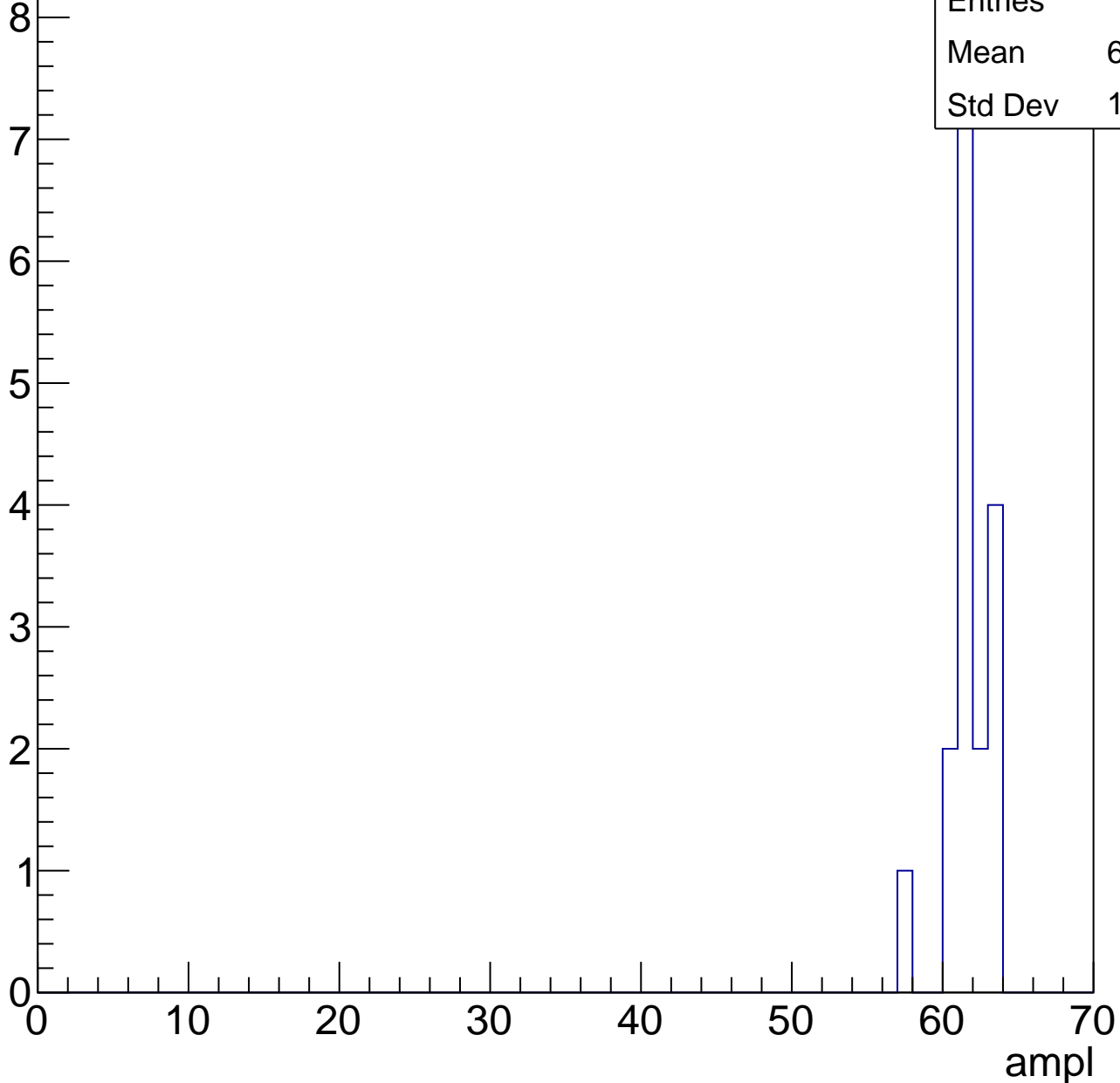


# B0L000S, U18-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.24
Std Dev	1.436





# B0L000S, U18-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch1, adc0

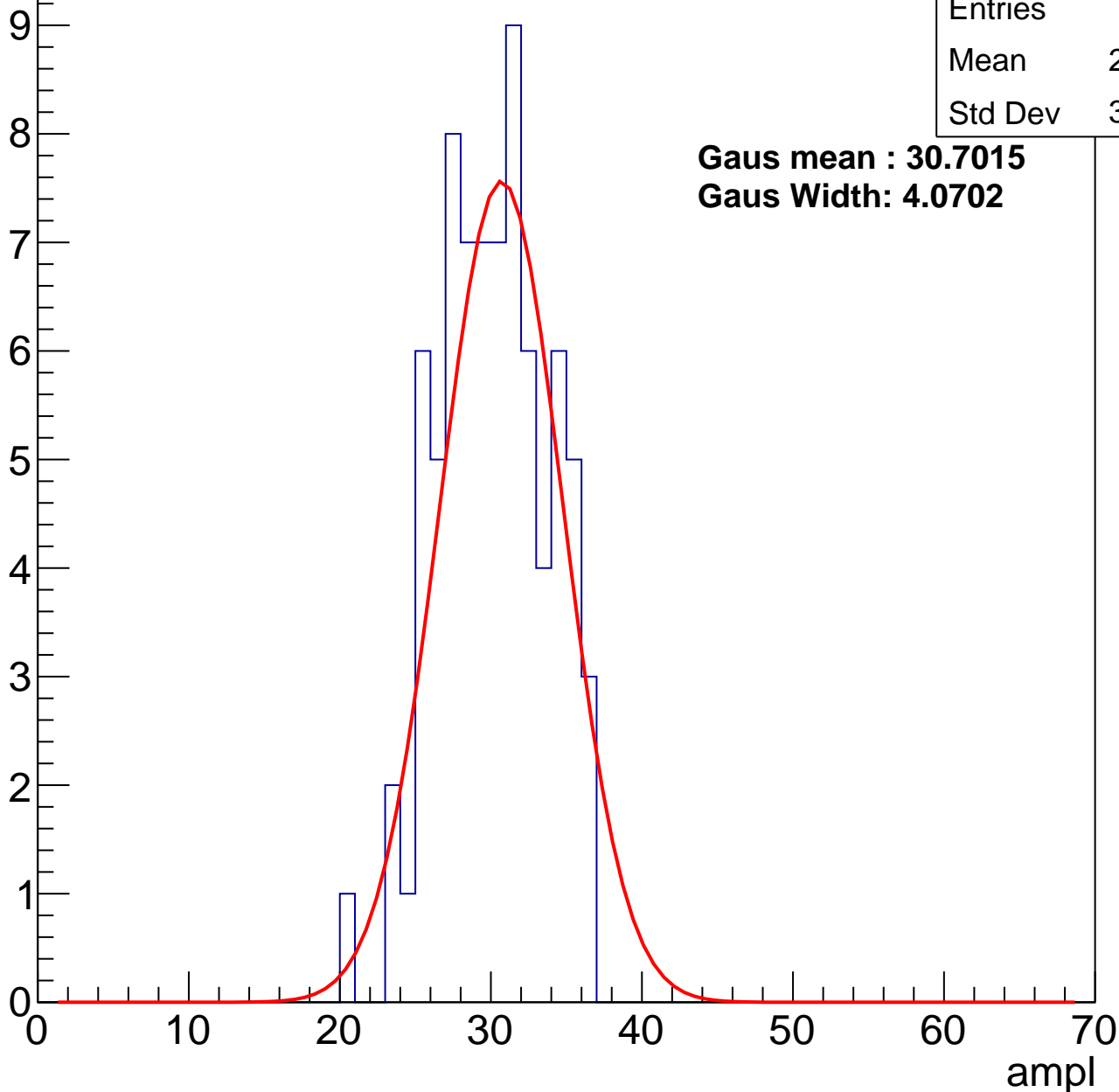
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	29.68
Std Dev	3.533

**Gaus mean : 30.7015**

**Gaus Width: 4.0702**



# B0L000S, U18-ch1, adc1

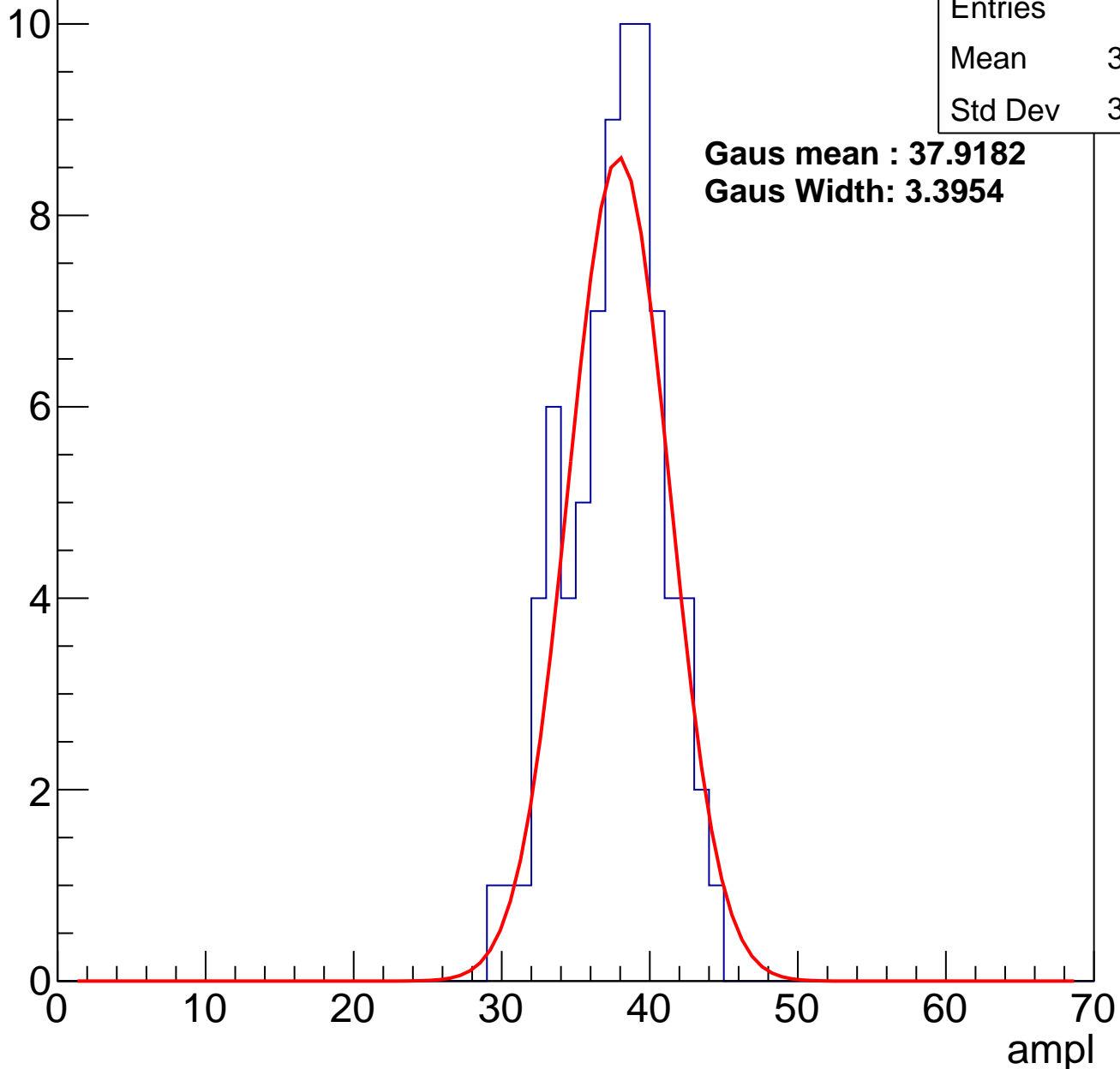
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	37.16
Std Dev	3.253

**Gaus mean : 37.9182**

**Gaus Width: 3.3954**

Entry



# B0L000S, U18-ch1, adc2

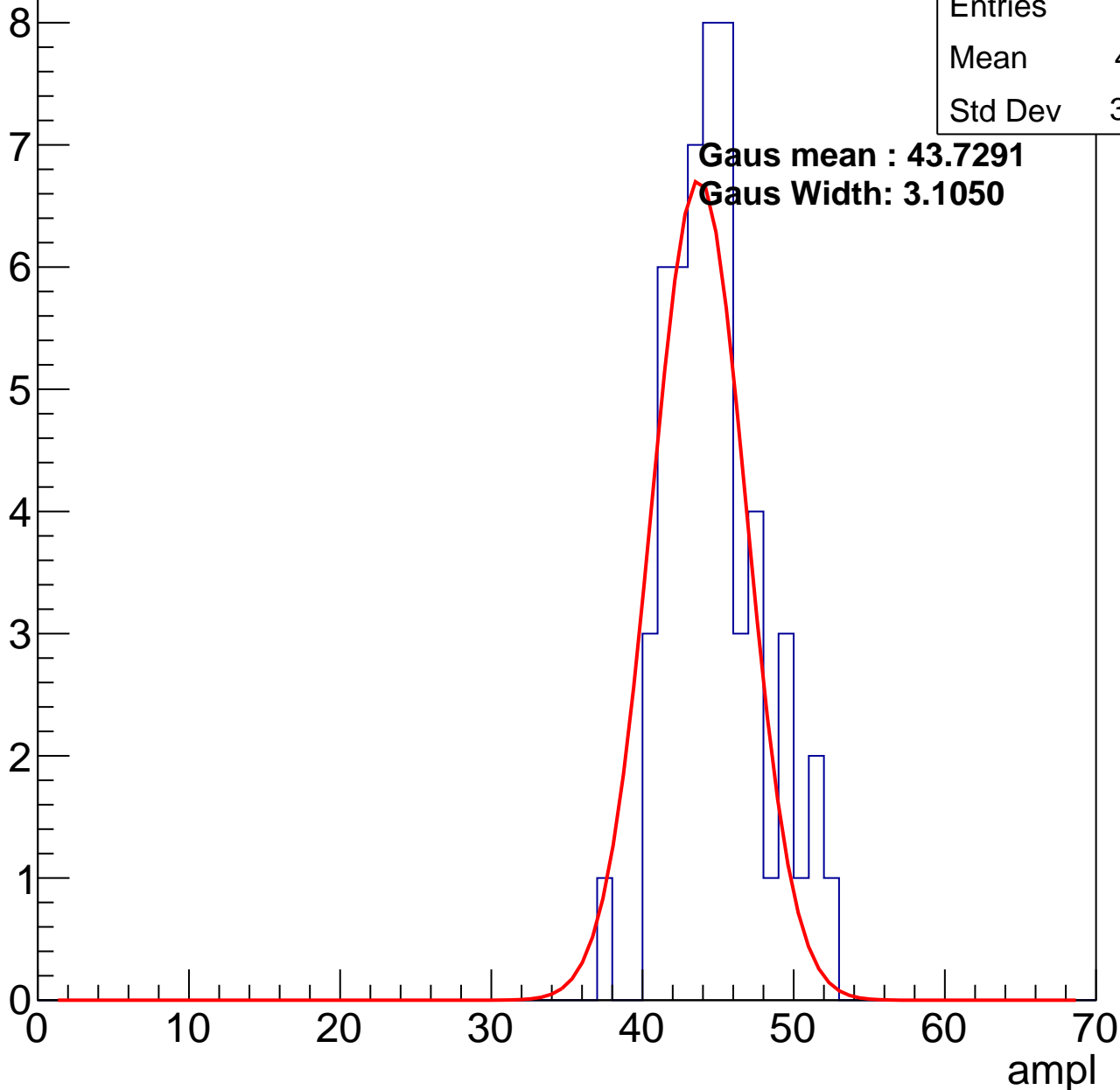
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.31
Std Dev	3.108

**Gaus mean : 43.7291**

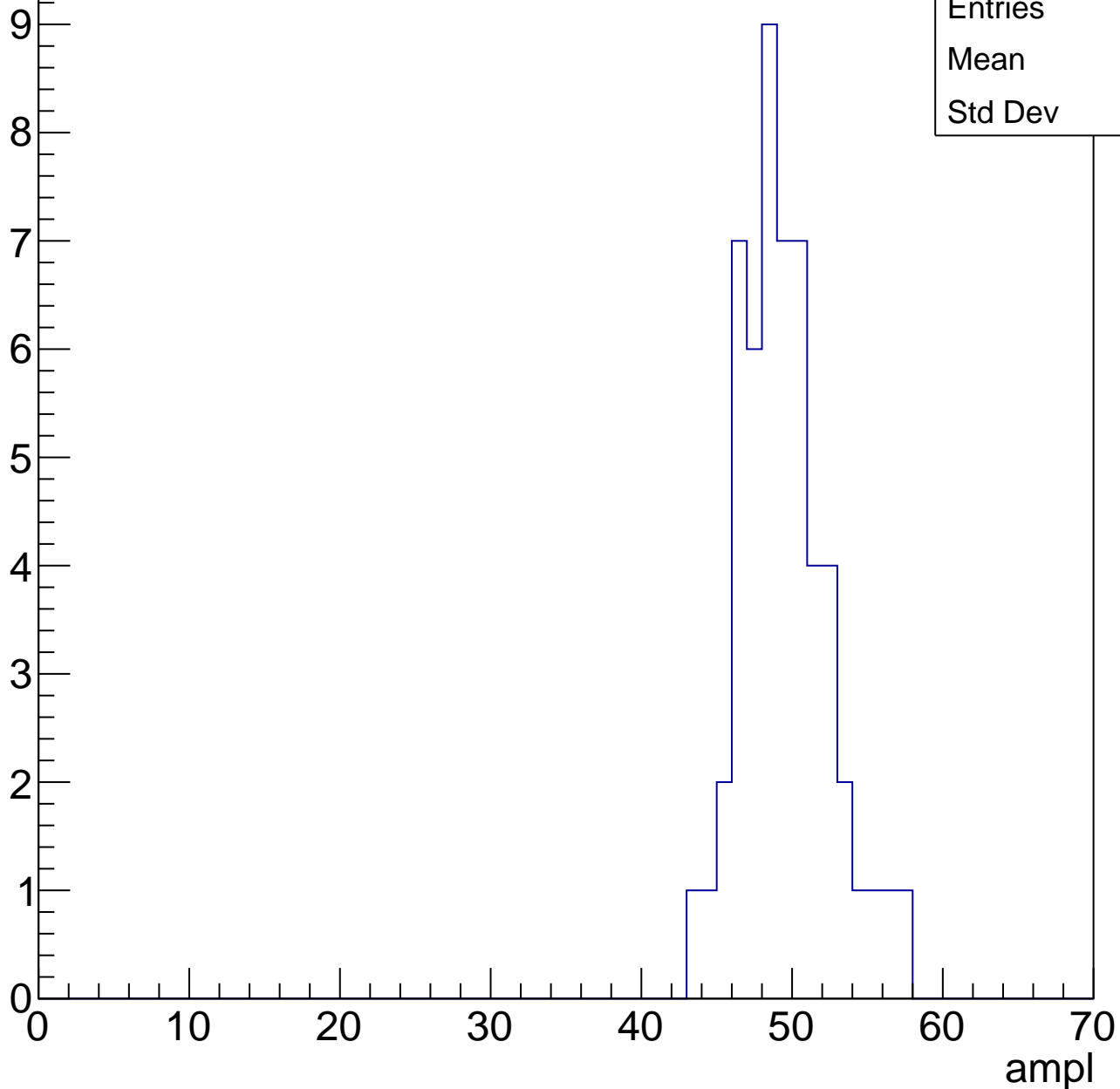
**Gaus Width: 3.1050**



# B0L000S, U18-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



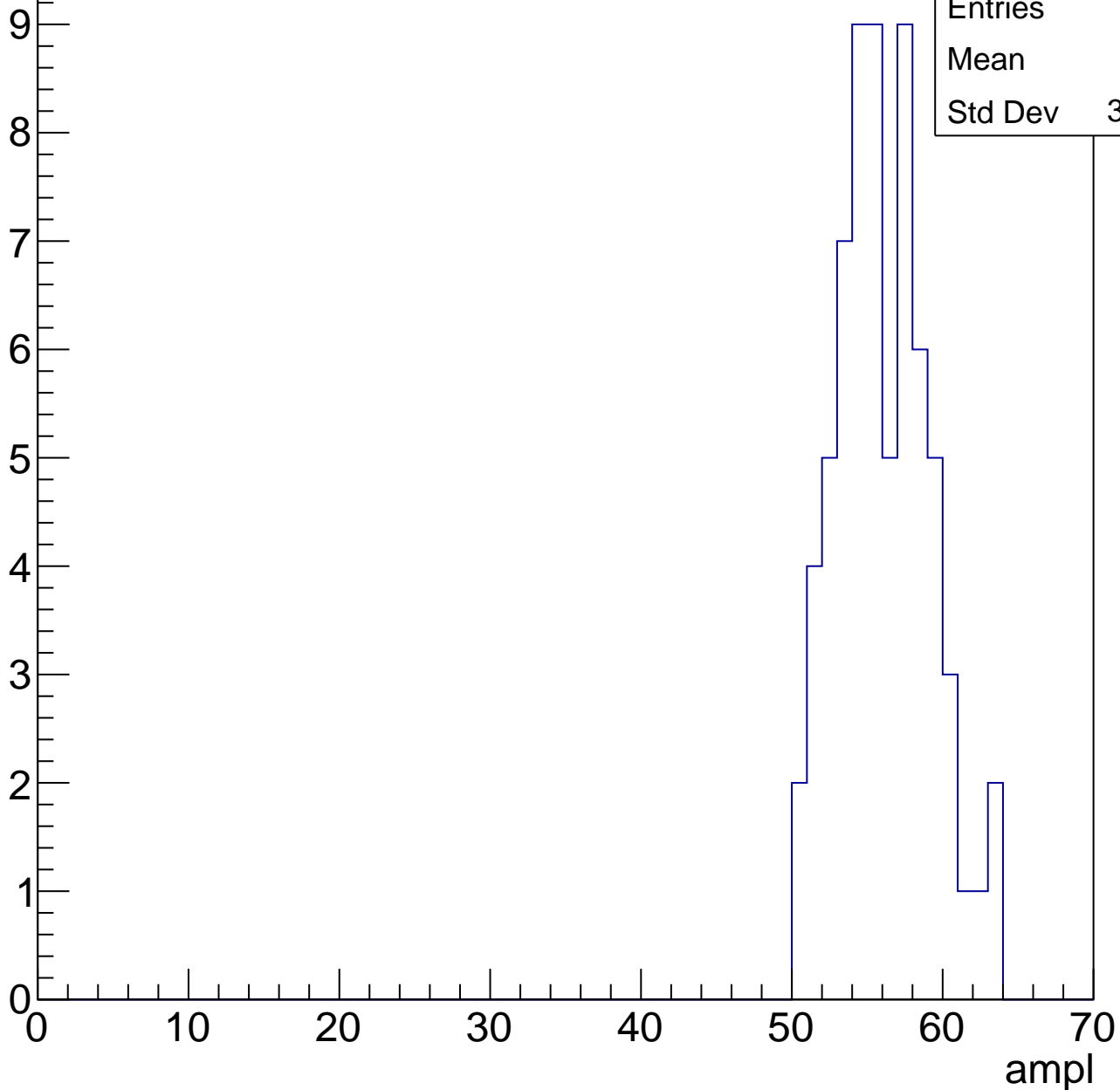
Entries	54
Mean	49
Std Dev	2.9

# B0L000S, U18-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	55.6
Std Dev	3.054

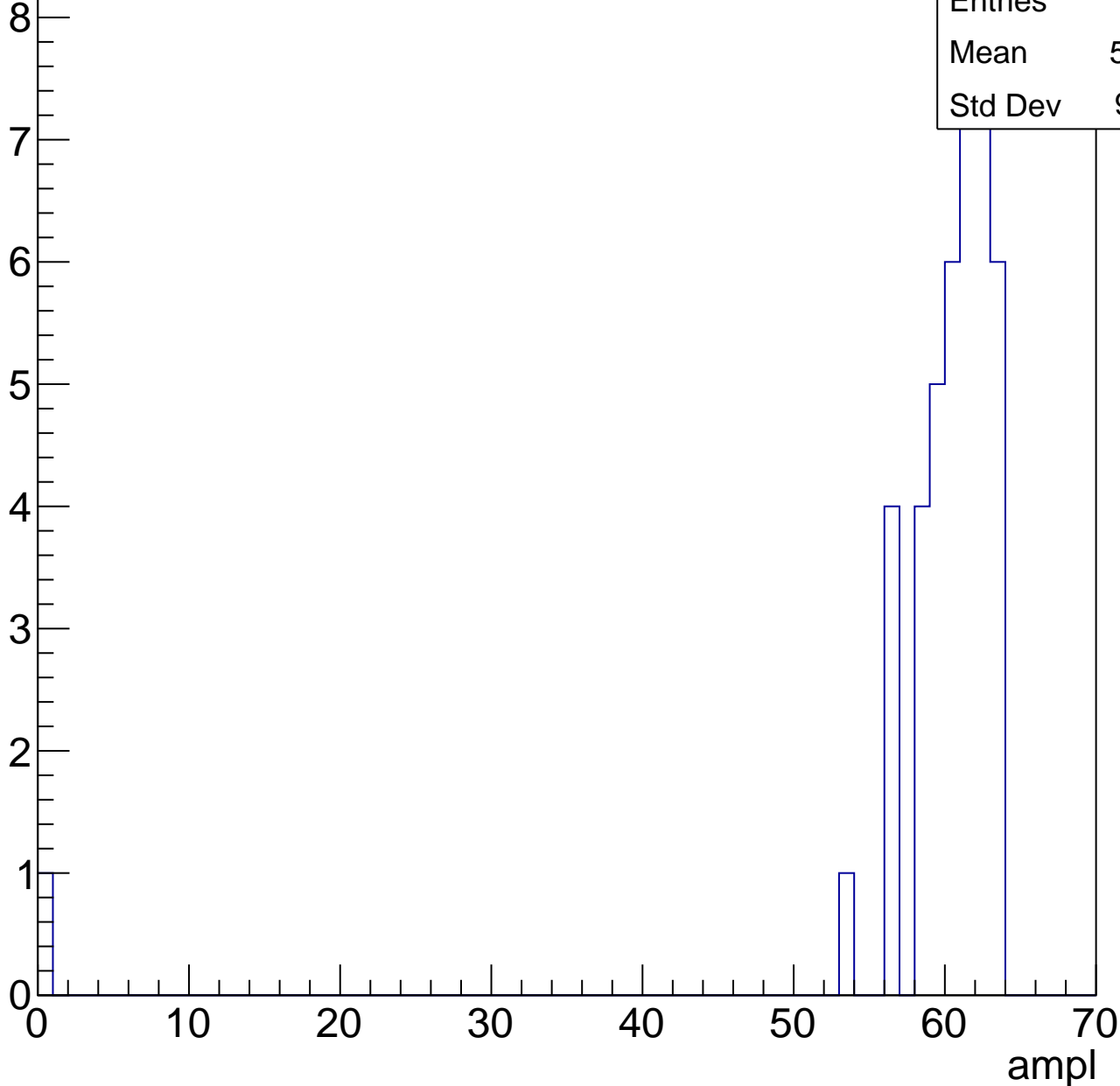


# B0L000S, U18-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.74
Std Dev	9.351



# B0L000S, U18-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	61.8
Std Dev	1.166



# B0L000S, U18-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch2, adc0

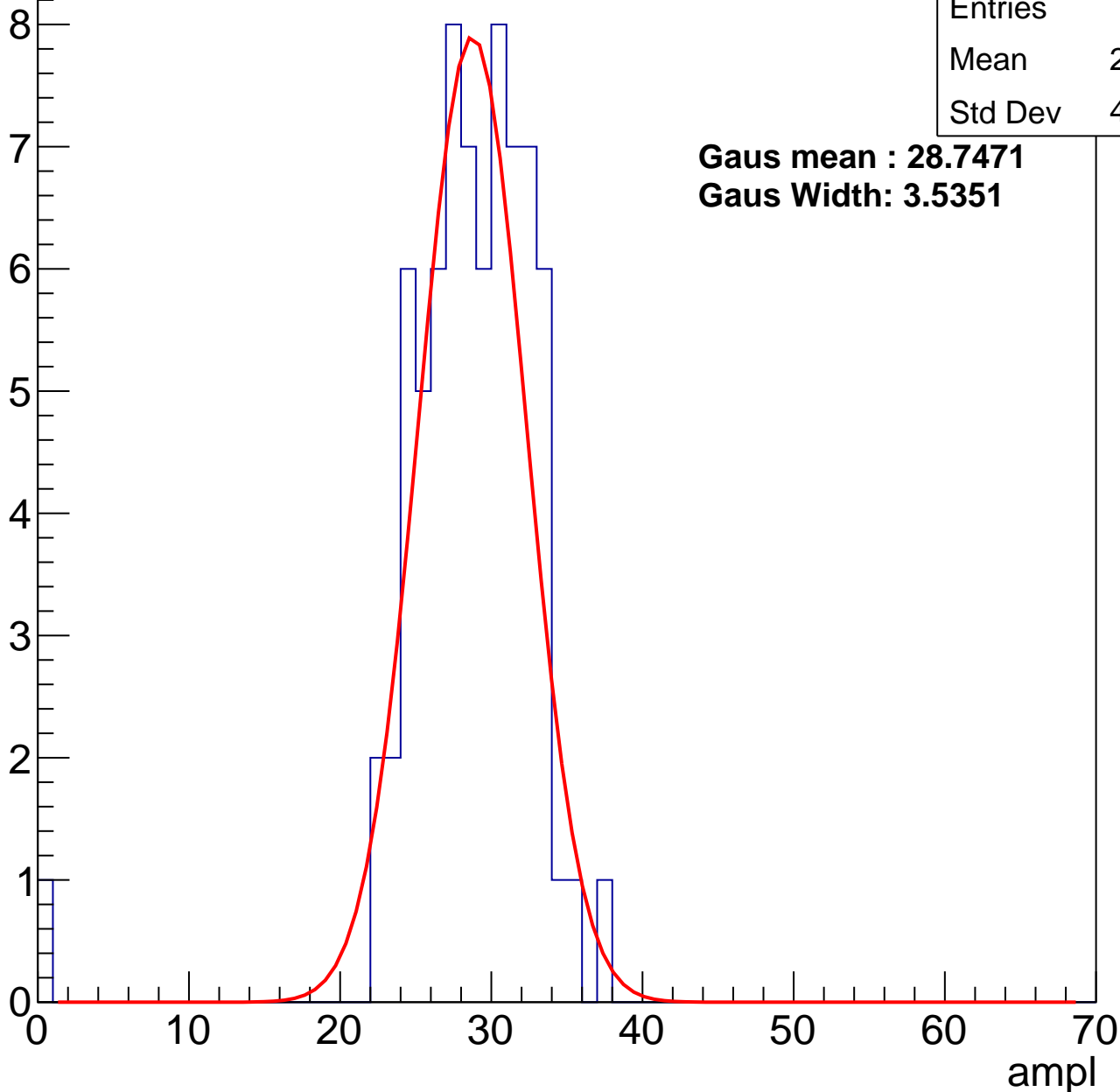
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.19
Std Dev	4.658

**Gaus mean : 28.7471**

**Gaus Width: 3.5351**



# B0L000S, U18-ch2, adc1

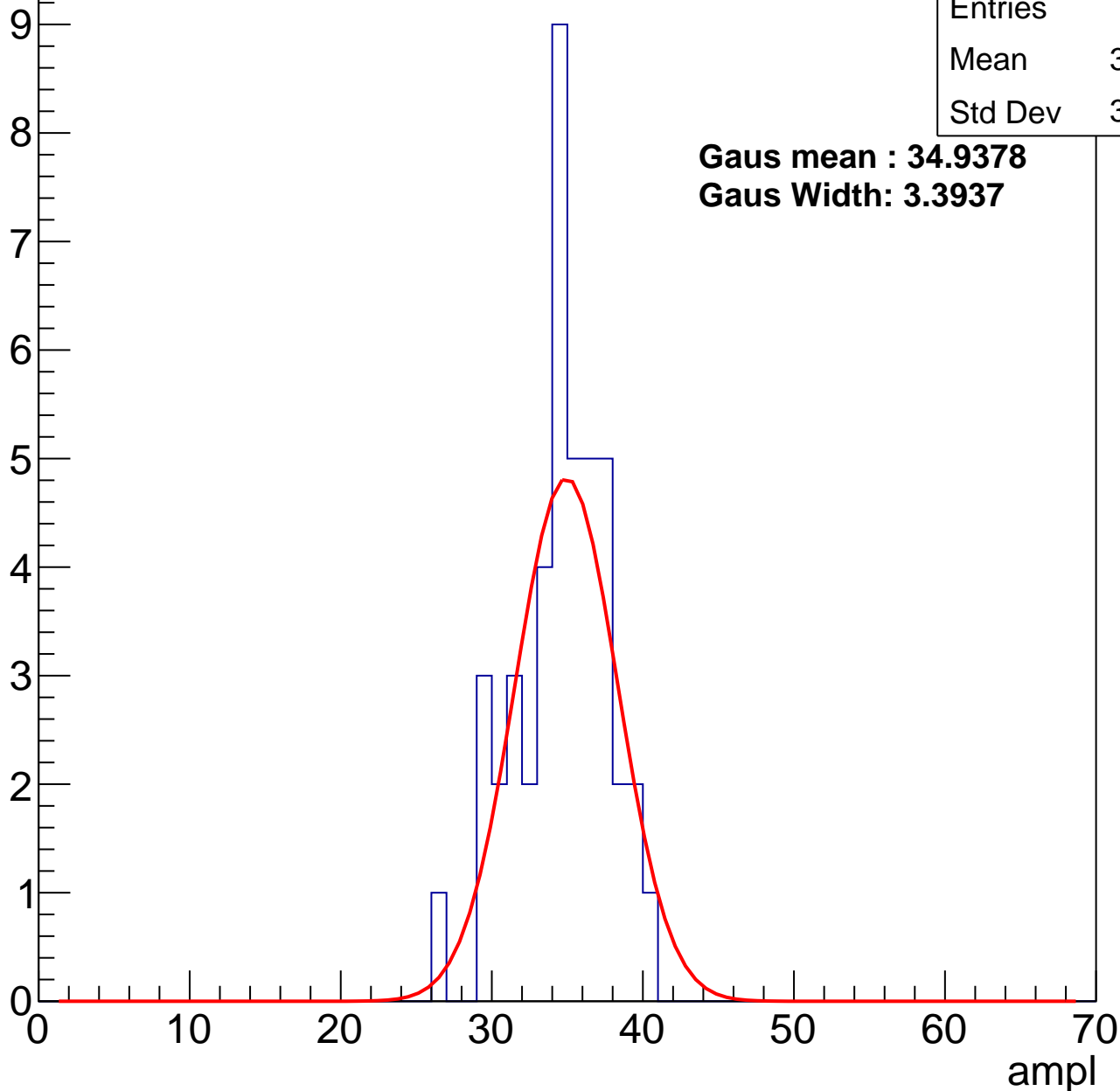
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	34.14
Std Dev	3.004

**Gaus mean : 34.9378**

**Gaus Width: 3.3937**



# B0L000S, U18-ch2, adc2

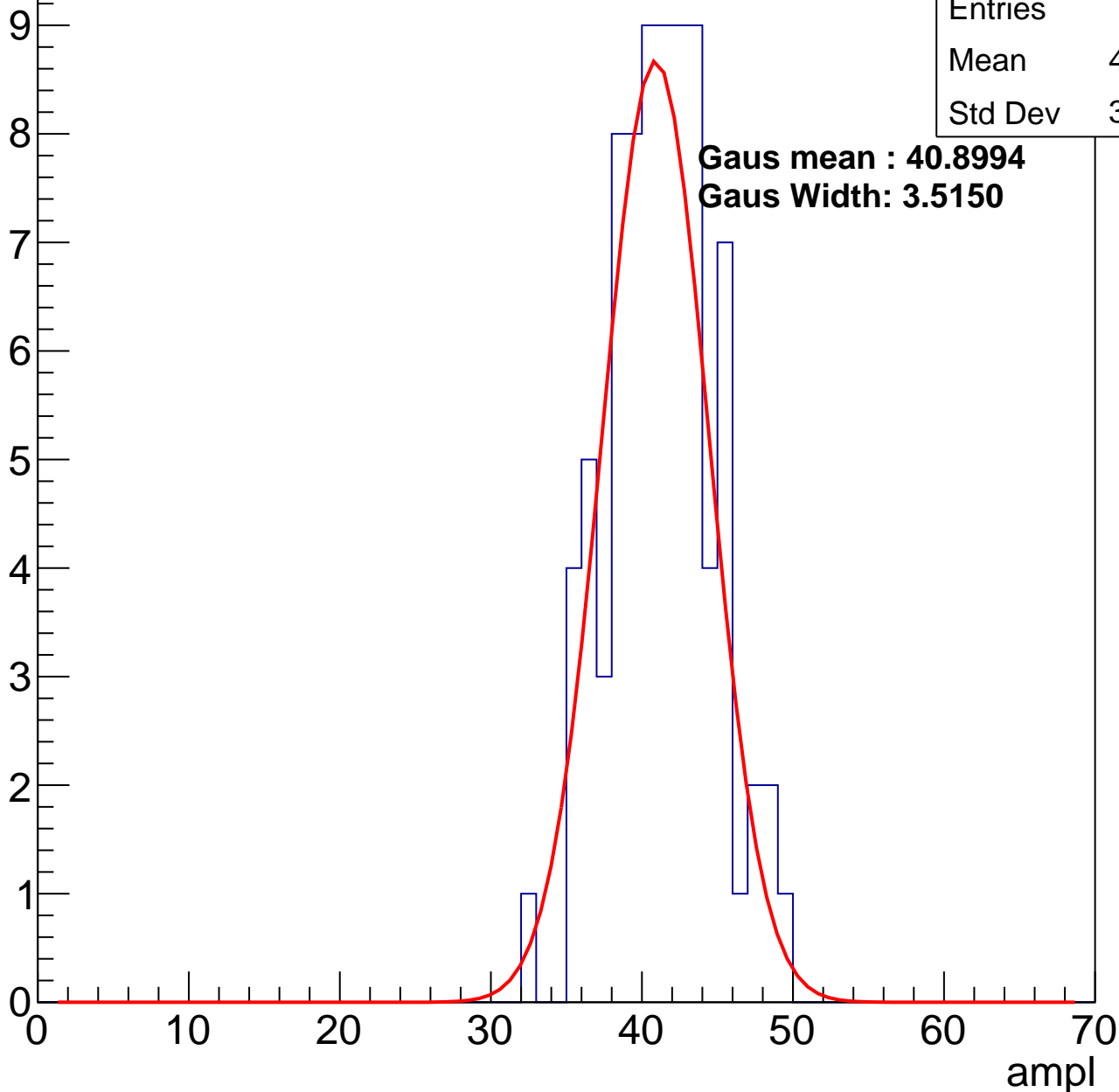
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	40.84
Std Dev	3.423

**Gaus mean : 40.8994**

**Gaus Width: 3.5150**

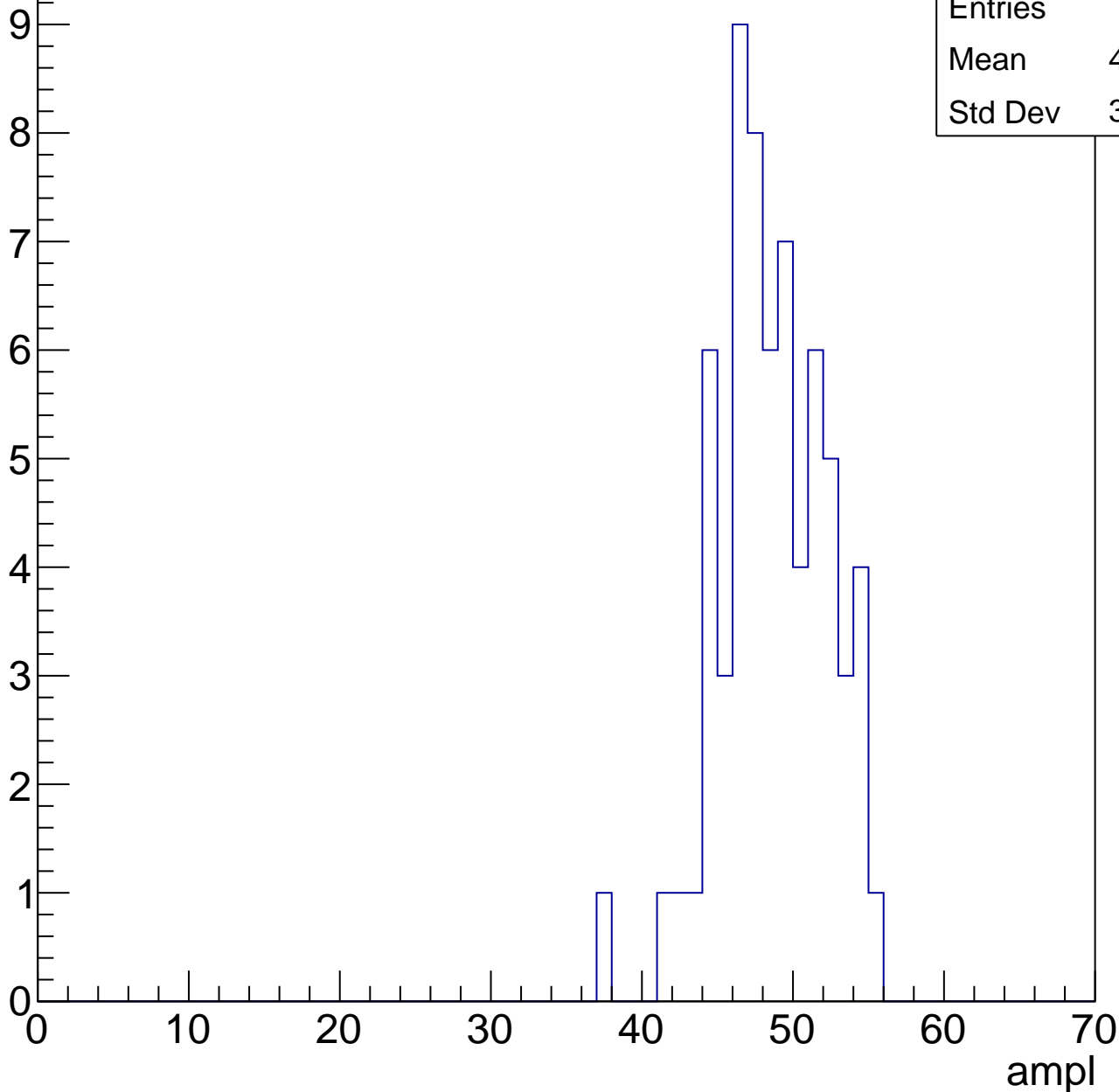


# B0L000S, U18-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.17
Std Dev	3.527

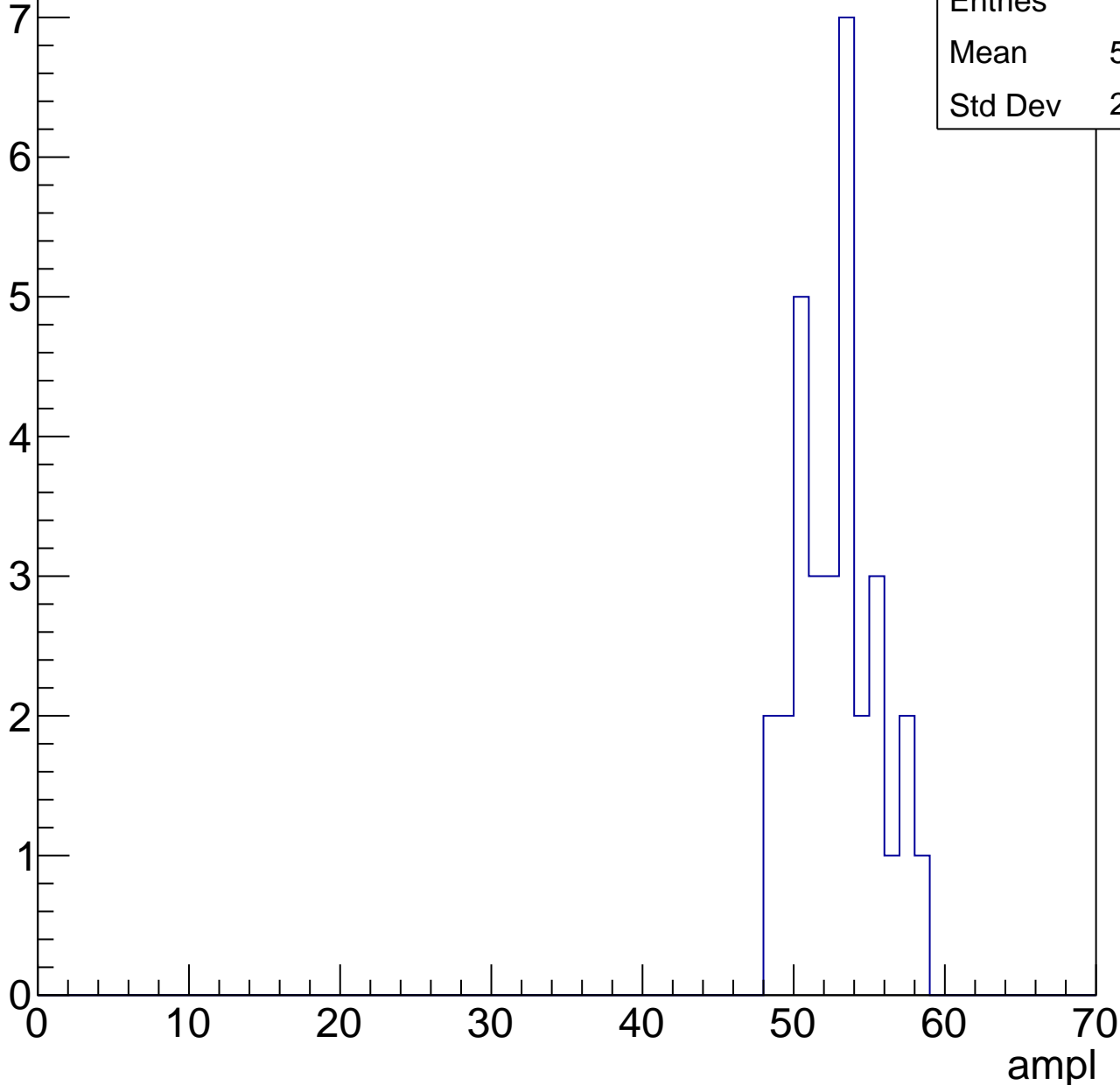


# B0L000S, U18-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

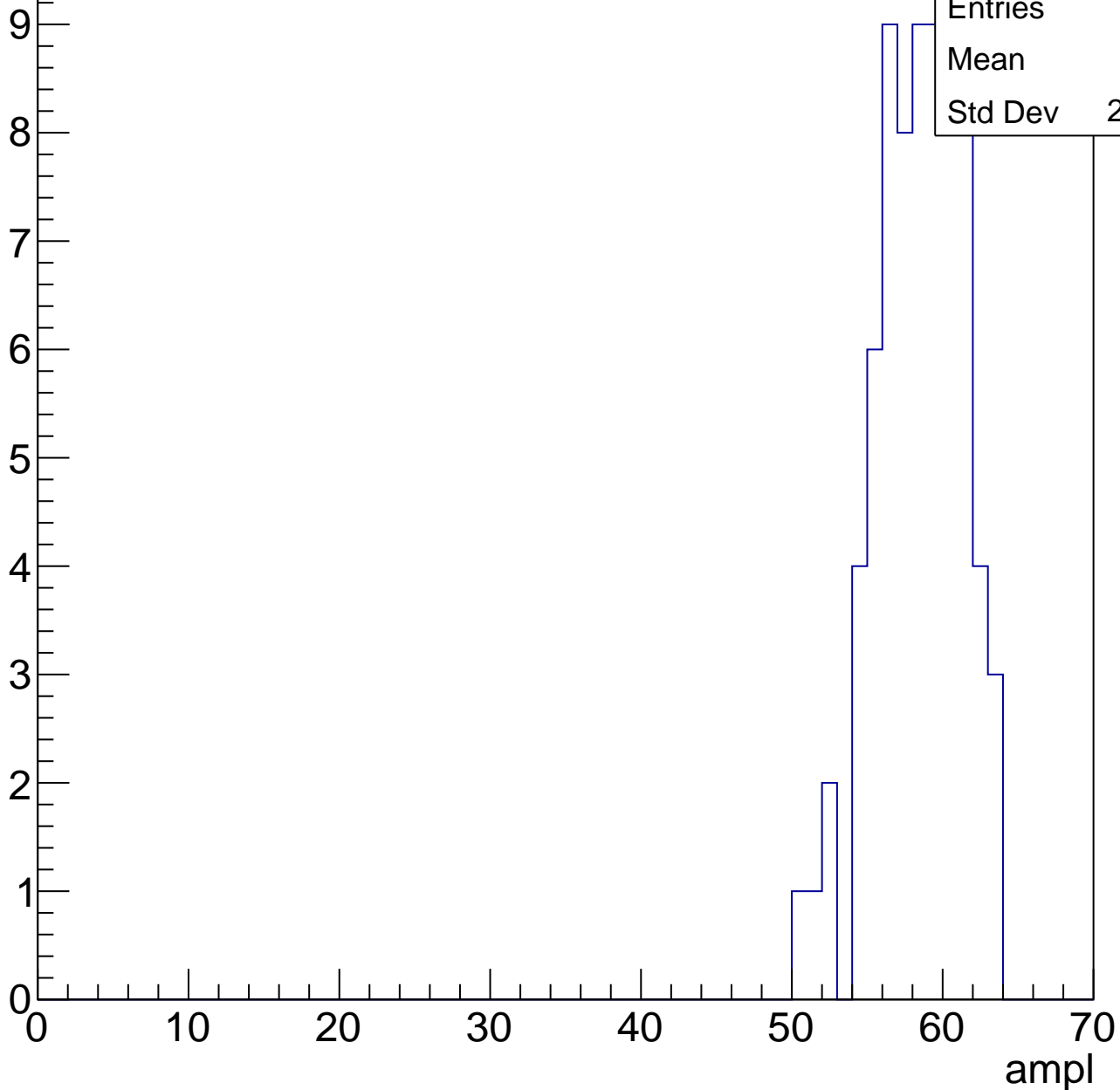
Entries	31
Mean	52.42
Std Dev	2.612



# B0L000S, U18-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

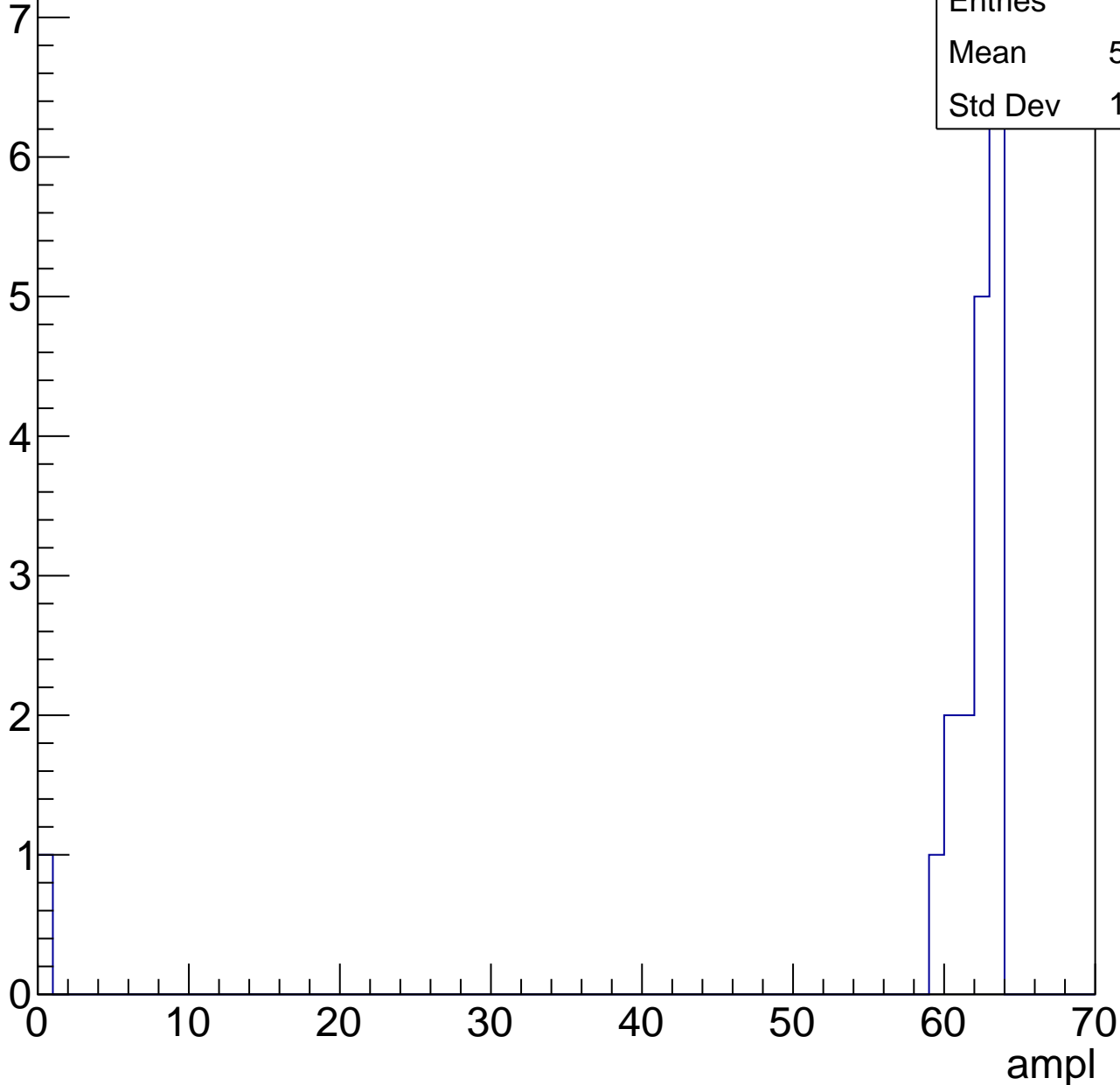


# B0L000S, U18-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	58.44
Std Dev	14.23

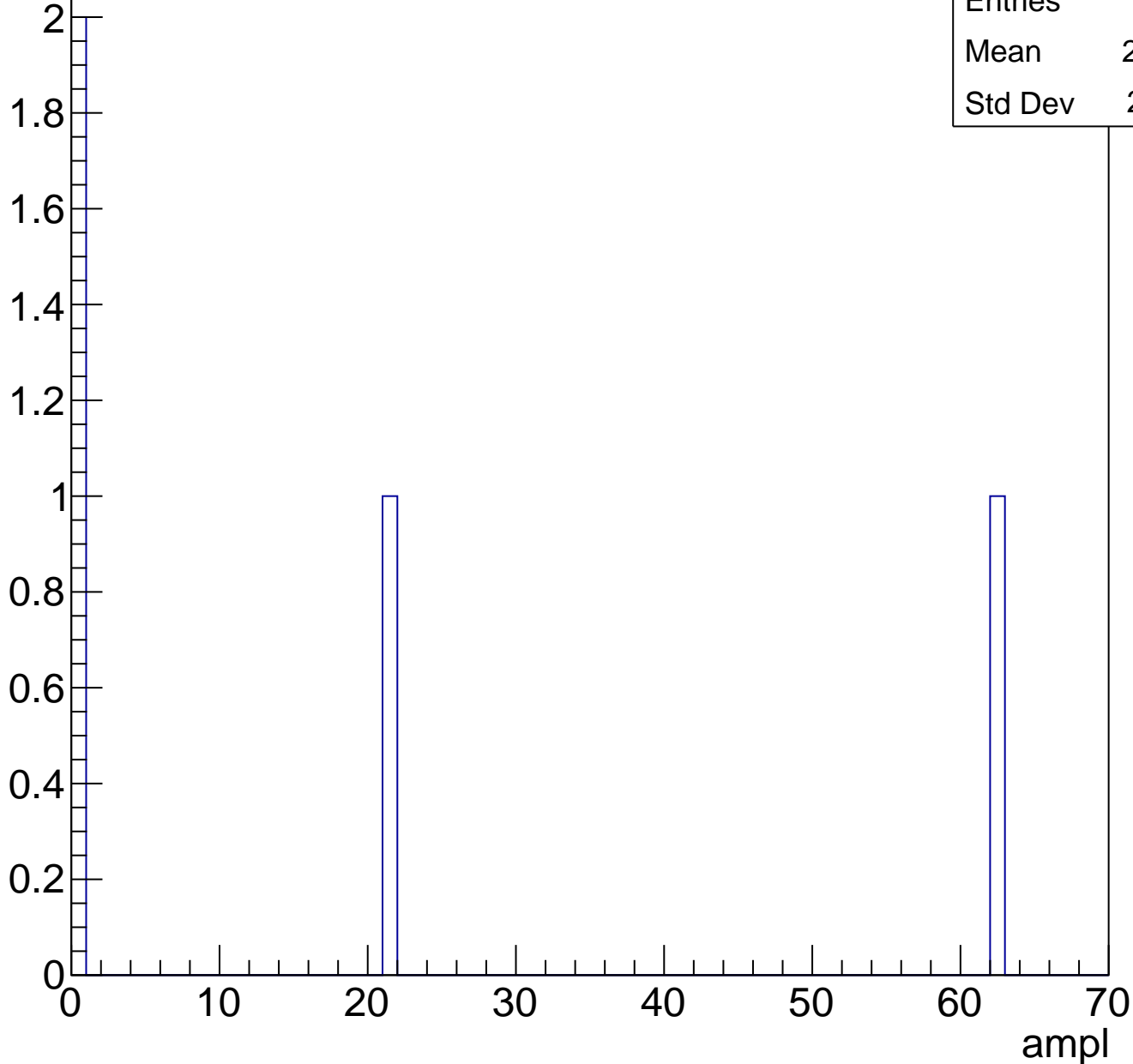




# B0L000S, U18-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

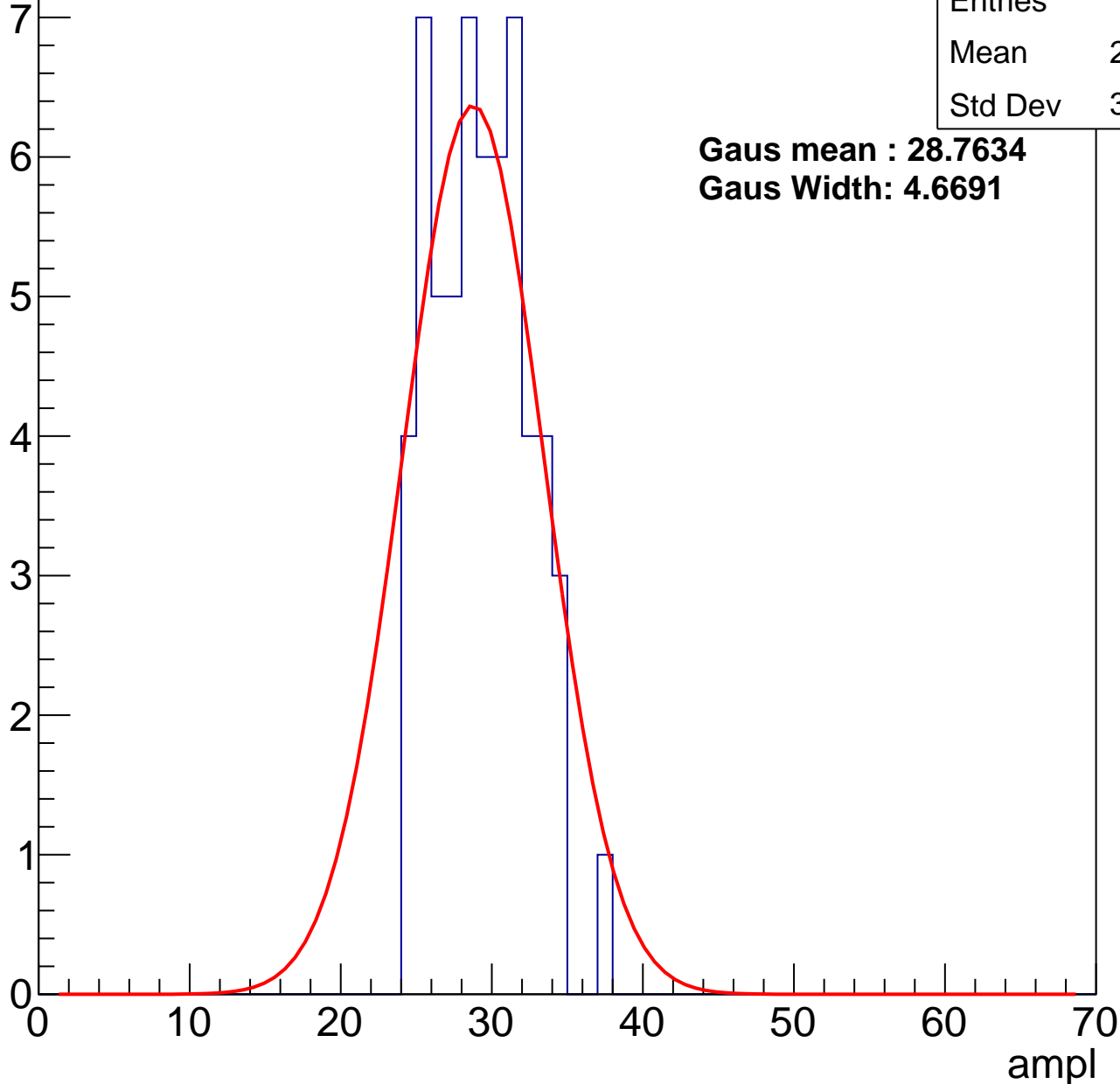


Entries	4
Mean	20.75
Std Dev	25.31

# B0L000S, U18-ch3, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch3, adc1

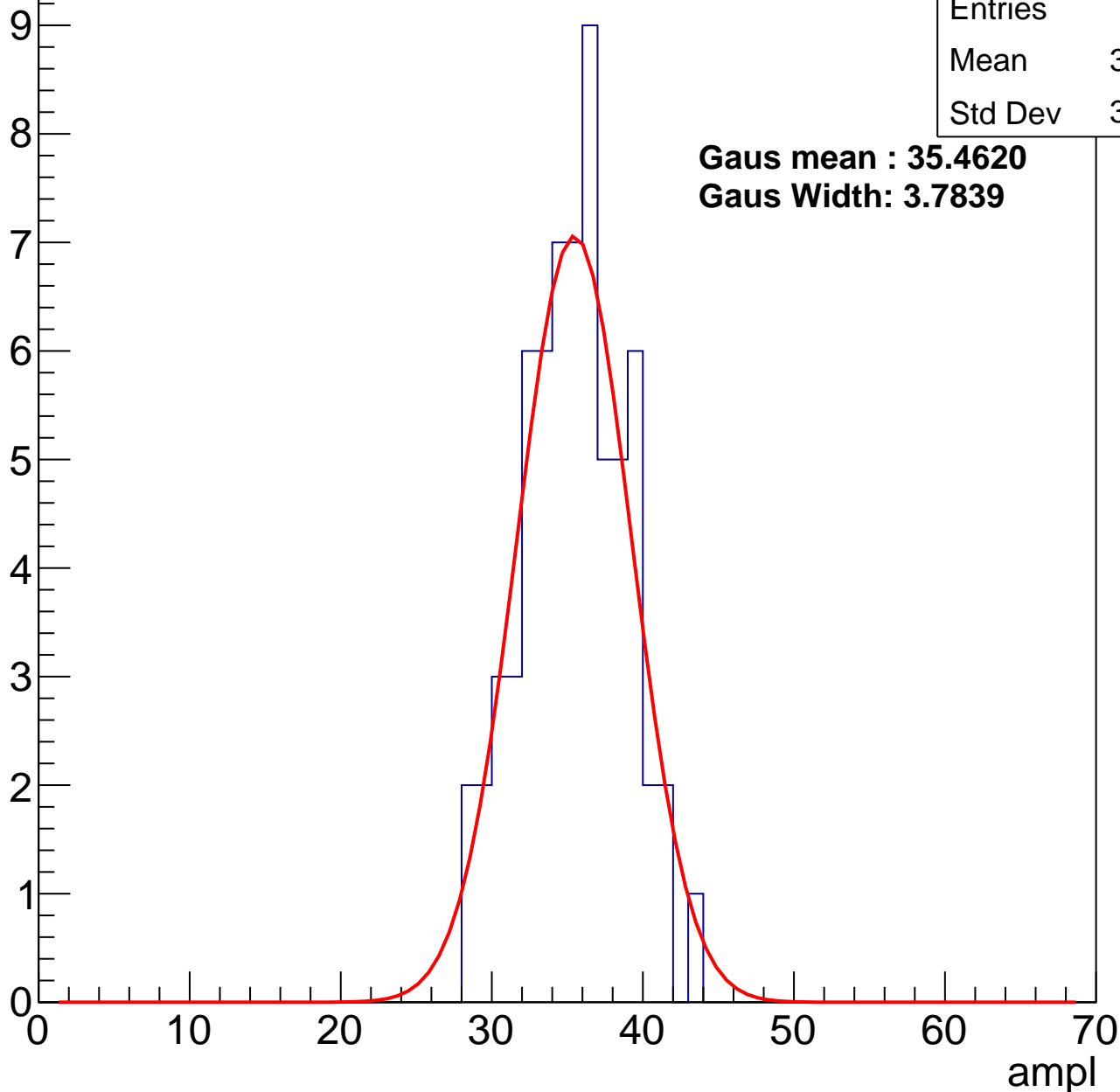
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	34.97
Std Dev	3.335

**Gaus mean : 35.4620**

**Gaus Width: 3.7839**



# B0L000S, U18-ch3, adc2

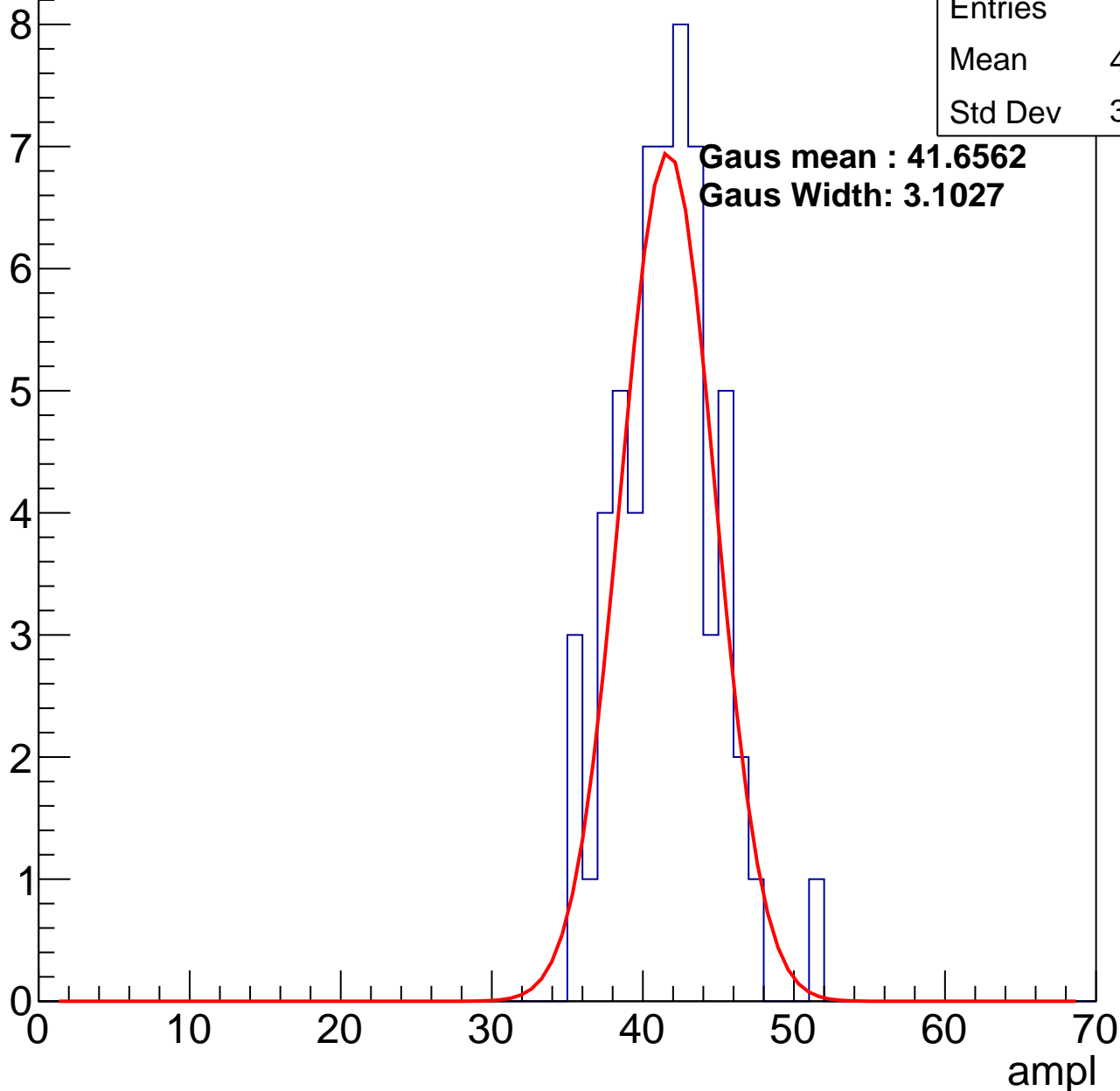
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	41.14
Std Dev	3.197

**Gaus mean : 41.6562**

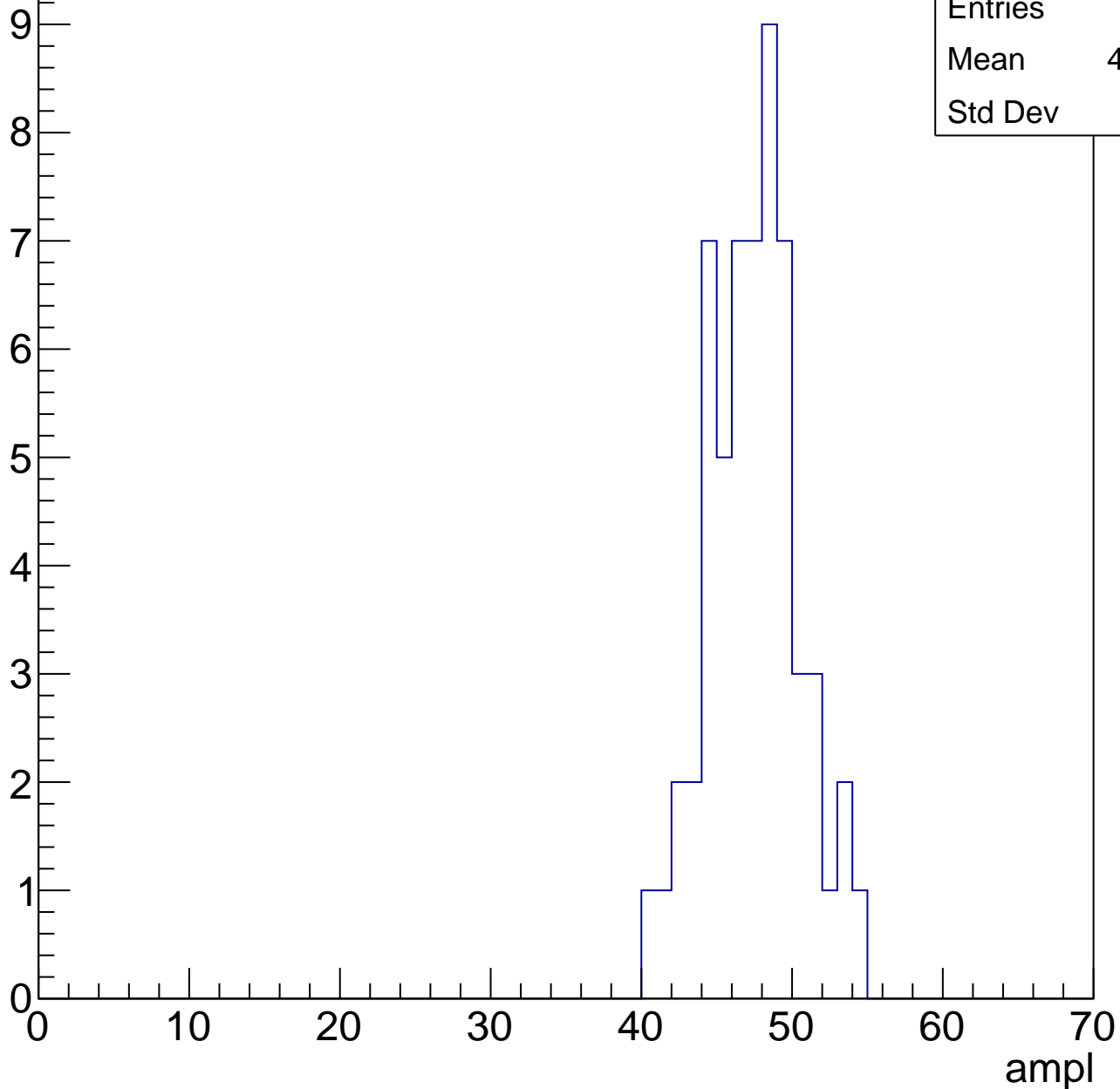
**Gaus Width: 3.1027**



# B0L000S, U18-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

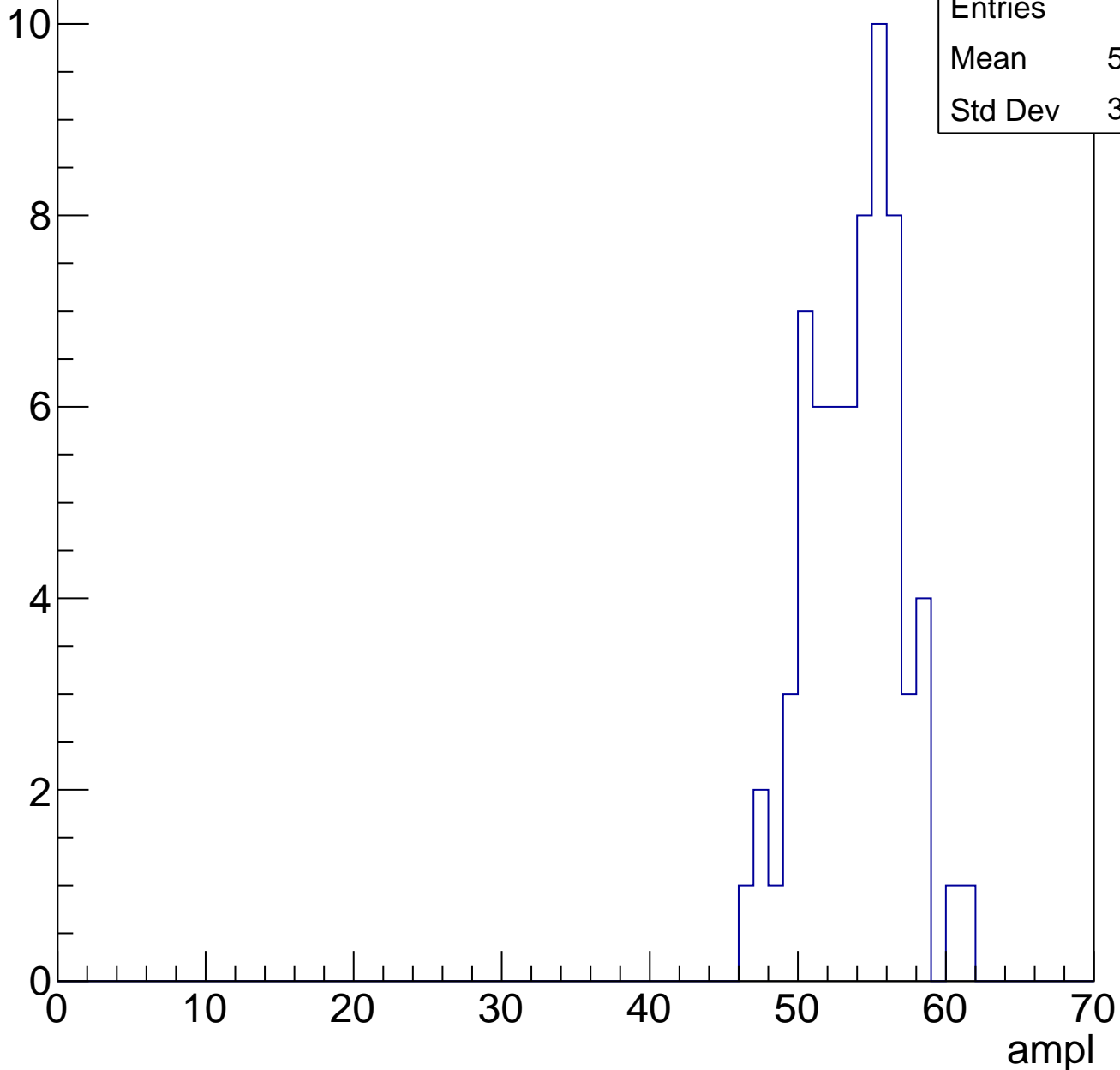


# B0L000S, U18-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	53.36
Std Dev	3.137

Entry

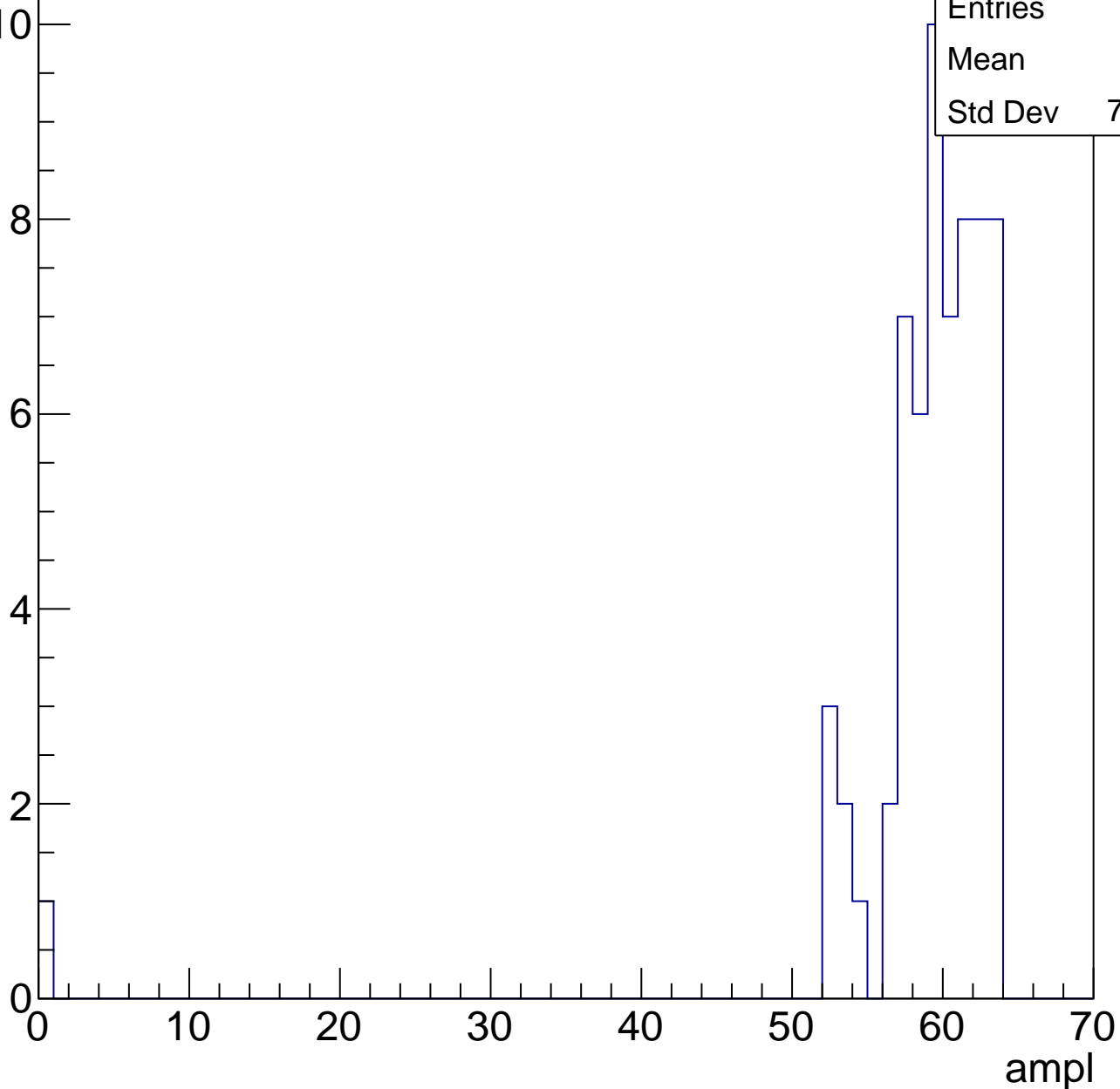


# B0L000S, U18-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	58.3
Std Dev	7.954



# B0L000S, U18-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.57
Std Dev	1.178

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch4, adc0

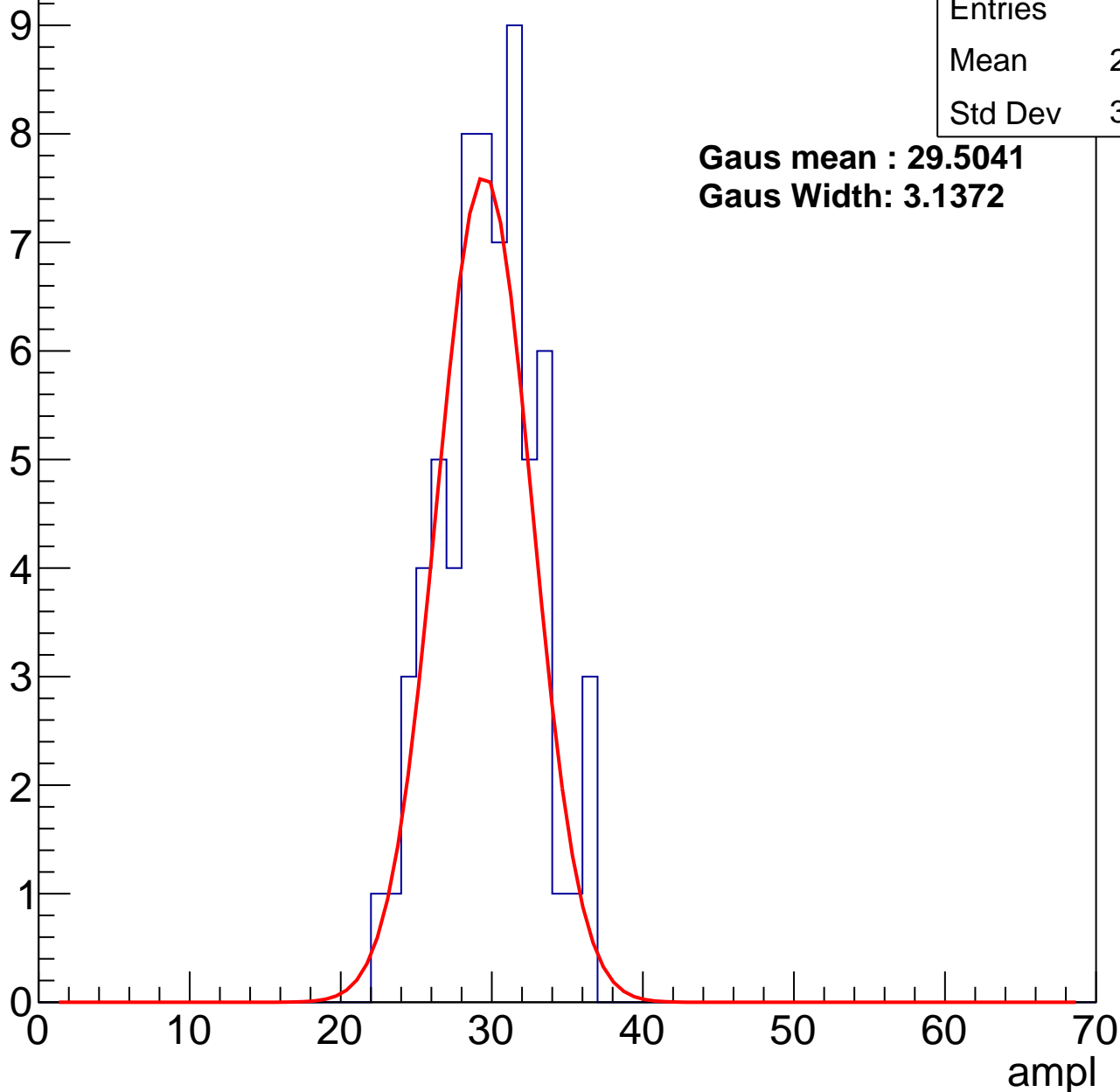
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.32
Std Dev	3.206

**Gaus mean : 29.5041**

**Gaus Width: 3.1372**



# B0L000S, U18-ch4, adc1

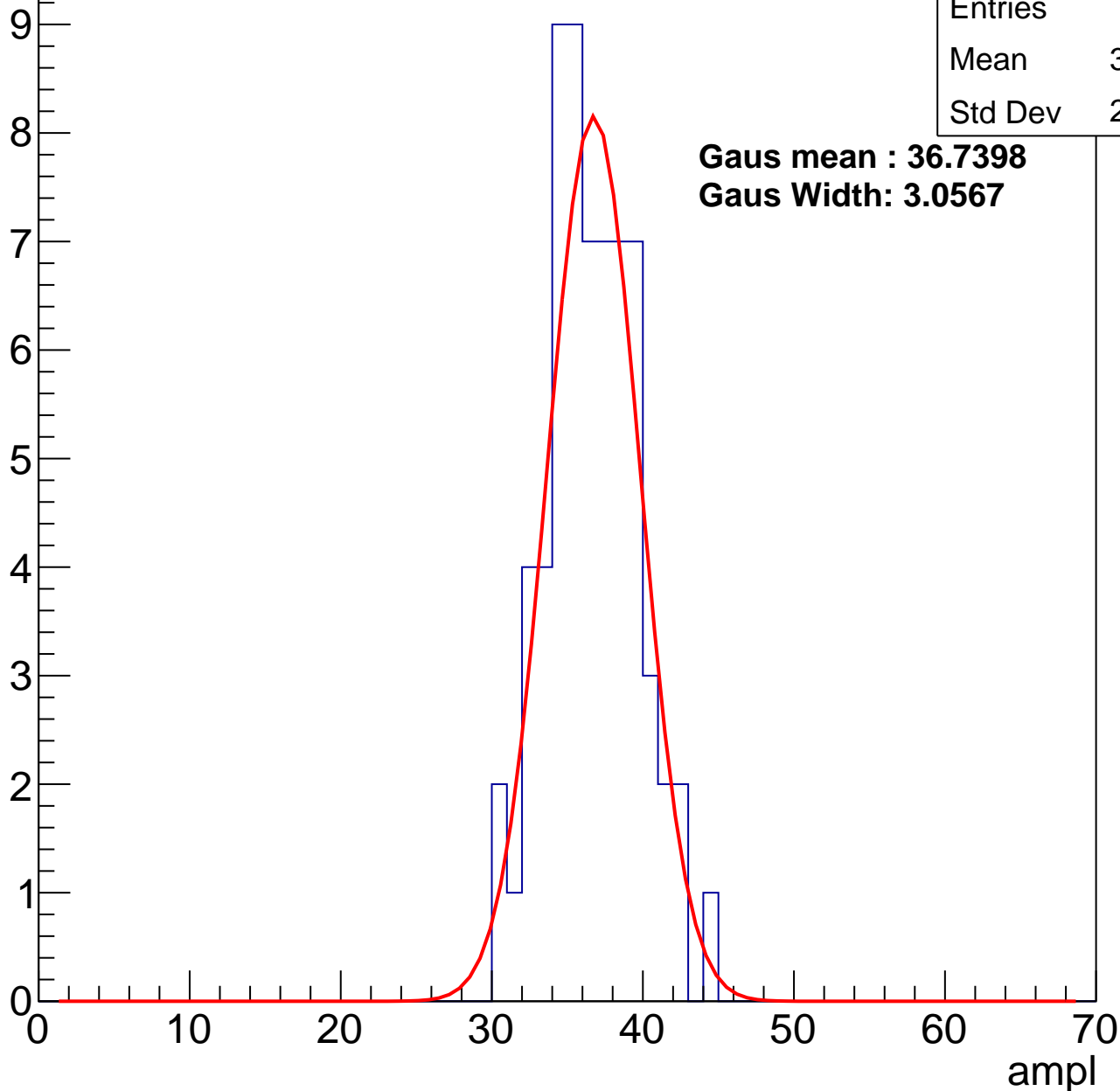
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.18
Std Dev	2.966

**Gaus mean : 36.7398**

**Gaus Width: 3.0567**



# B0L000S, U18-ch4, adc2

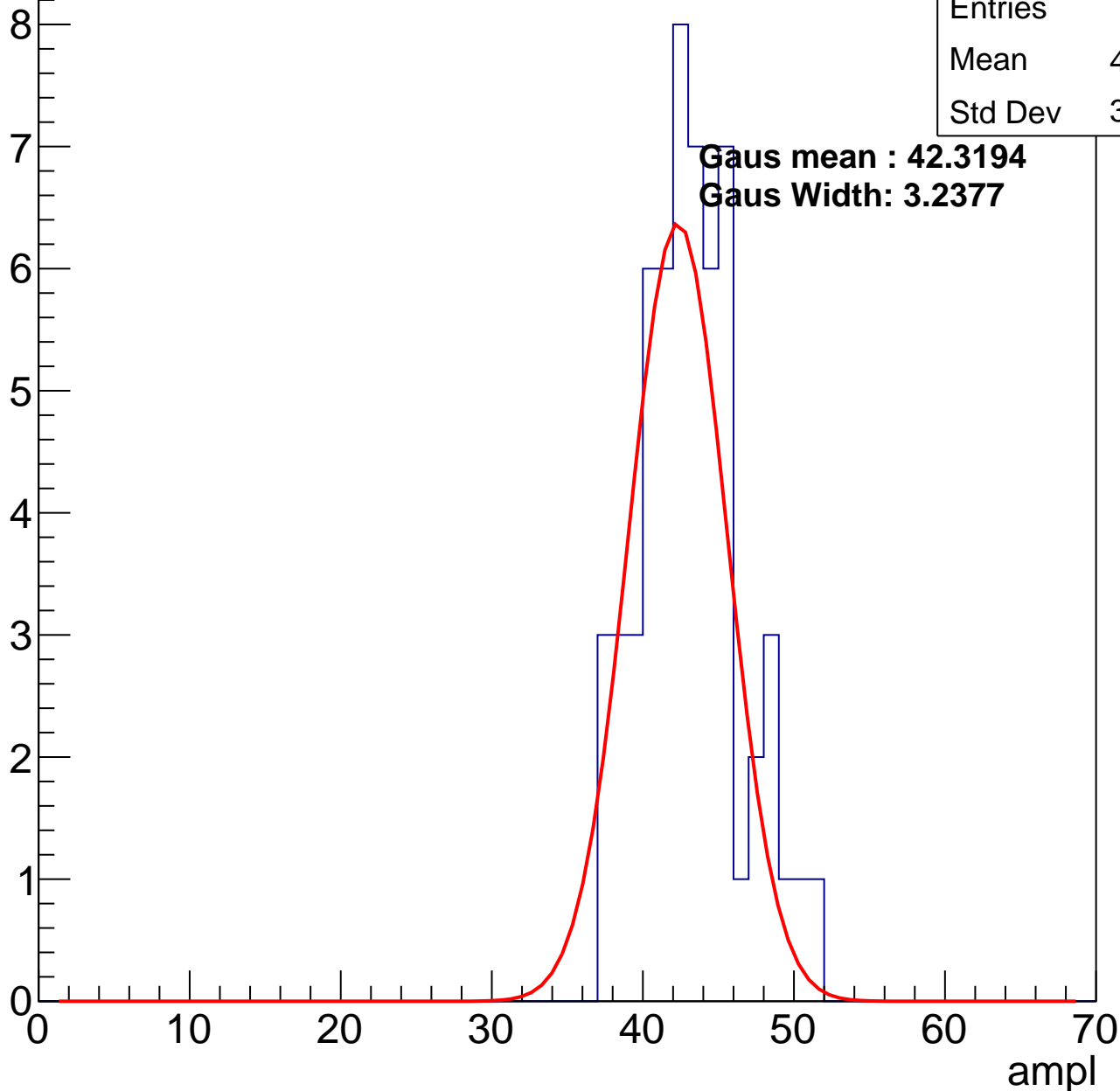
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.72
Std Dev	3.247

**Gaus mean : 42.3194**

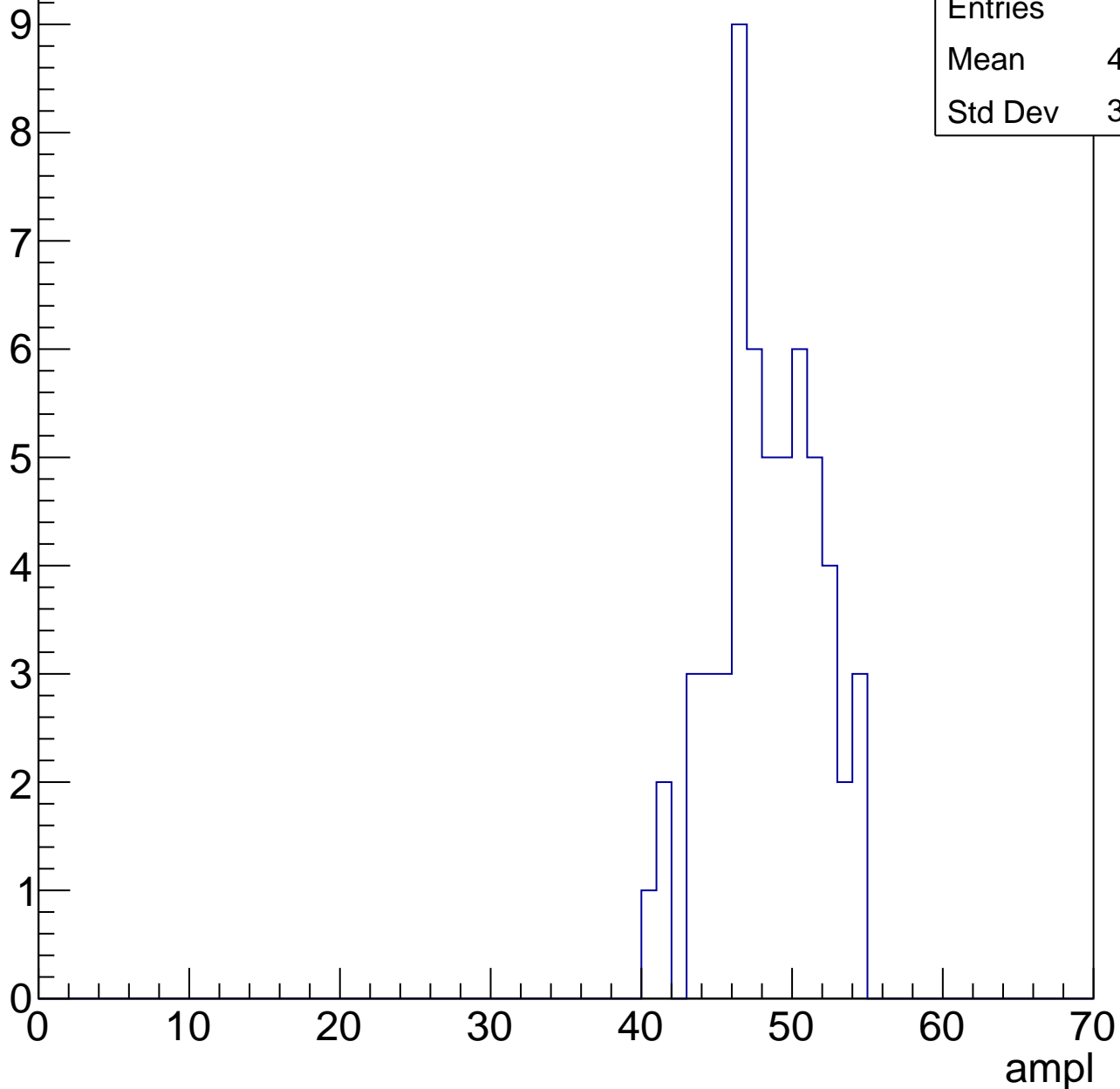
**Gaus Width: 3.2377**



# B0L000S, U18-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

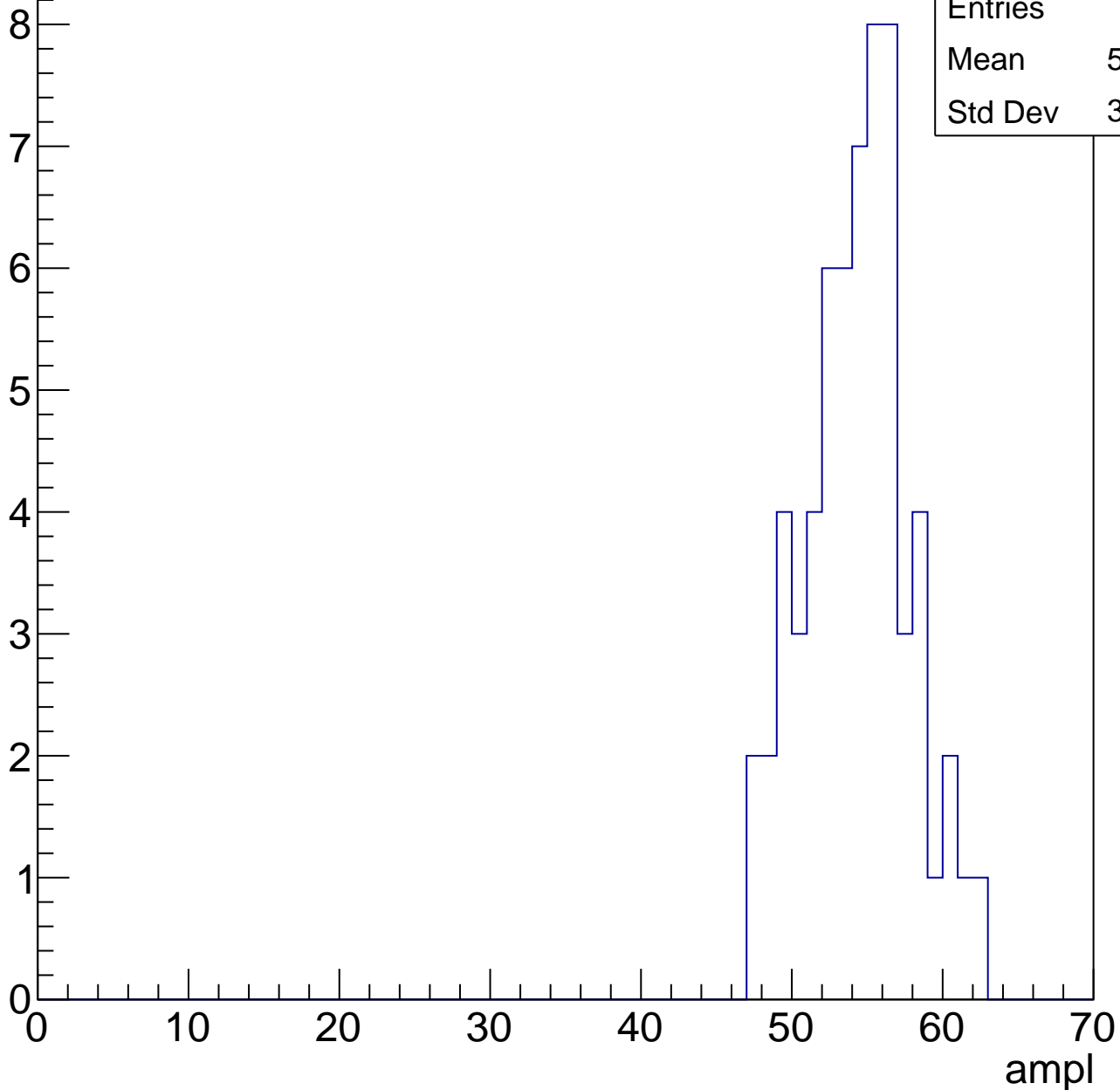


# B0L000S, U18-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	53.89
Std Dev	3.418



# B0L000S, U18-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	59.2
Std Dev	2.481

ampl

0

10

20

30

40

50

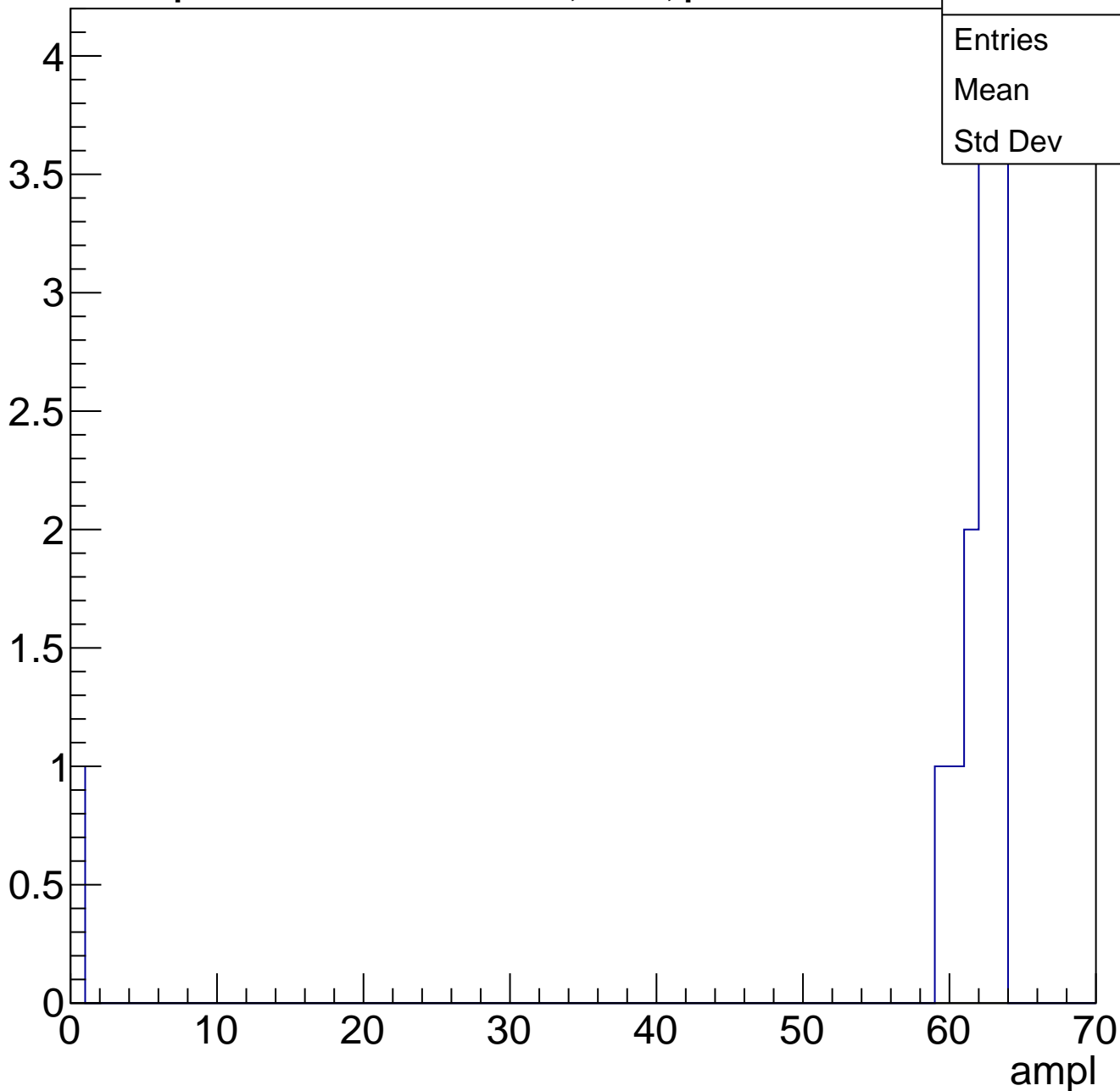
60

70

# B0L000S, U18-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

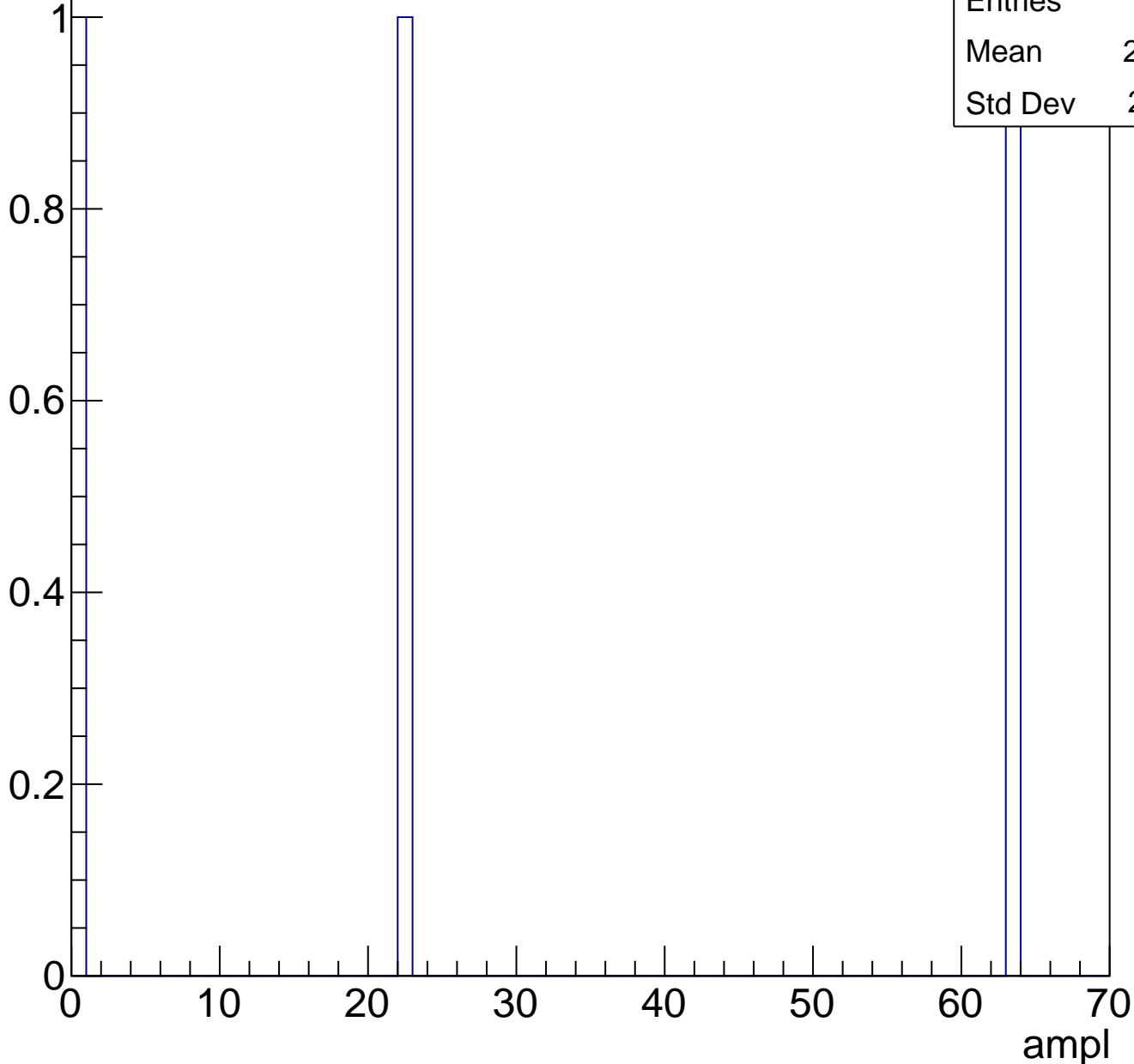




# B0L000S, U18-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B0L000S, U18-ch5, adc0

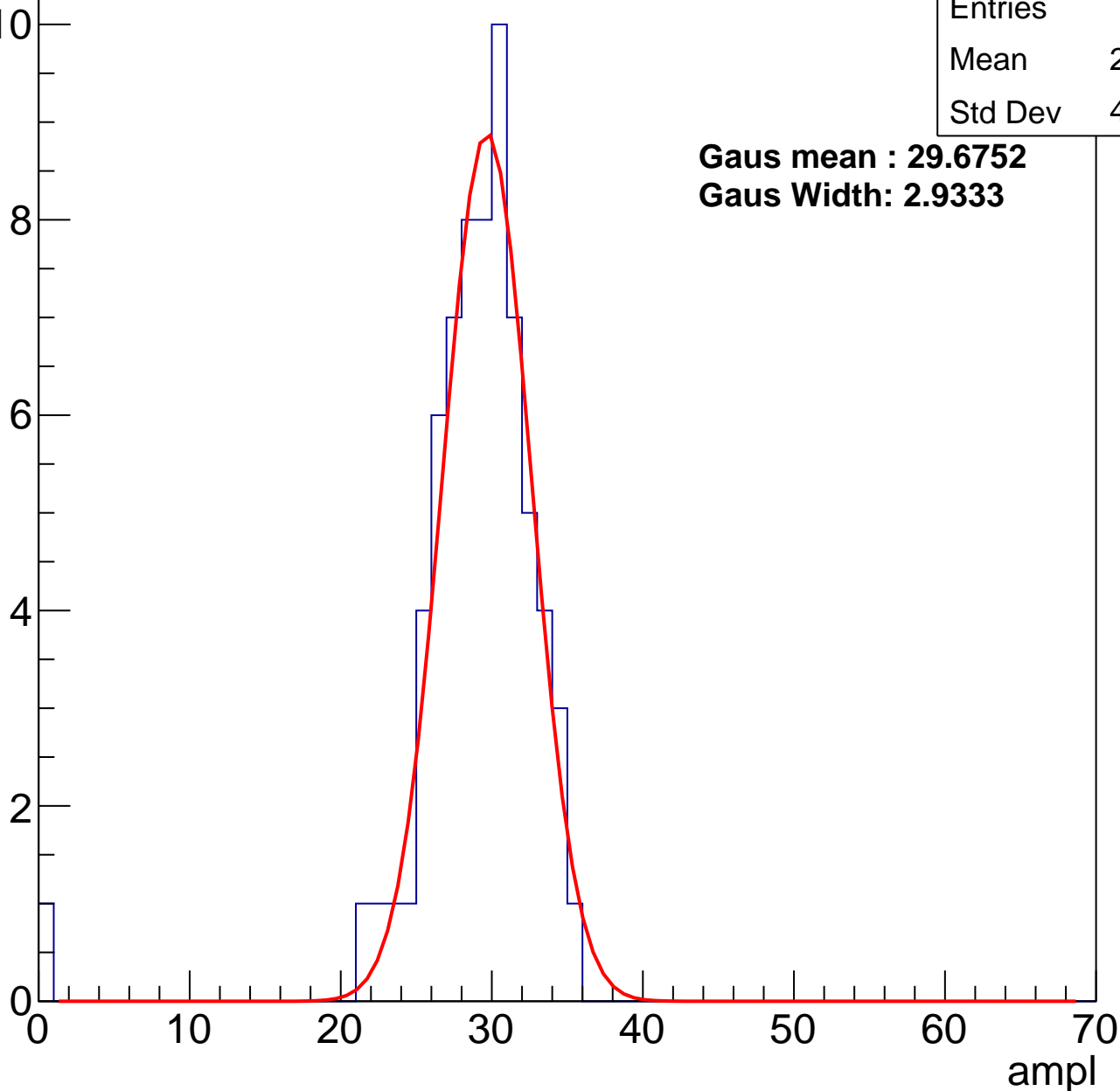
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28.49
Std Dev	4.552

**Gaus mean : 29.6752**

**Gaus Width: 2.9333**



# B0L000S, U18-ch5, adc1

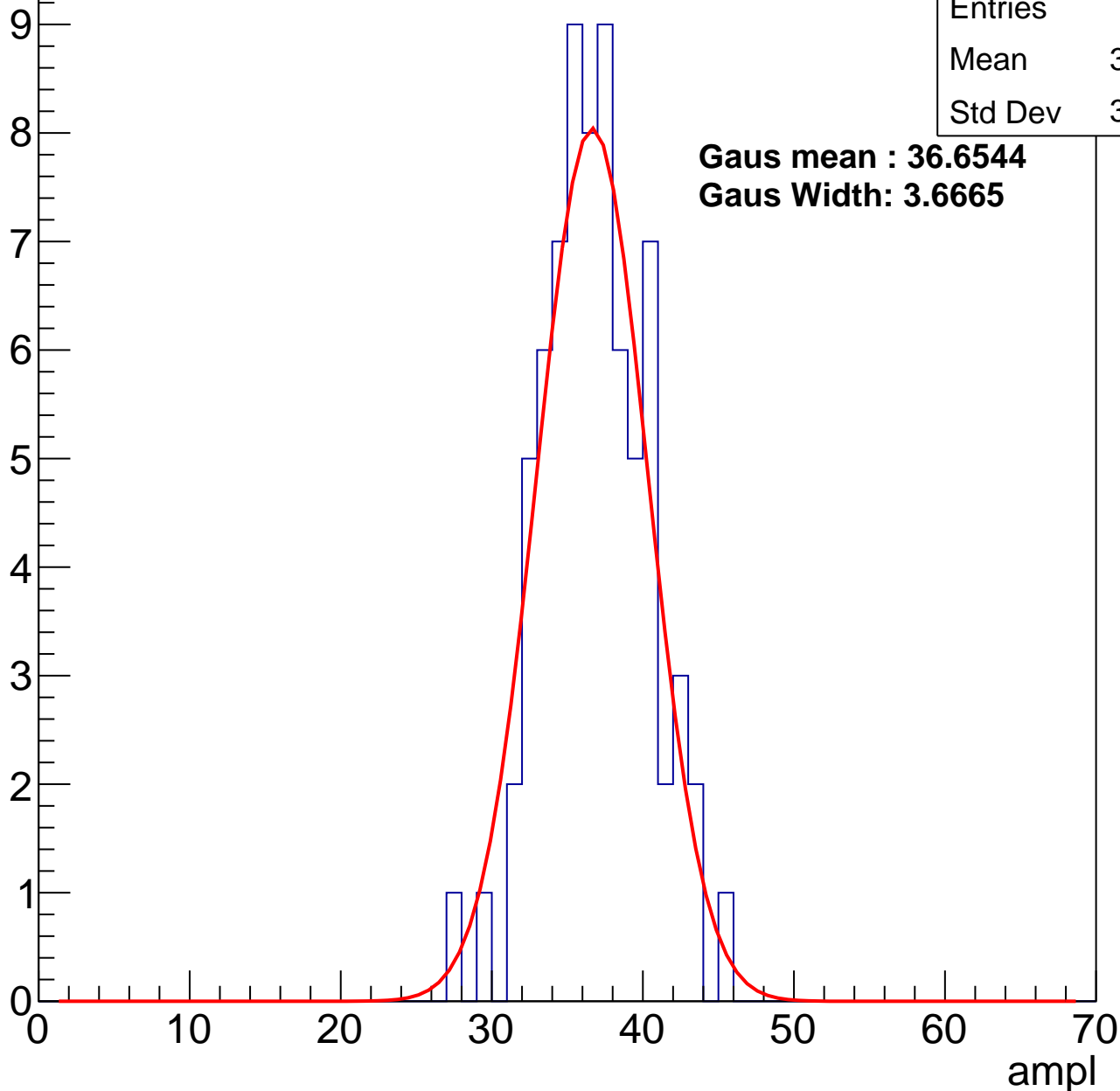
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.38
Std Dev	3.424

**Gaus mean : 36.6544**

**Gaus Width: 3.6665**



# B0L000S, U18-ch5, adc2

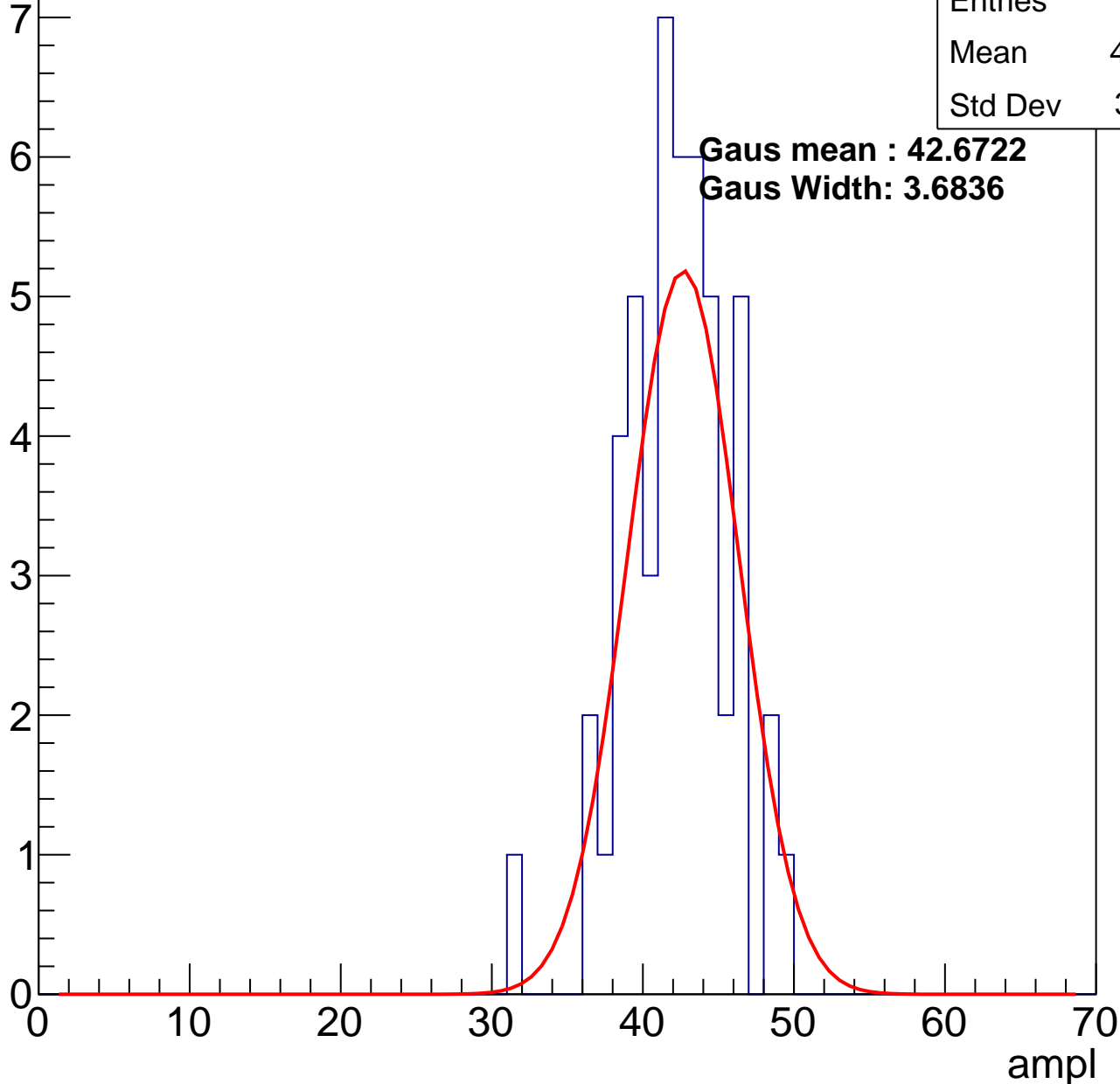
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	41.78
Std Dev	3.431

**Gaus mean : 42.6722**

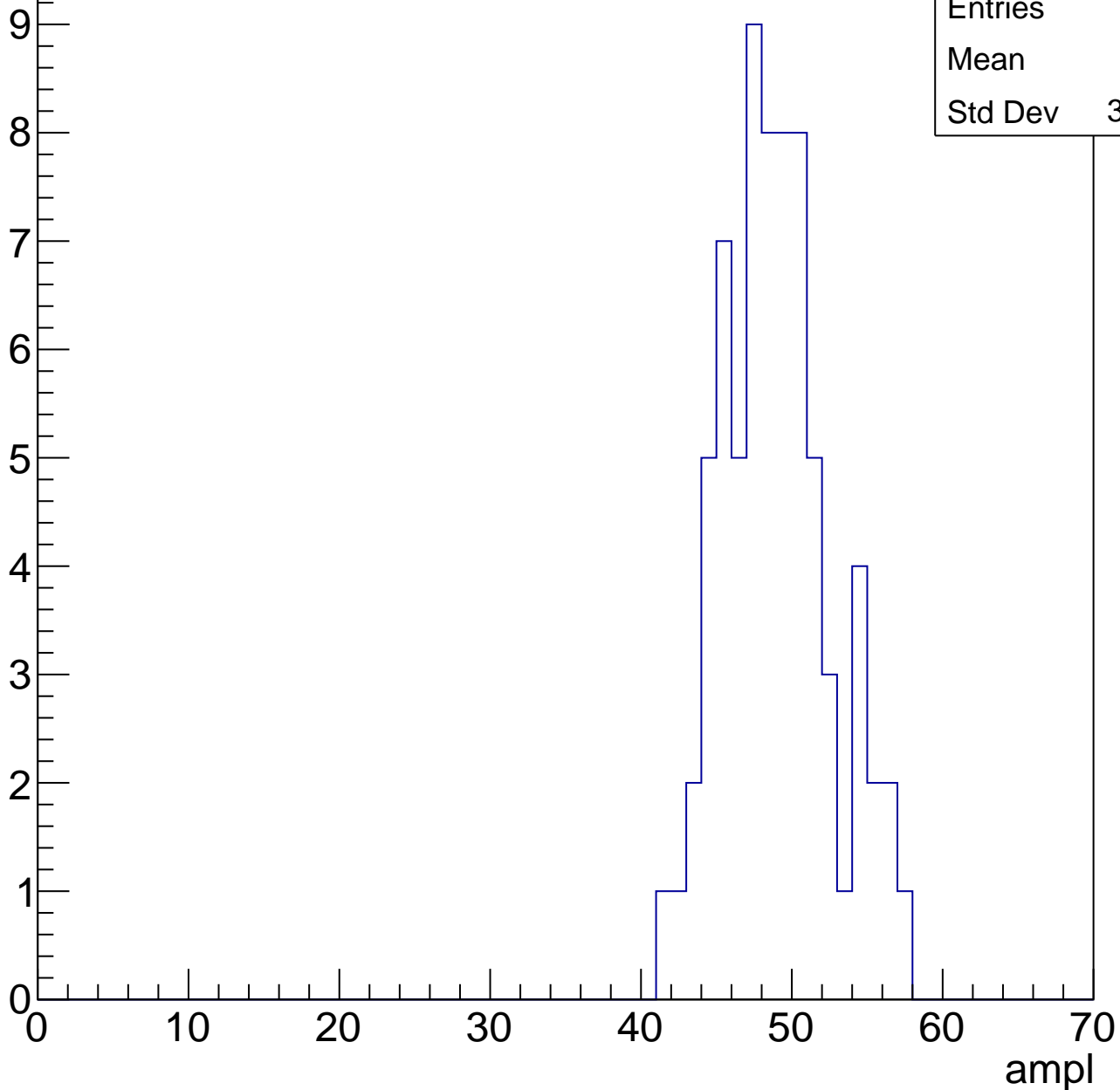
**Gaus Width: 3.6836**



# B0L000S, U18-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



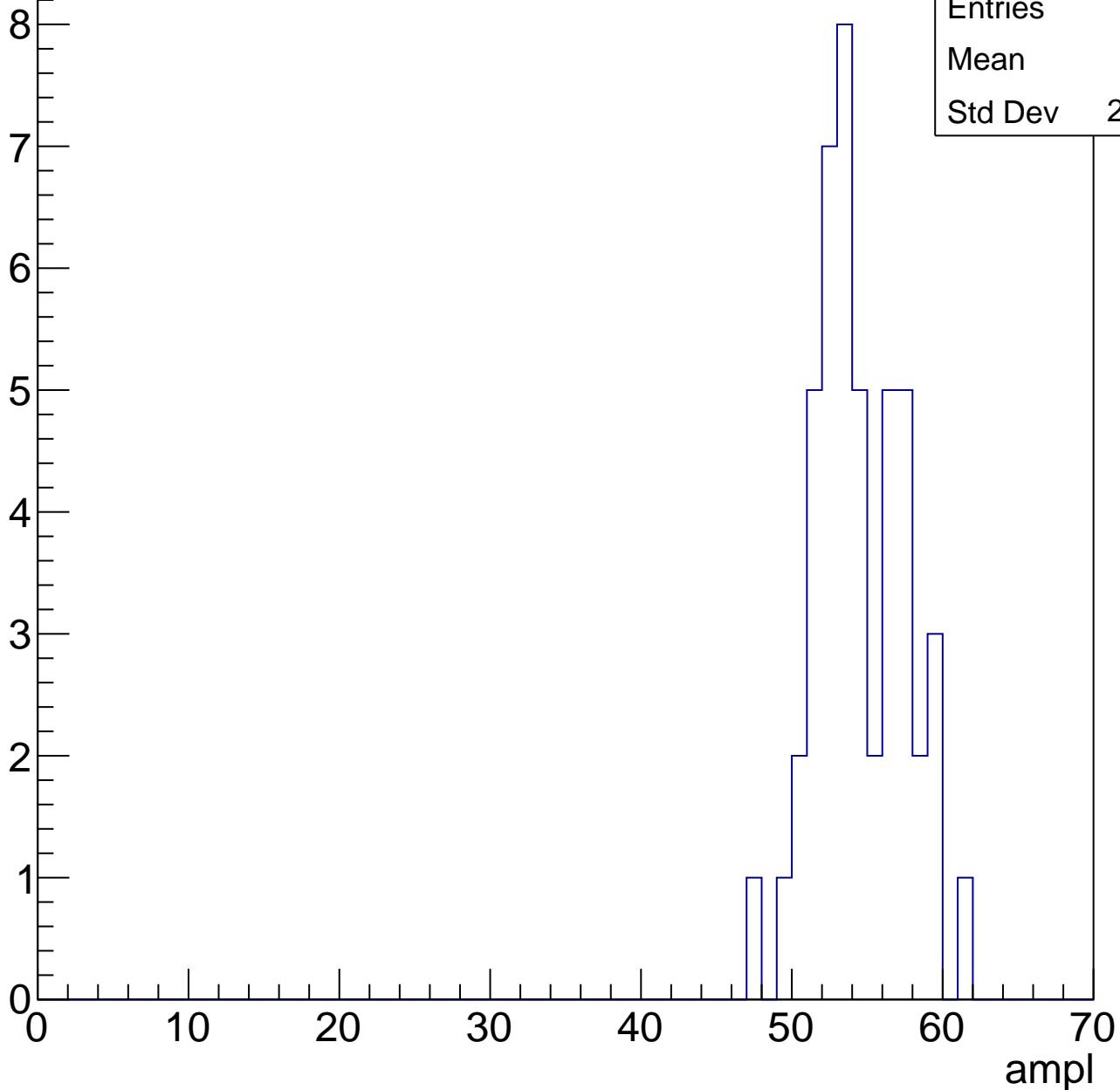
Entries	72
Mean	48.5
Std Dev	3.532

# B0L000S, U18-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	54
Std Dev	2.954

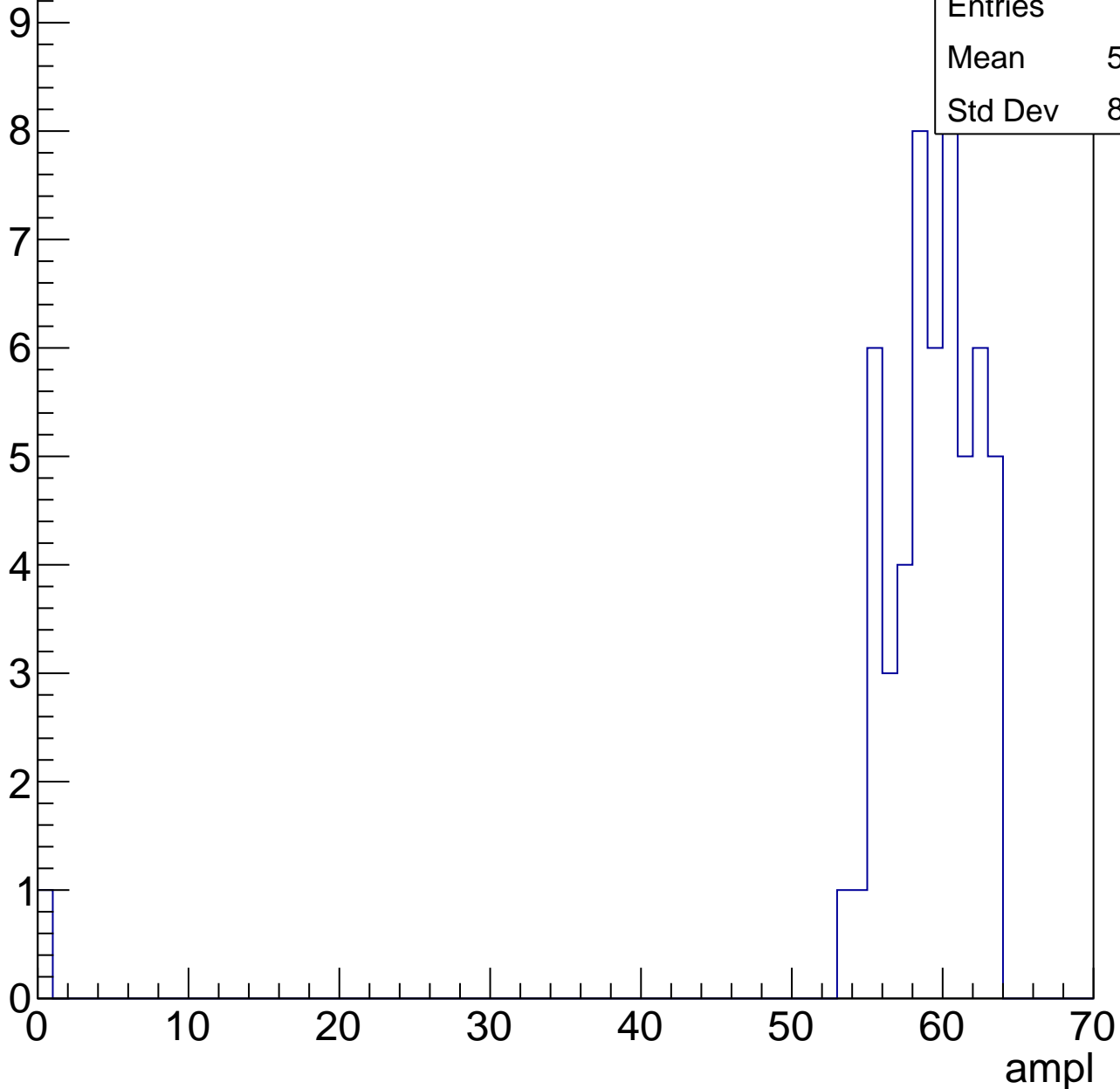


# B0L000S, U18-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	57.87
Std Dev	8.293

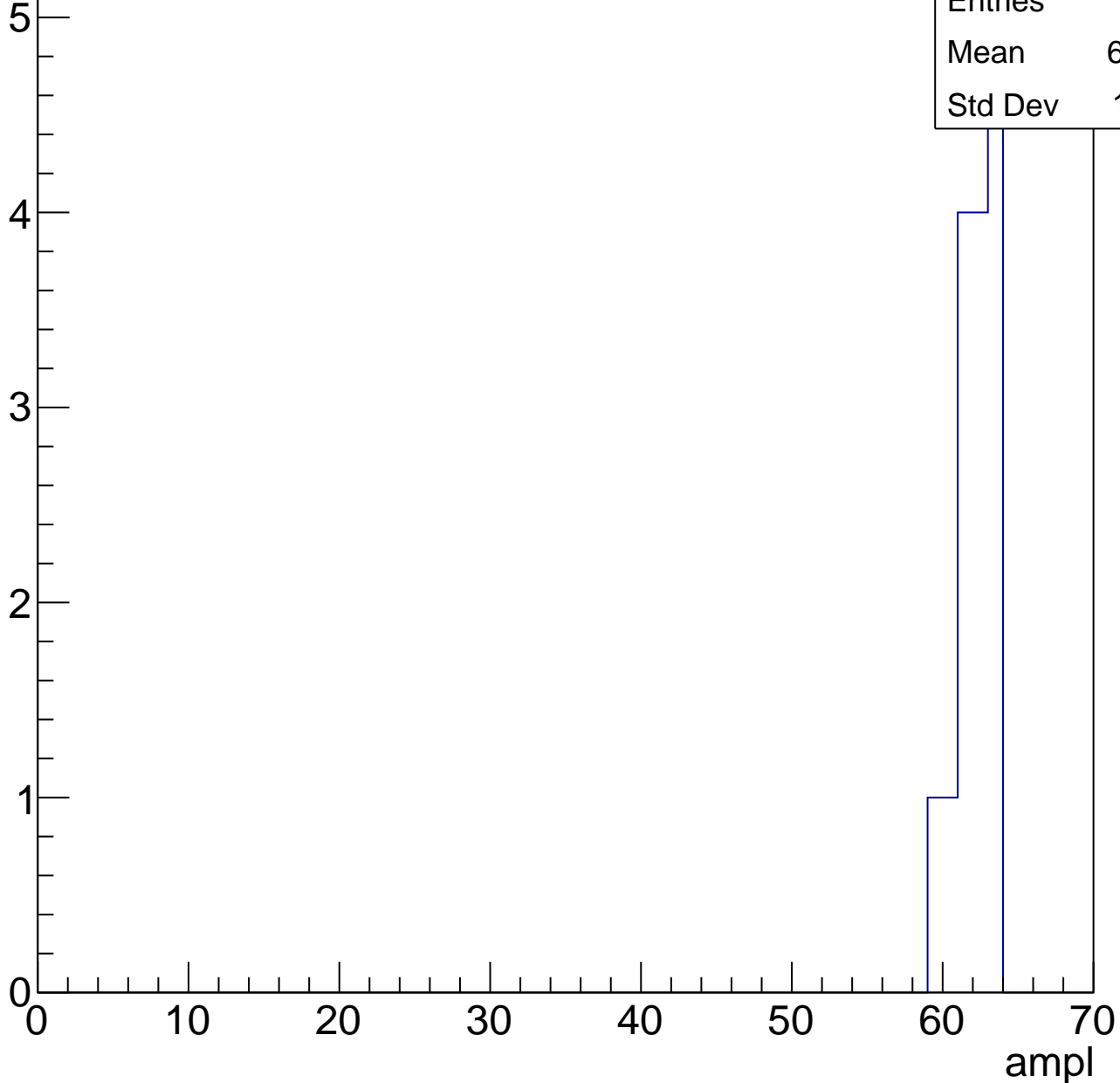


# B0L000S, U18-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	61.73
Std Dev	1.181





# B0L000S, U18-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch6, adc0

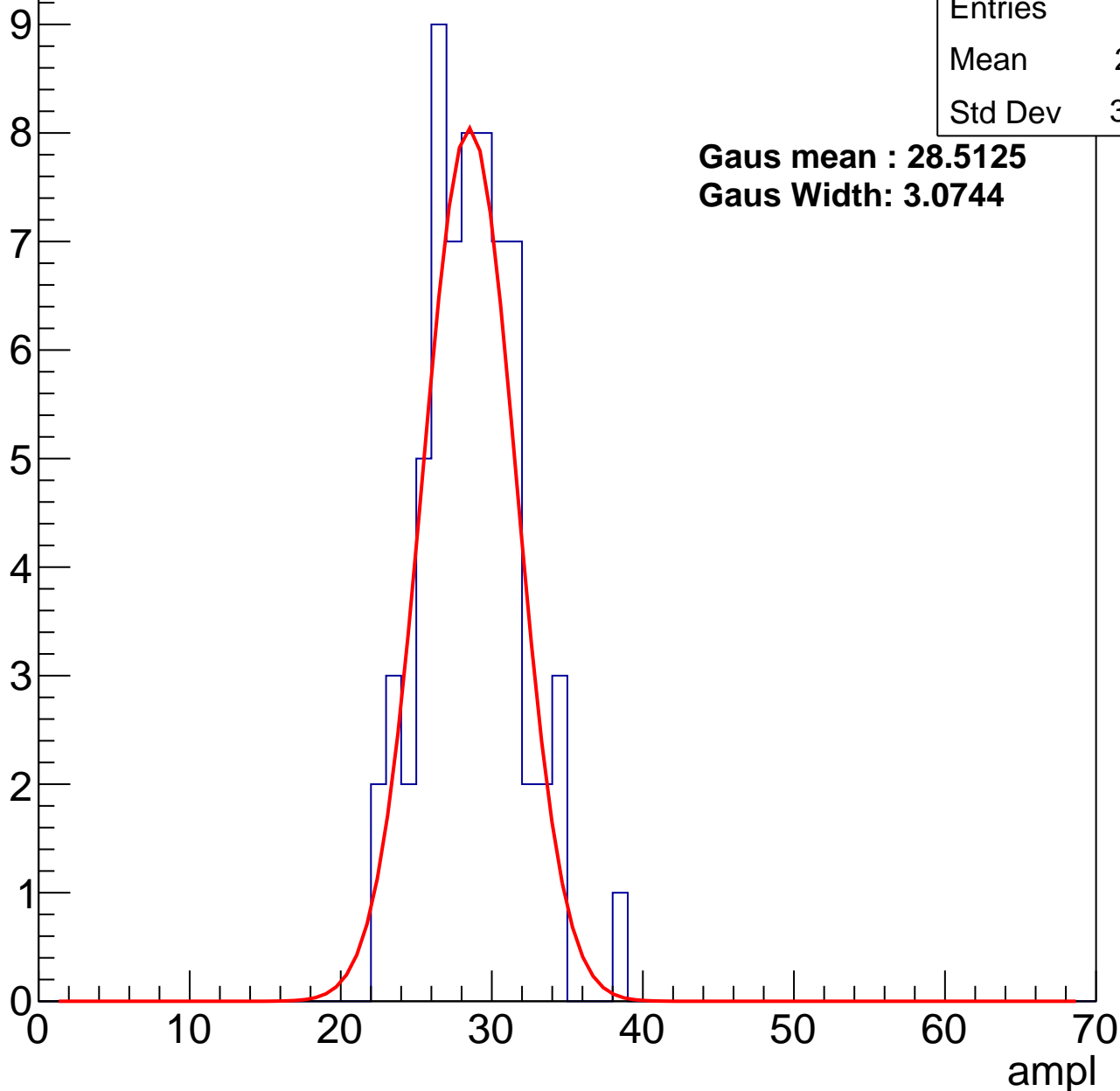
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.21
Std Dev	3.146

**Gaus mean : 28.5125**

**Gaus Width: 3.0744**



# B0L000S, U18-ch6, adc1

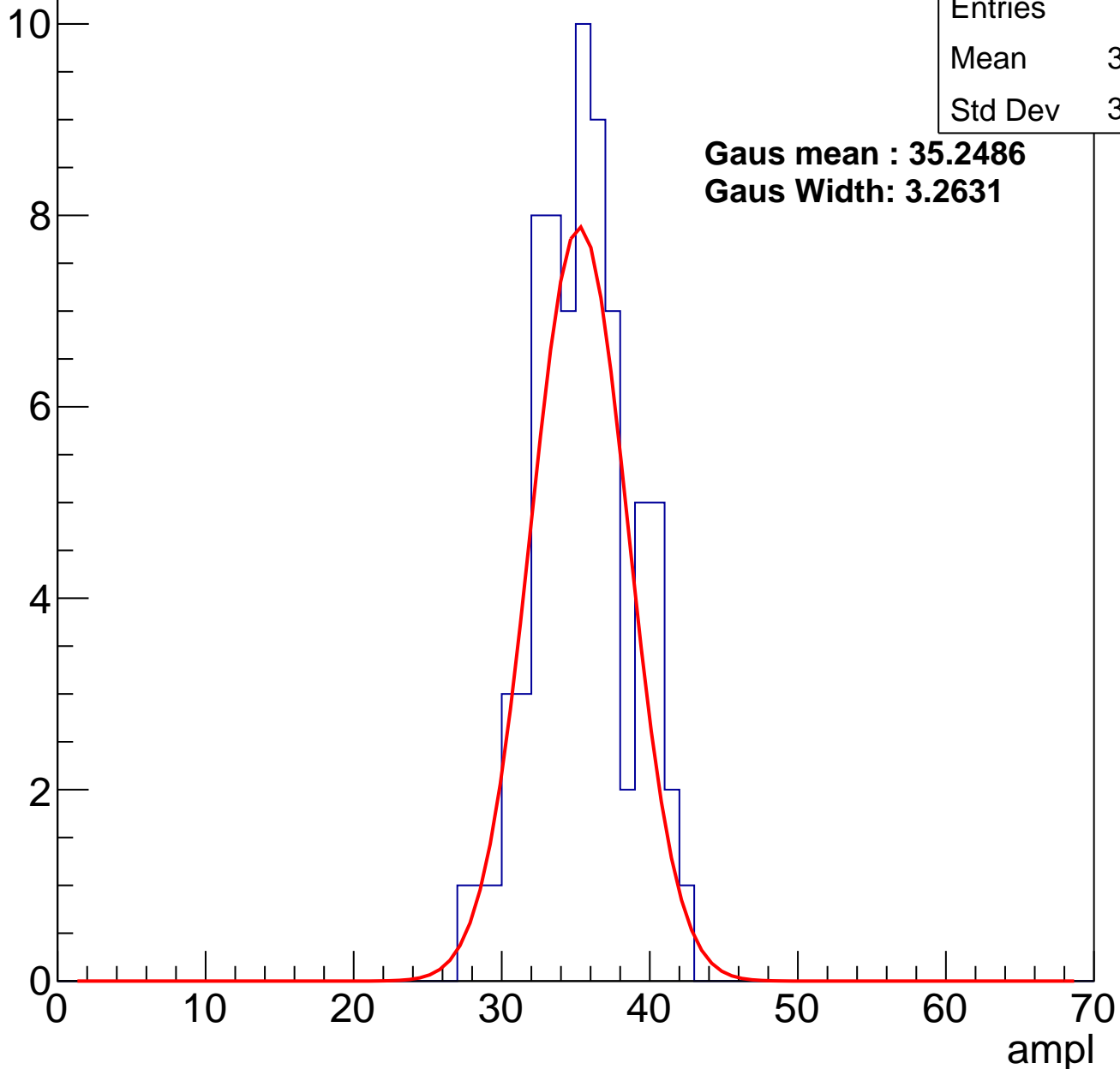
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	34.97
Std Dev	3.235

**Gaus mean : 35.2486**

**Gaus Width: 3.2631**

Entry



# B0L000S, U18-ch6, adc2

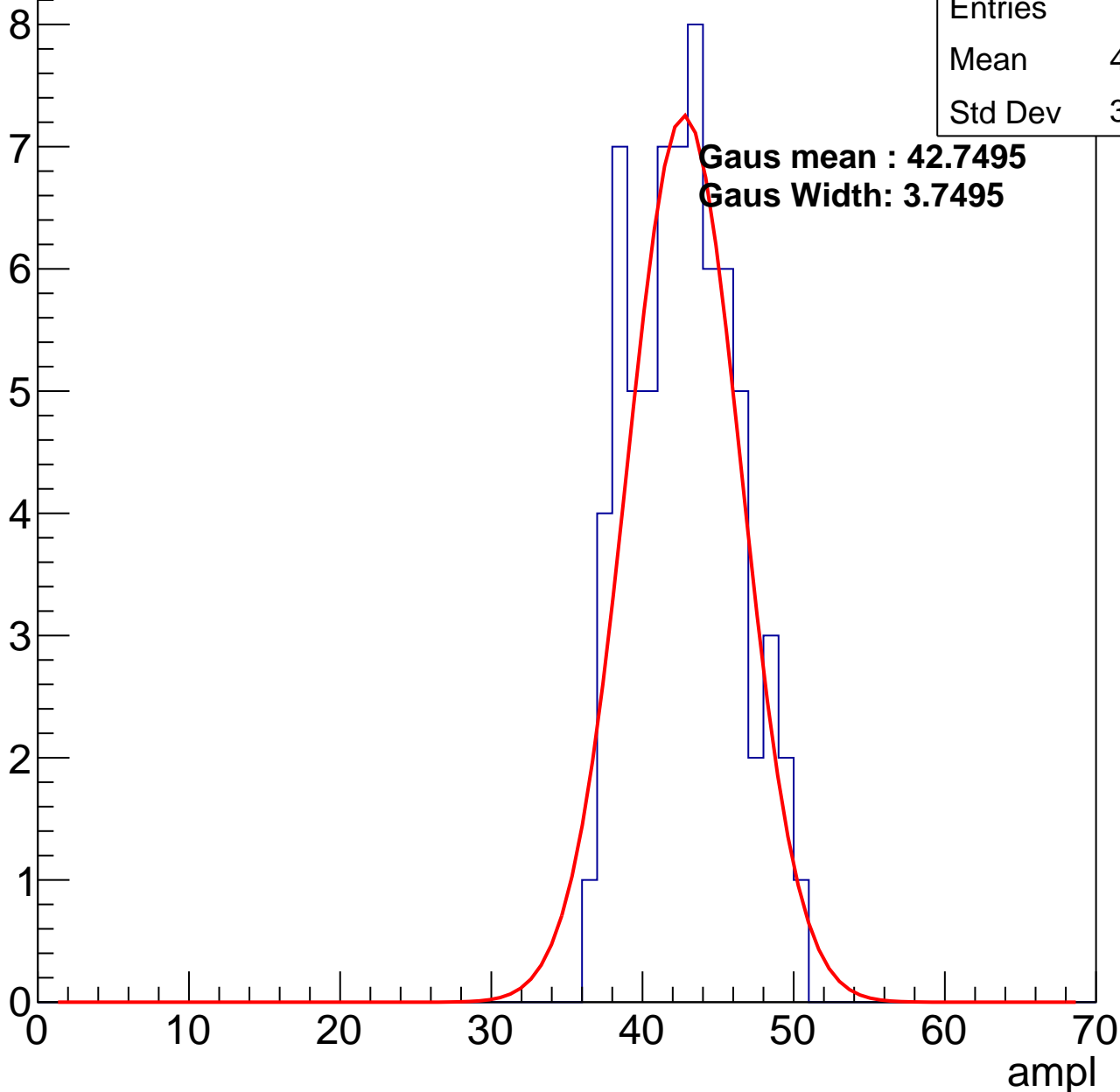
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.32
Std Dev	3.403

**Gaus mean : 42.7495**

**Gaus Width: 3.7495**

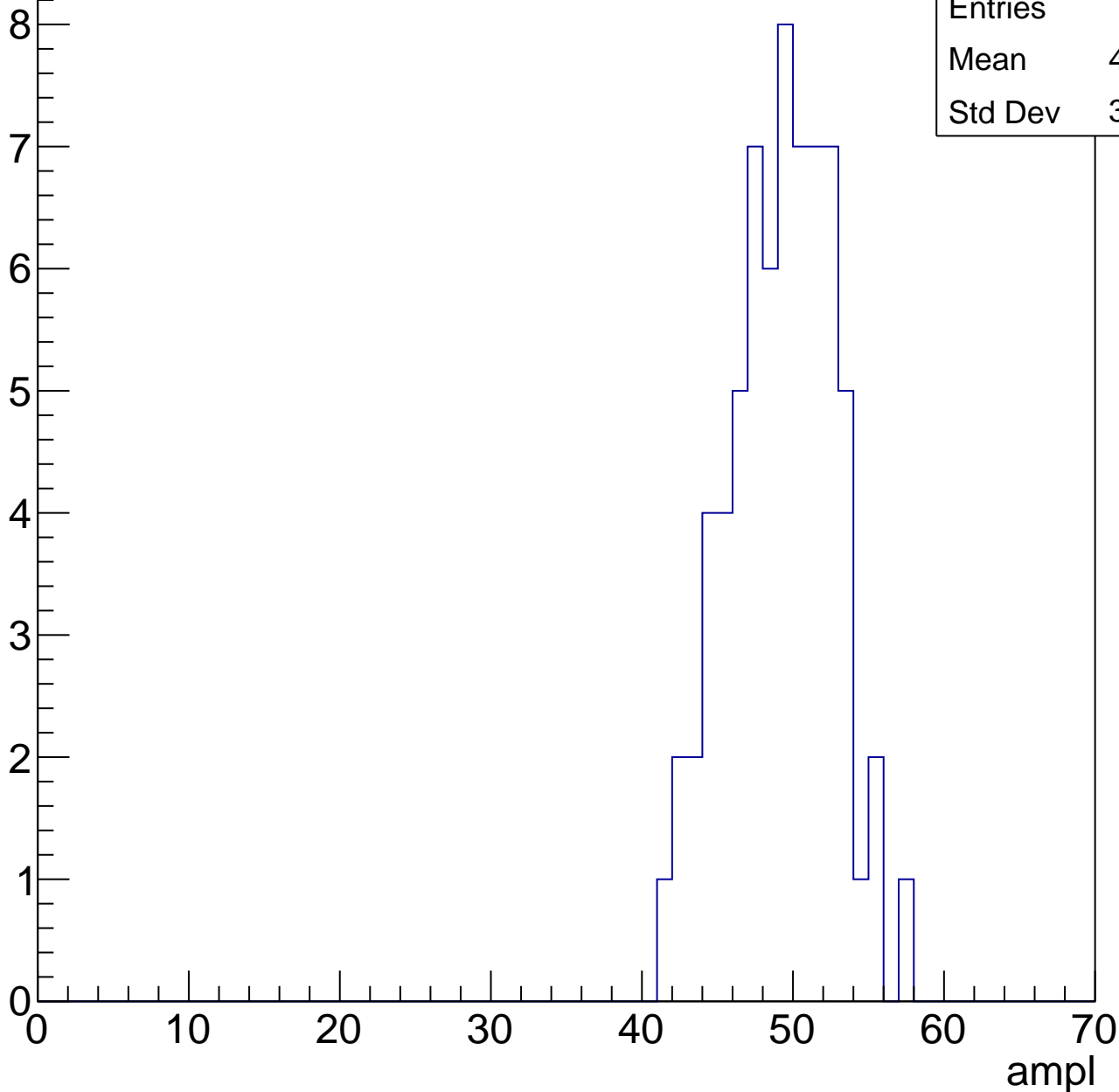


# B0L000S, U18-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	48.74
Std Dev	3.429

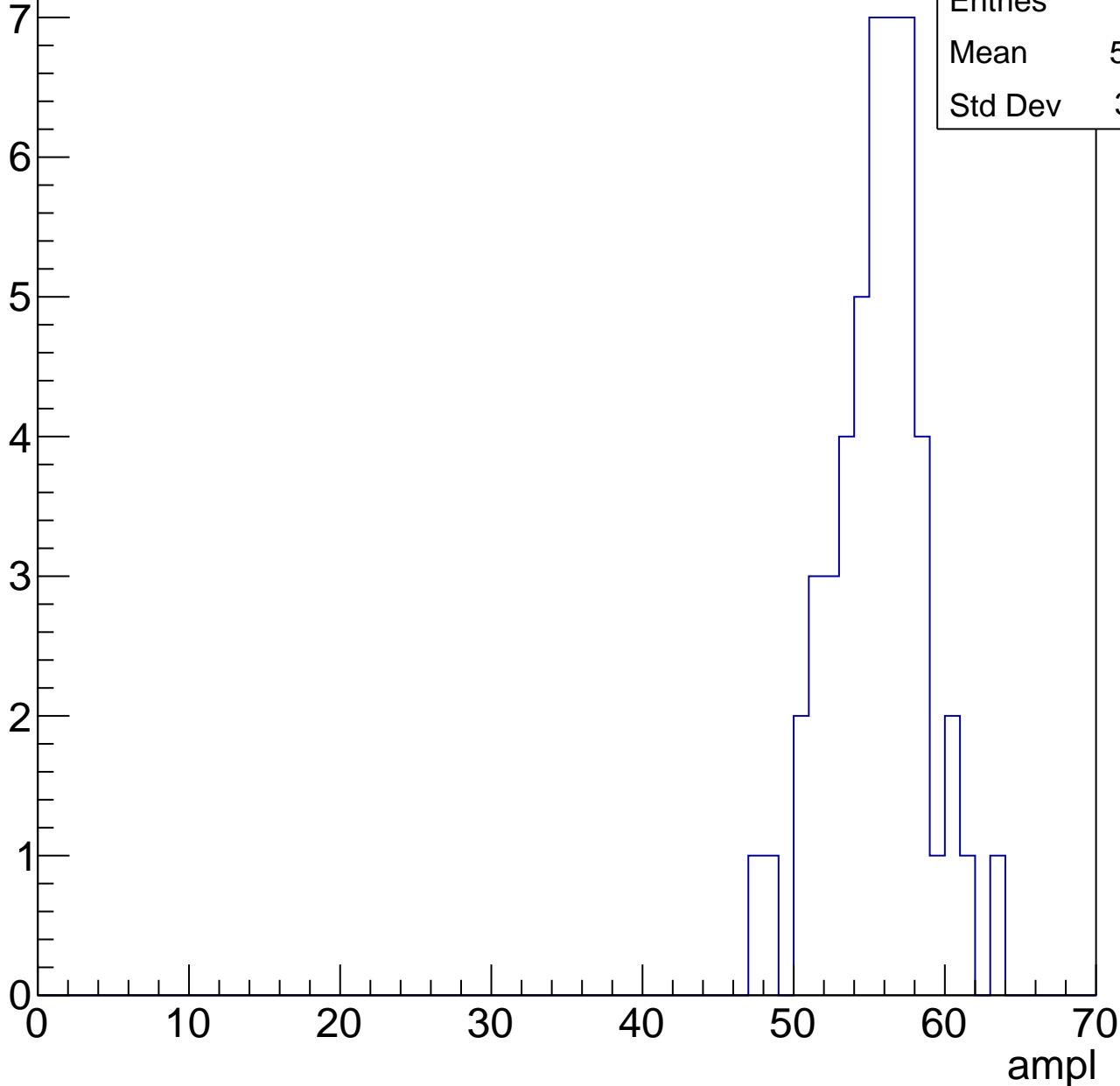


# B0L000S, U18-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	55.04
Std Dev	3.181

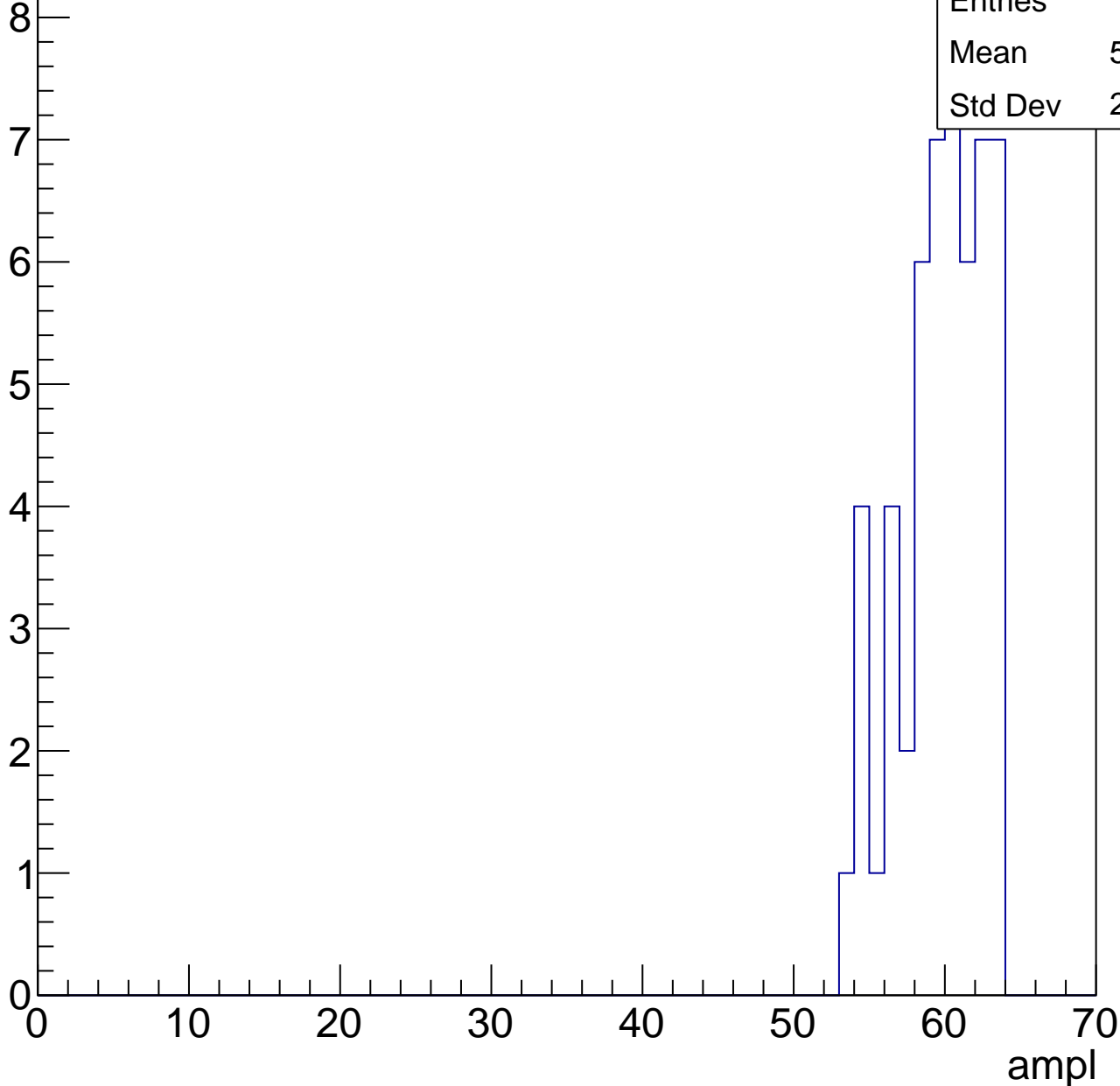


# B0L000S, U18-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

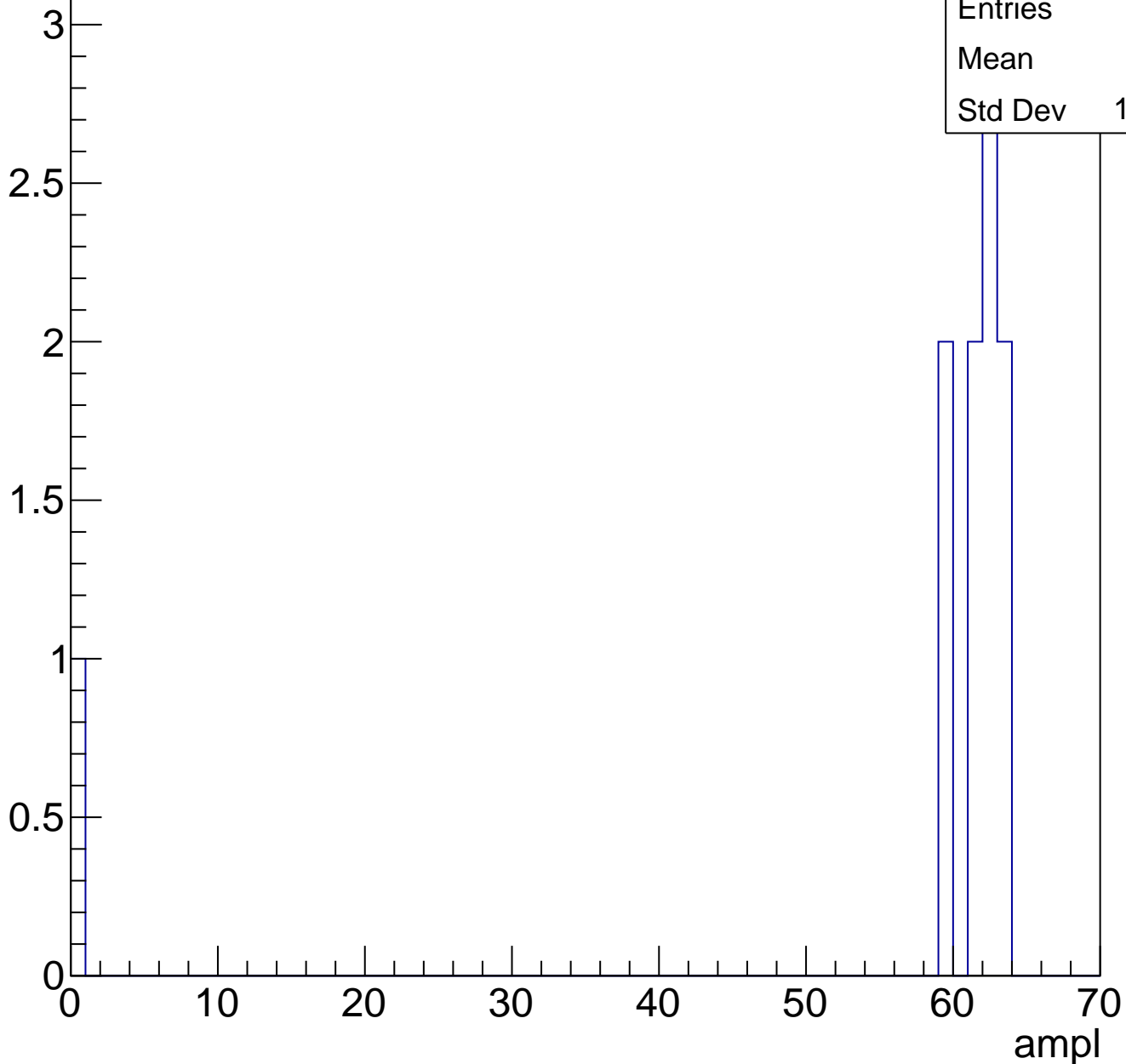
Entries	53
Mean	59.32
Std Dev	2.759



# B0L000S, U18-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

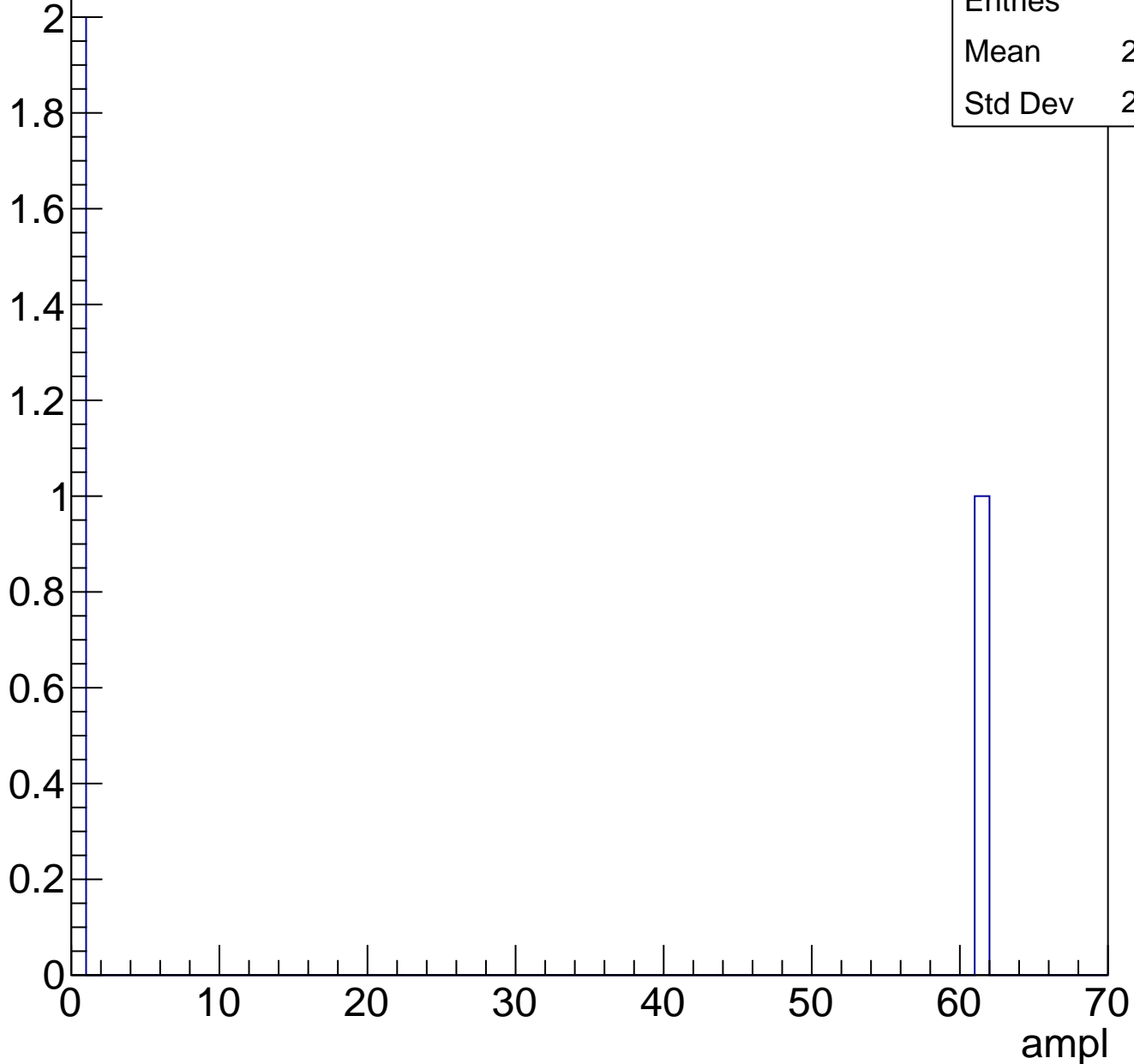




# B0L000S, U18-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch7, adc0

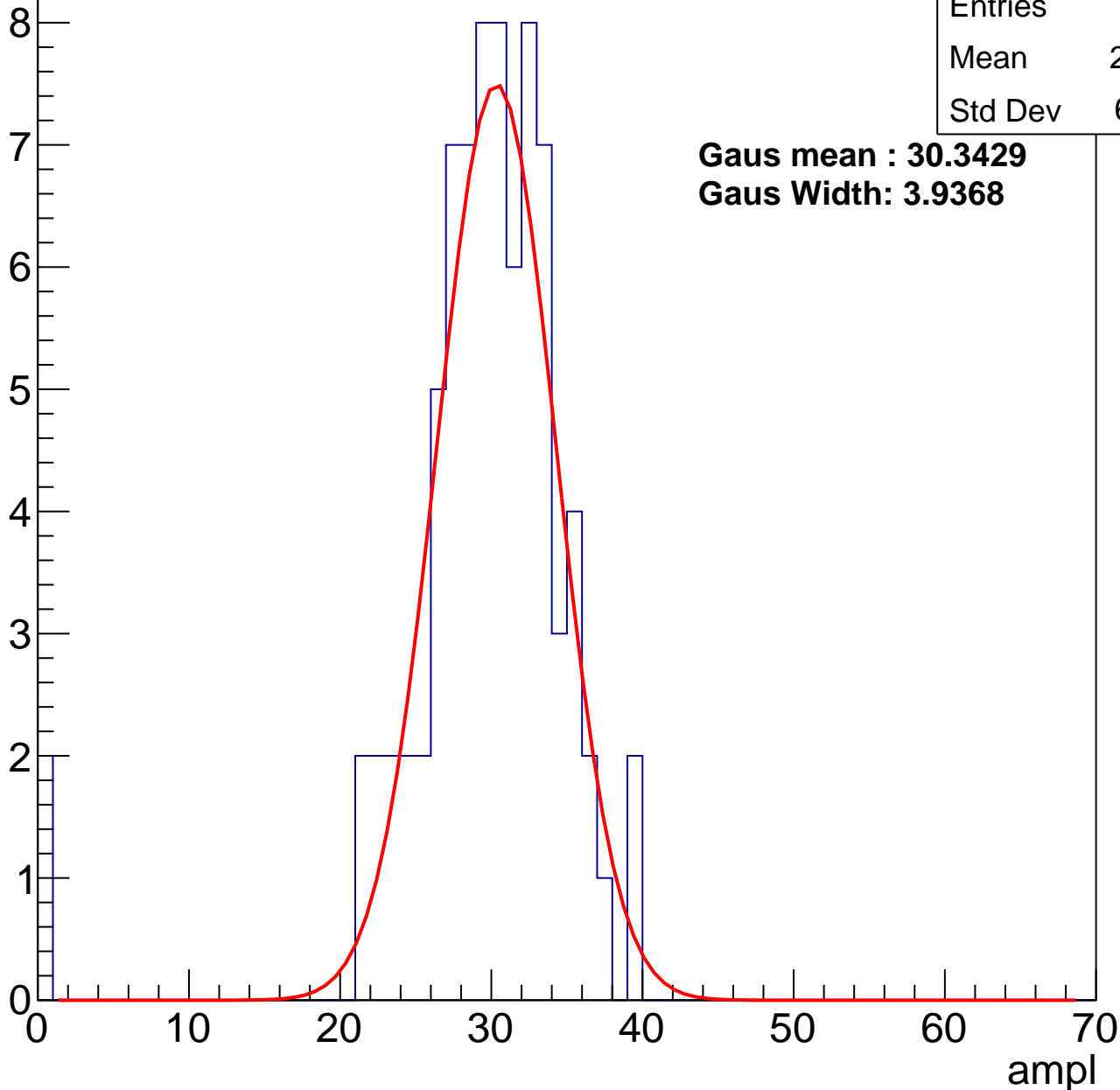
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	28.99
Std Dev	6.061

**Gaus mean : 30.3429**

**Gaus Width: 3.9368**



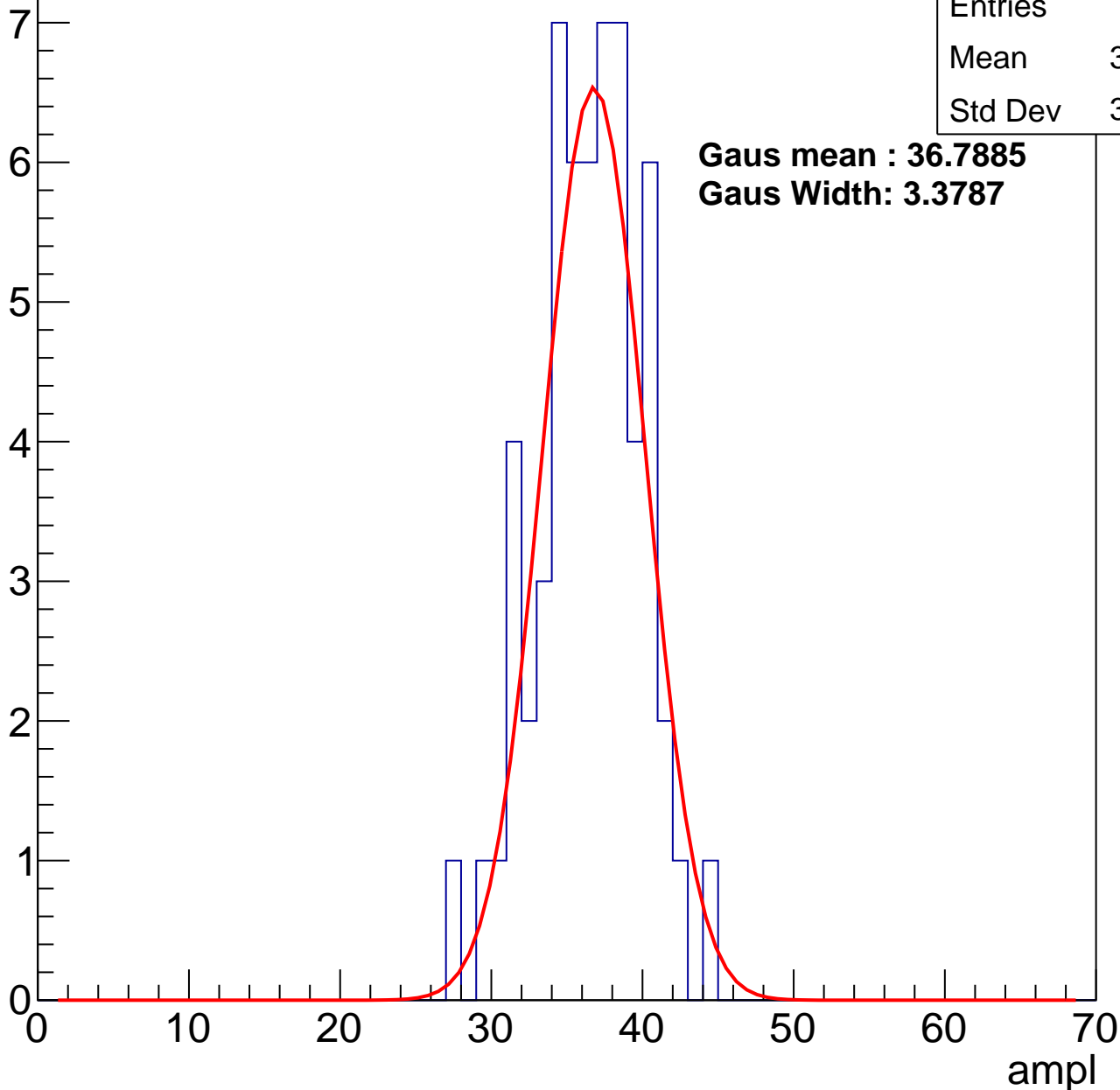
# B0L000S, U18-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.03
Std Dev	3.385

**Gaus mean : 36.7885**  
**Gaus Width: 3.3787**



# B0L000S, U18-ch7, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

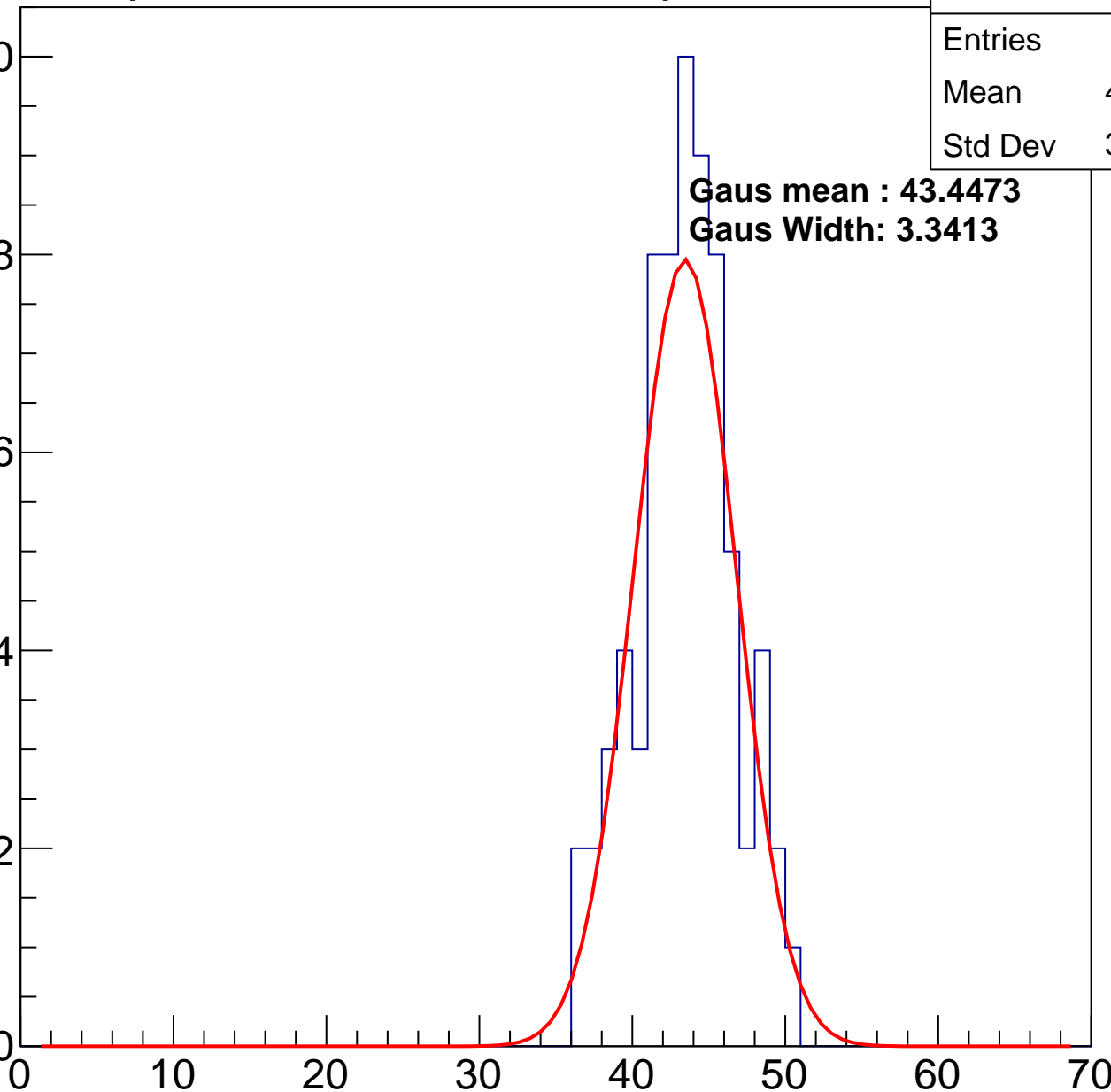
Entries	71
Mean	42.96
Std Dev	3.173

**Gaus mean : 43.4473**

**Gaus Width: 3.3413**

10  
8  
6  
4  
2  
0

ampl

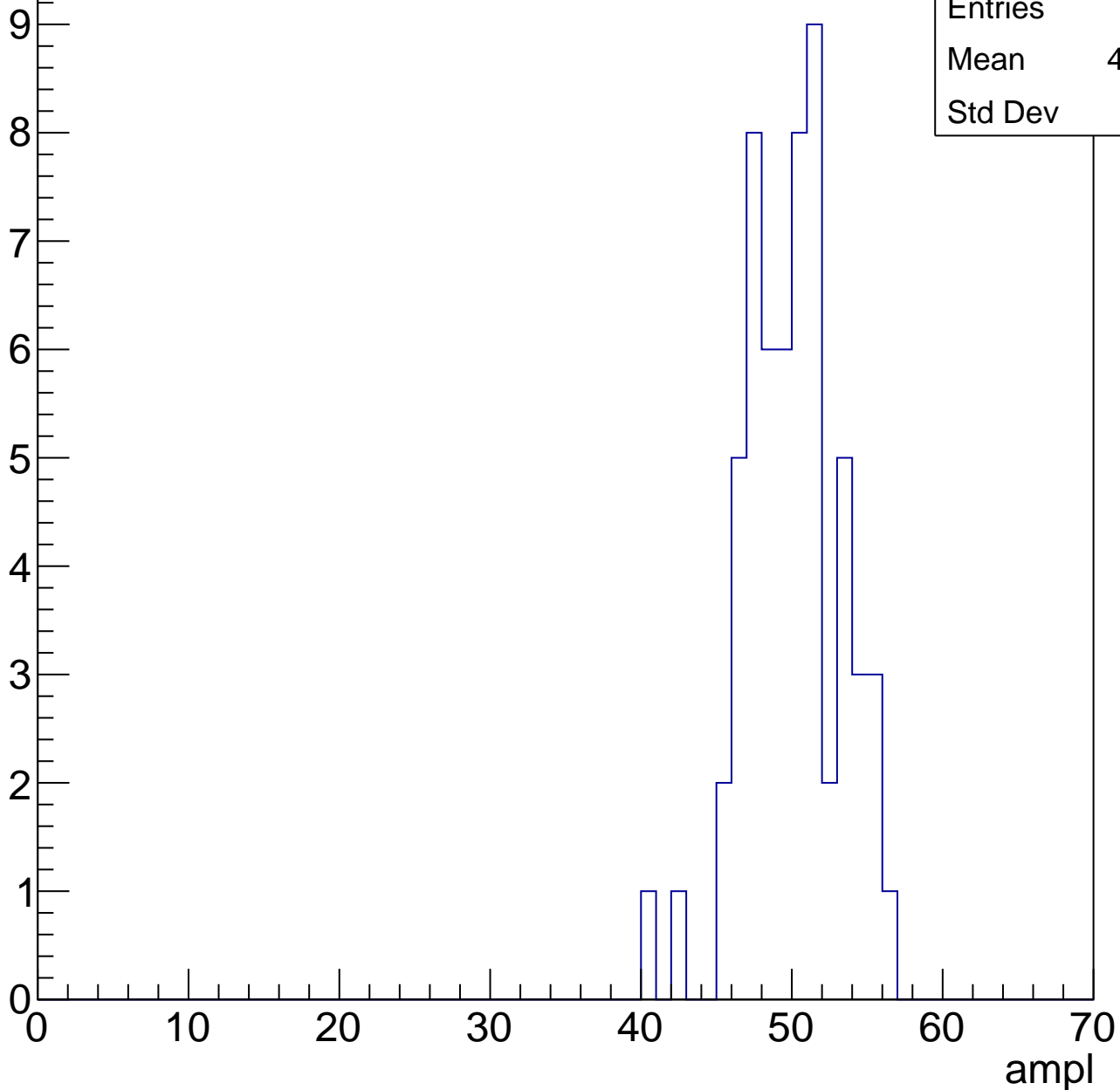


# B0L000S, U18-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.52
Std Dev	3.17

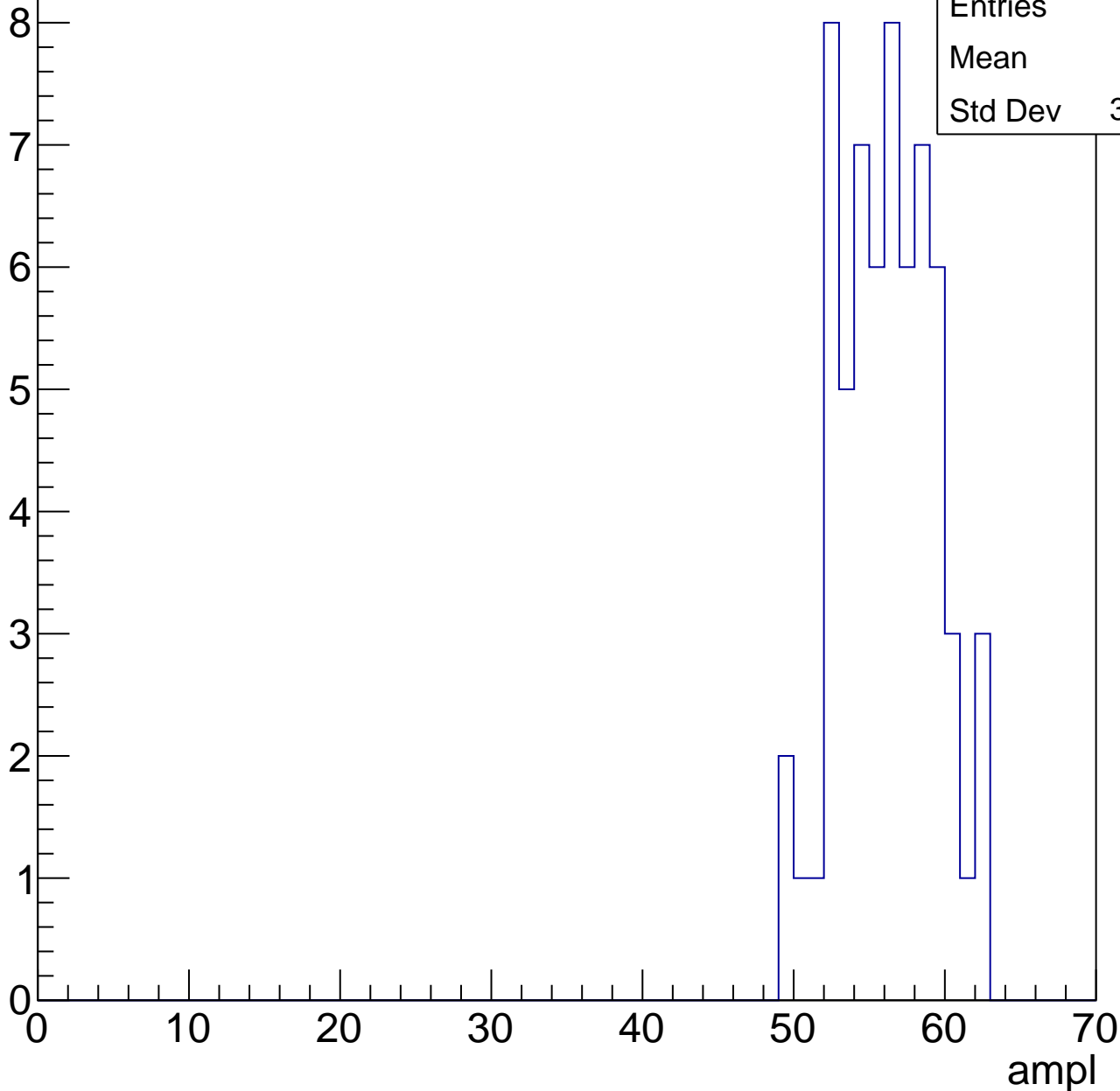


# B0L000S, U18-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	55.7
Std Dev	3.136

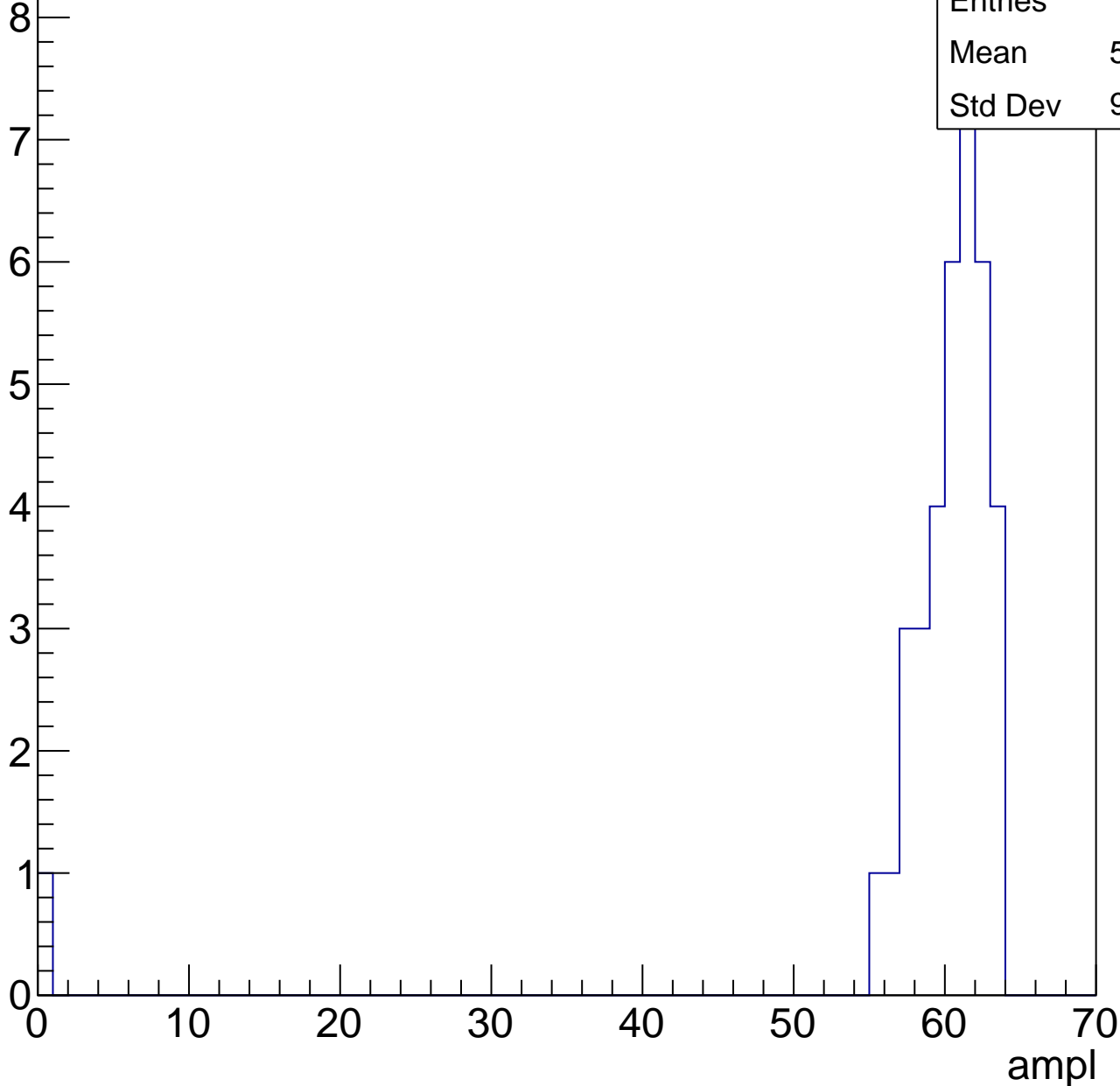


# B0L000S, U18-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.49
Std Dev	9.956

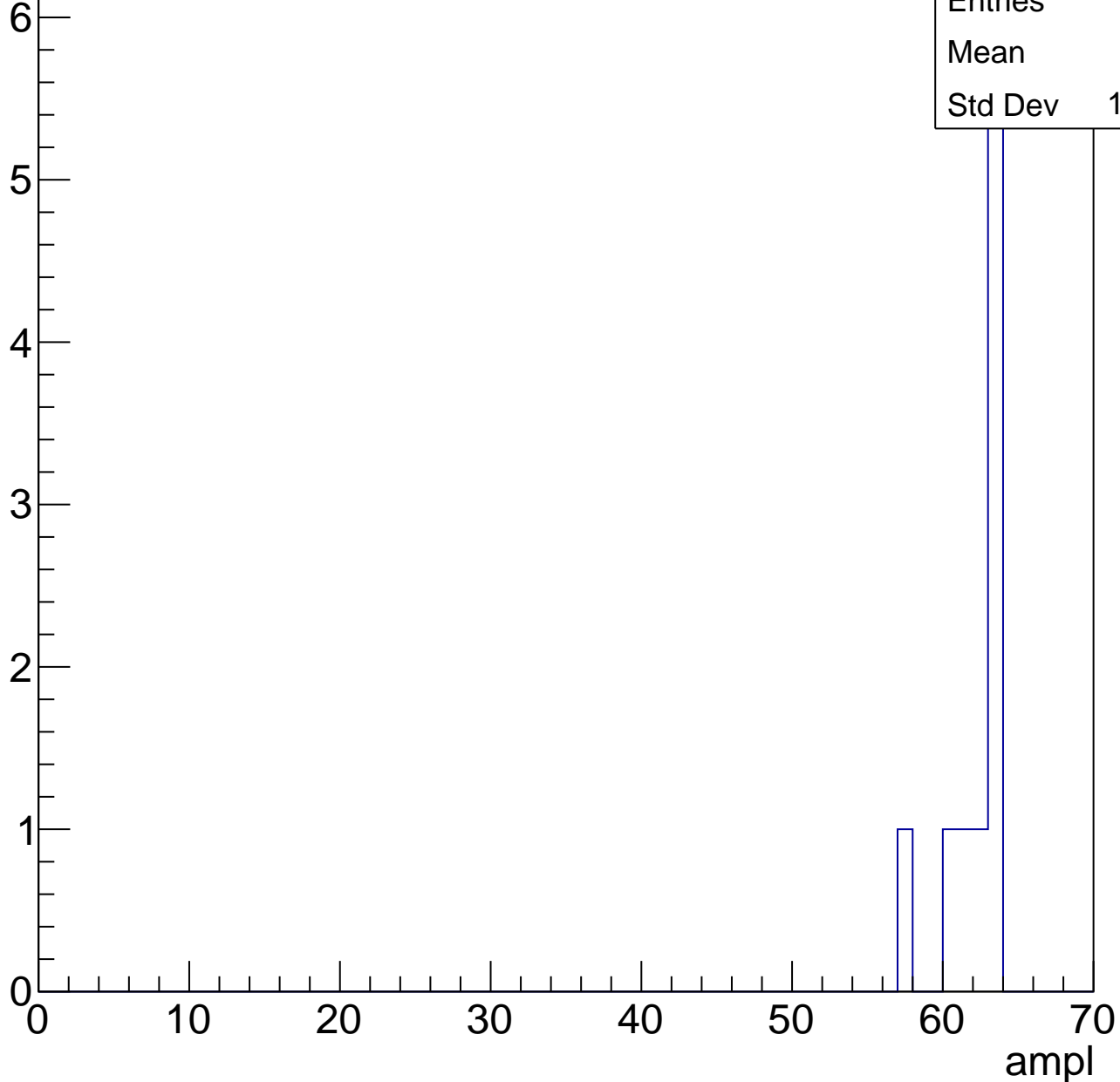


# B0L000S, U18-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	61.8
Std Dev	1.887





# B0L000S, U18-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U18-ch8, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	31.72
Std Dev	2.96

**Gaus mean : 32.0346**

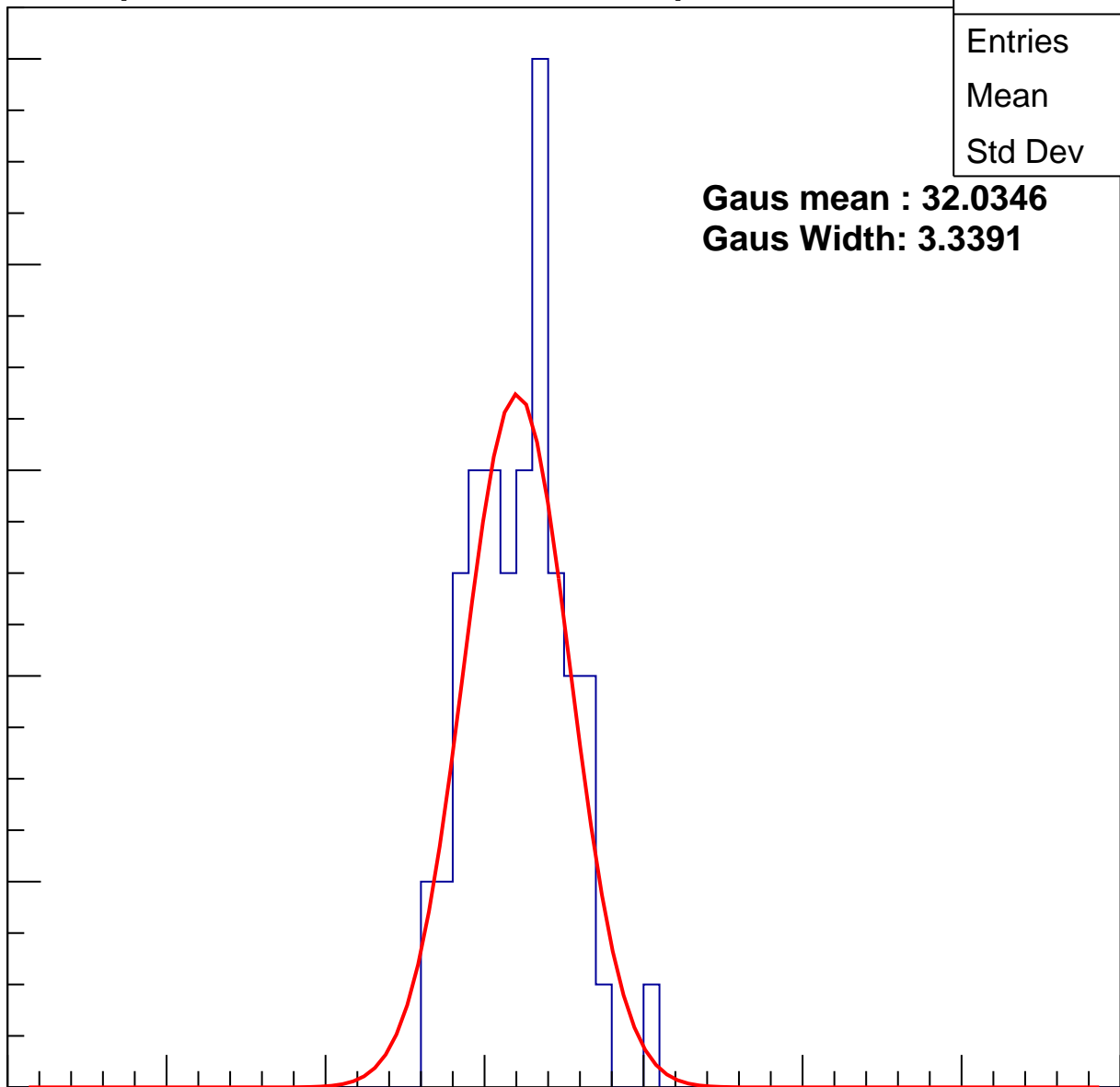
**Gaus Width: 3.3391**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch8, adc1

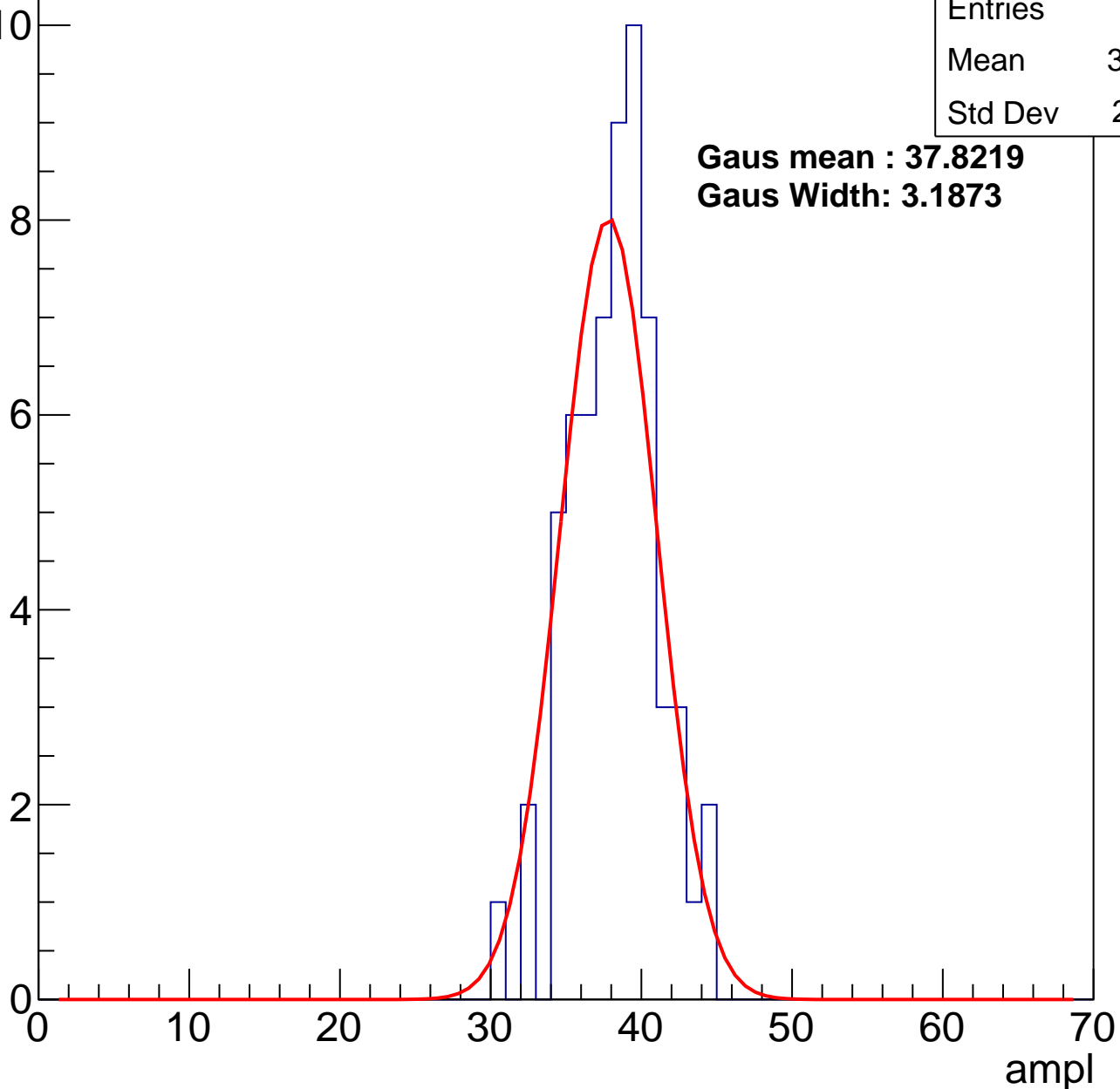
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.76
Std Dev	2.861

**Gaus mean : 37.8219**

**Gaus Width: 3.1873**



# B0L000S, U18-ch8, adc2

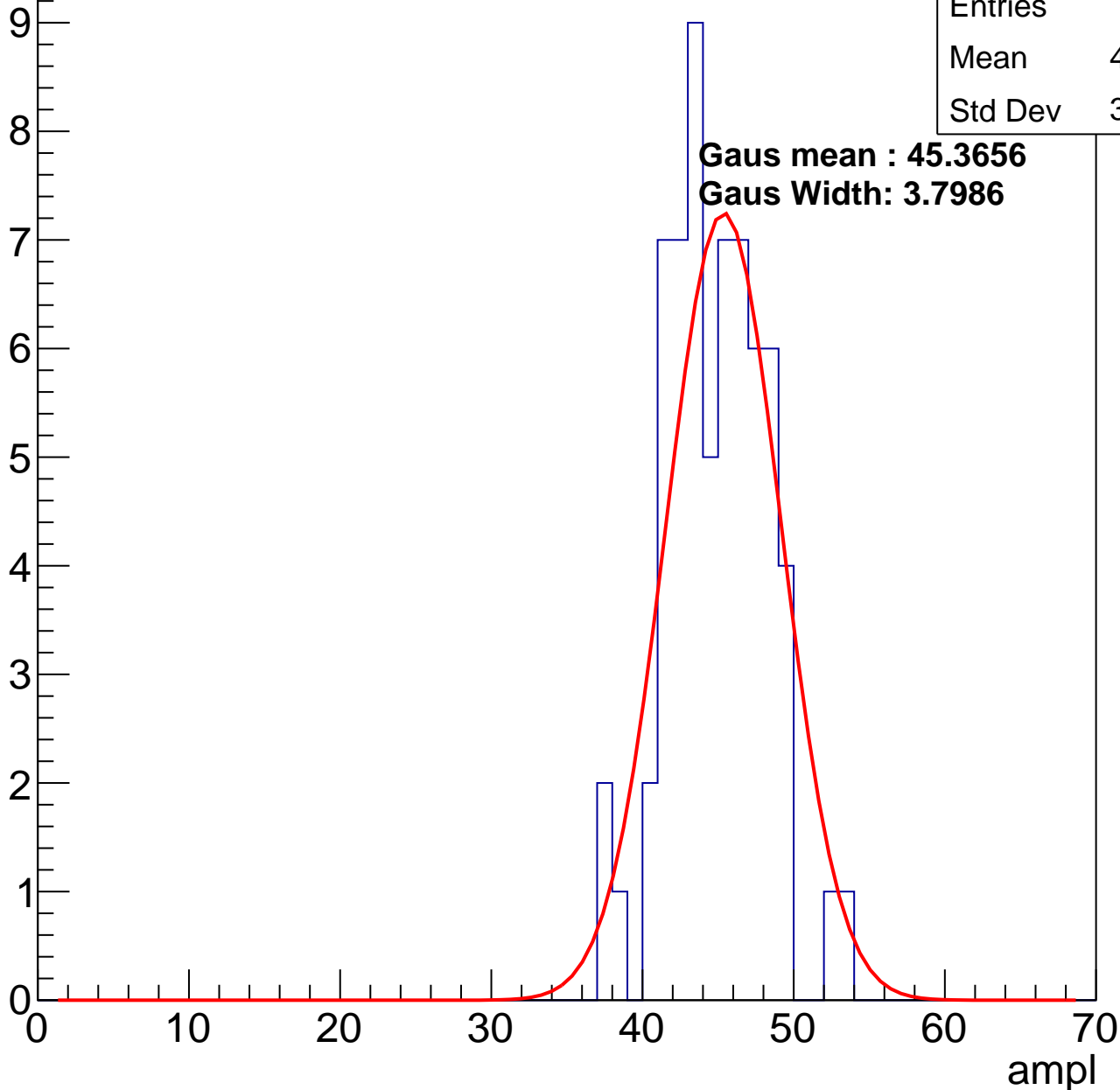
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	44.43
Std Dev	3.244

**Gaus mean : 45.3656**

**Gaus Width: 3.7986**

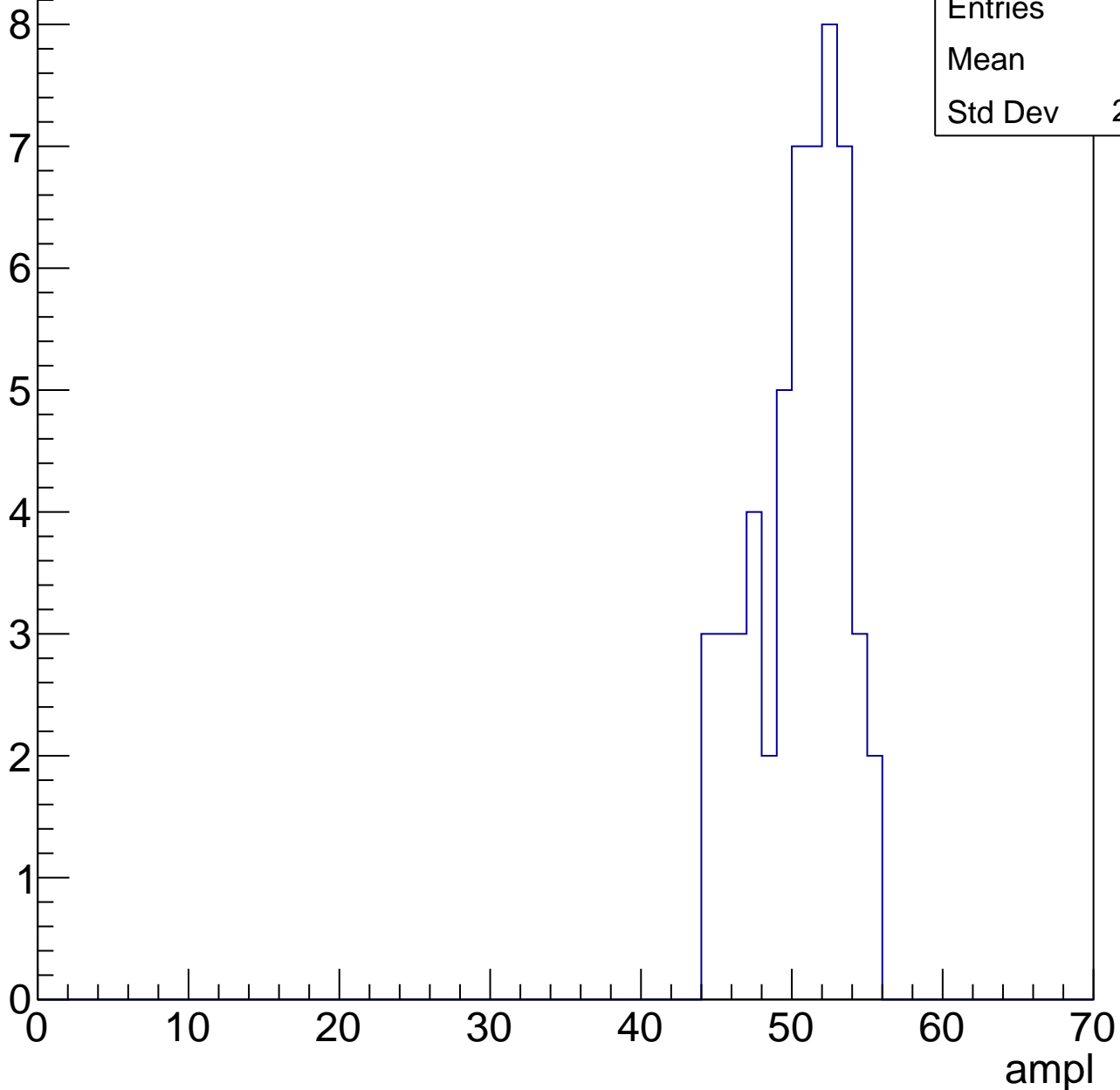


# B0L000S, U18-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	50
Std Dev	2.981

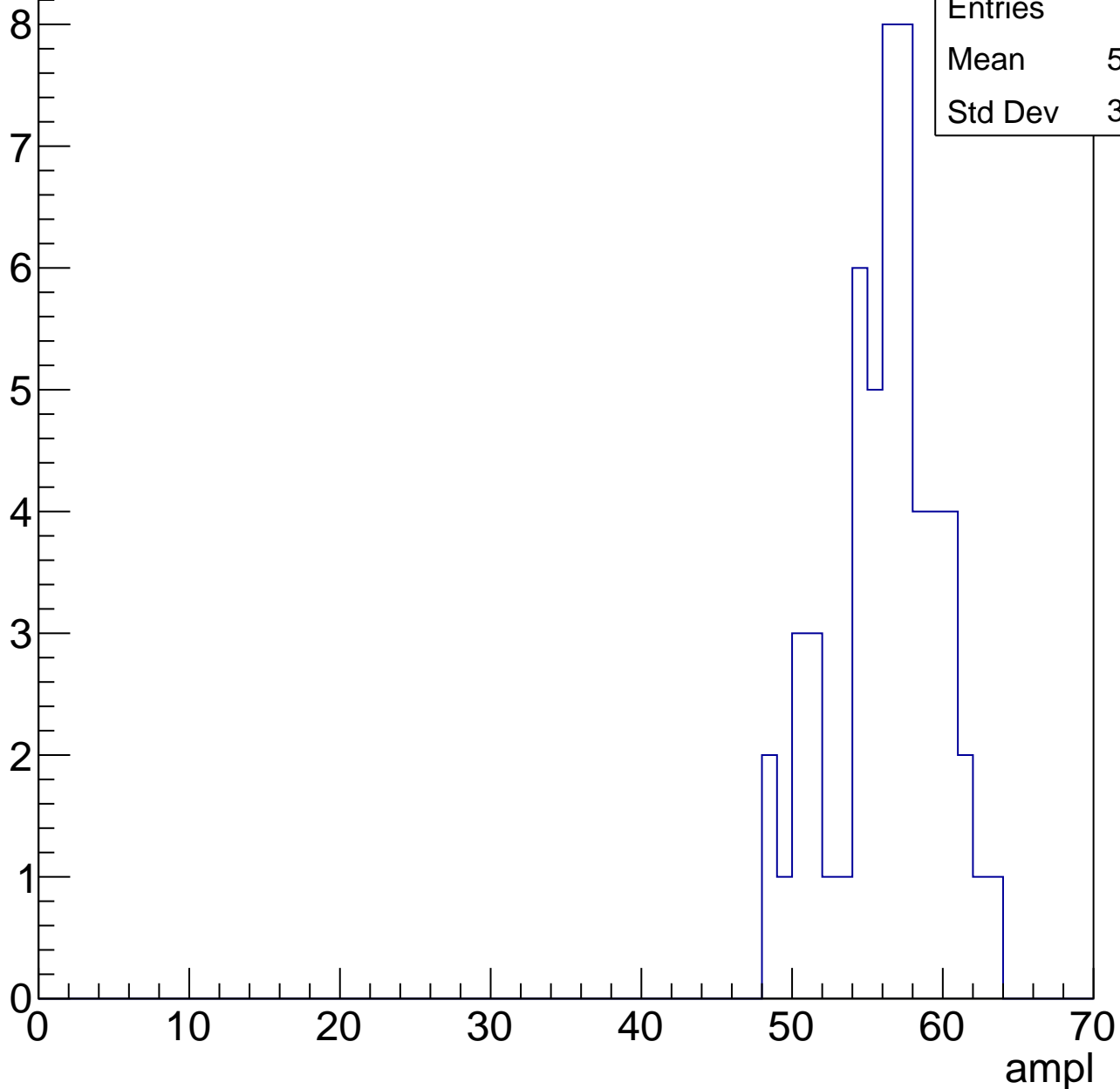


# B0L000S, U18-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.76
Std Dev	3.522

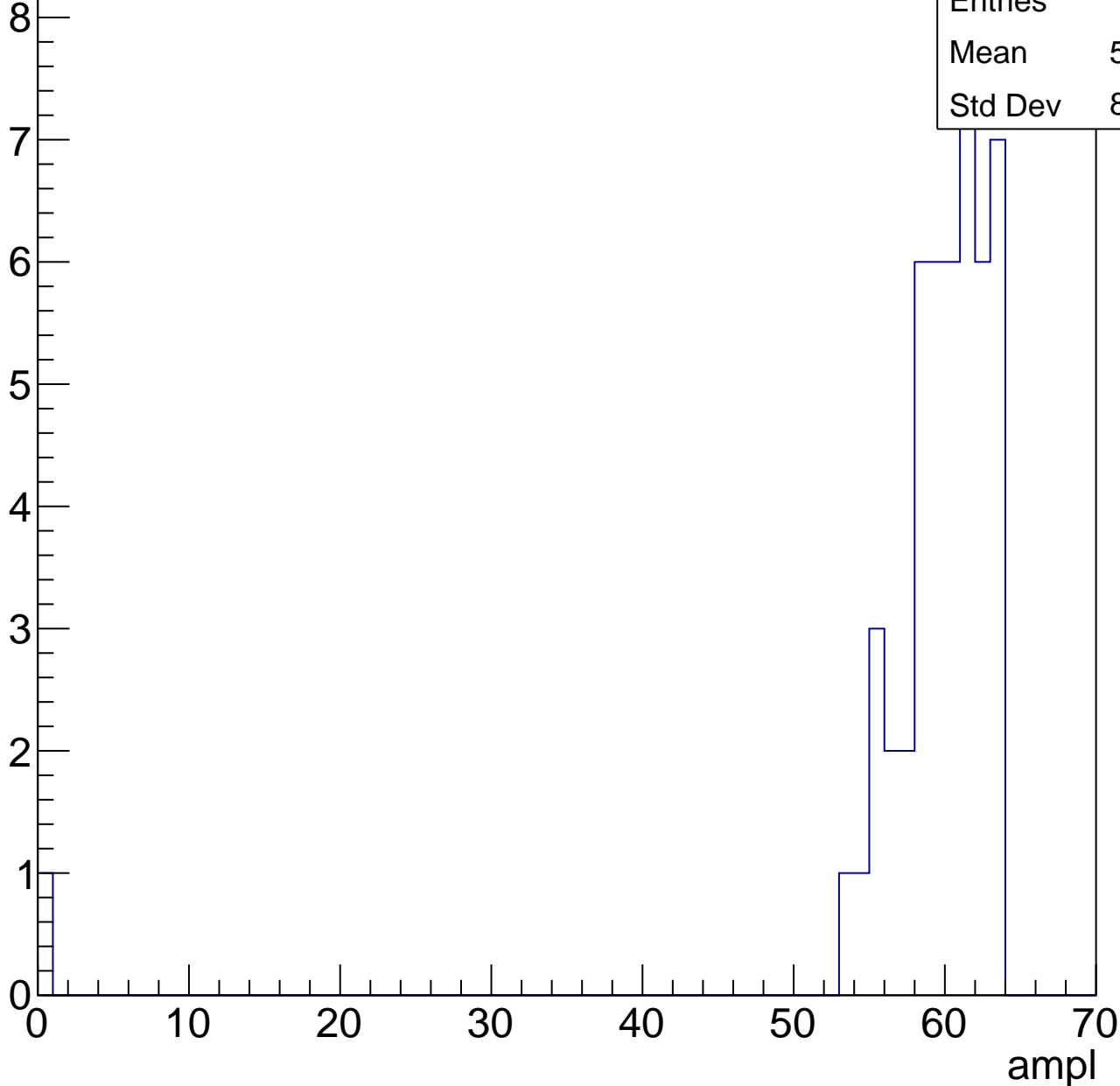


# B0L000S, U18-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.39
Std Dev	8.815



# B0L000S, U18-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

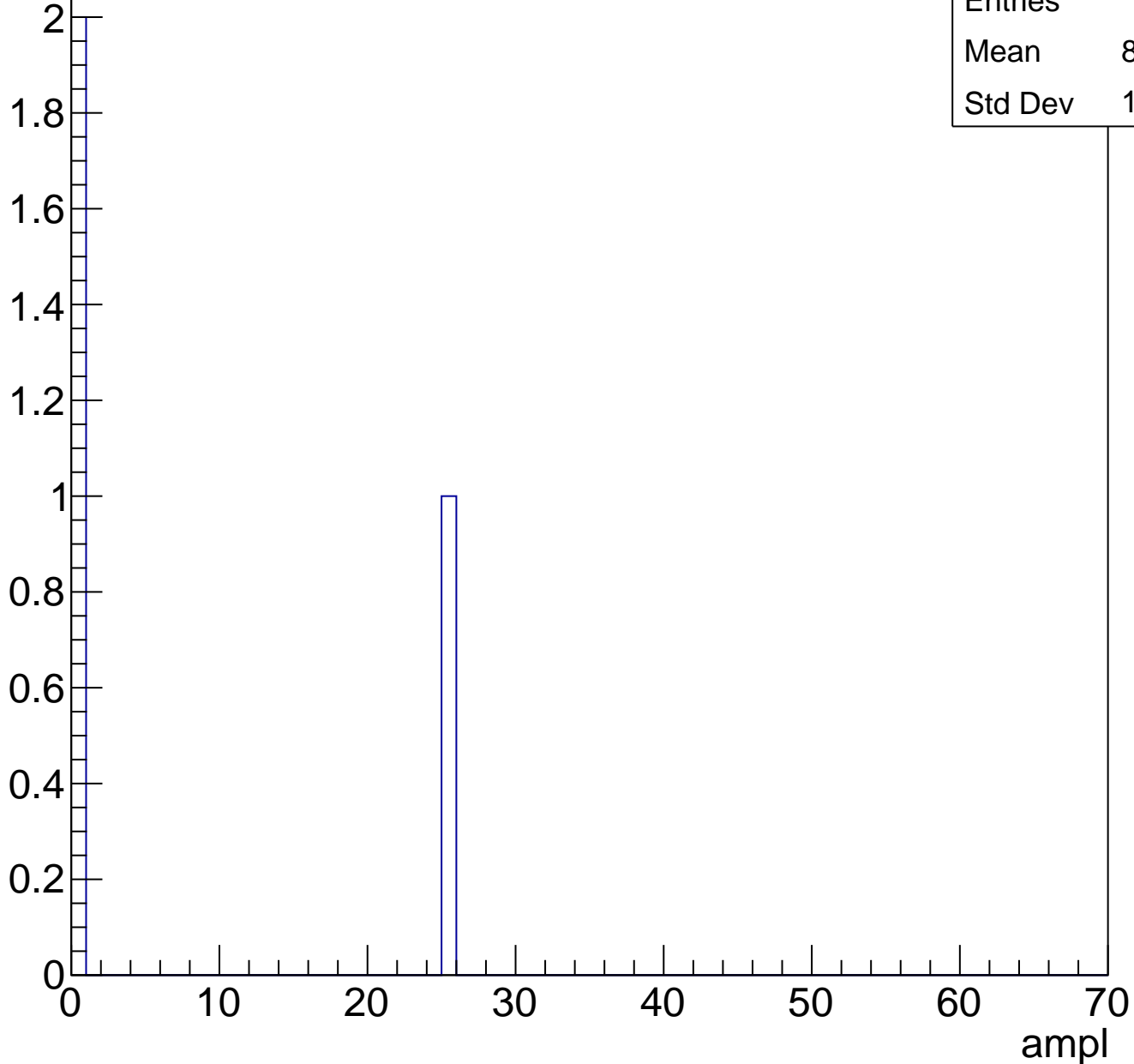




# B0L000S, U18-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

# B0L000S, U18-ch9, adc0

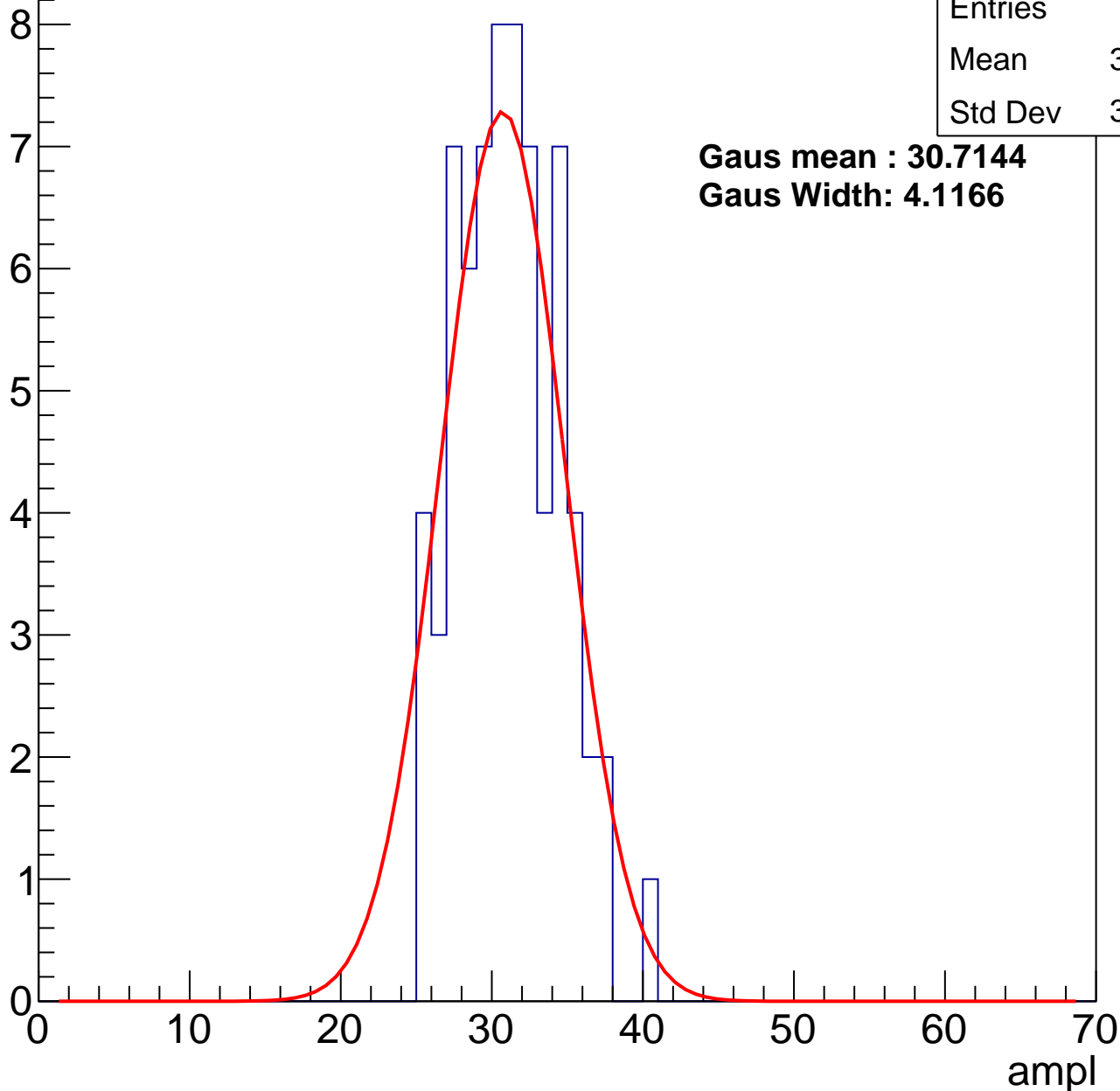
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.66
Std Dev	3.308

**Gaus mean : 30.7144**

**Gaus Width: 4.1166**



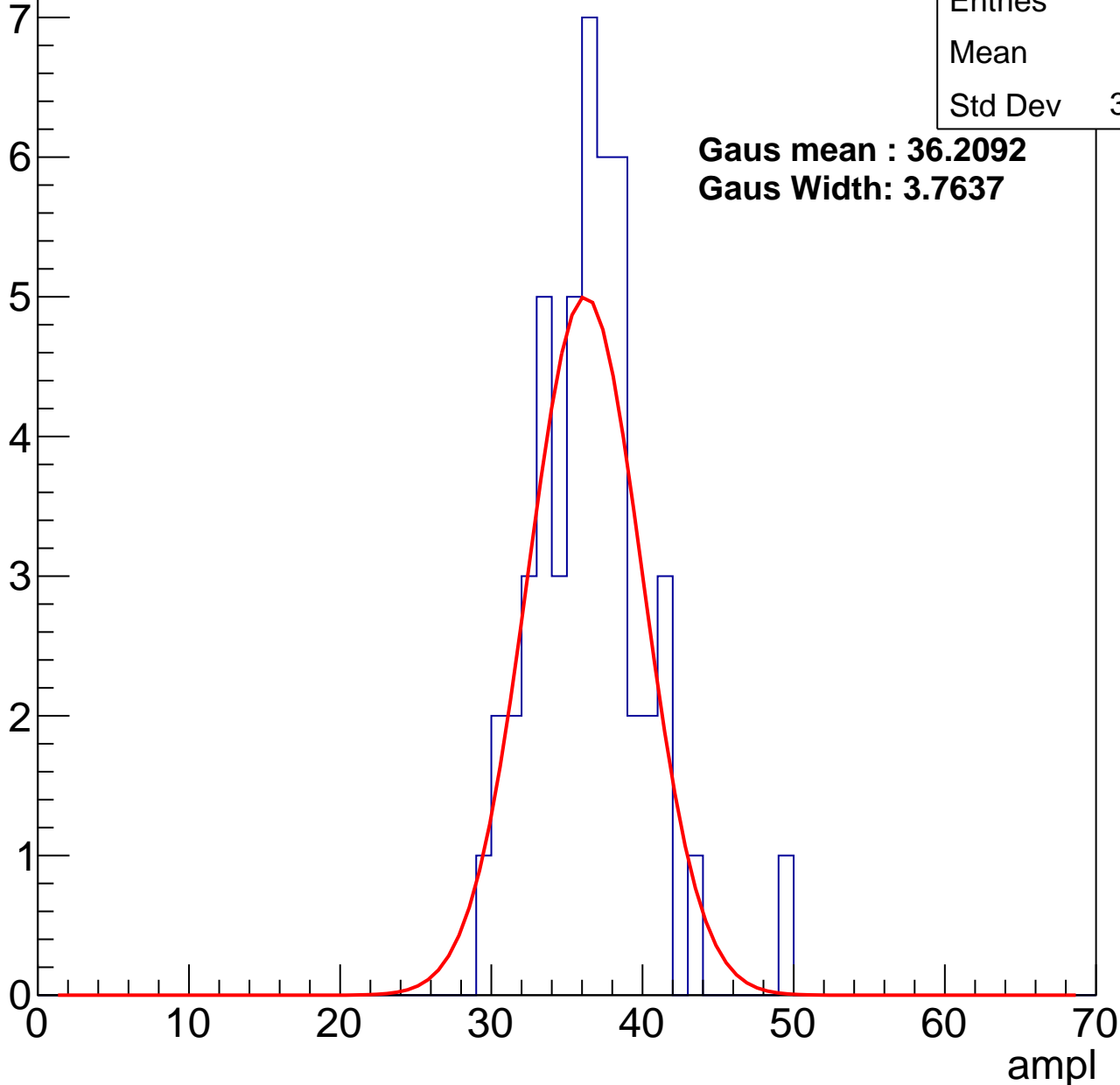
# B0L000S, U18-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36
Std Dev	3.653

**Gaus mean : 36.2092**  
**Gaus Width: 3.7637**



# B0L000S, U18-ch9, adc2

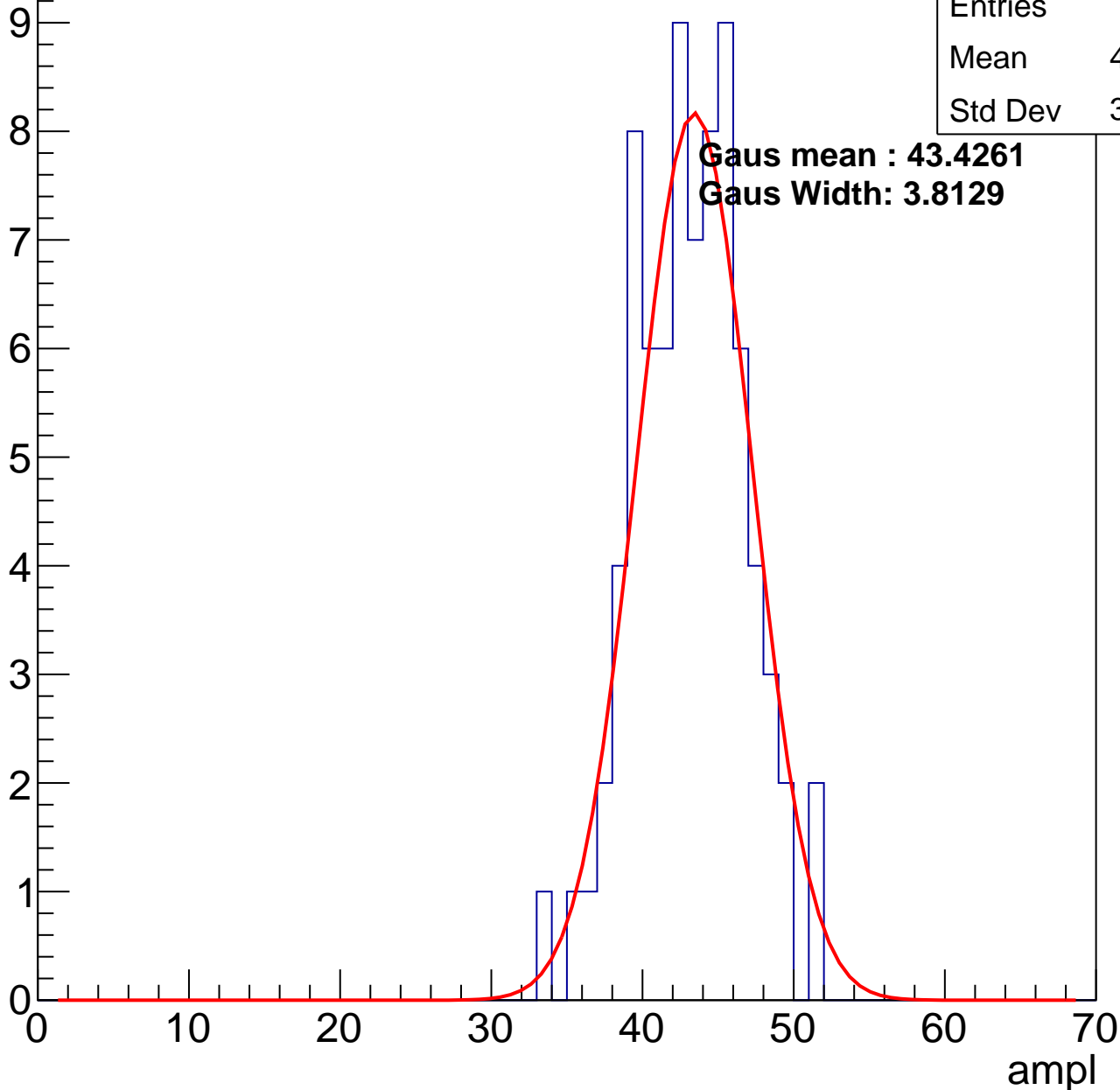
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	42.68
Std Dev	3.609

**Gaus mean : 43.4261**

**Gaus Width: 3.8129**



# B0L000S, U18-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

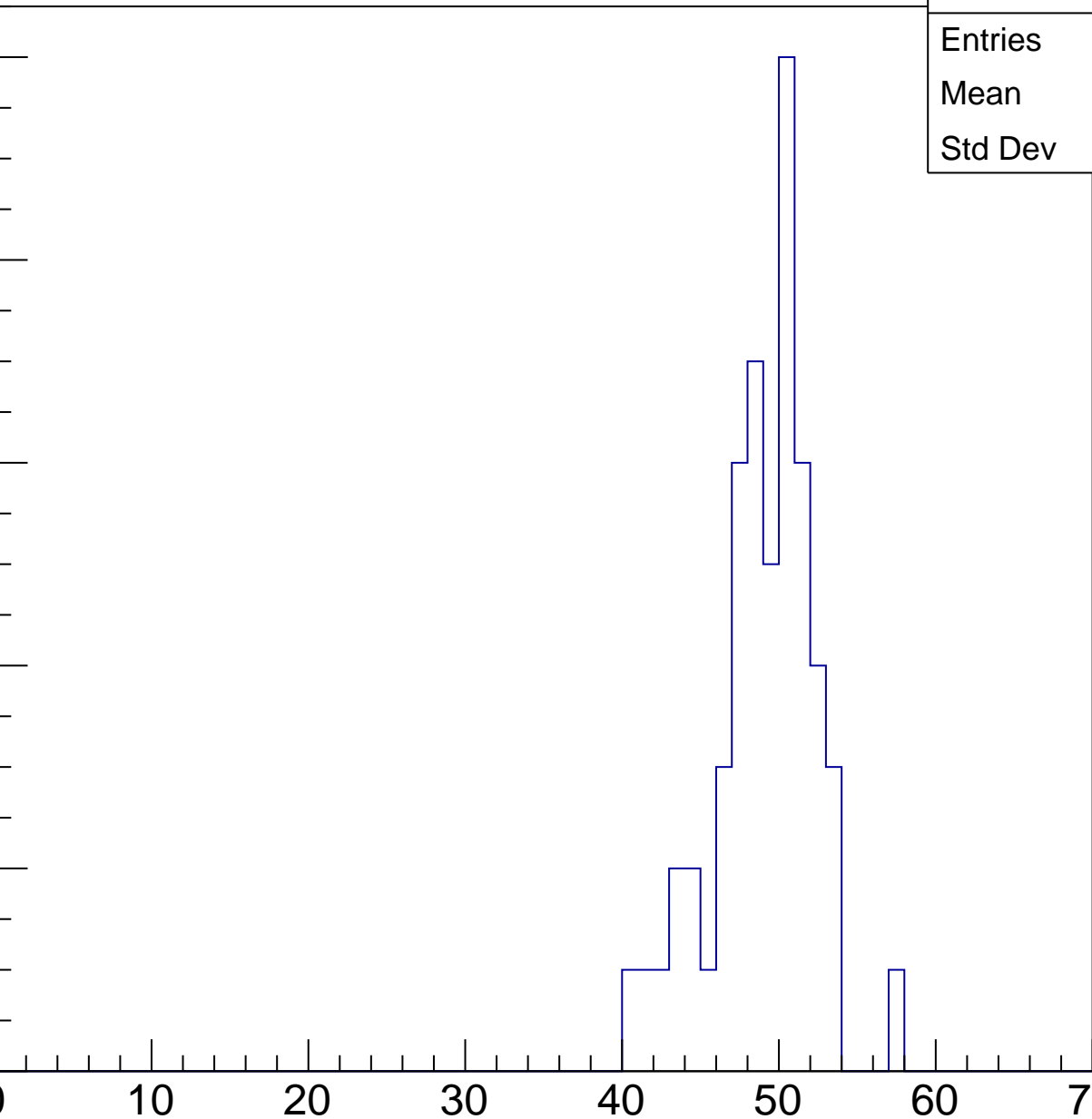
Entries	53
Mean	48.55
Std Dev	3.254

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

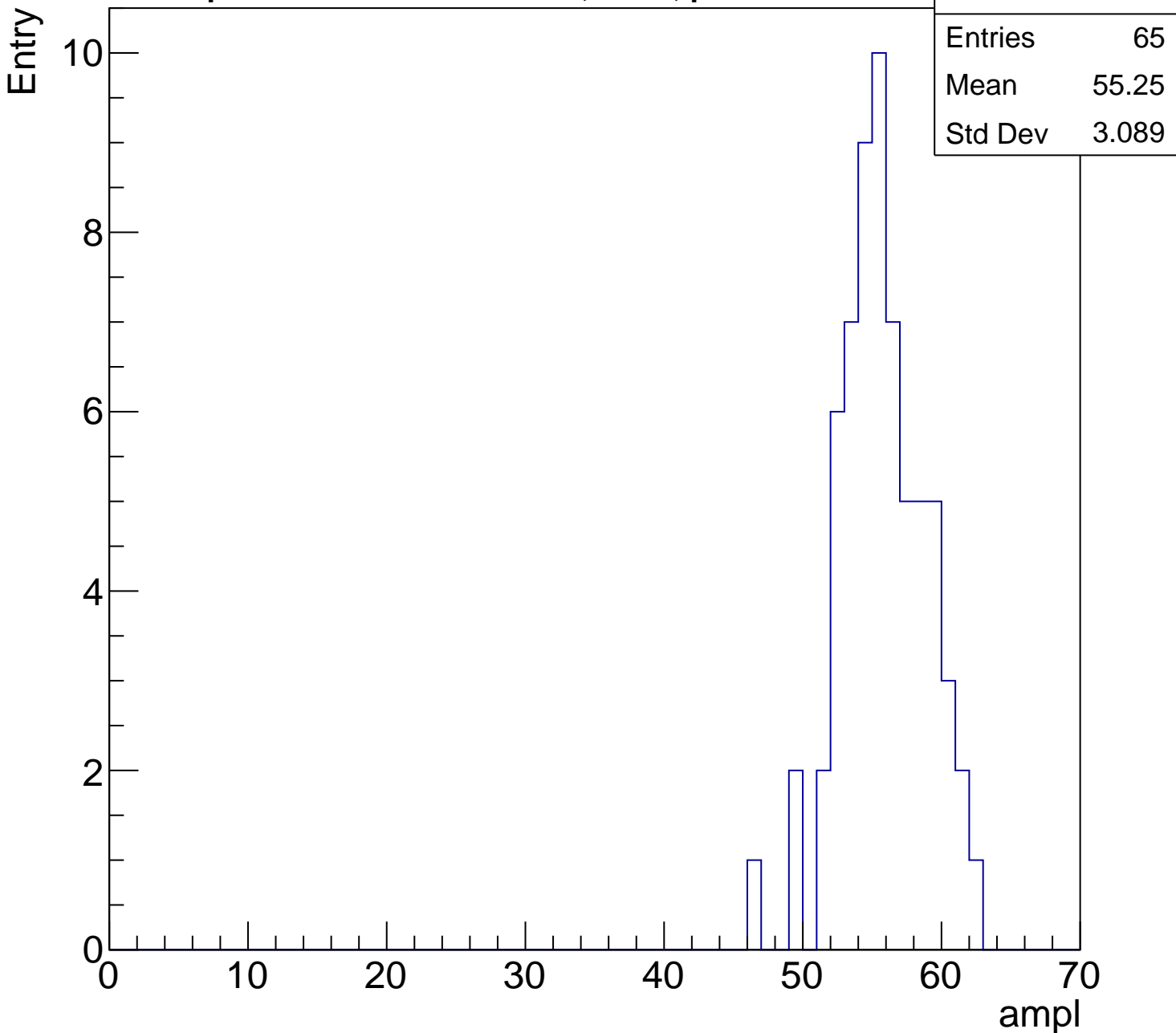
Entries	65
Mean	55.25
Std Dev	3.089

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	58.36
Std Dev	9.17

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

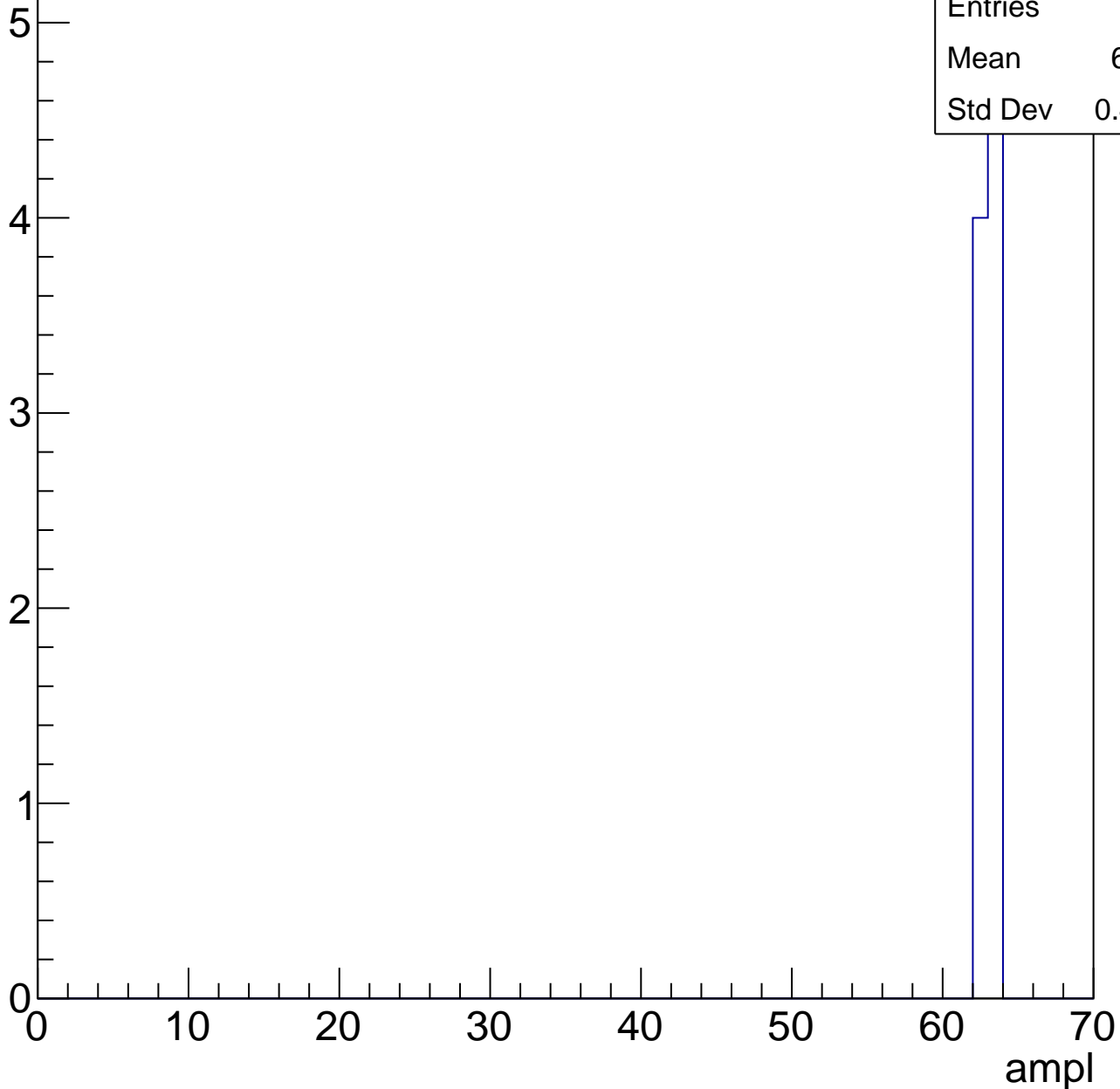
8

# B0L000S, U18-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.56
Std Dev	0.4969





# B0L000S, U18-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U18-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	30.09
Std Dev	3.268

**Gaus mean : 30.6805**

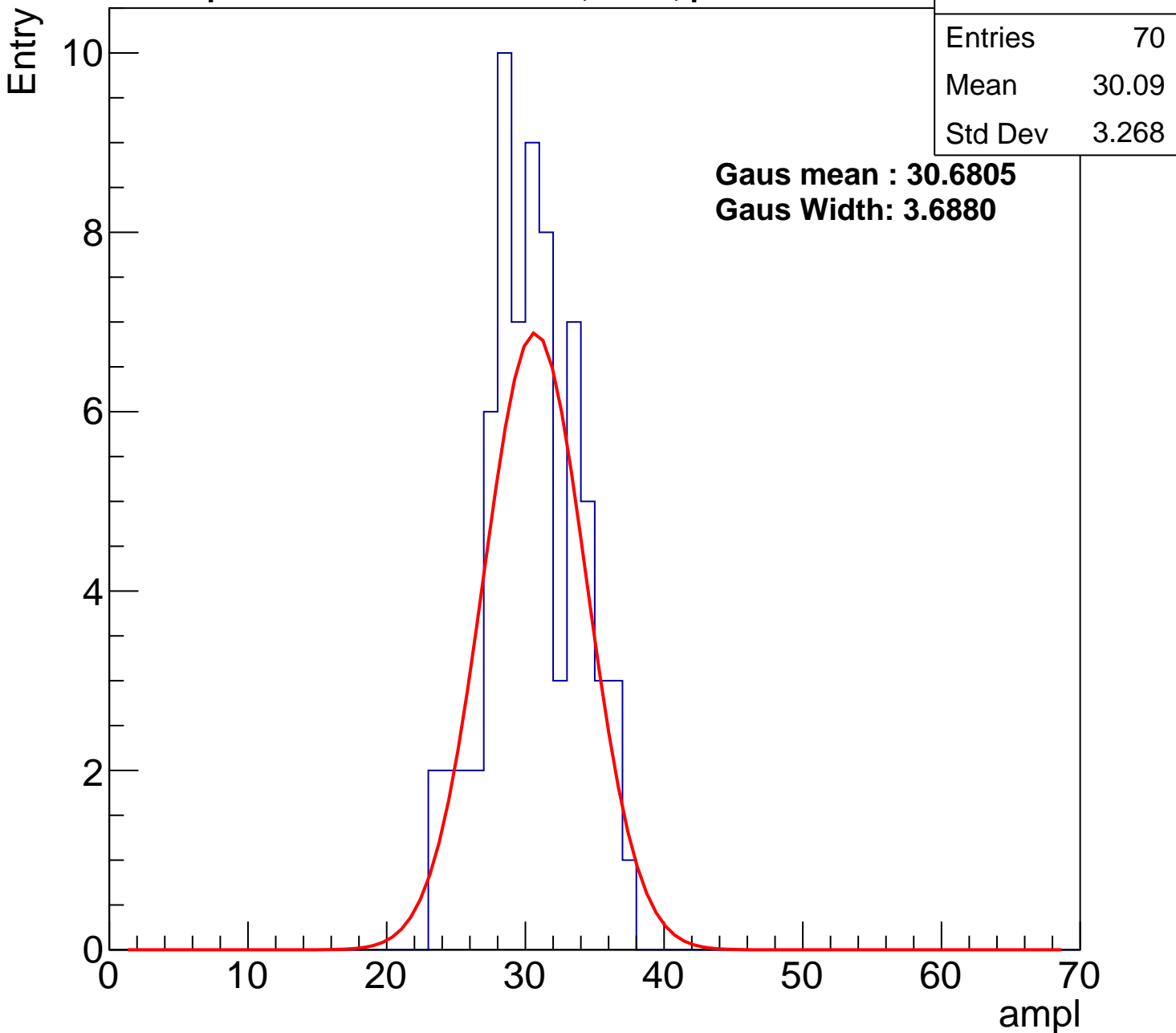
**Gaus Width: 3.6880**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch10, adc1

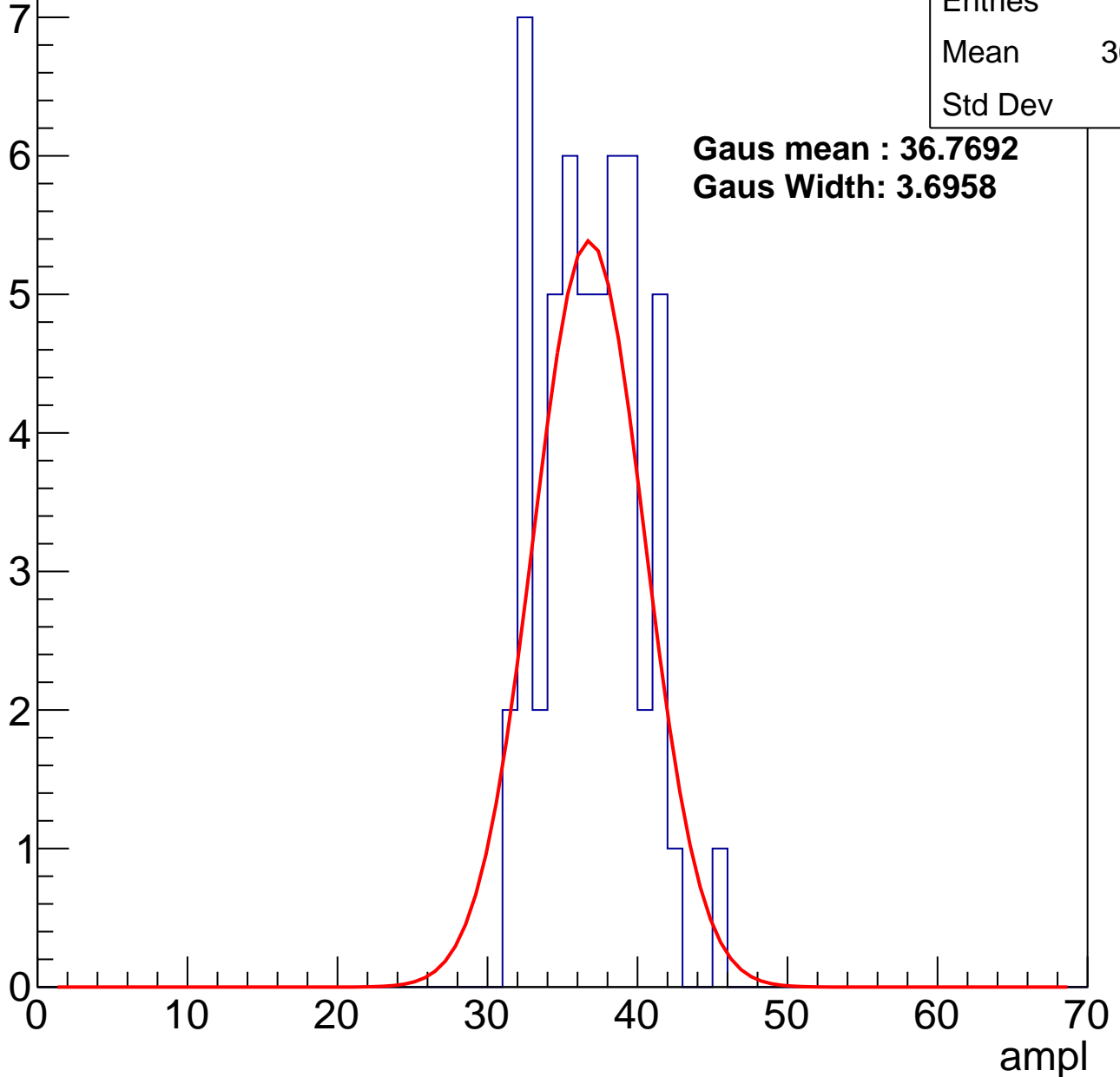
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.43
Std Dev	3.23

**Gaus mean : 36.7692**

**Gaus Width: 3.6958**



# B0L000S, U18-ch10, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	41.81
Std Dev	3.394

**Gaus mean : 42.3472**

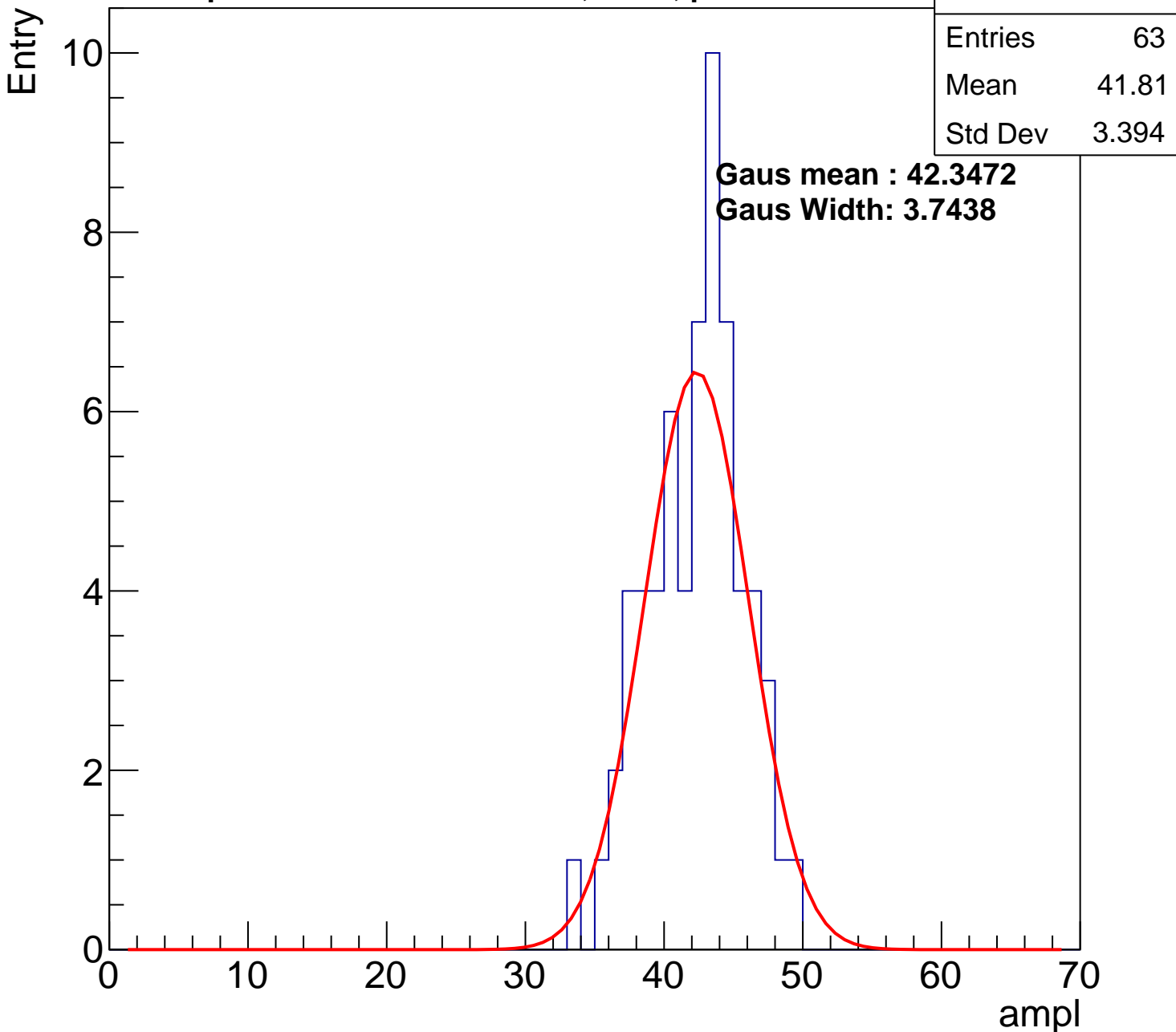
**Gaus Width: 3.7438**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

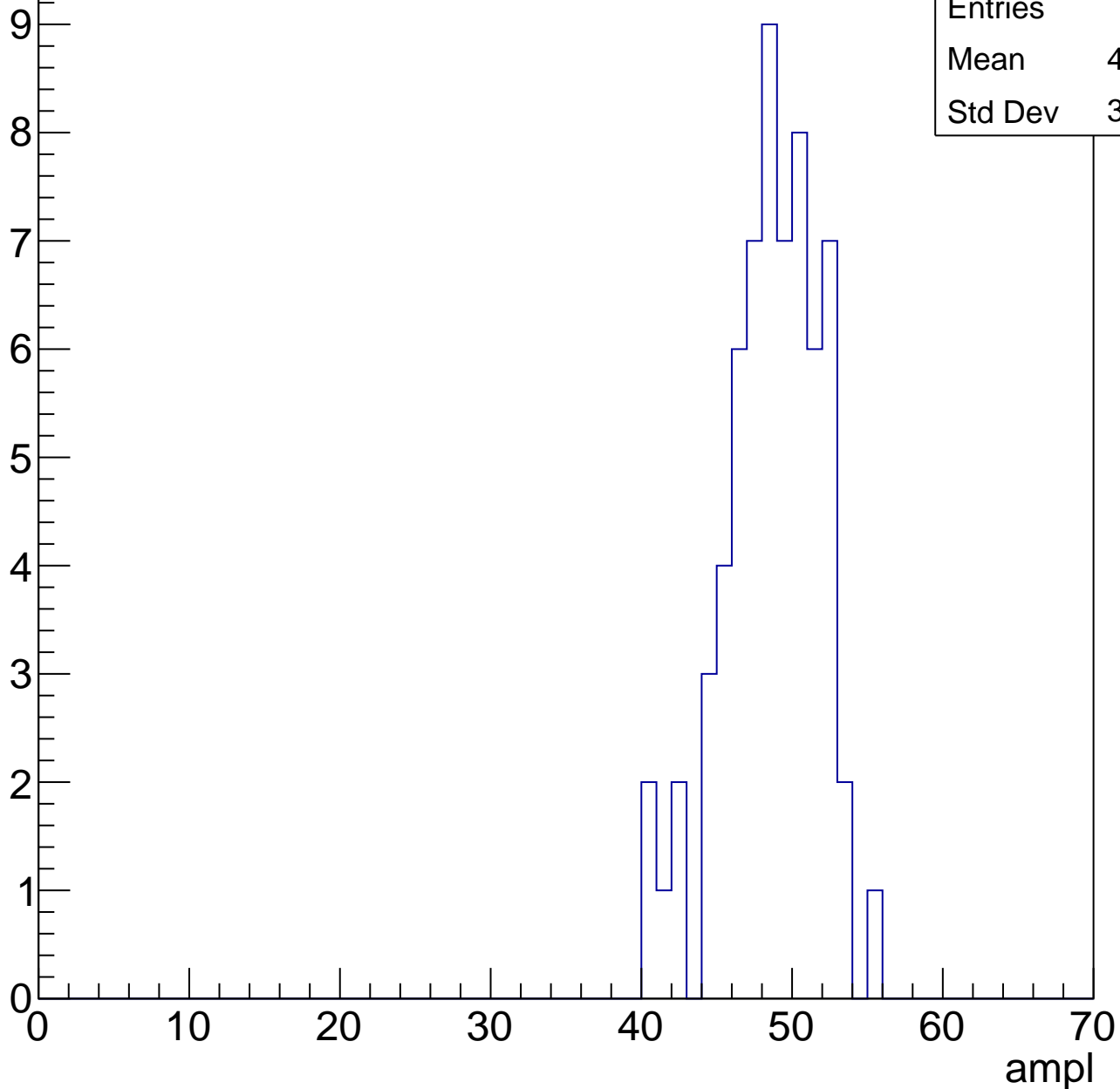


# B0L000S, U18-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.12
Std Dev	3.203

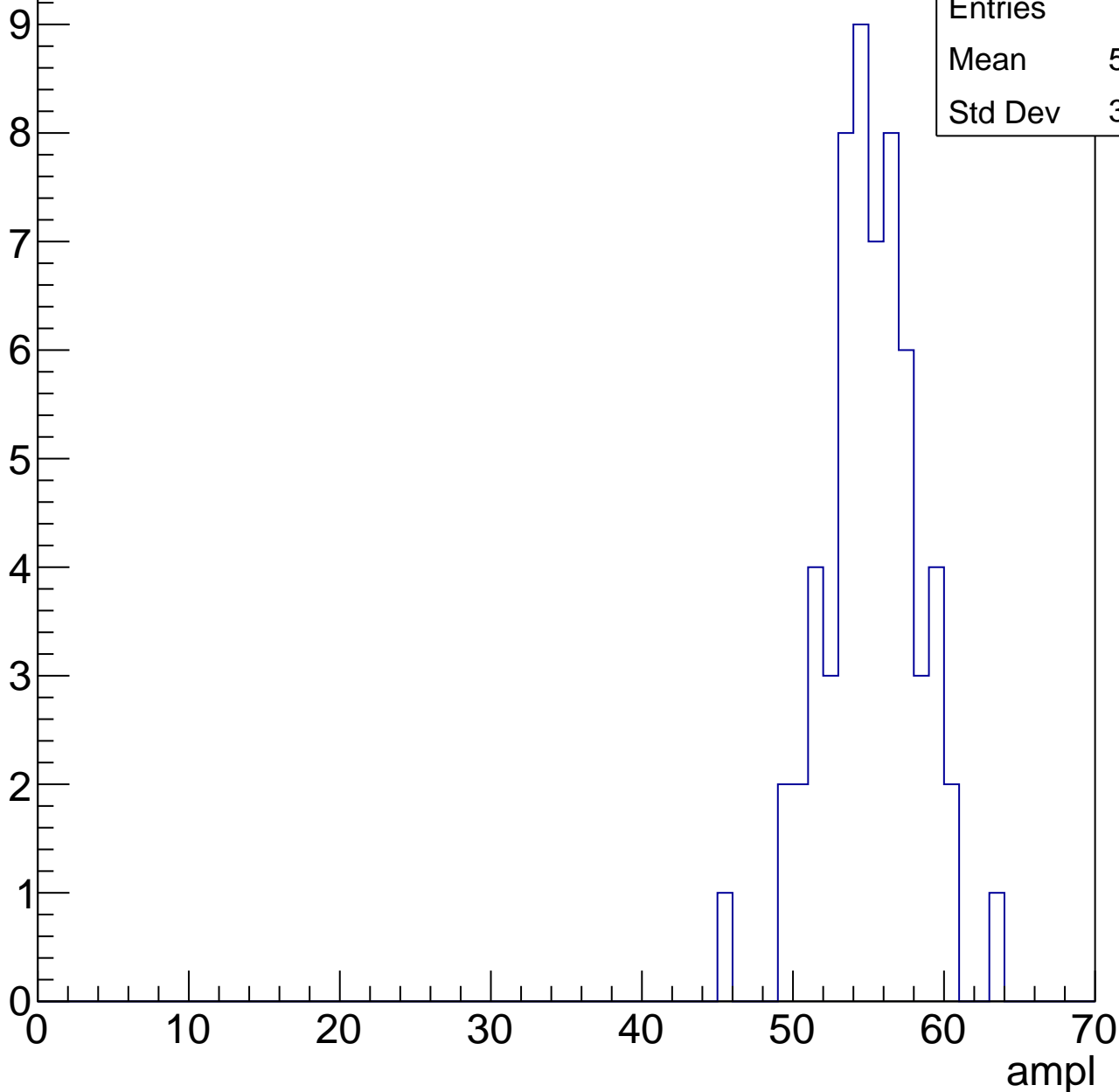


# B0L000S, U18-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.68
Std Dev	3.122

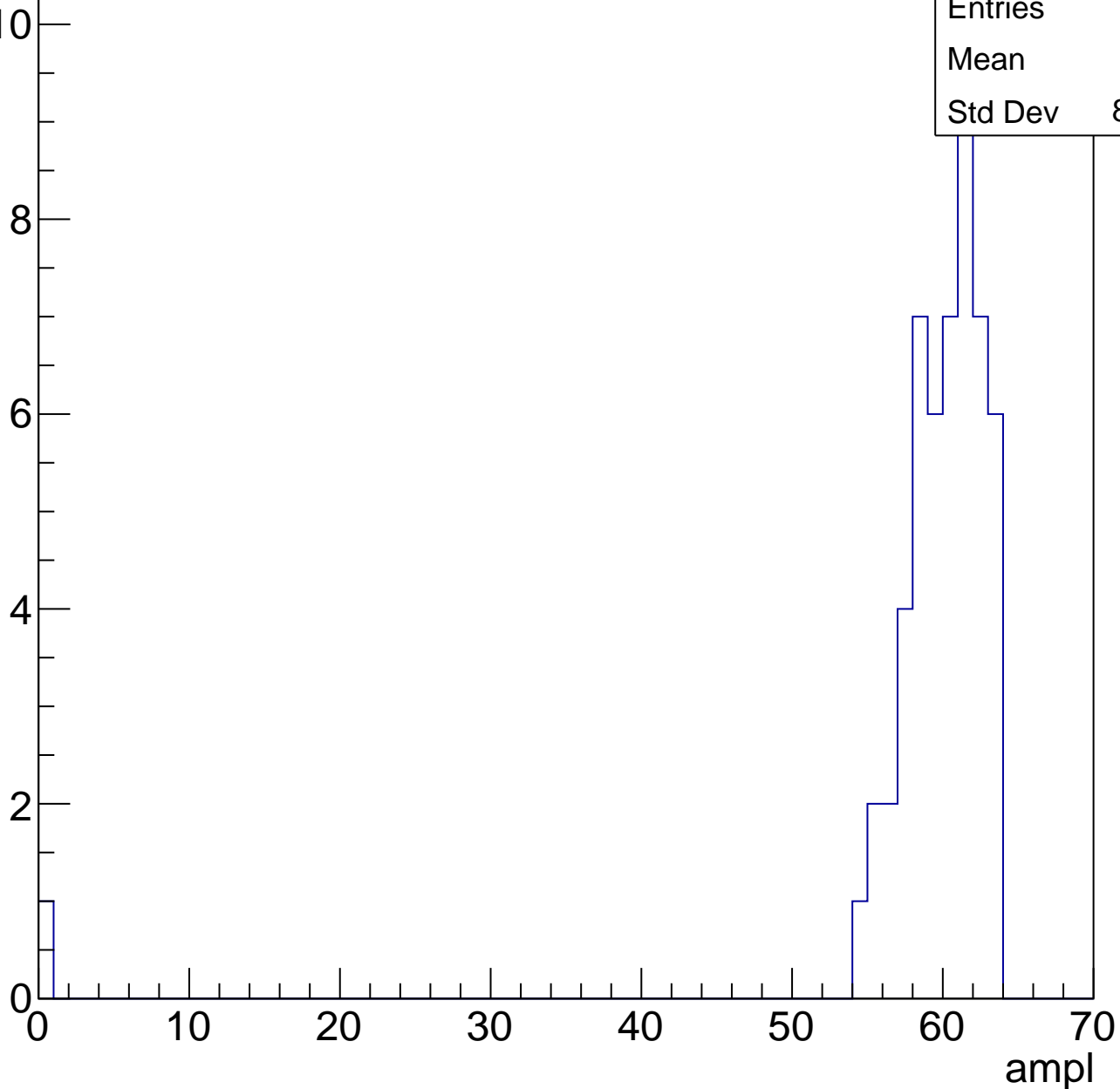


# B0L000S, U18-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

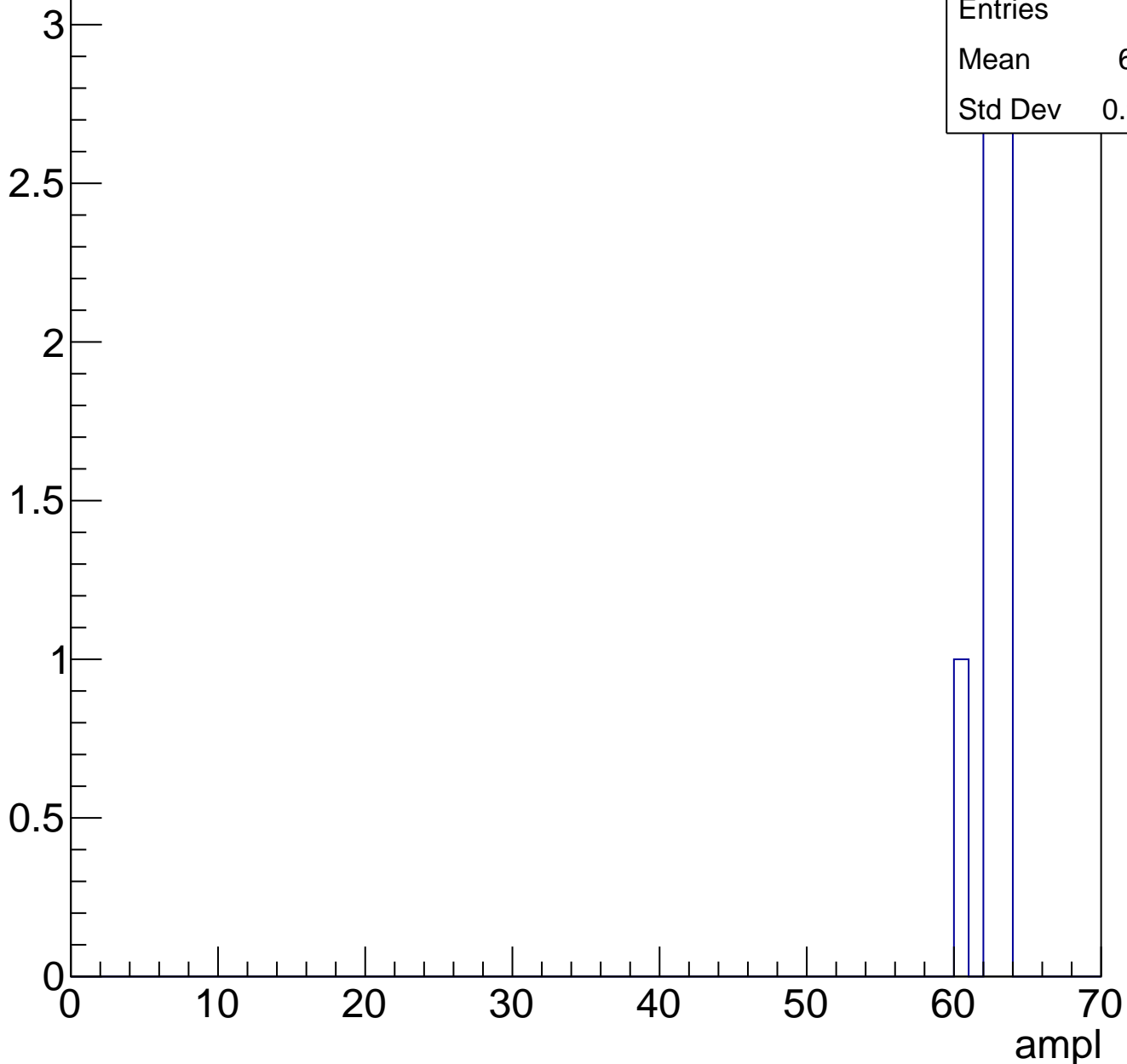
Entries	53
Mean	58.6
Std Dev	8.441



# B0L000S, U18-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U18-ch11, adc0

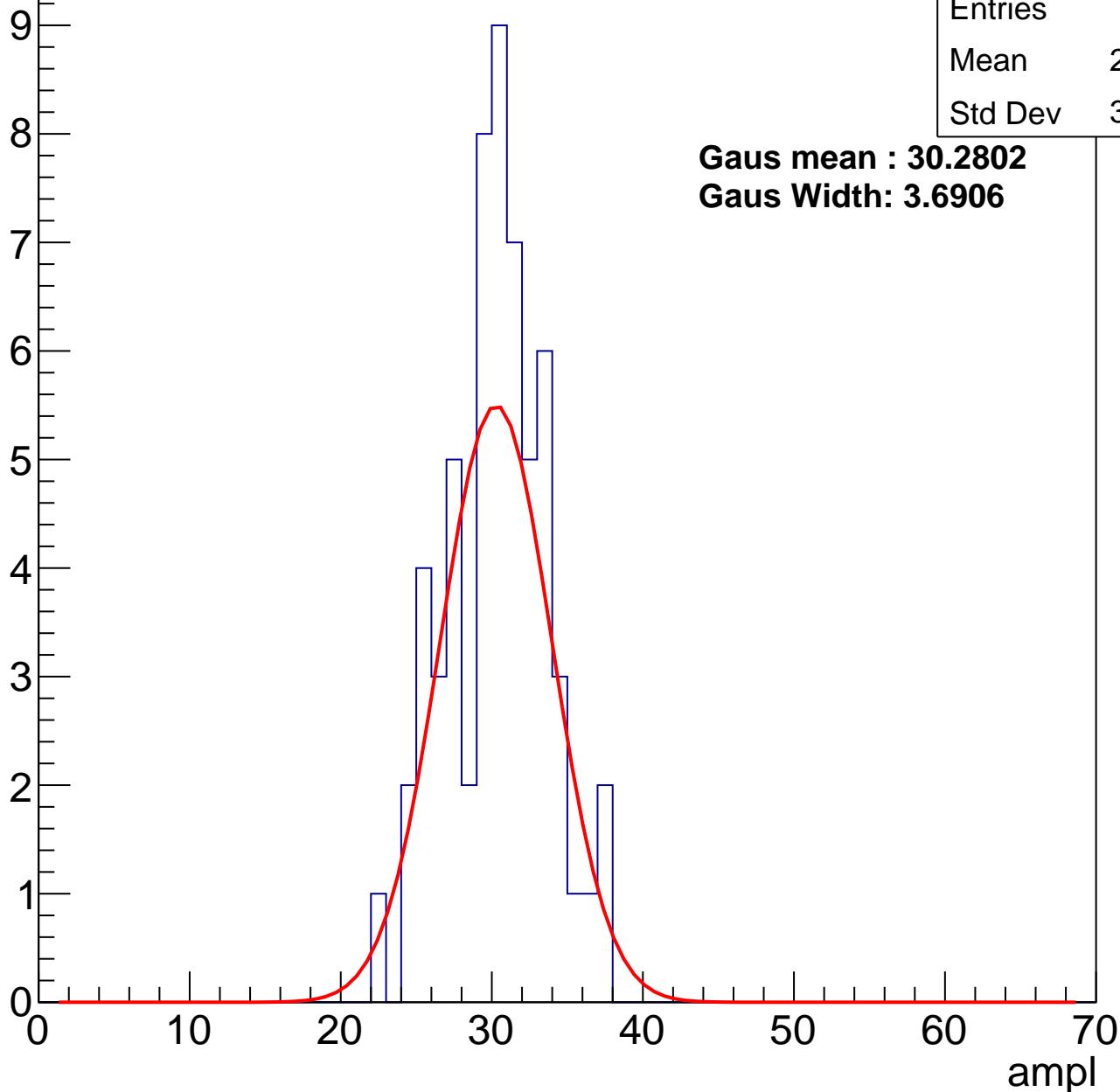
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.88
Std Dev	3.273

**Gaus mean : 30.2802**

**Gaus Width: 3.6906**



# B0L000S, U18-ch11, adc1

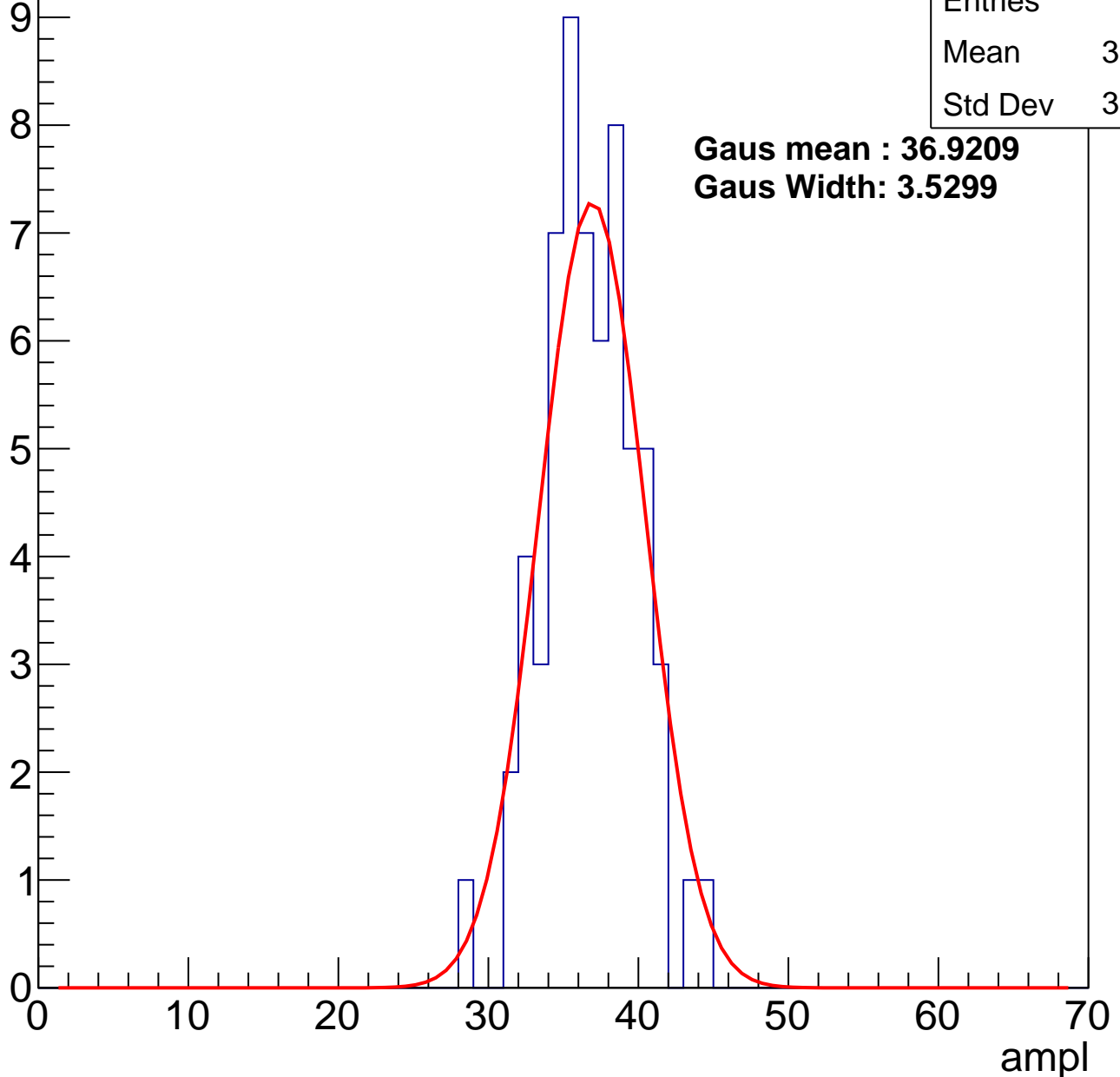
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.34
Std Dev	3.074

**Gaus mean : 36.9209**

**Gaus Width: 3.5299**



# B0L000S, U18-ch11, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	42.68
Std Dev	3.295

**Gaus mean : 43.0954**

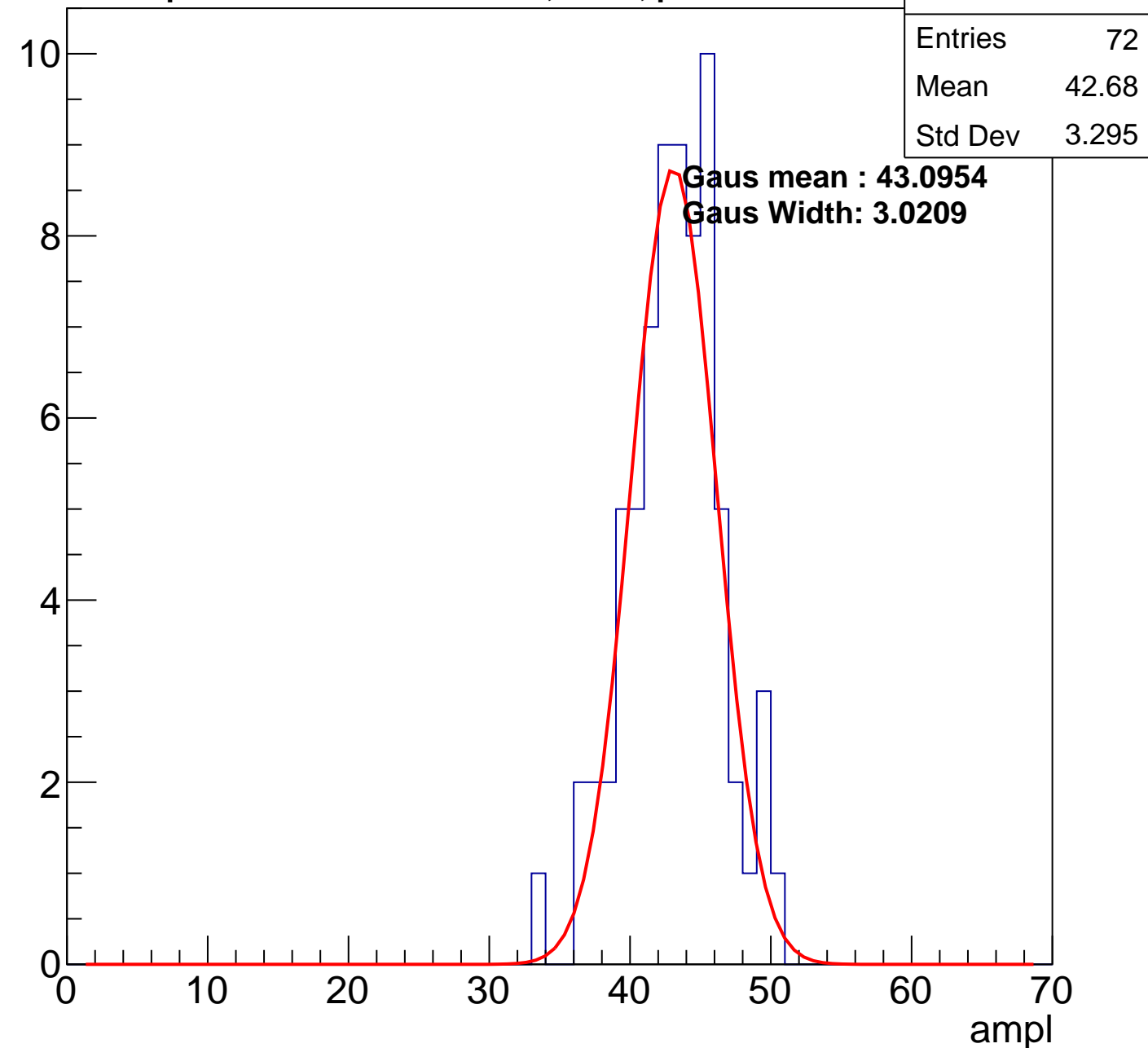
**Gaus Width: 3.0209**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

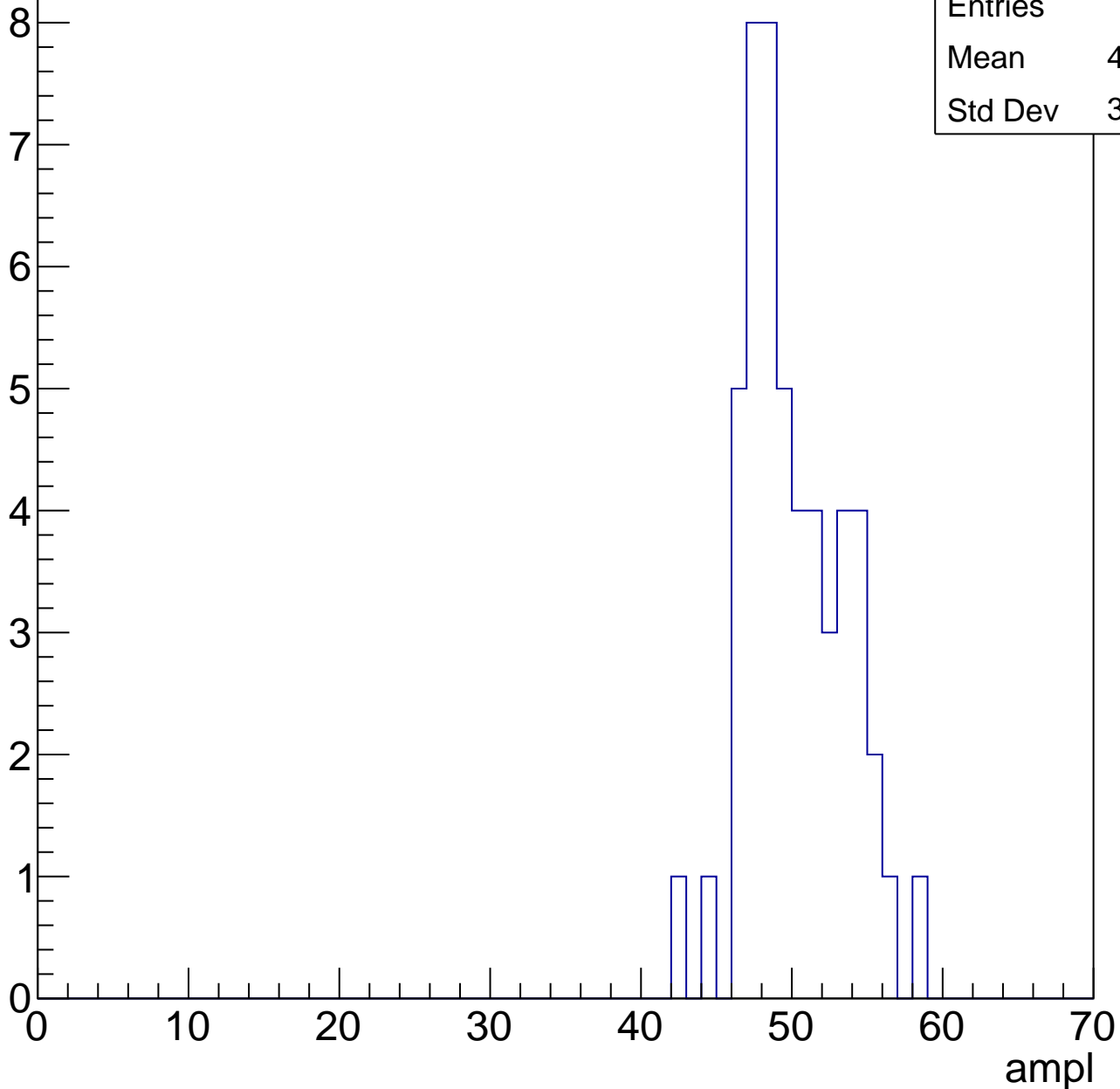


# B0L000S, U18-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	49.67
Std Dev	3.282



# B0L000S, U18-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries 58

Mean 54.4

Std Dev 3.243

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

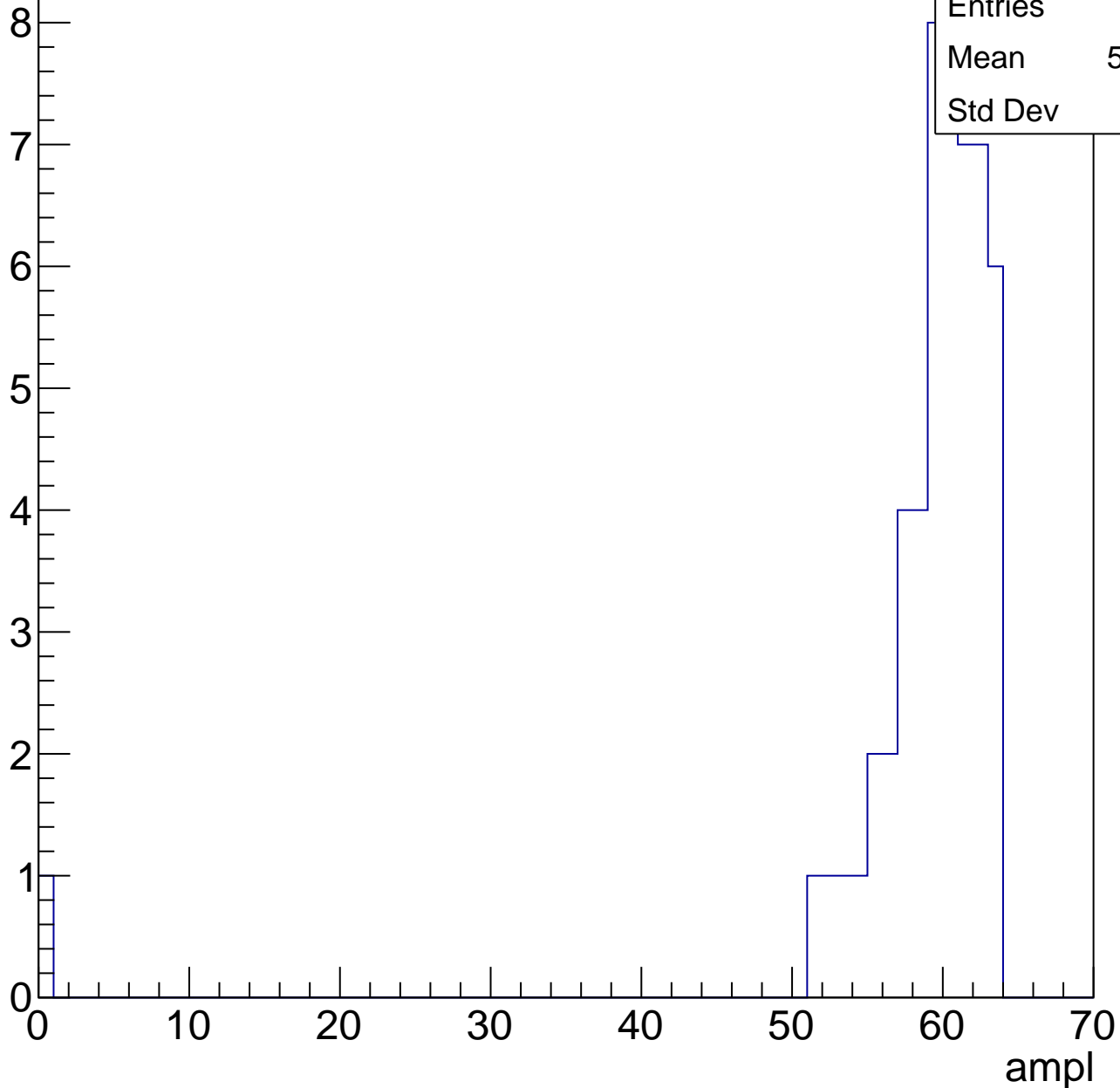
70

ampl

# B0L000S, U18-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

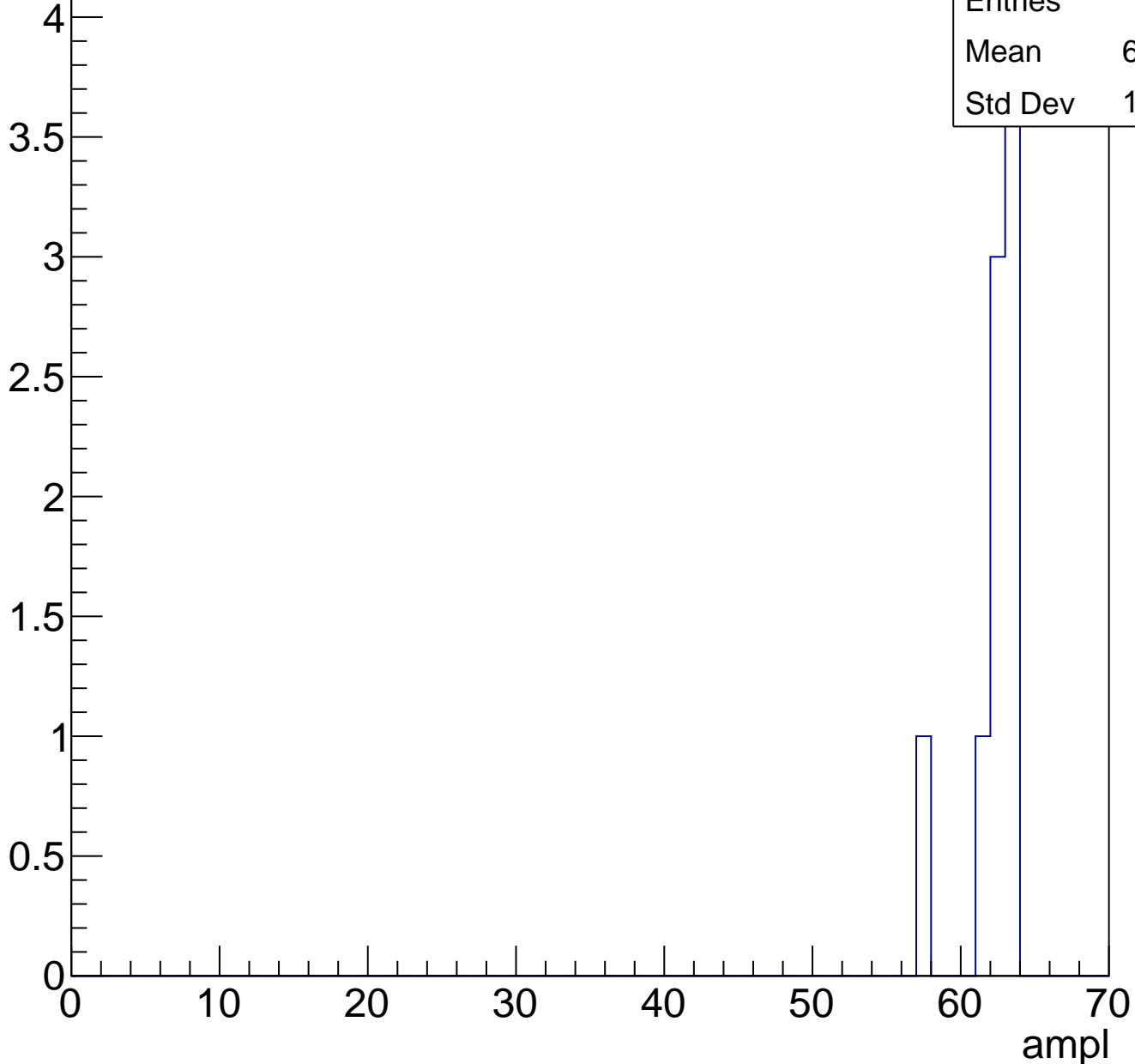
Entry



# B0L000S, U18-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch12, adc0

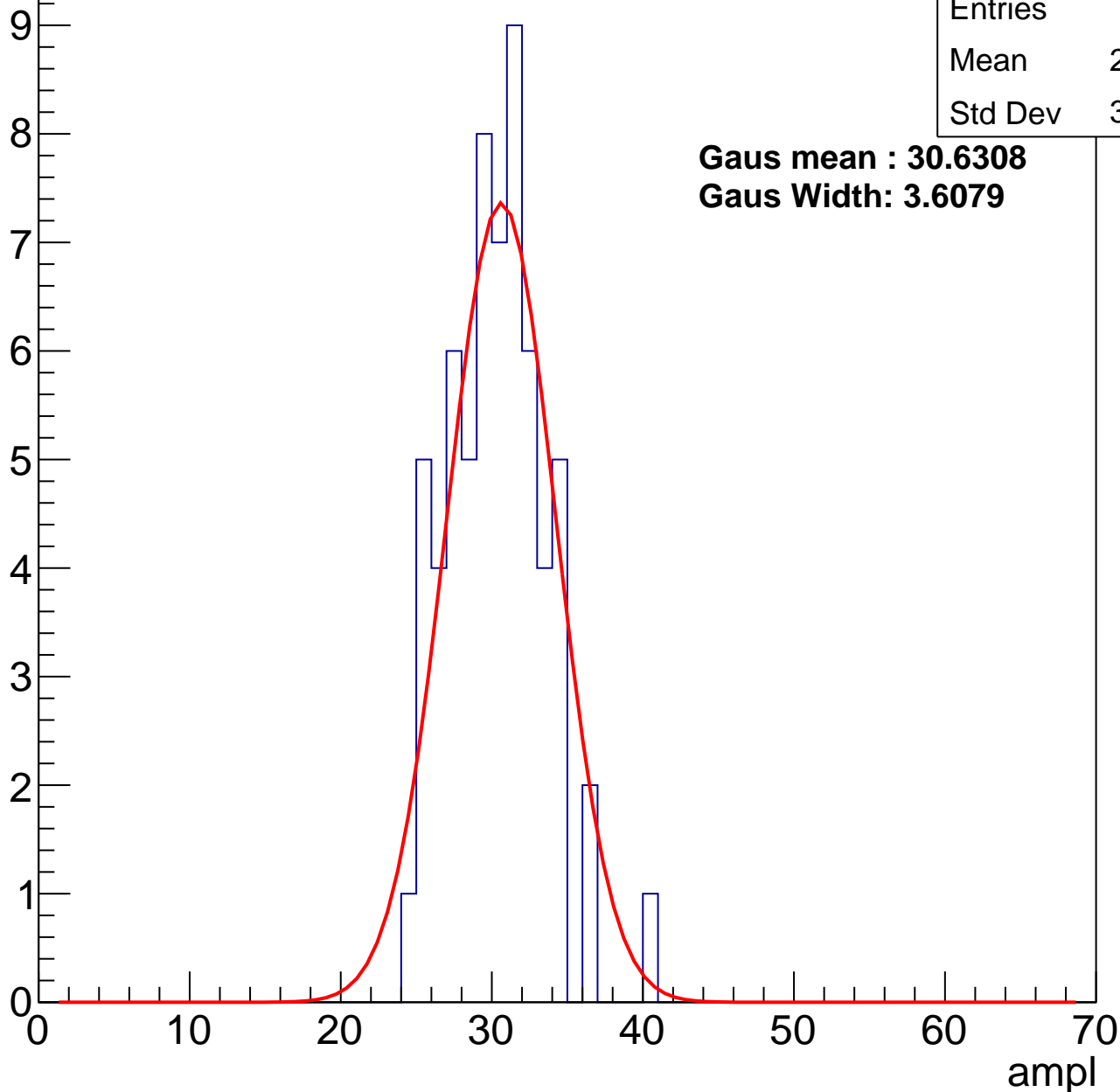
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.87
Std Dev	3.155

**Gaus mean : 30.6308**

**Gaus Width: 3.6079**



# B0L000S, U18-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	36.18
Std Dev	3.252

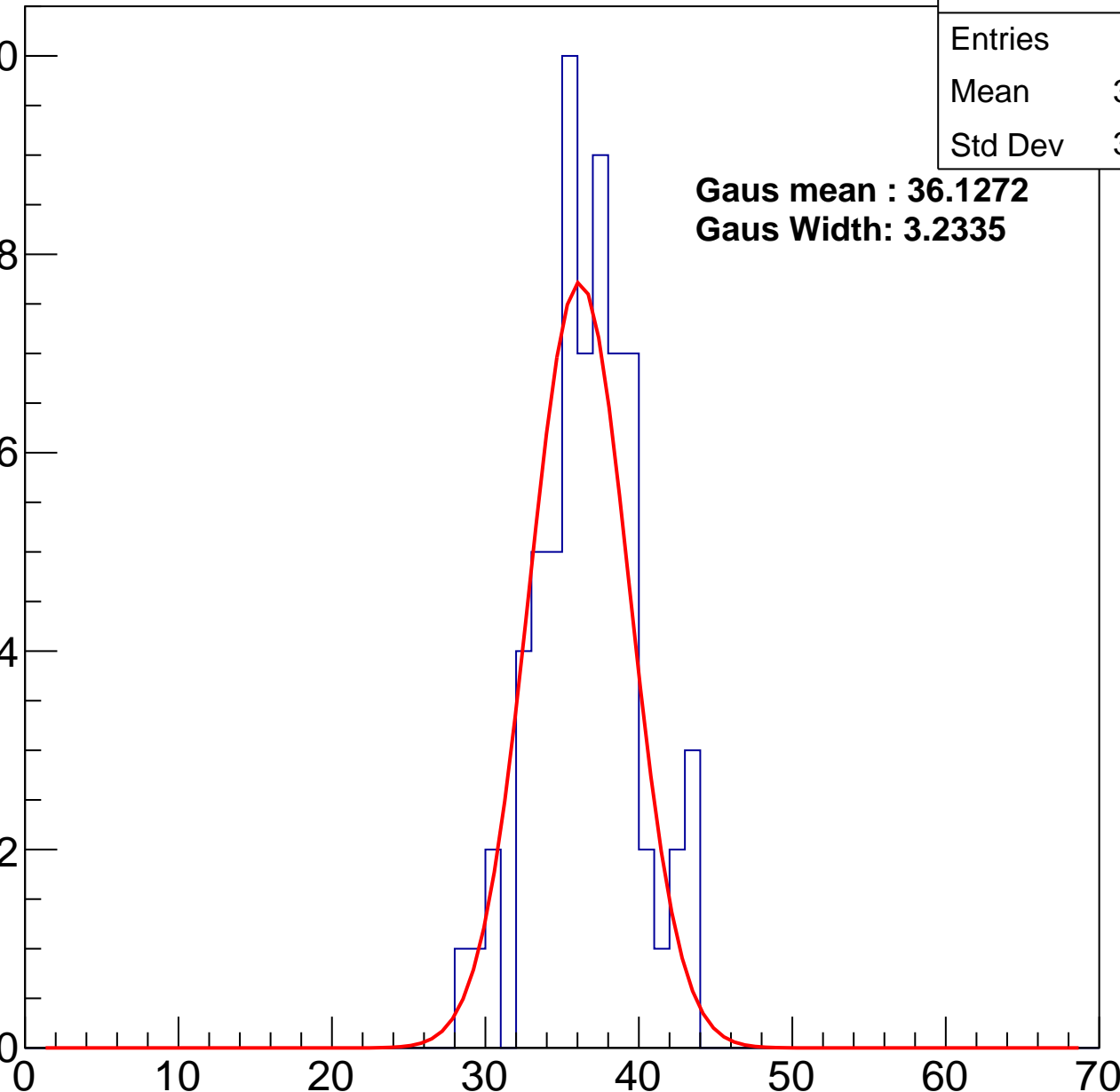
**Gaus mean : 36.1272**

**Gaus Width: 3.2335**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U18-ch12, adc2

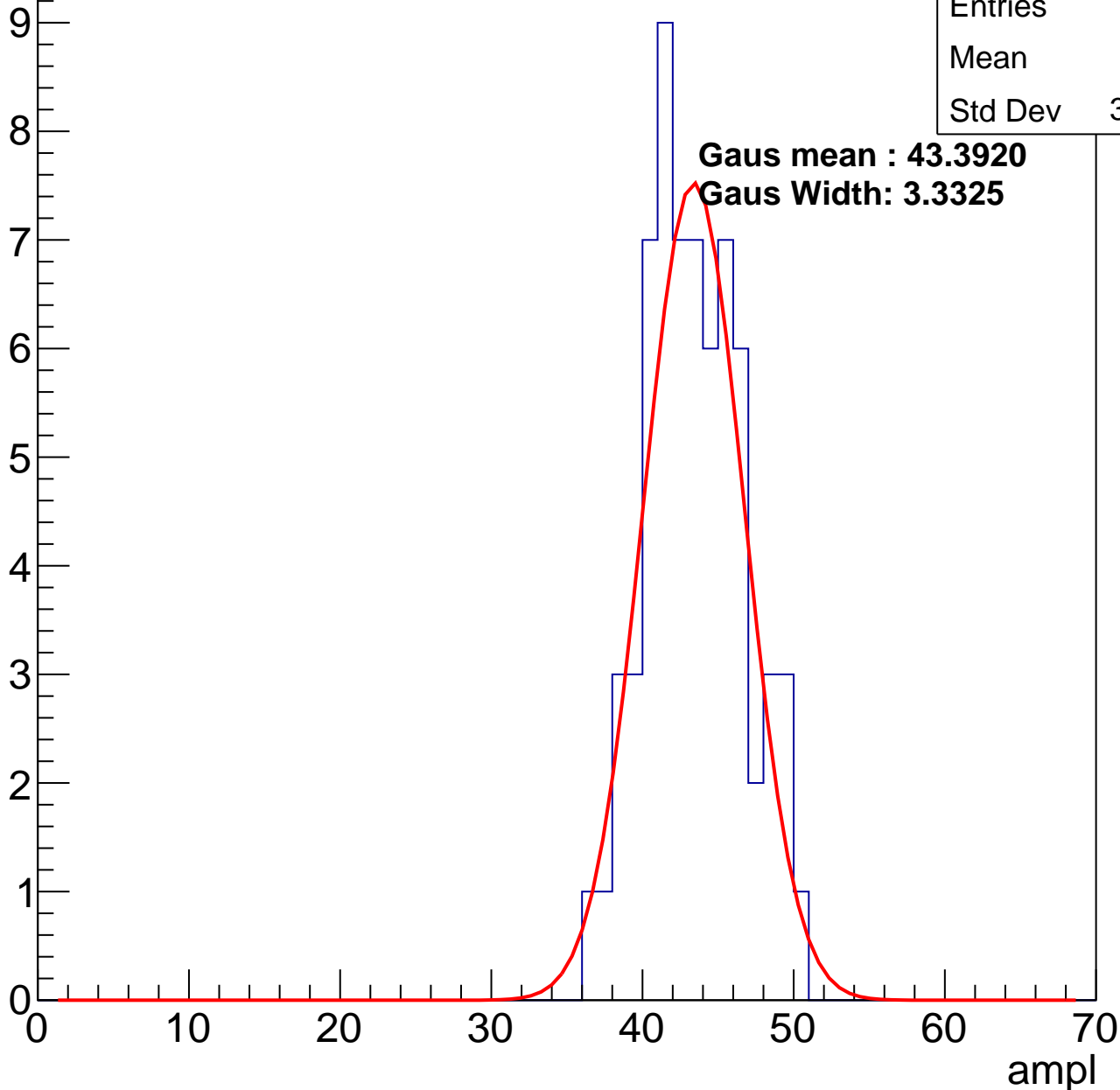
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43
Std Dev	3.177

**Gaus mean : 43.3920**

**Gaus Width: 3.3325**

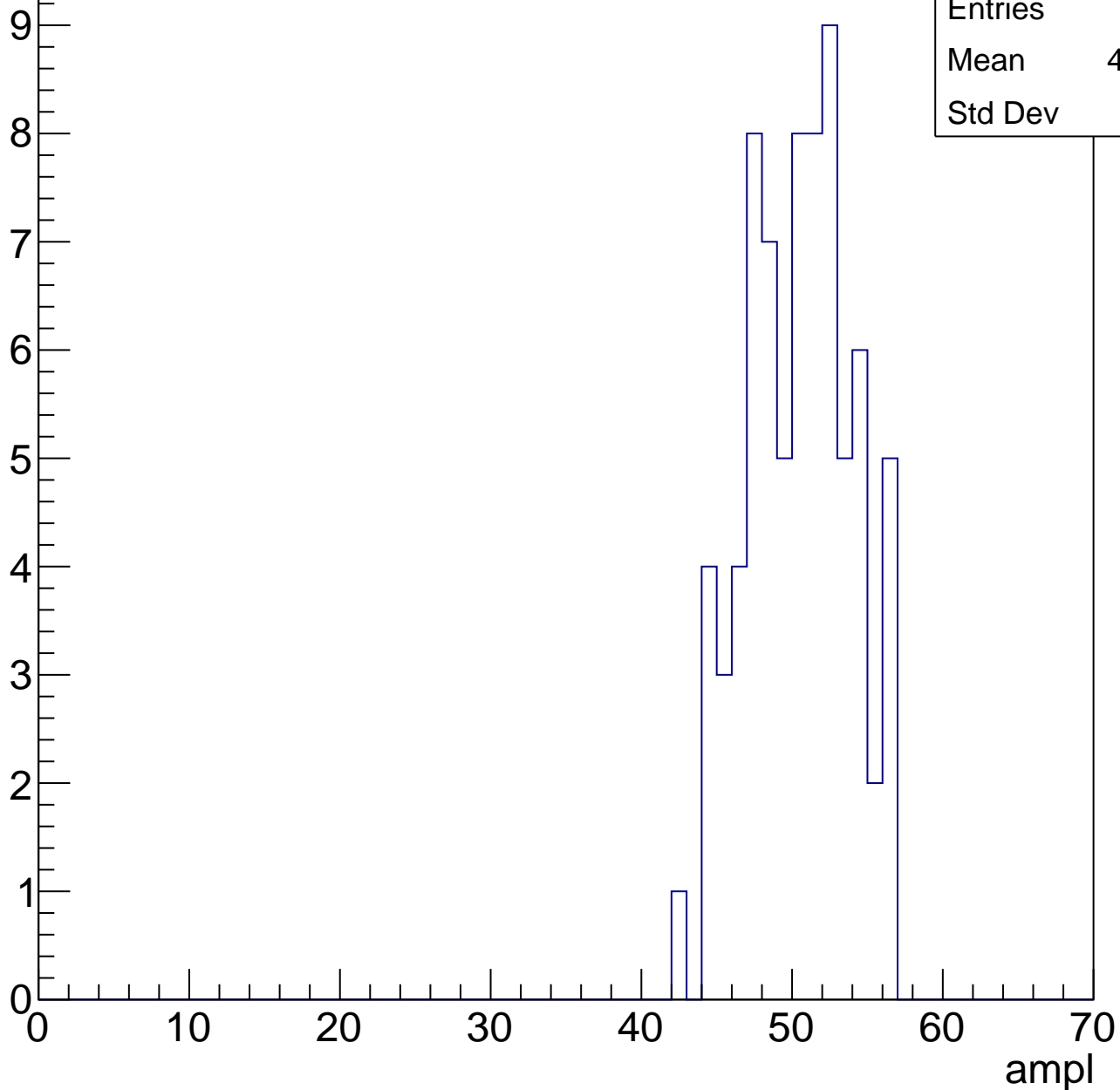


# B0L000S, U18-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	49.99
Std Dev	3.4

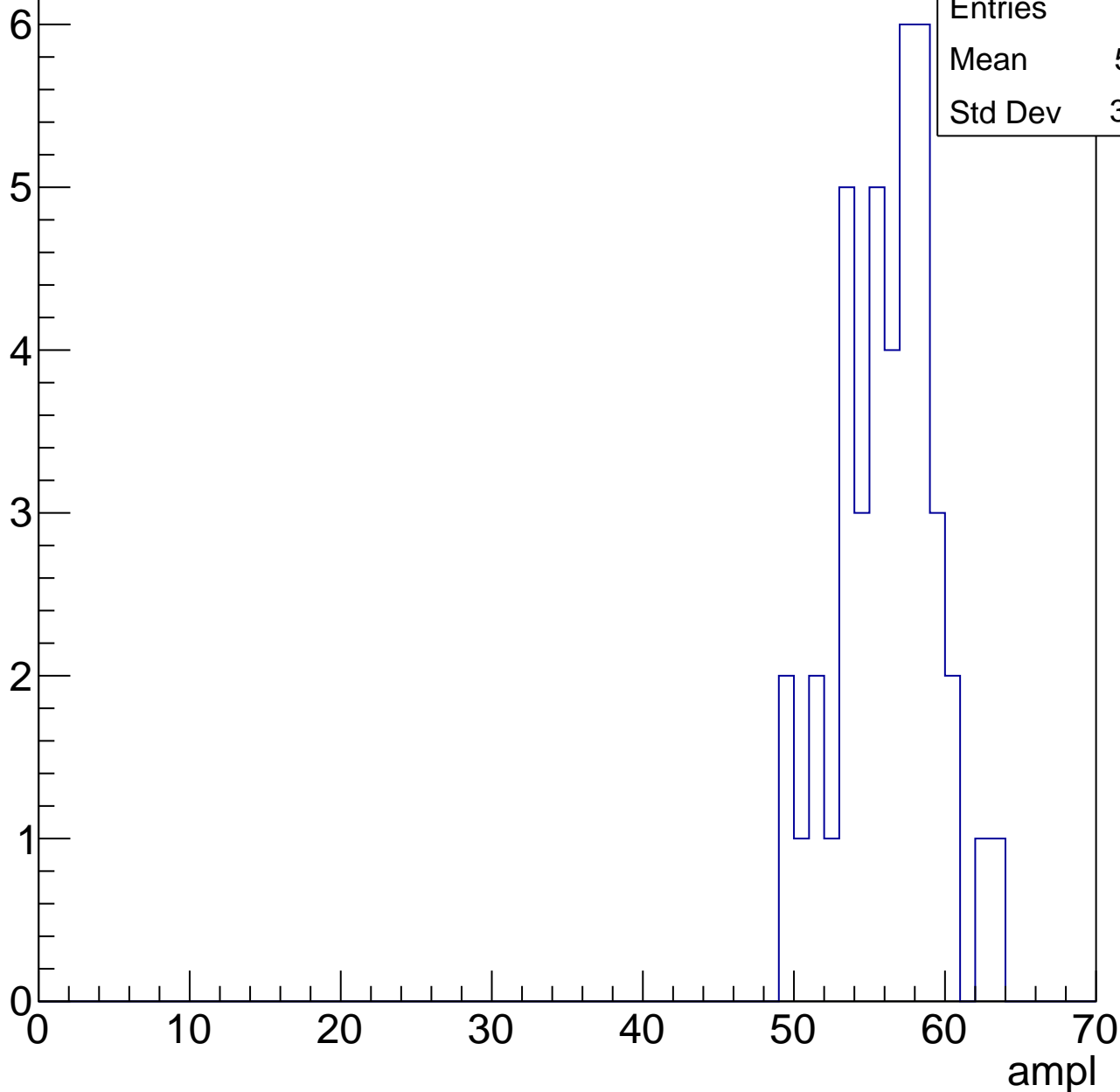


# B0L000S, U18-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	55.71
Std Dev	3.209

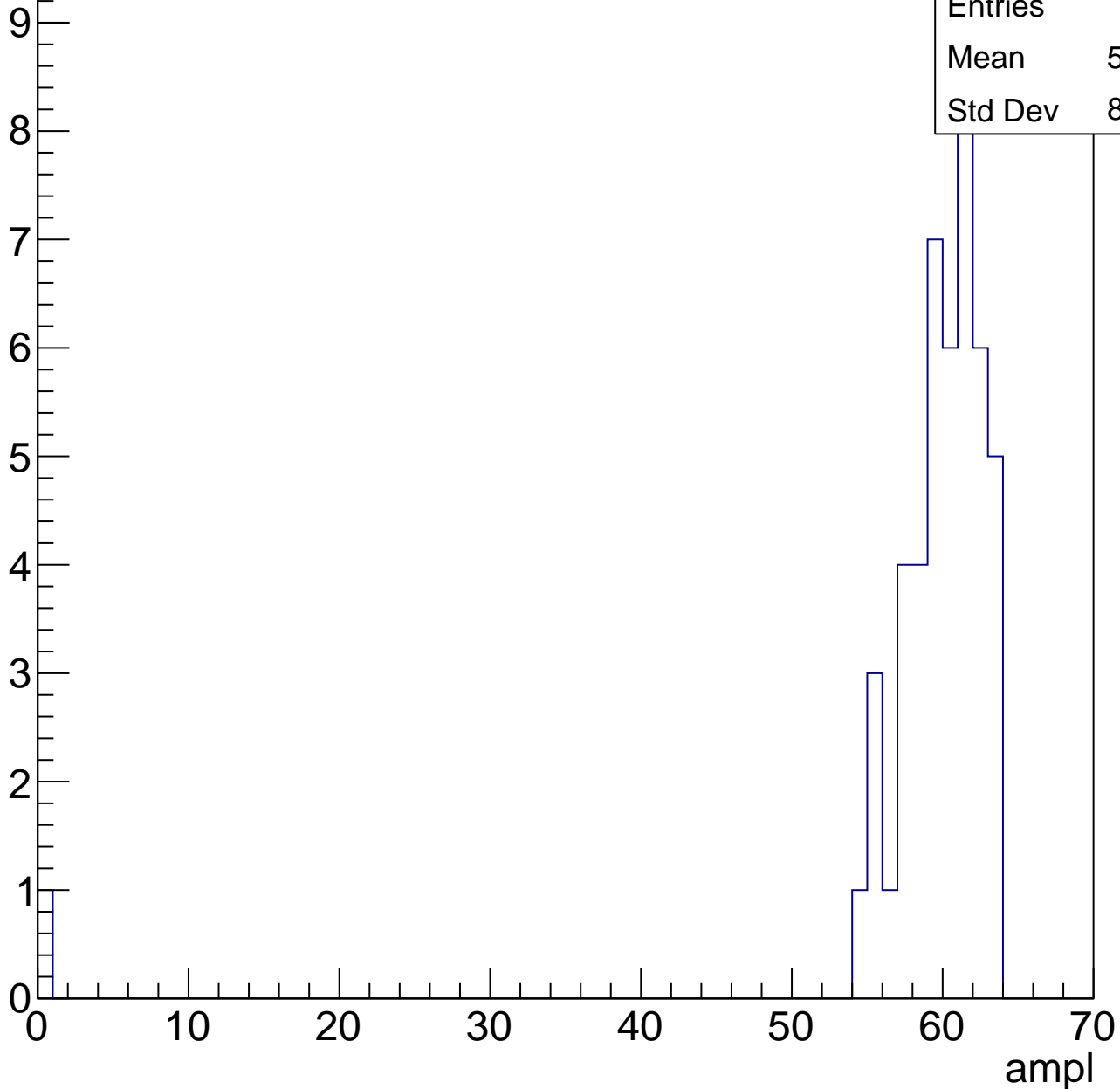


# B0L000S, U18-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

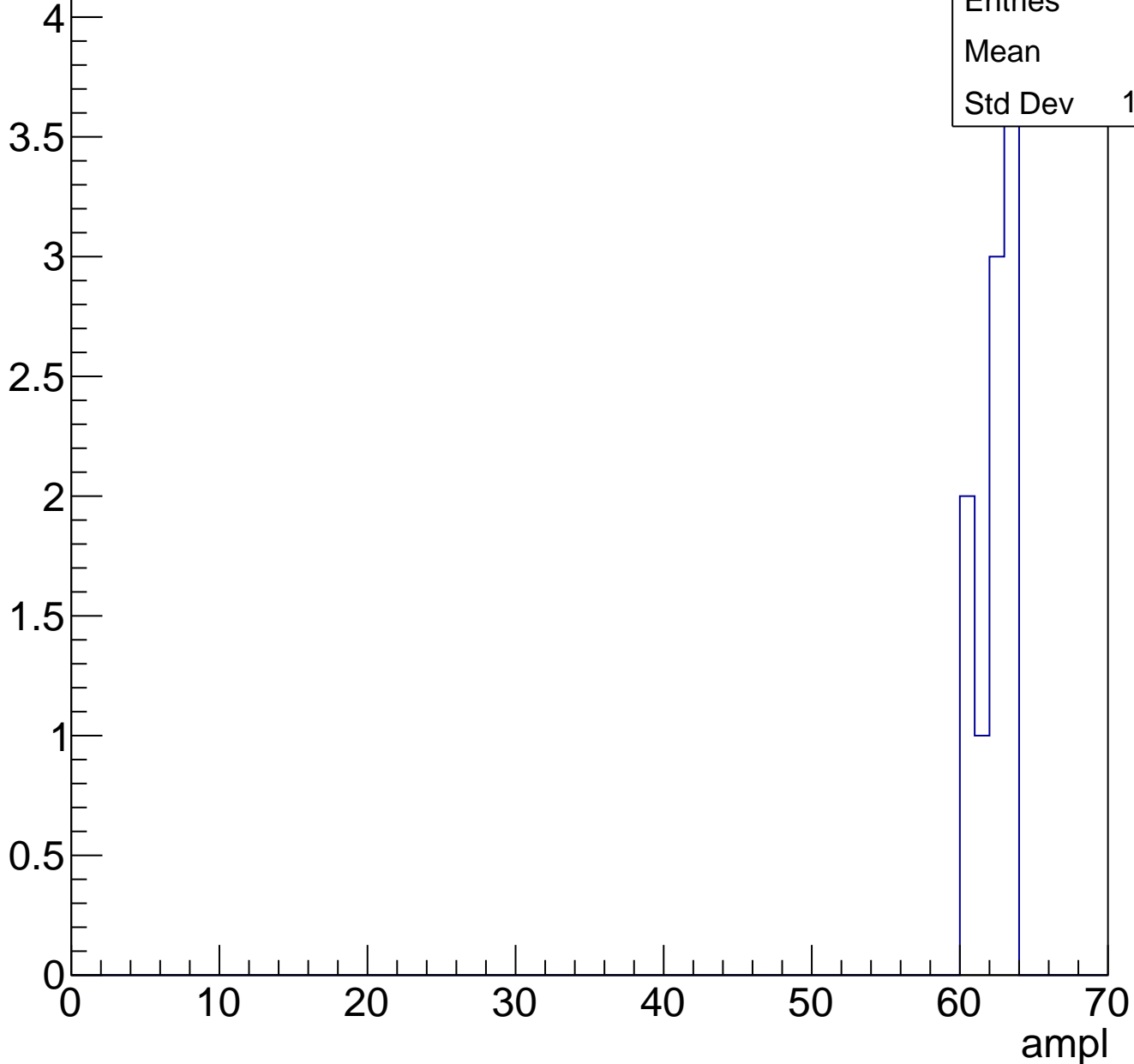
Entries	47
Mean	58.38
Std Dev	8.922



# B0L000S, U18-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch13, adc0

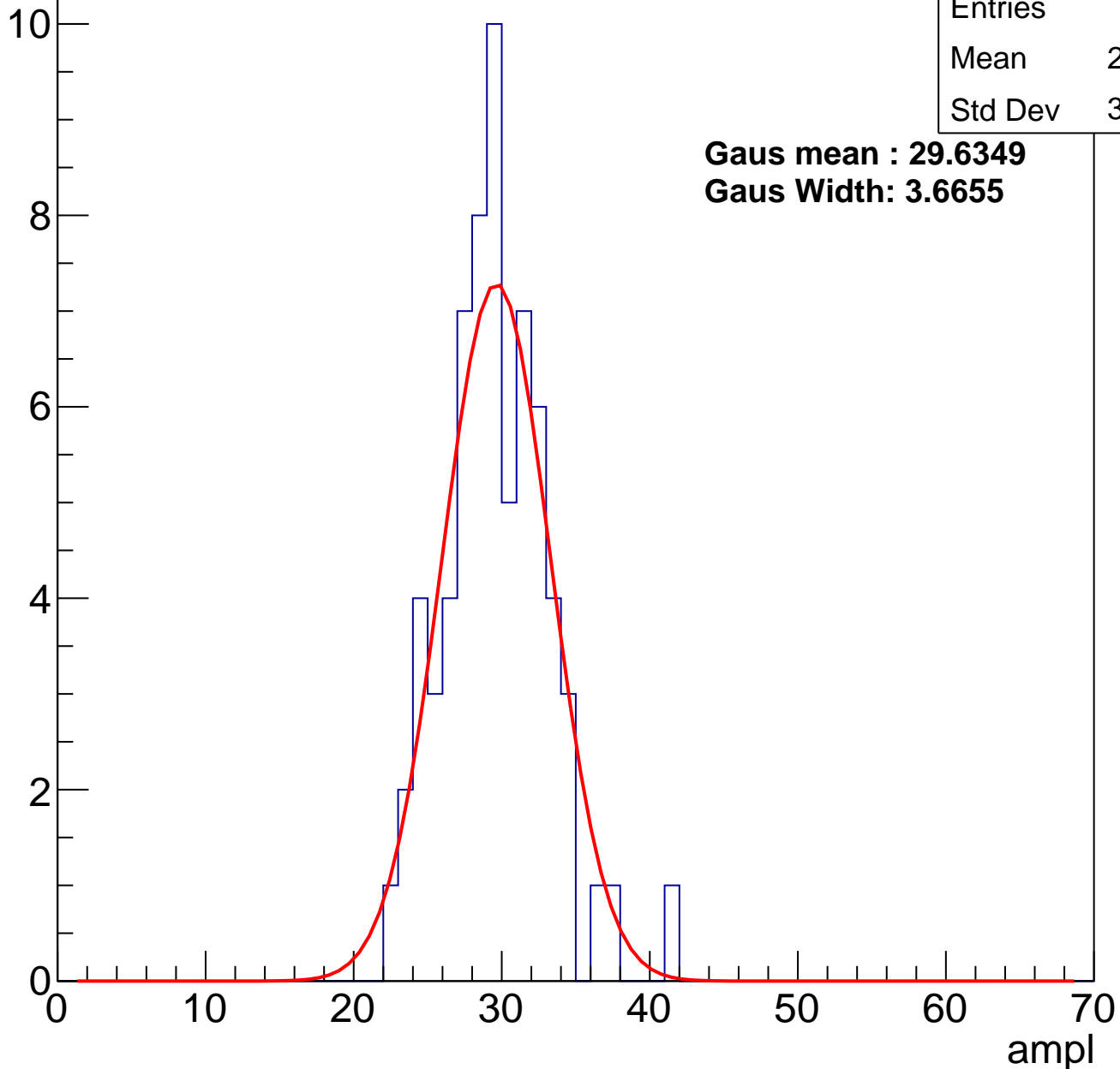
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	29.15
Std Dev	3.504

**Gaus mean : 29.6349**

**Gaus Width: 3.6655**

Entry



# B0L000S, U18-ch13, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	36.11
Std Dev	3.859

**Gaus mean : 36.5011**

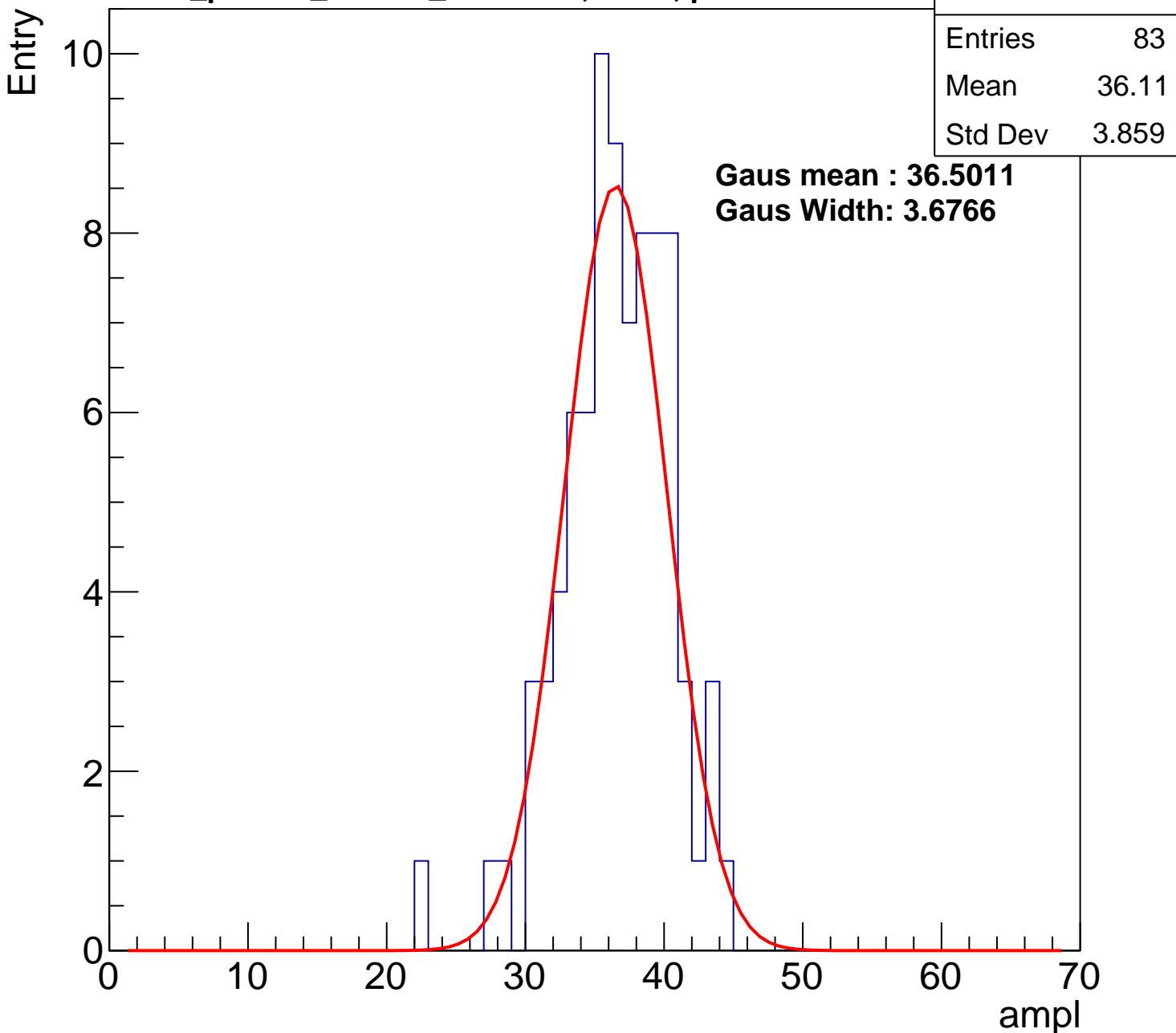
**Gaus Width: 3.6766**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch13, adc2

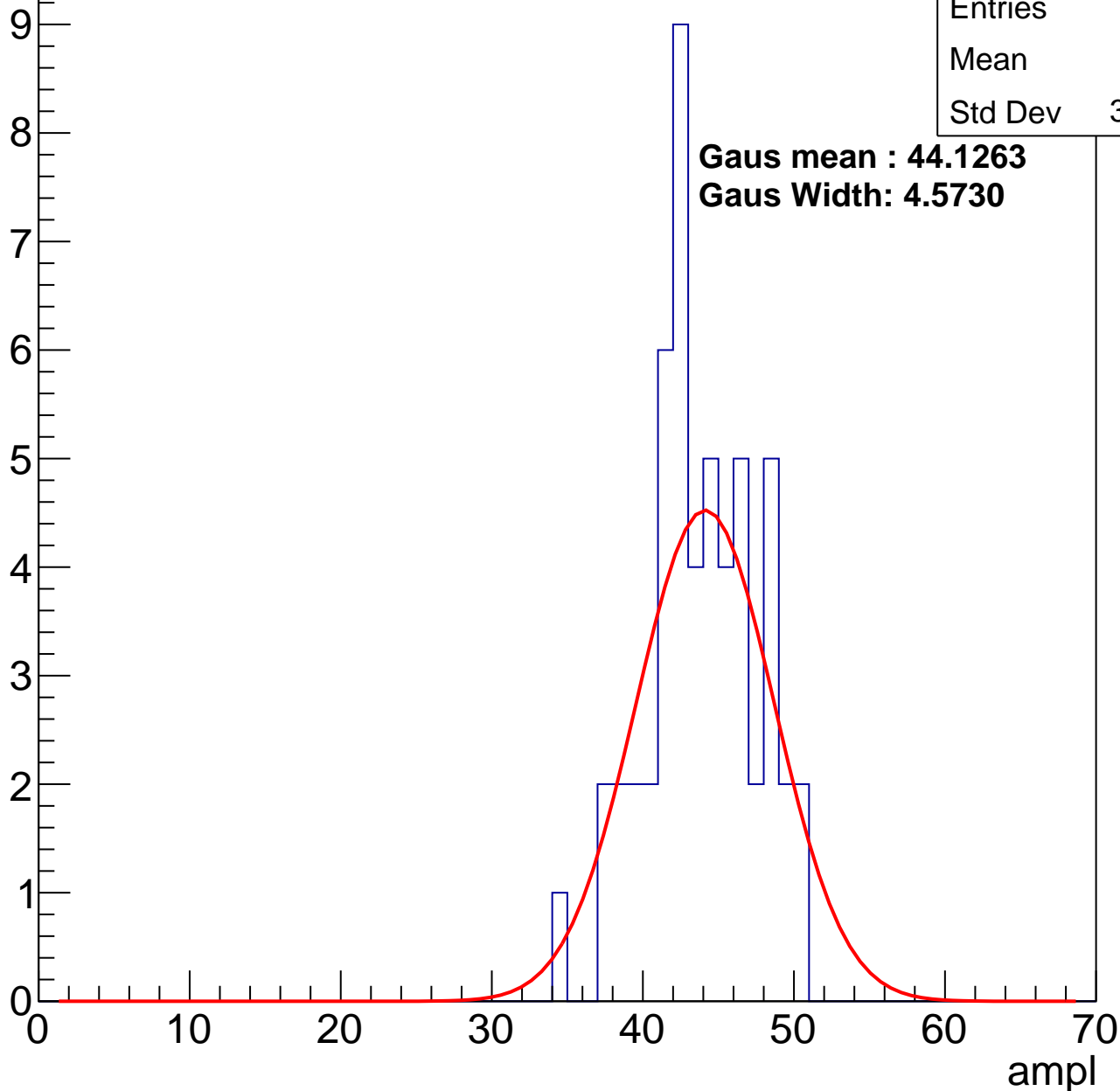
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.4
Std Dev	3.557

**Gaus mean : 44.1263**

**Gaus Width: 4.5730**

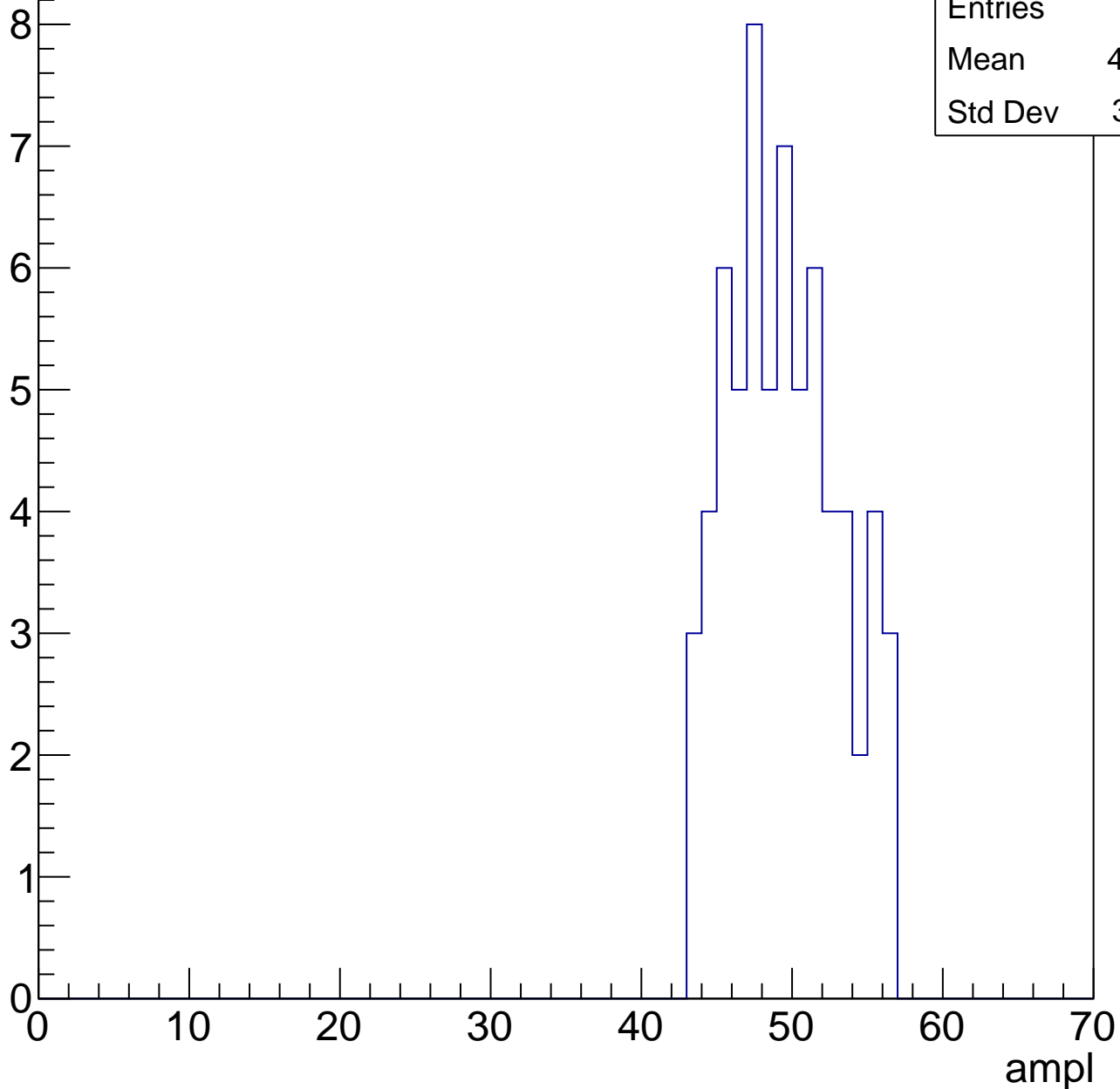


# B0L000S, U18-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	49.03
Std Dev	3.601

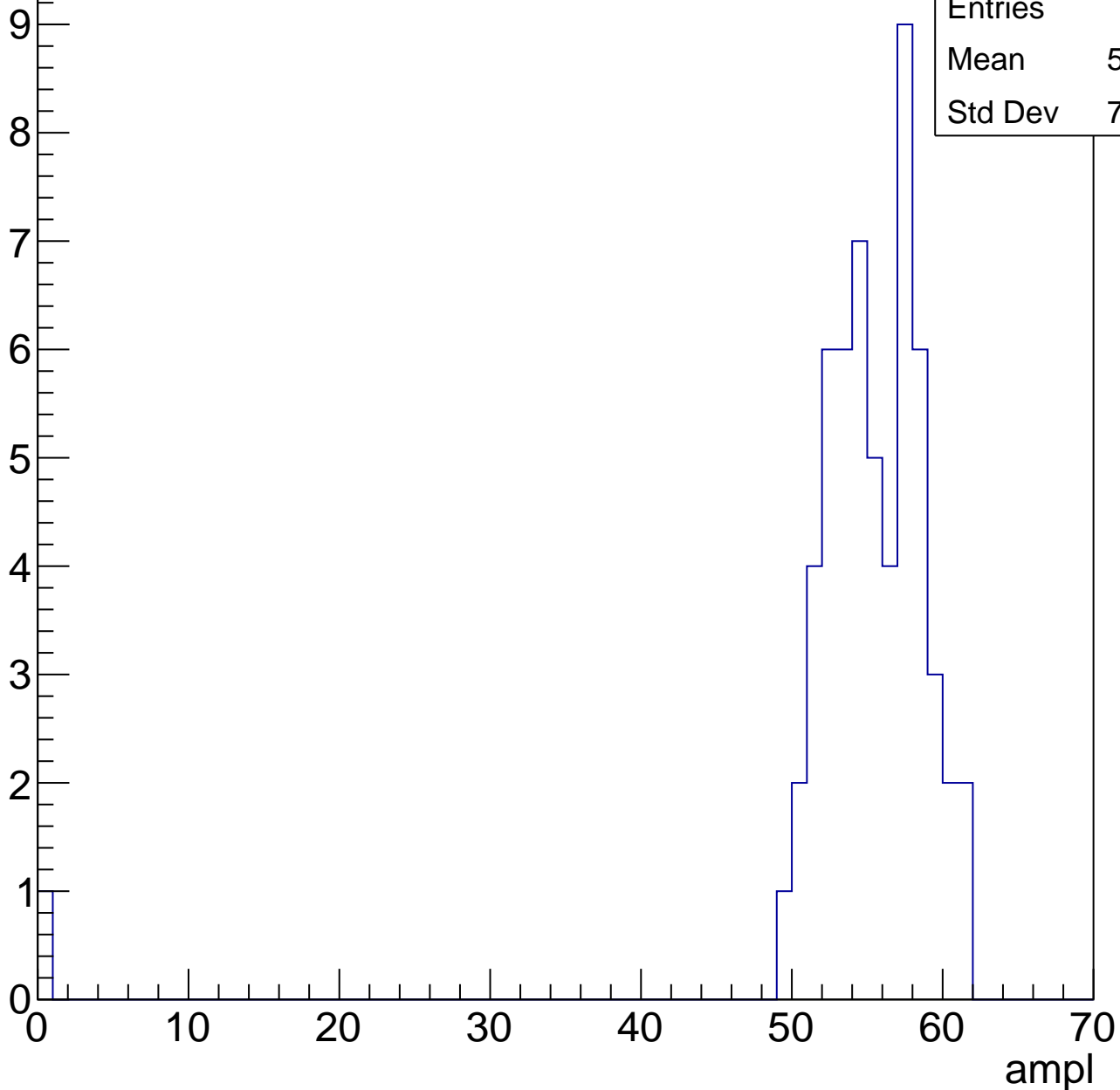


# B0L000S, U18-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	54.14
Std Dev	7.747

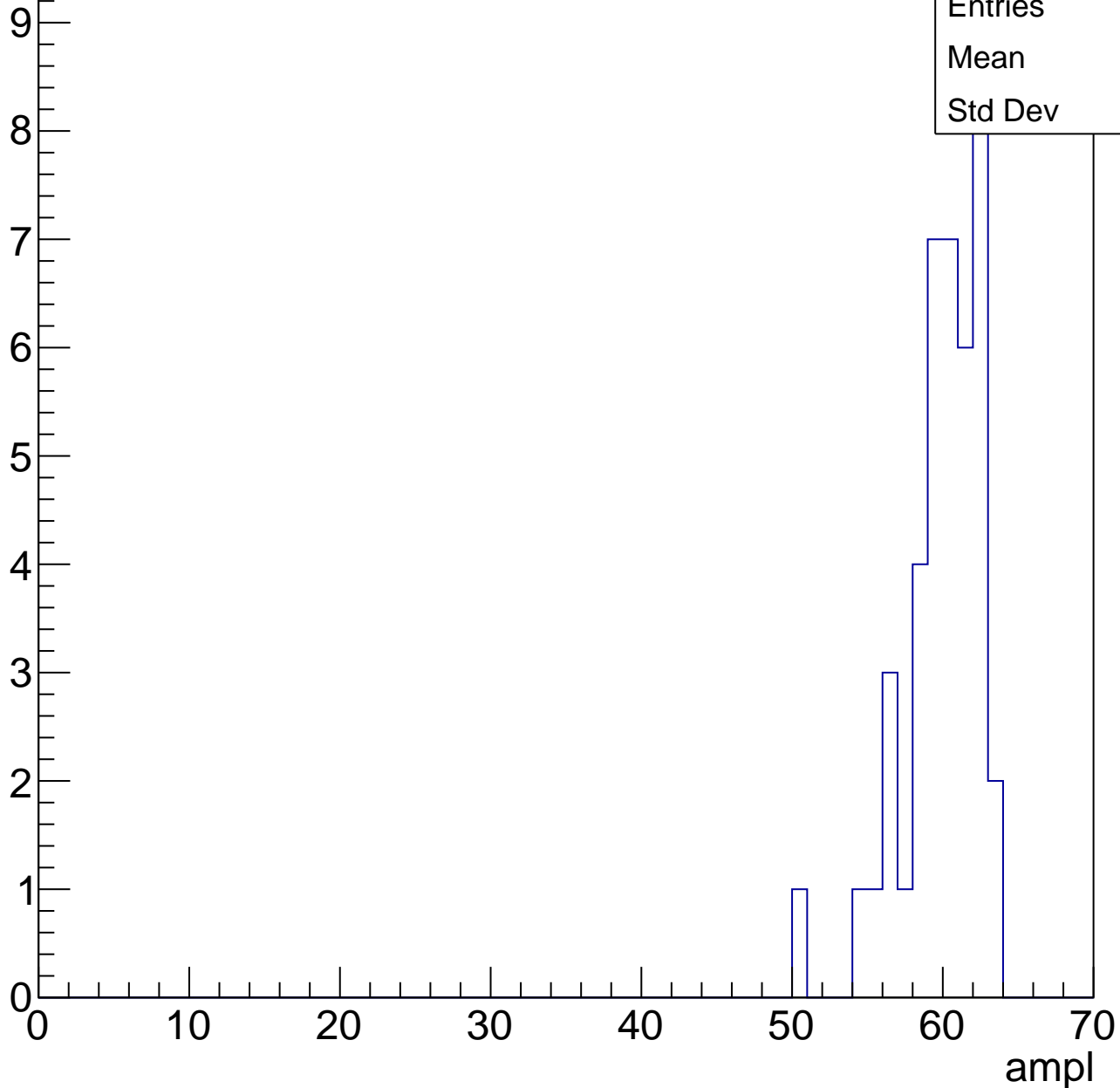


# B0L000S, U18-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	59.5
Std Dev	2.63

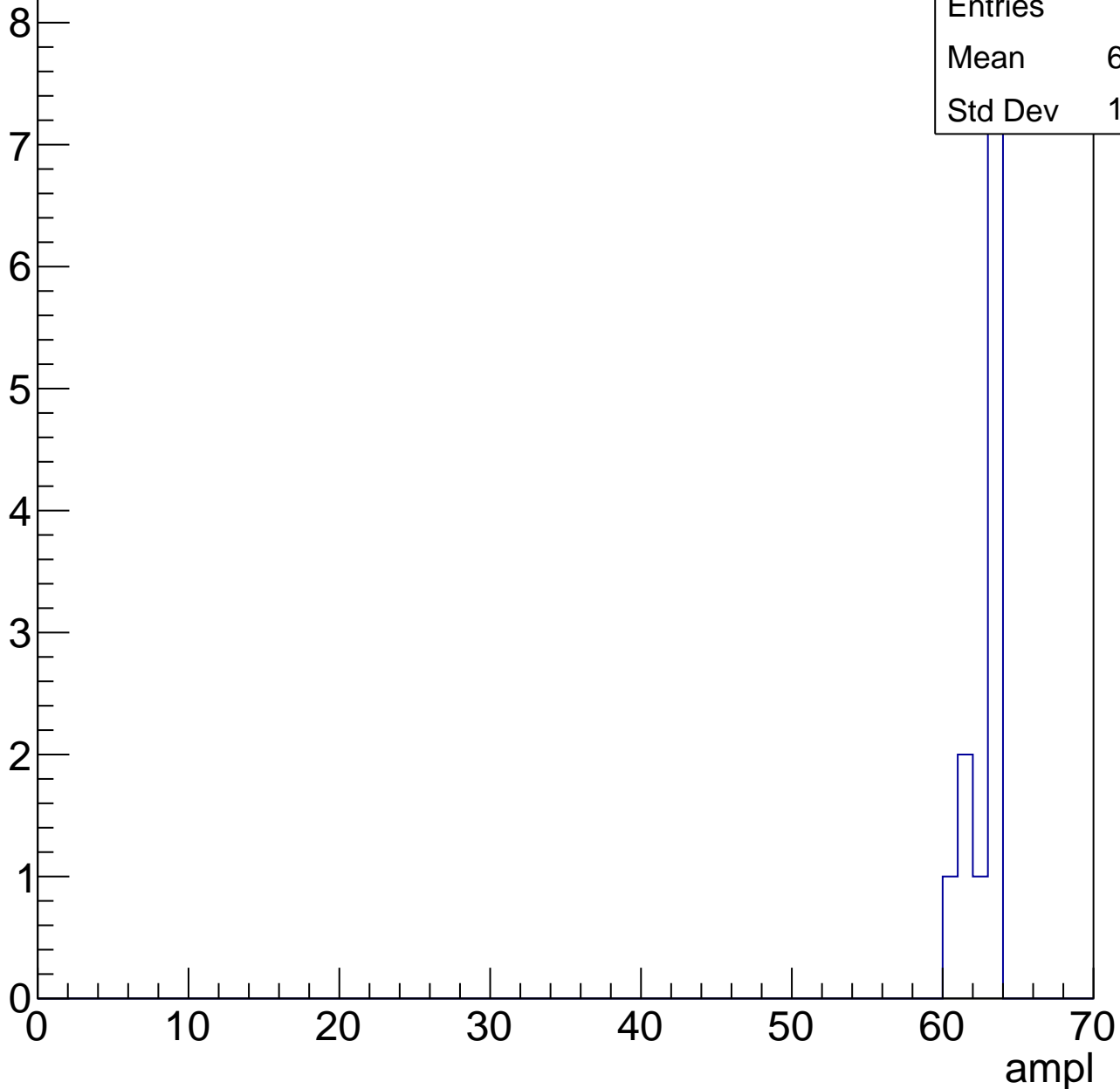


# B0L000S, U18-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	62.33
Std Dev	1.027





# B0L000S, U18-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch14, adc0

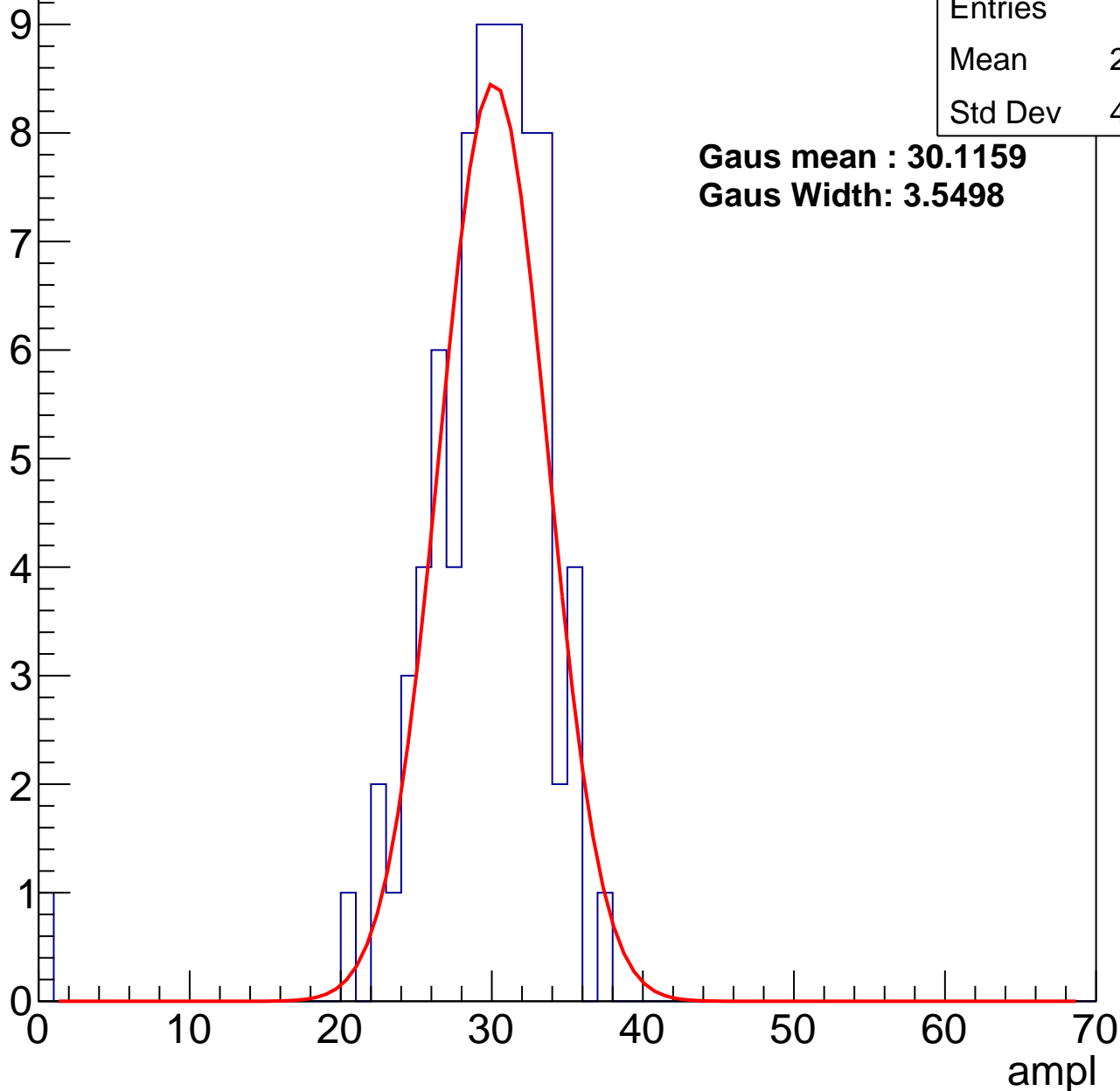
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	29.02
Std Dev	4.712

**Gaus mean : 30.1159**

**Gaus Width: 3.5498**



# B0L000S, U18-ch14, adc1

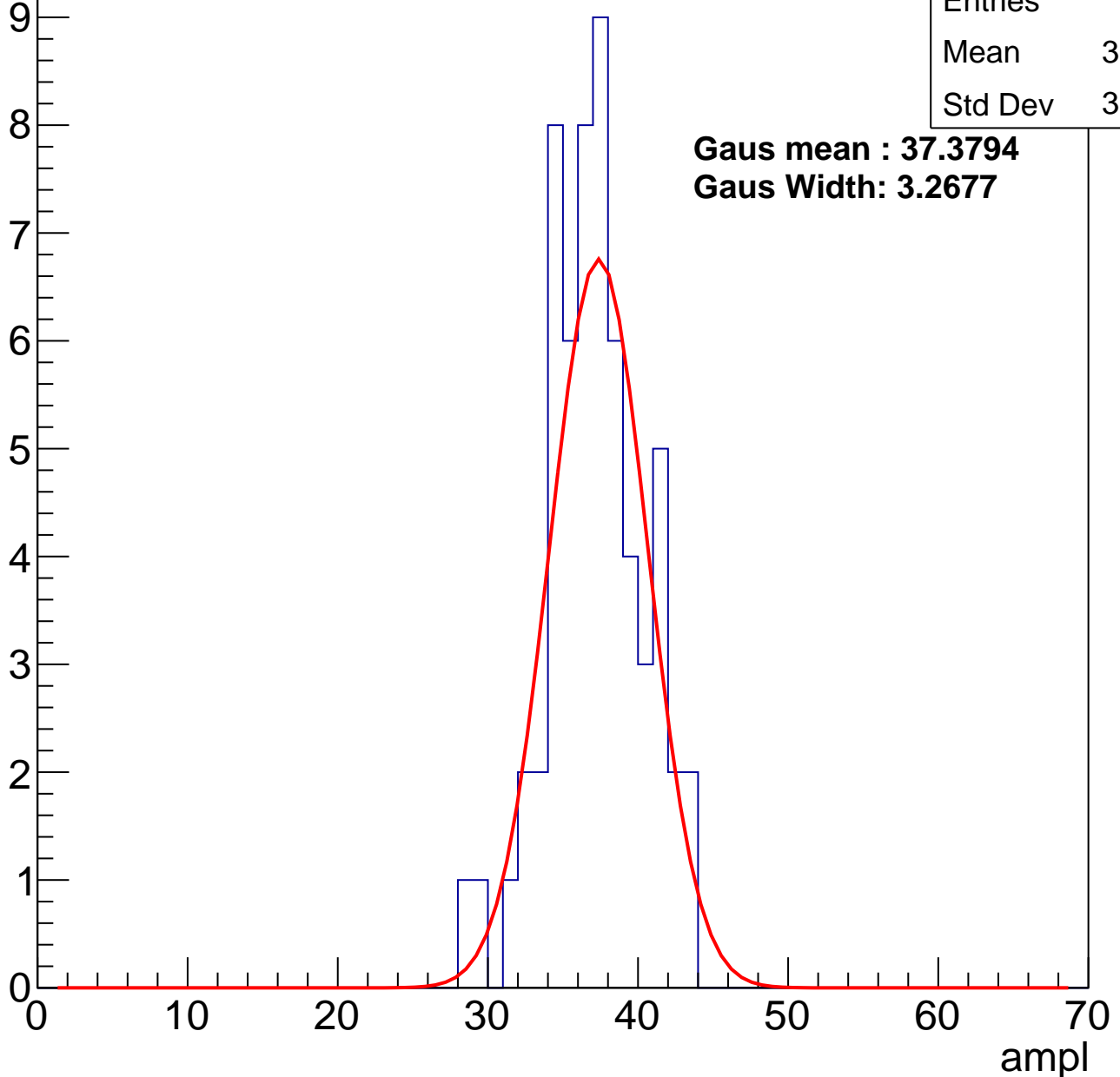
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.67
Std Dev	3.192

**Gaus mean : 37.3794**

**Gaus Width: 3.2677**



# B0L000S, U18-ch14, adc2

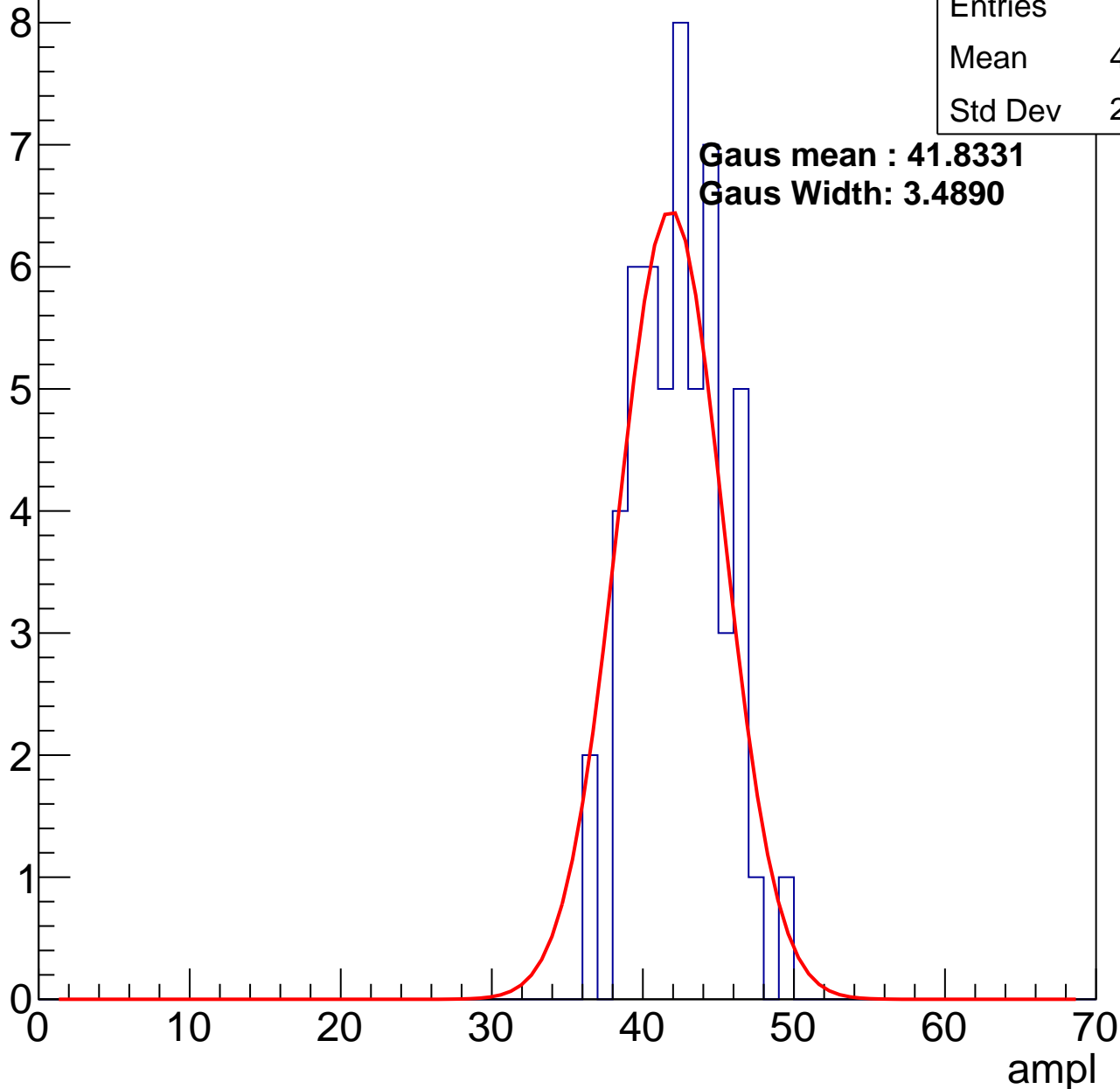
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	41.94
Std Dev	2.858

**Gaus mean : 41.8331**

**Gaus Width: 3.4890**

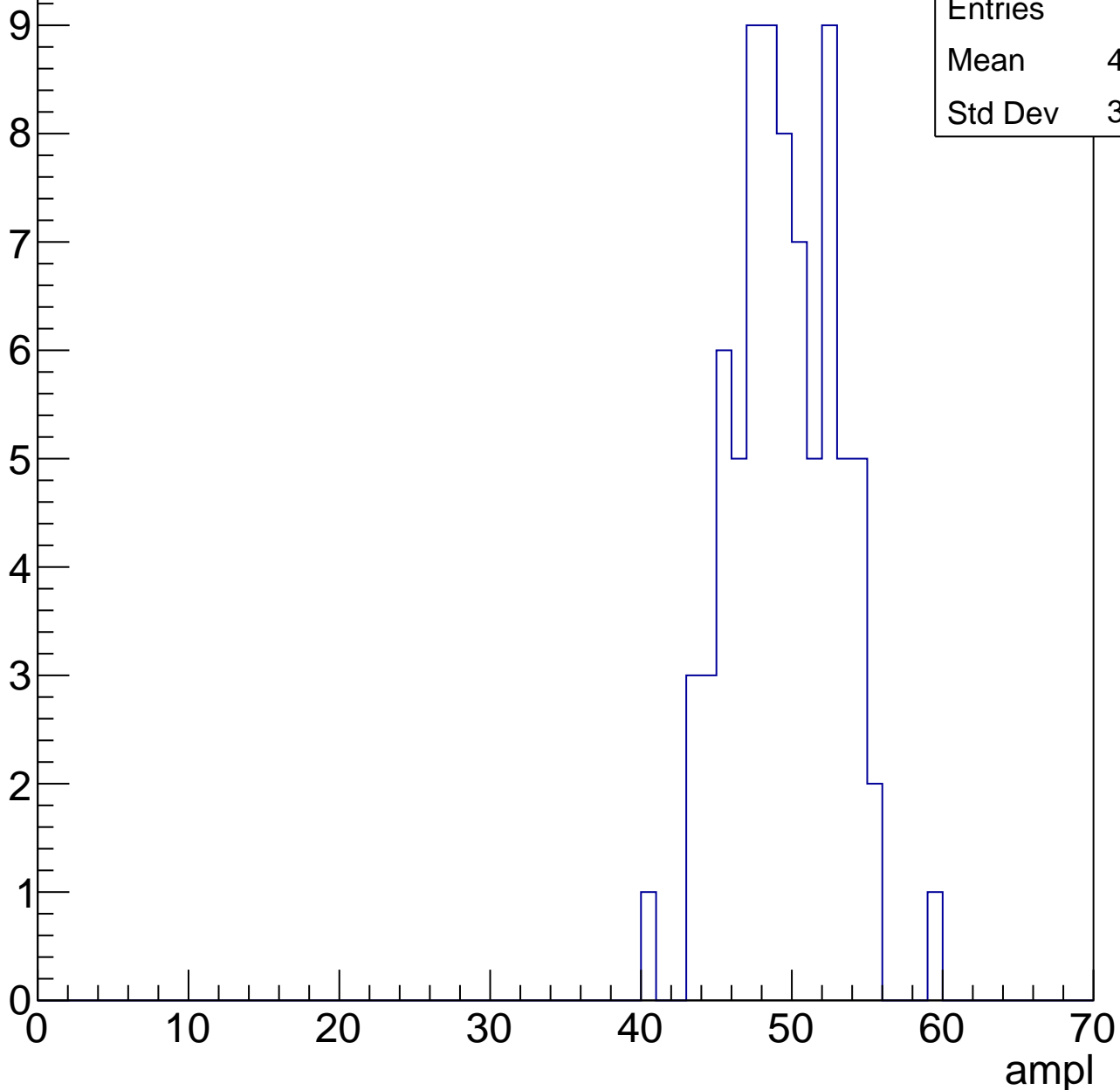


# B0L000S, U18-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	49.04
Std Dev	3.462

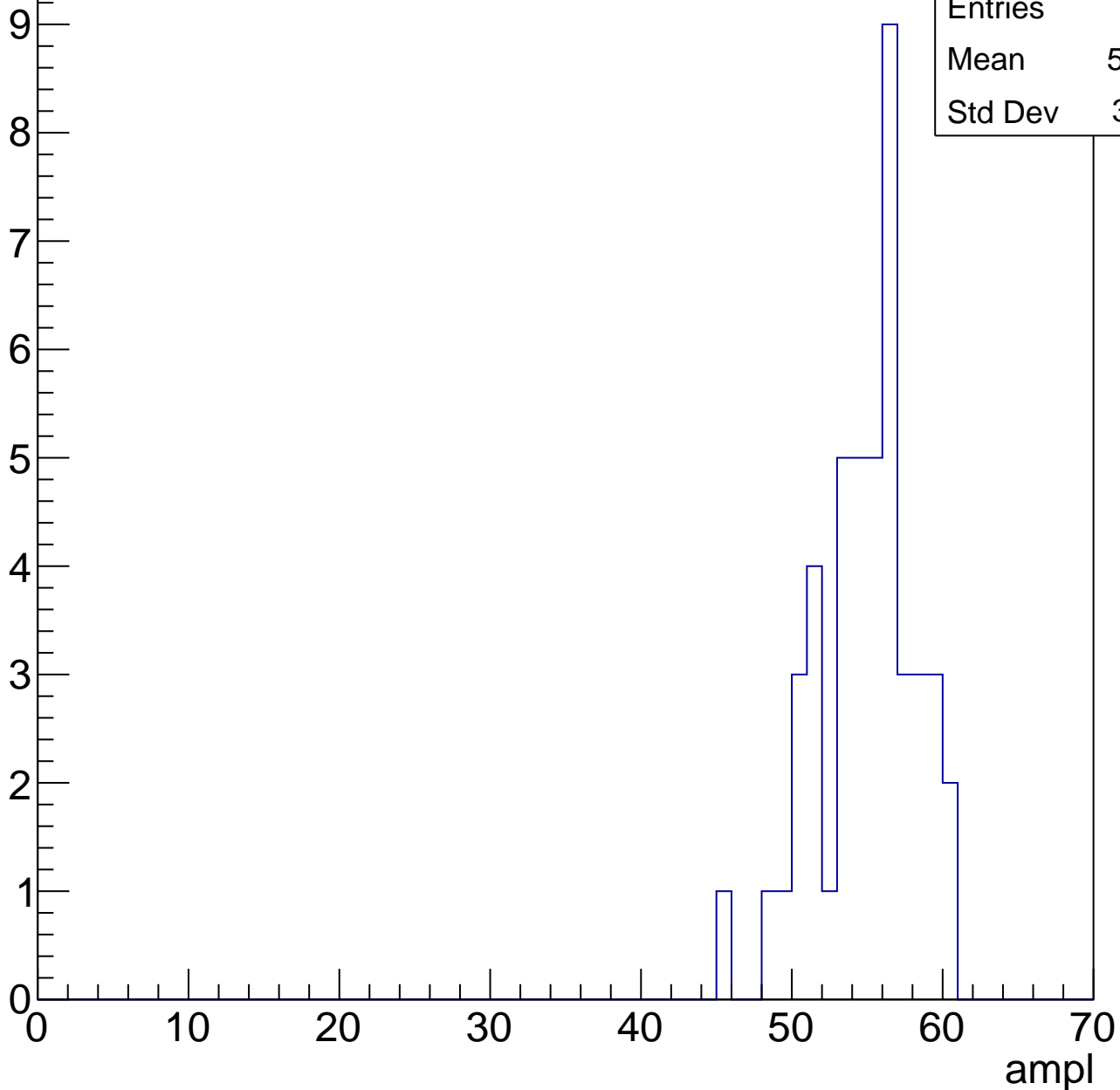


# B0L000S, U18-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	54.43
Std Dev	3.261

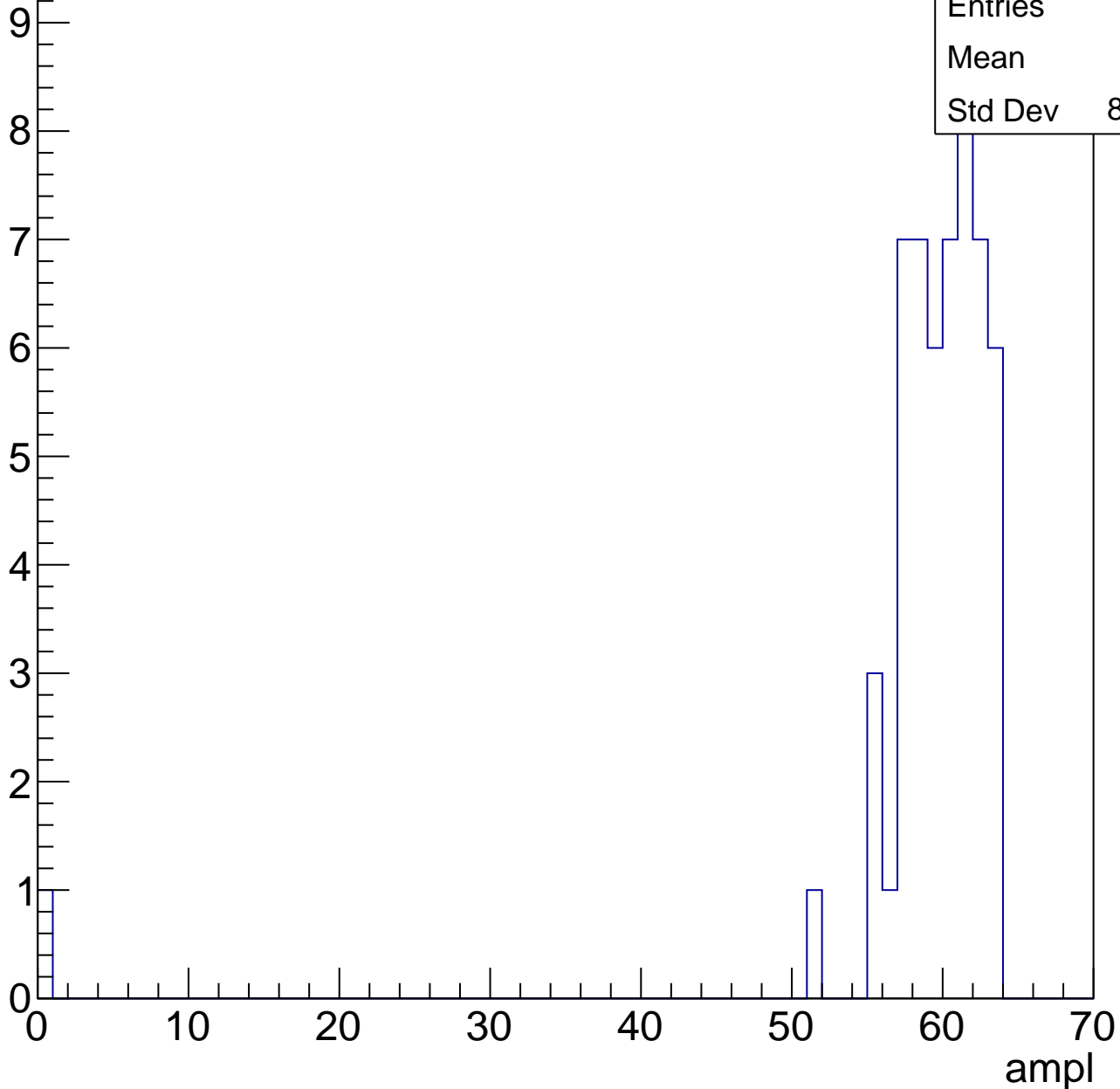


# B0L000S, U18-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.4
Std Dev	8.333



# B0L000S, U18-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

# B0L000S, U18-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	65
Mean	30.2
Std Dev	2.926

**Gaus mean : 30.2327**

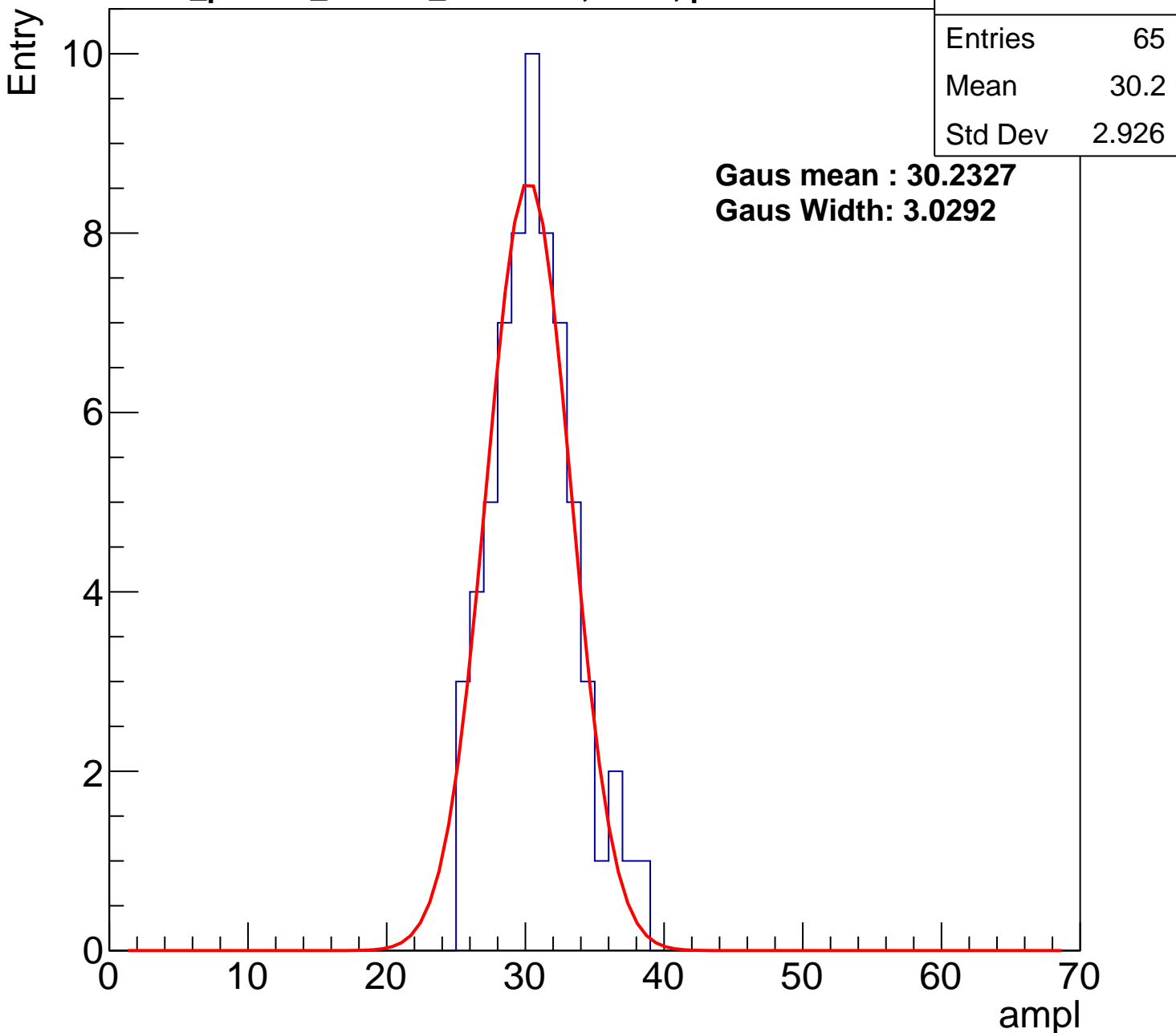
**Gaus Width: 3.0292**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



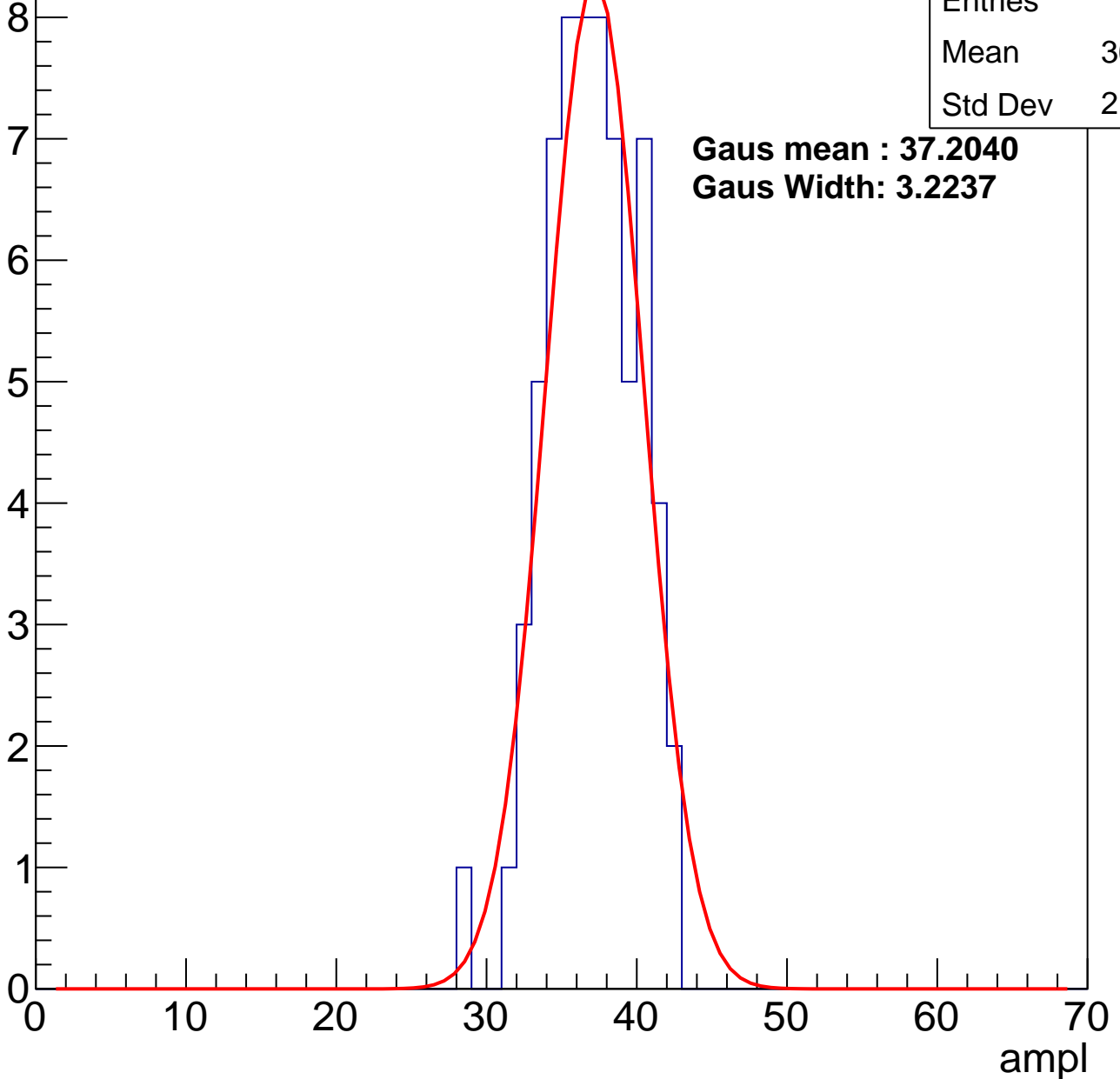
# B0L000S, U18-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.53
Std Dev	2.924

**Gaus mean : 37.2040**  
**Gaus Width: 3.2237**

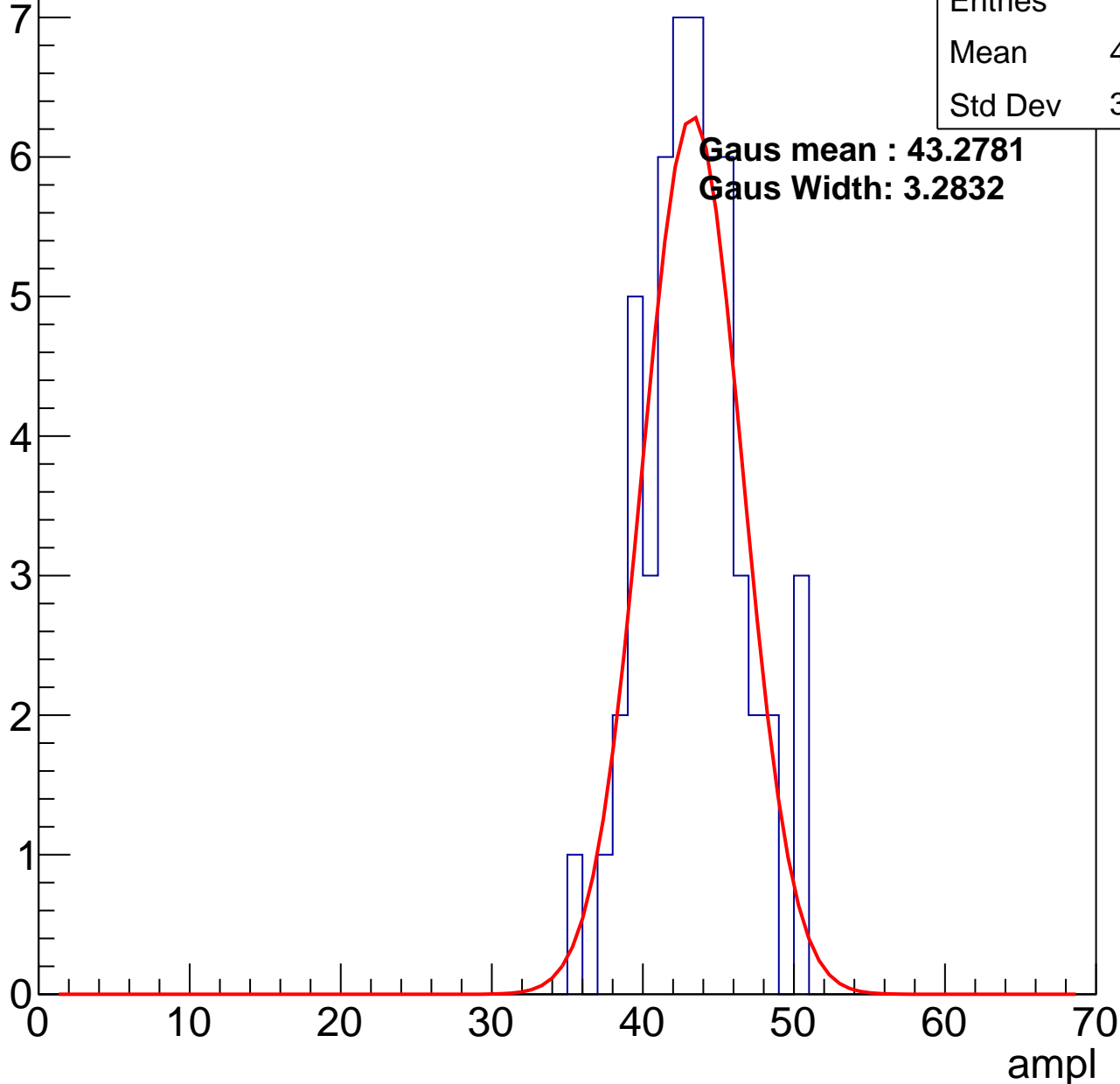


# B0L000S, U18-ch15, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	42.89
Std Dev	3.258

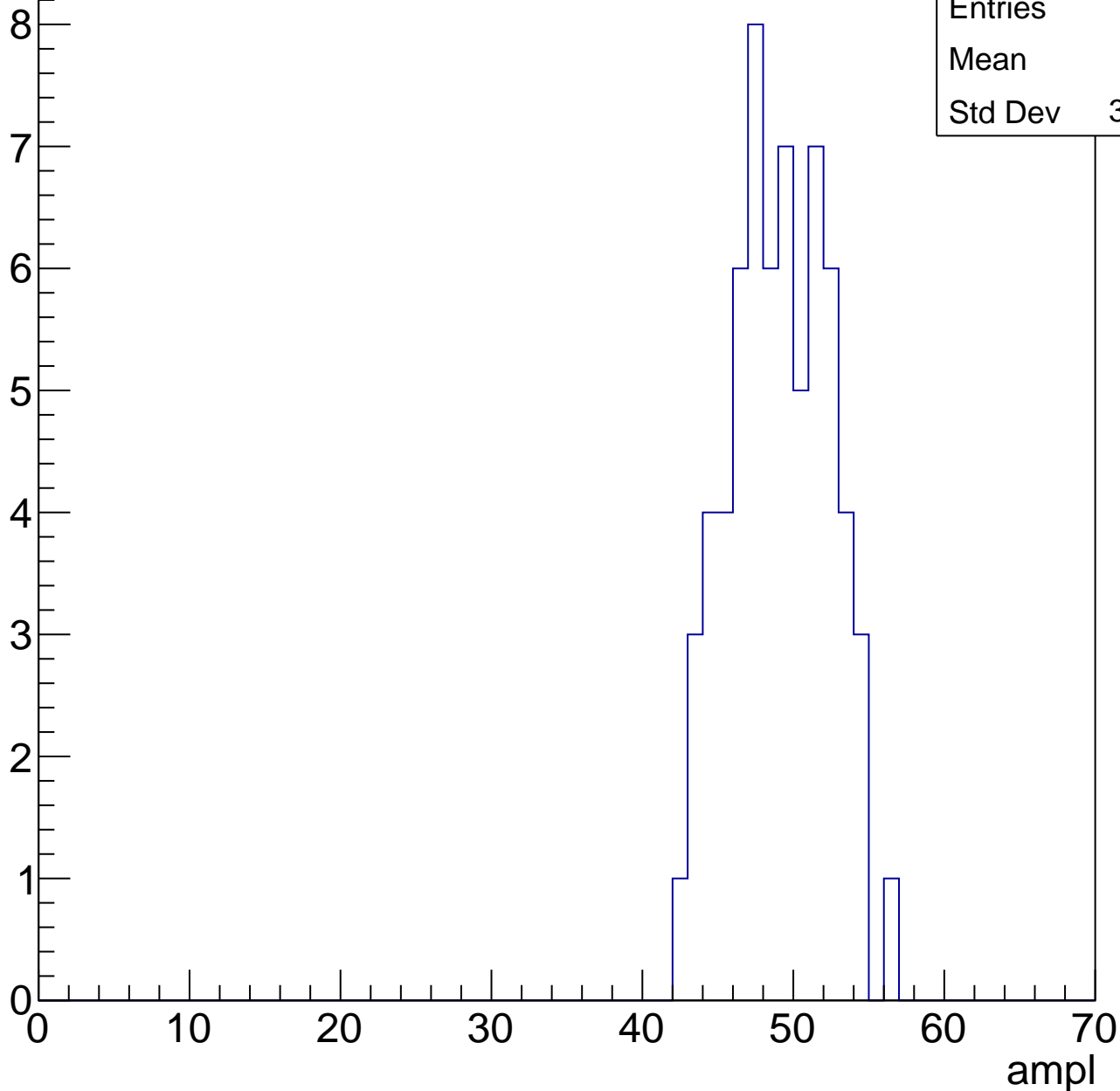


# B0L000S, U18-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.6
Std Dev	3.229

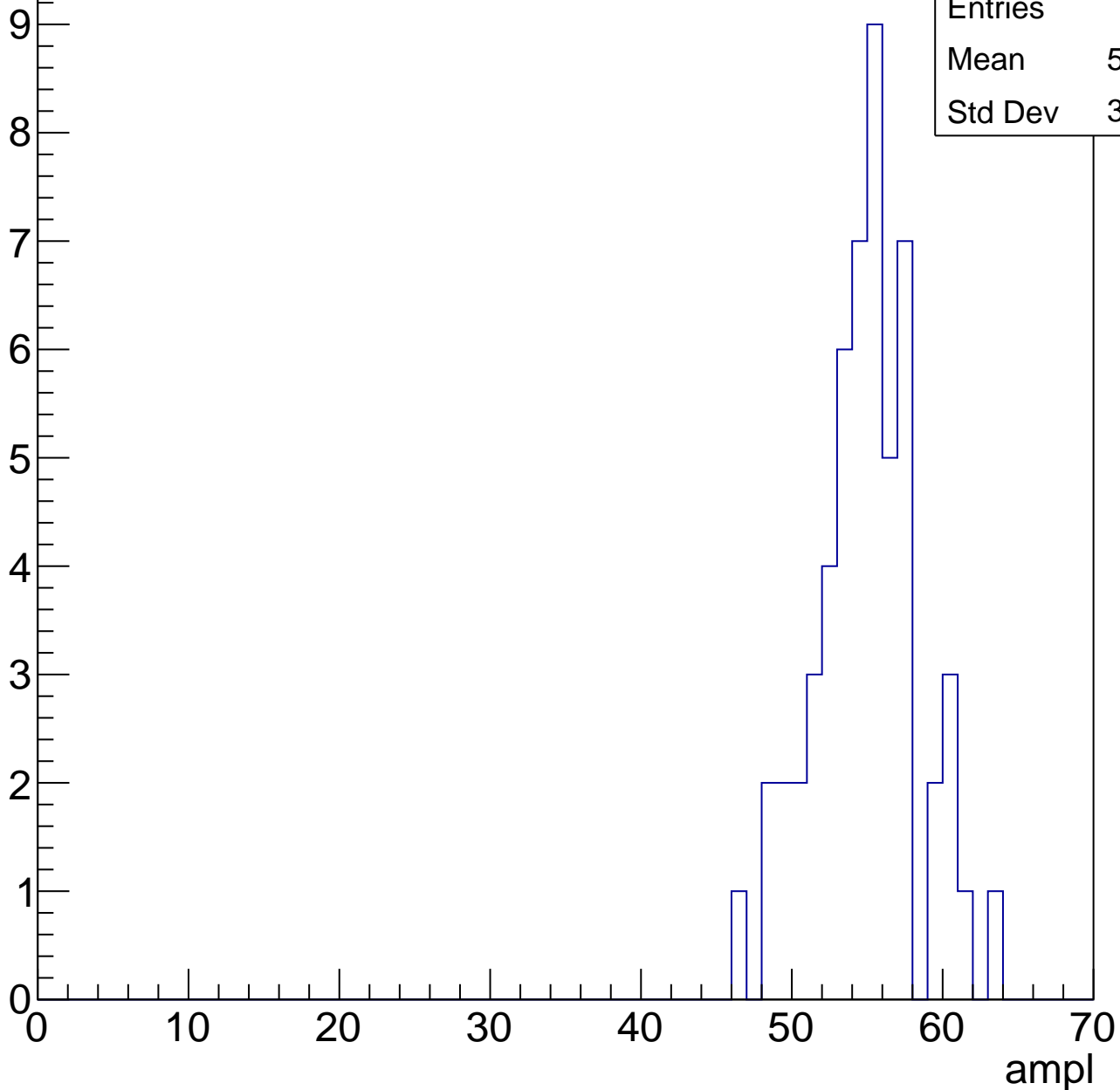


# B0L000S, U18-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.42
Std Dev	3.404



# B0L000S, U18-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	55
Mean	58.44
Std Dev	8.362

Entry

10

8

6

4

2

0

0

10

20

30

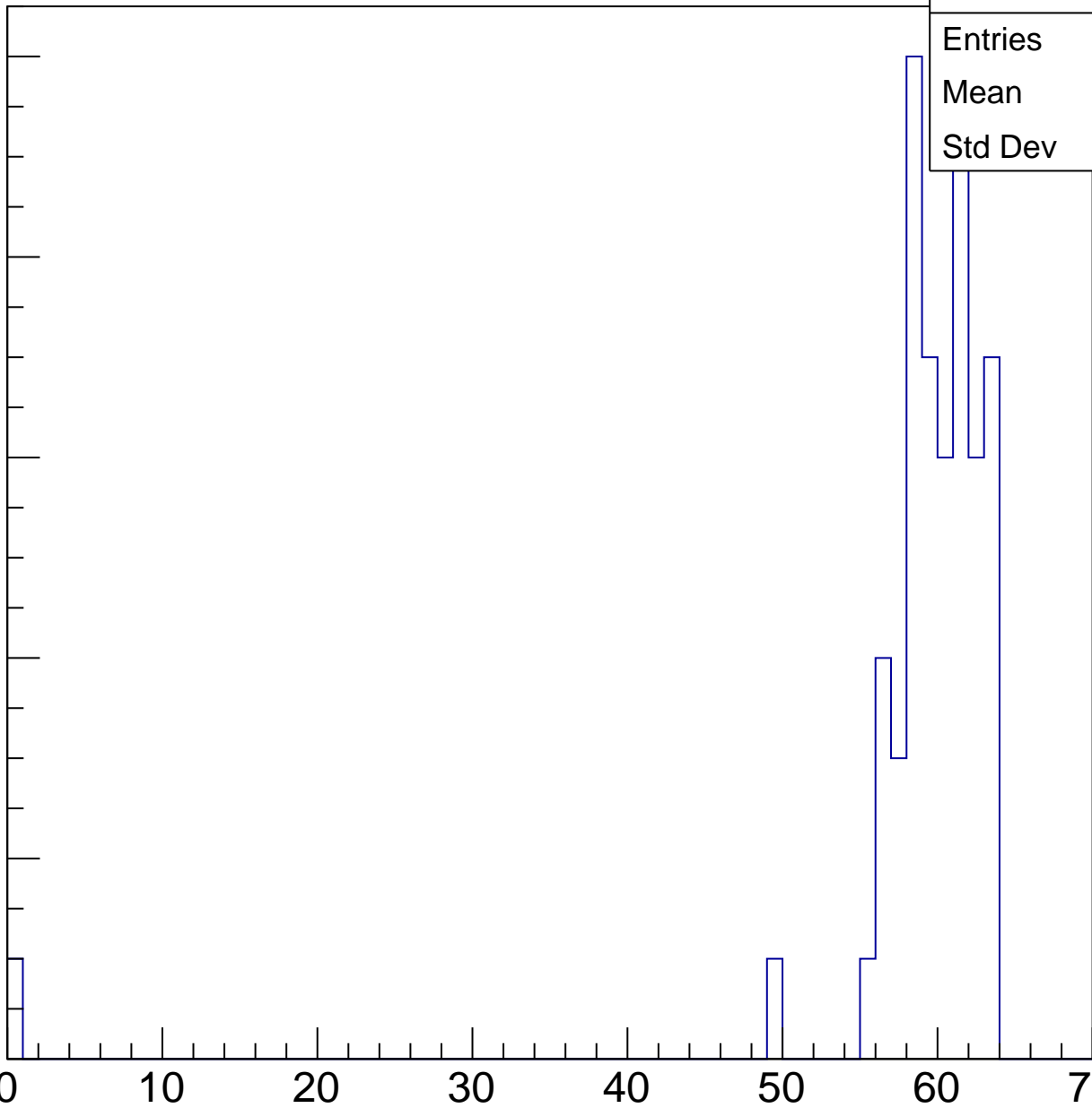
40

50

60

70

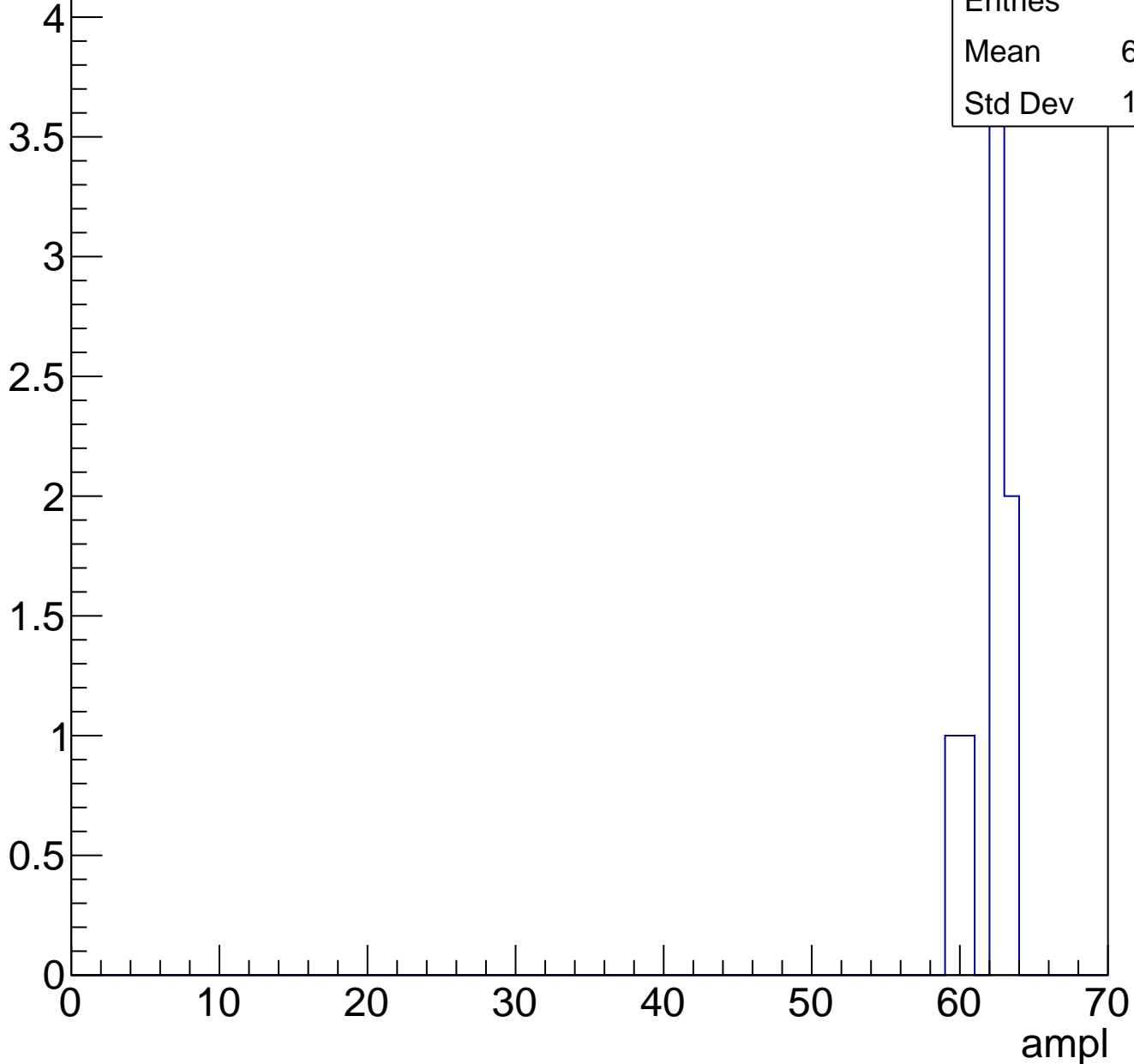
ampl



# B0L000S, U18-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl

# B0L000S, U18-ch16, adc0

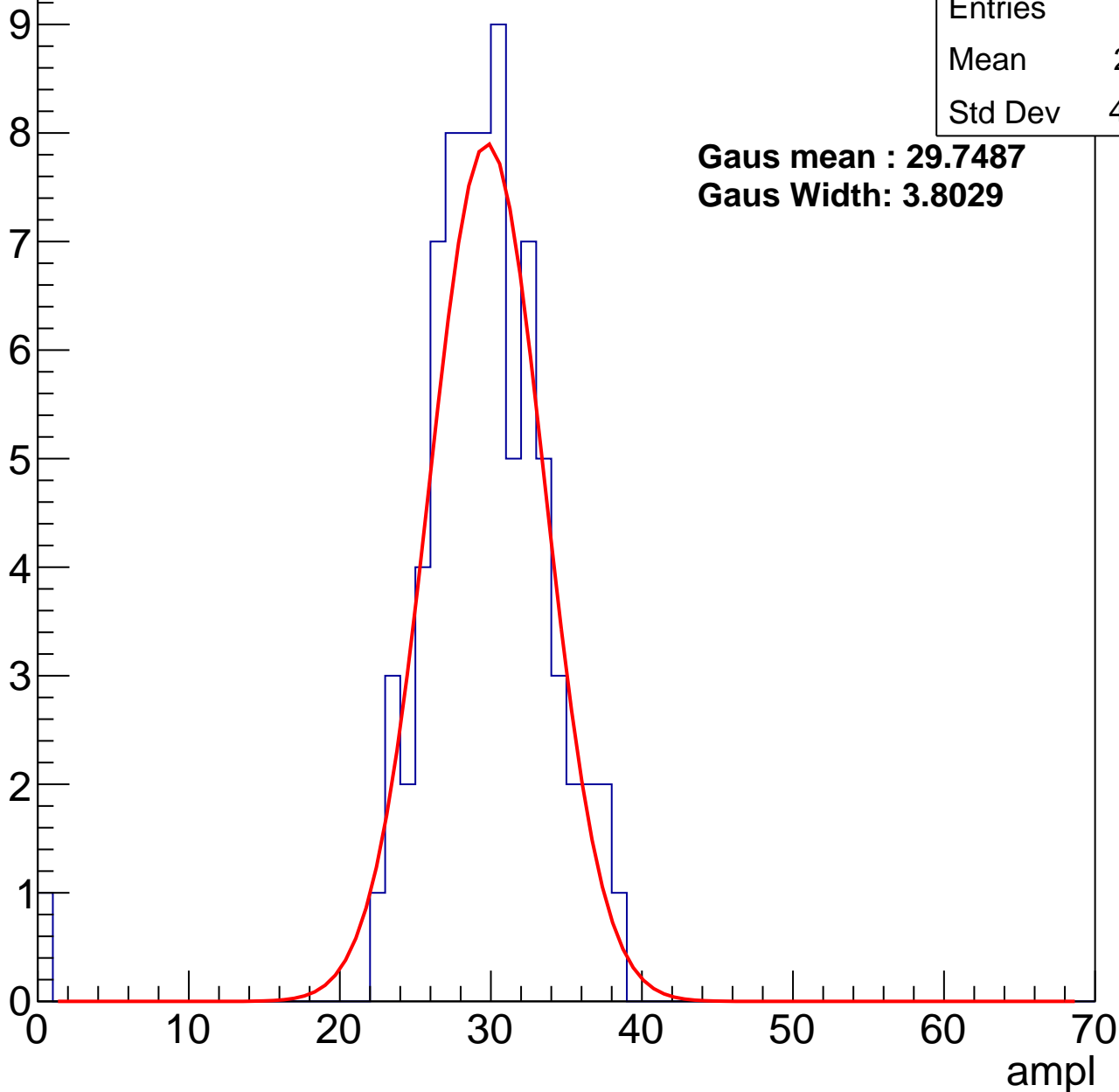
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	29.01
Std Dev	4.858

**Gaus mean : 29.7487**

**Gaus Width: 3.8029**



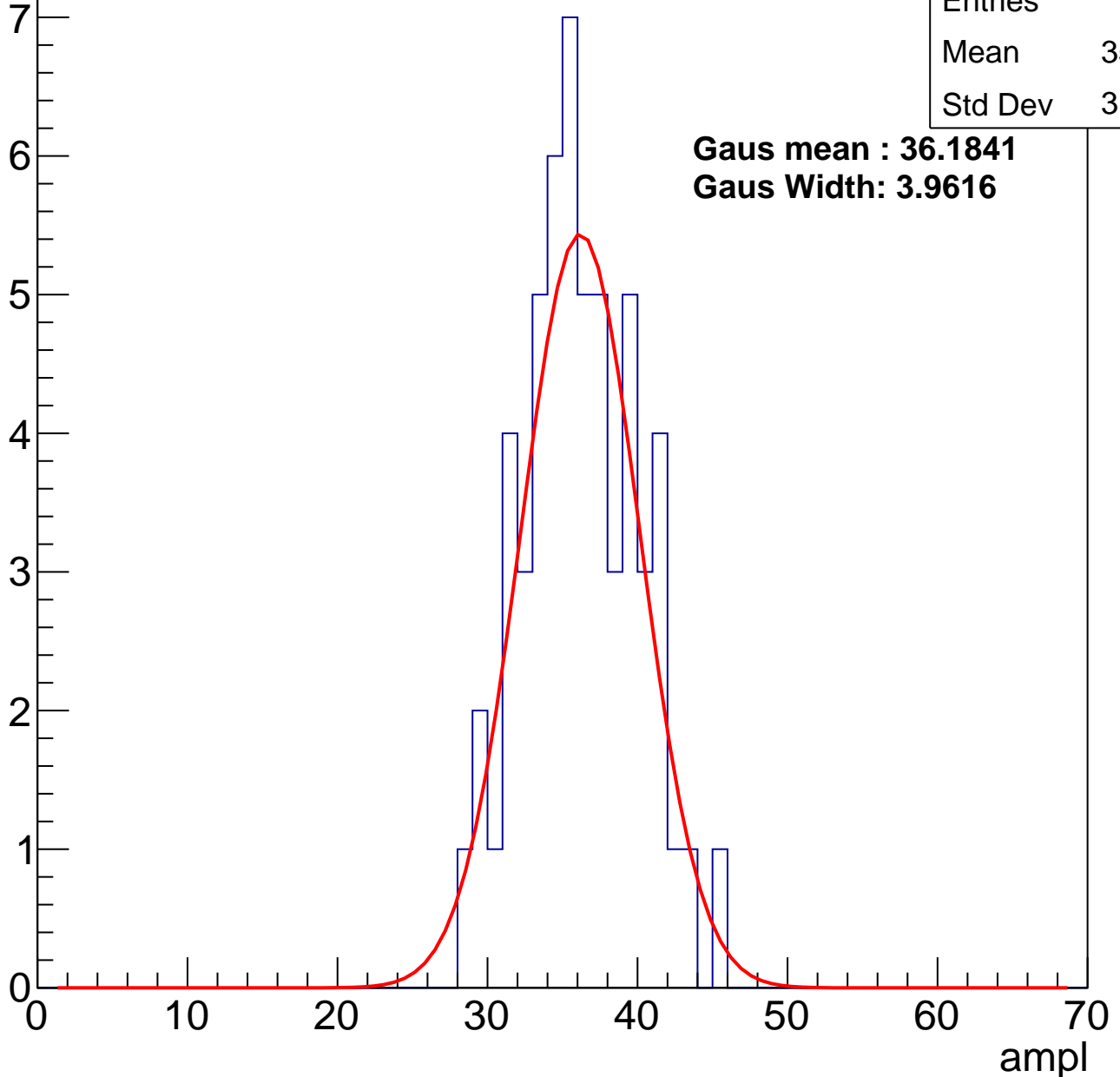
# B0L000S, U18-ch16, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.75
Std Dev	3.734

**Gaus mean : 36.1841**  
**Gaus Width: 3.9616**



# B0L000S, U18-ch16, adc2

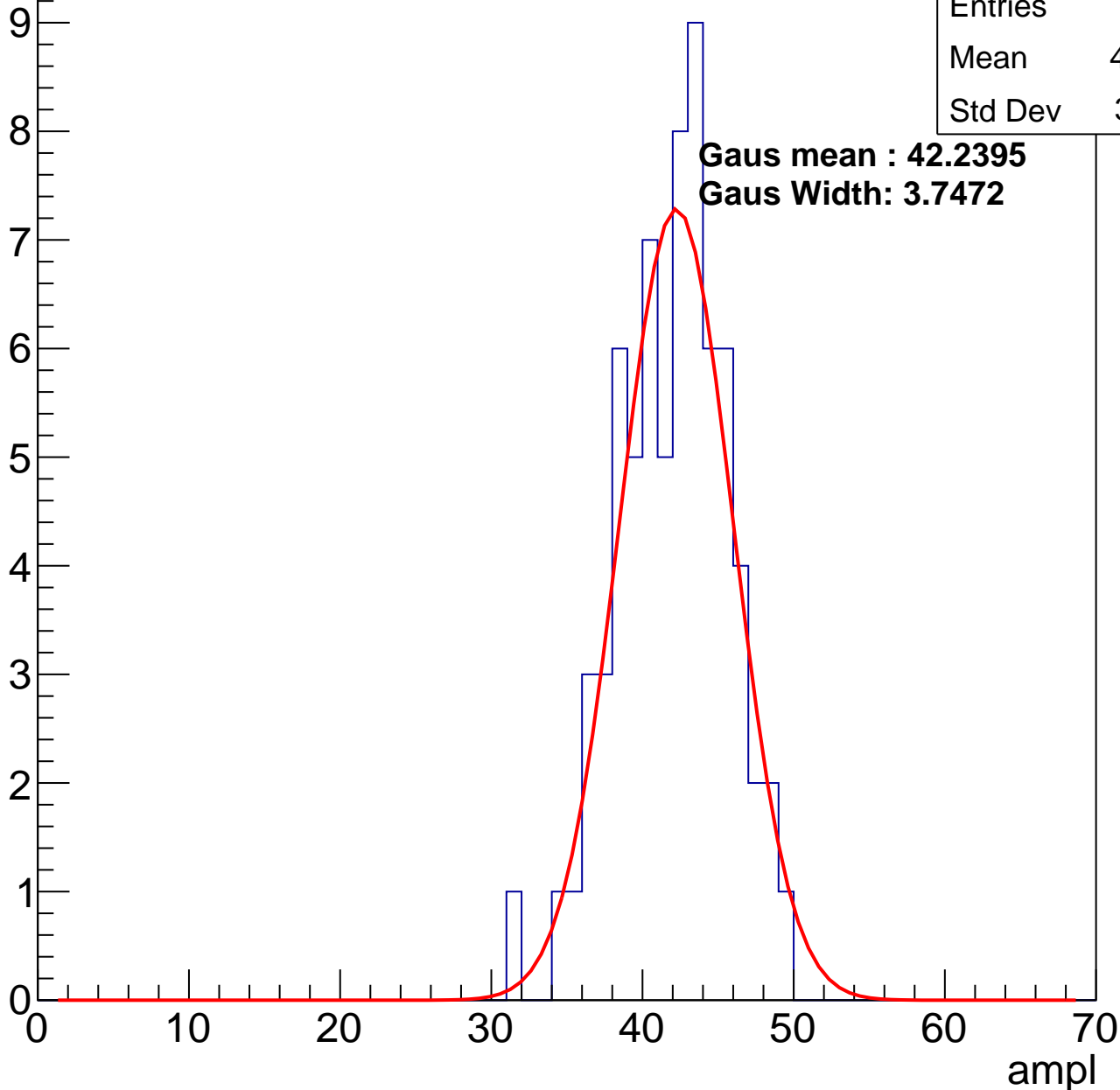
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.53
Std Dev	3.581

**Gaus mean : 42.2395**

**Gaus Width: 3.7472**

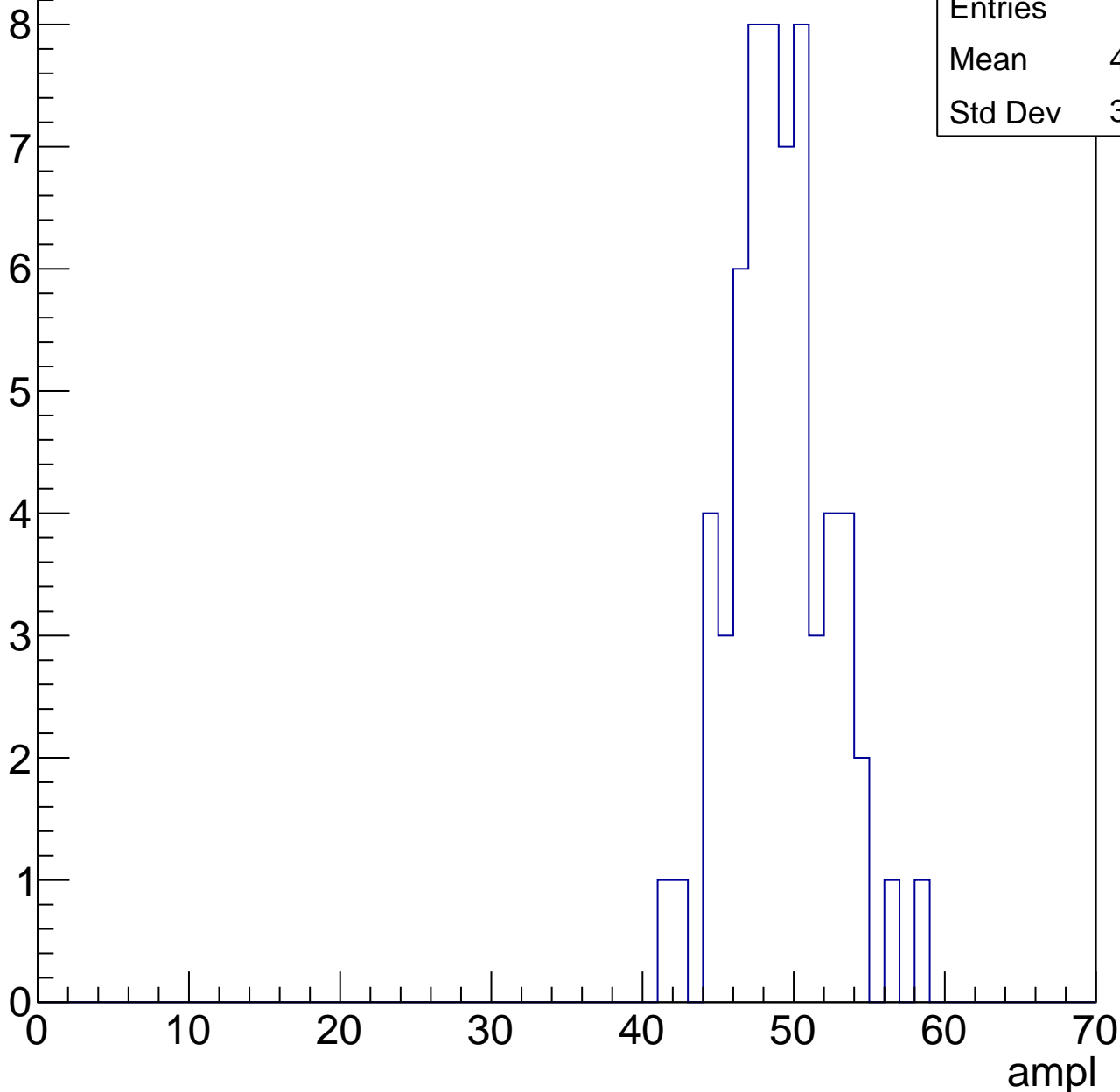


# B0L000S, U18-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.66
Std Dev	3.259

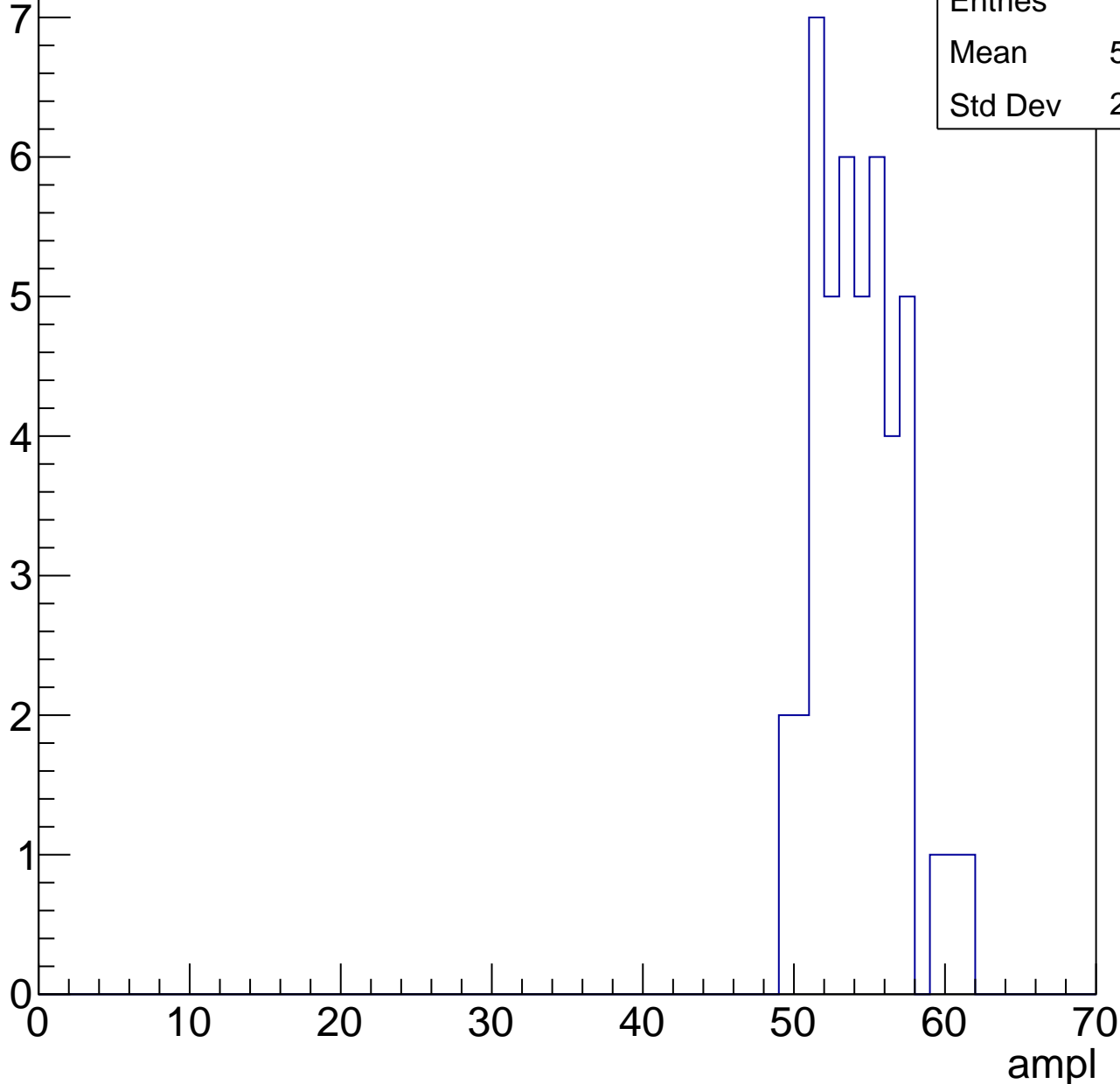


# B0L000S, U18-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

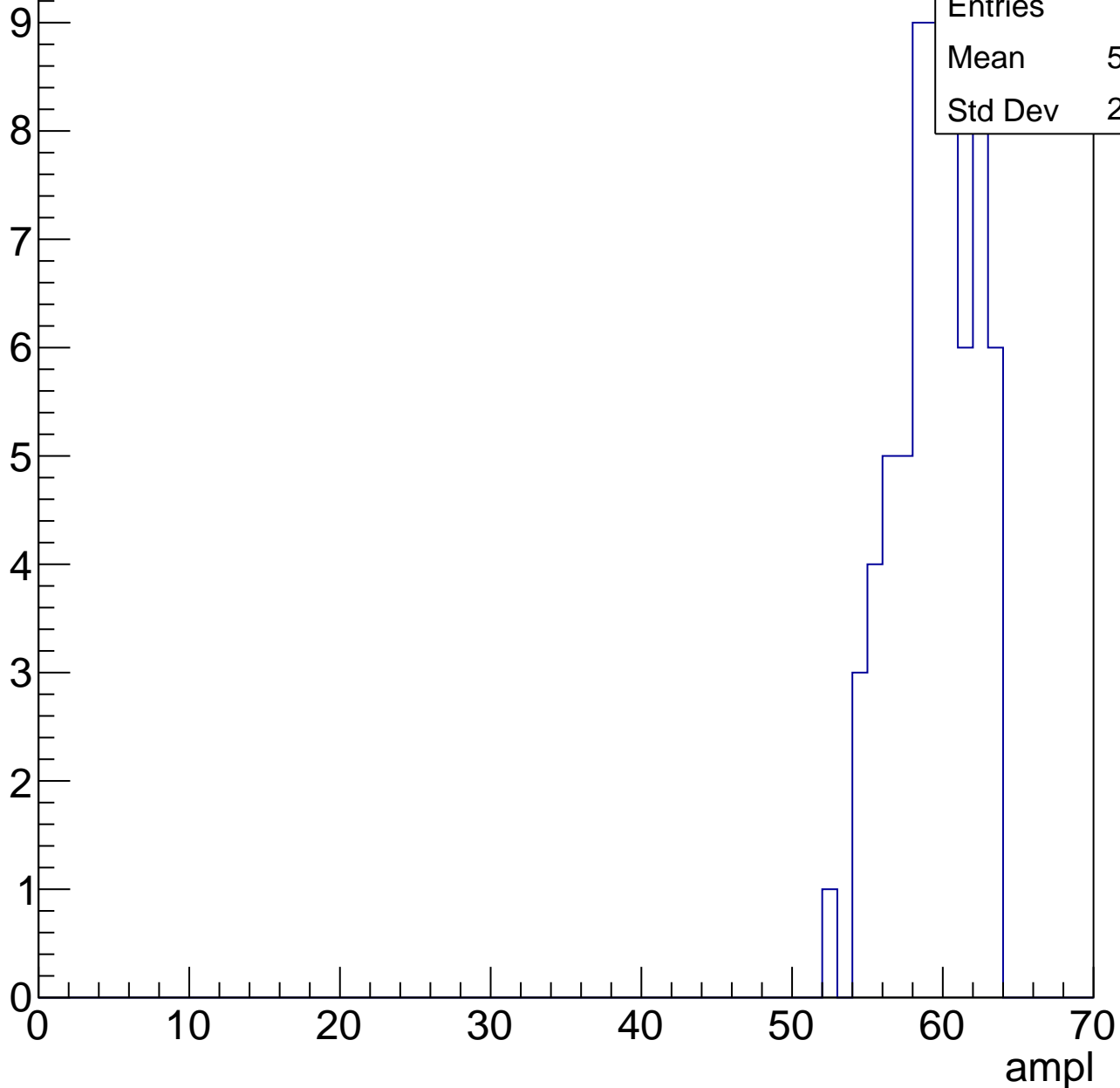
Entries	45
Mean	53.82
Std Dev	2.775



# B0L000S, U18-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

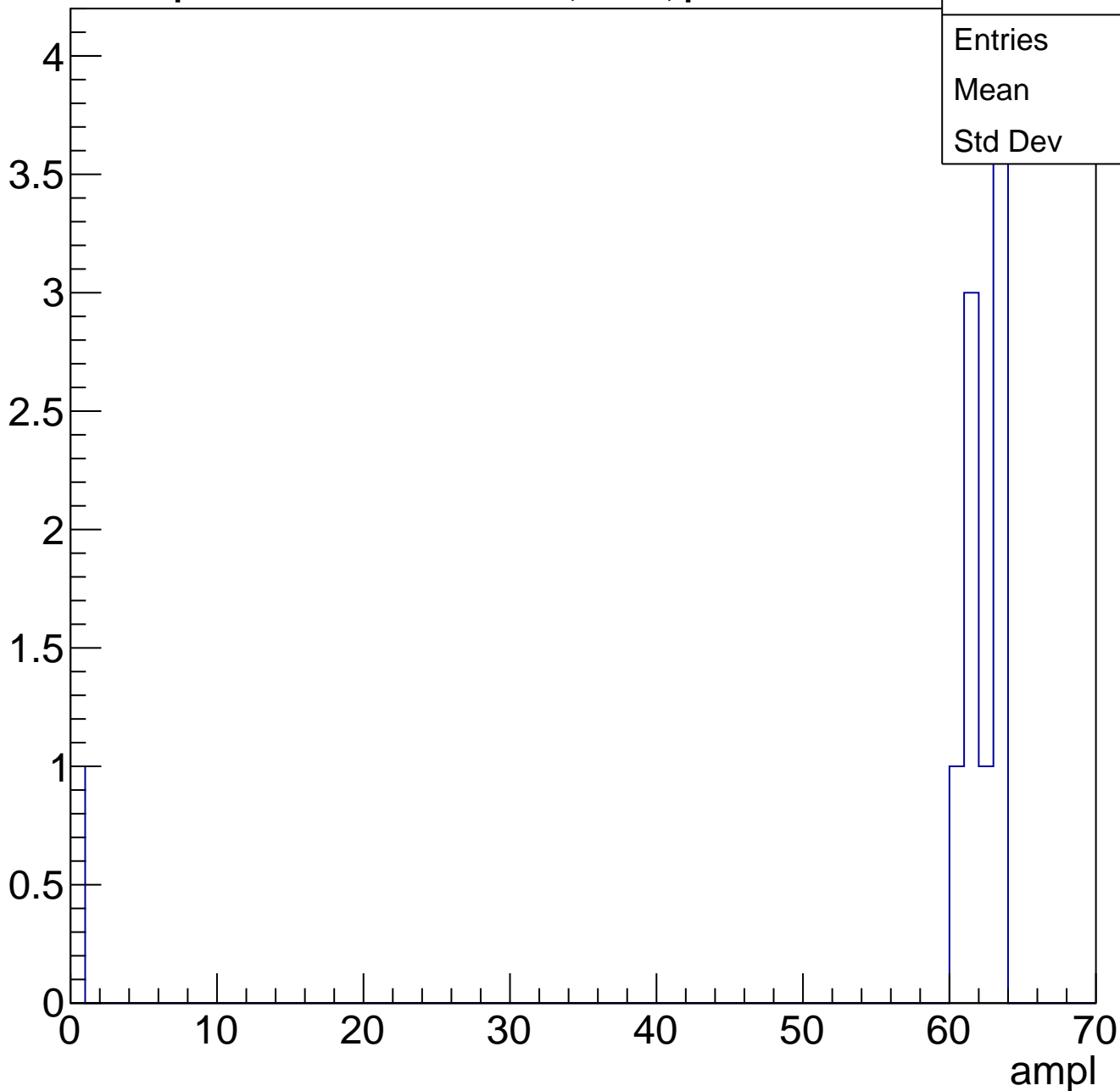


Entries	65
Mean	58.98
Std Dev	2.692

# B0L000S, U18-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch17, adc0

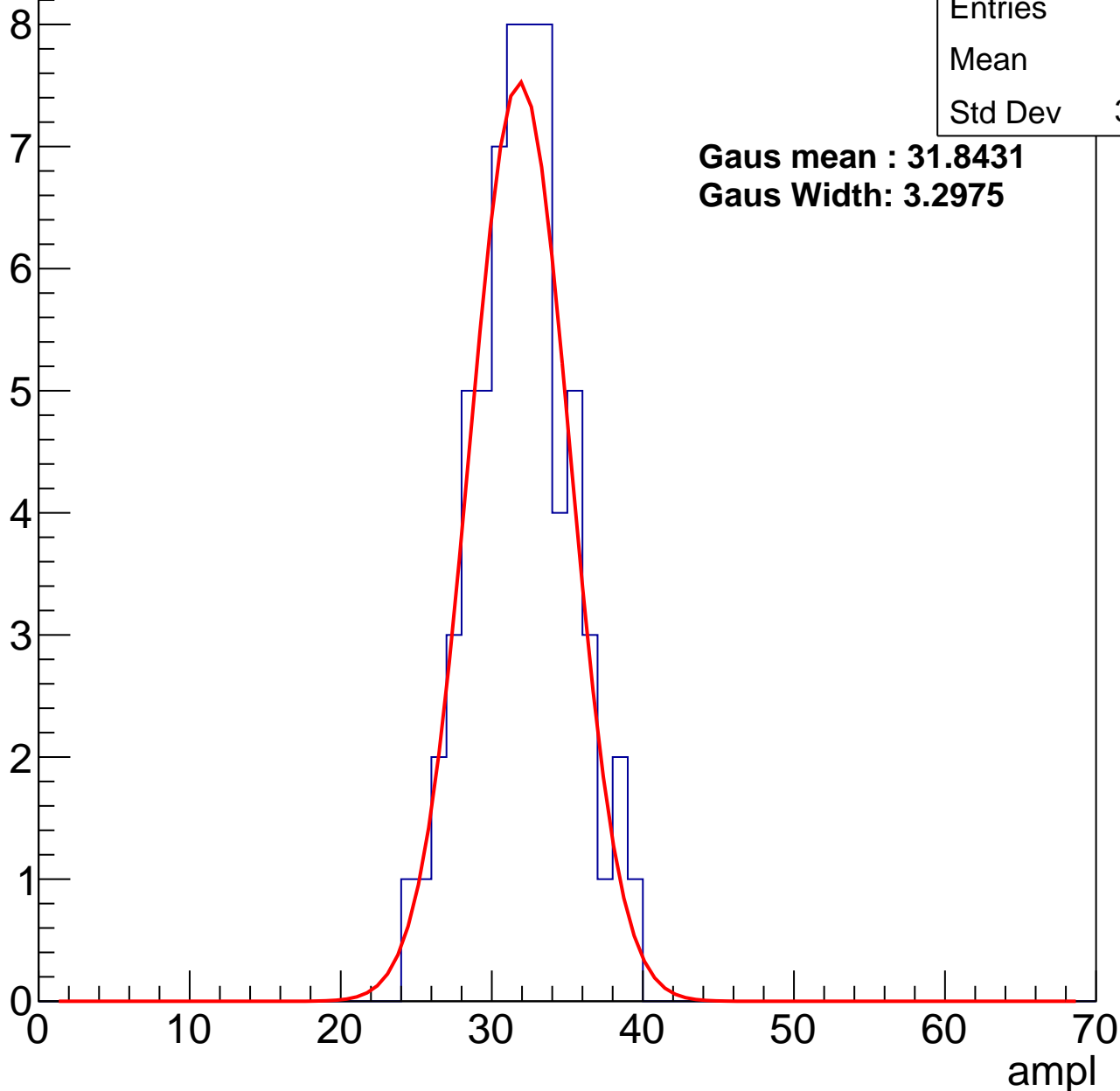
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	31.5
Std Dev	3.231

**Gaus mean : 31.8431**

**Gaus Width: 3.2975**



# B0L000S, U18-ch17, adc1

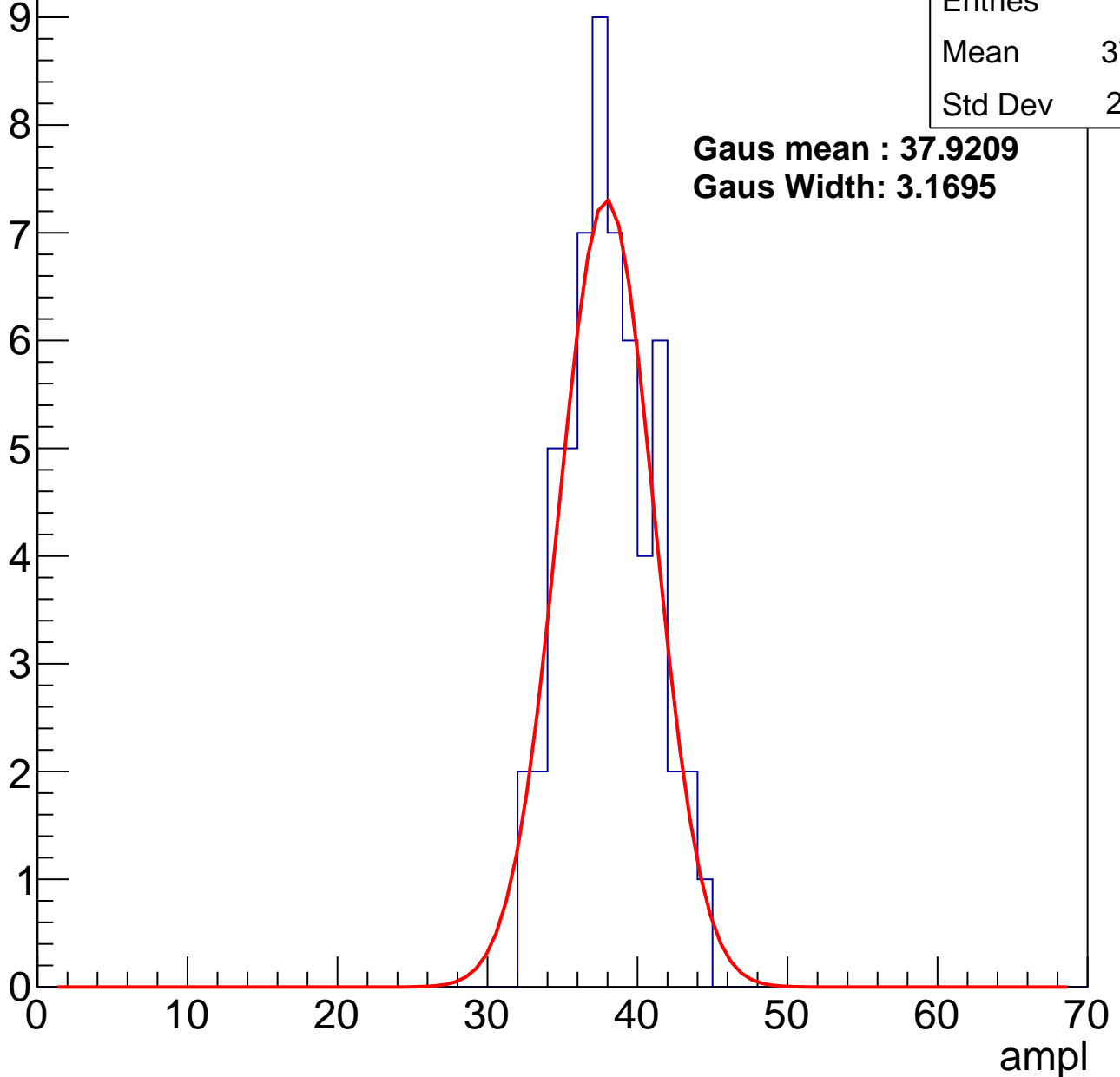
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	37.59
Std Dev	2.841

**Gaus mean : 37.9209**

**Gaus Width: 3.1695**



# B0L000S, U18-ch17, adc2

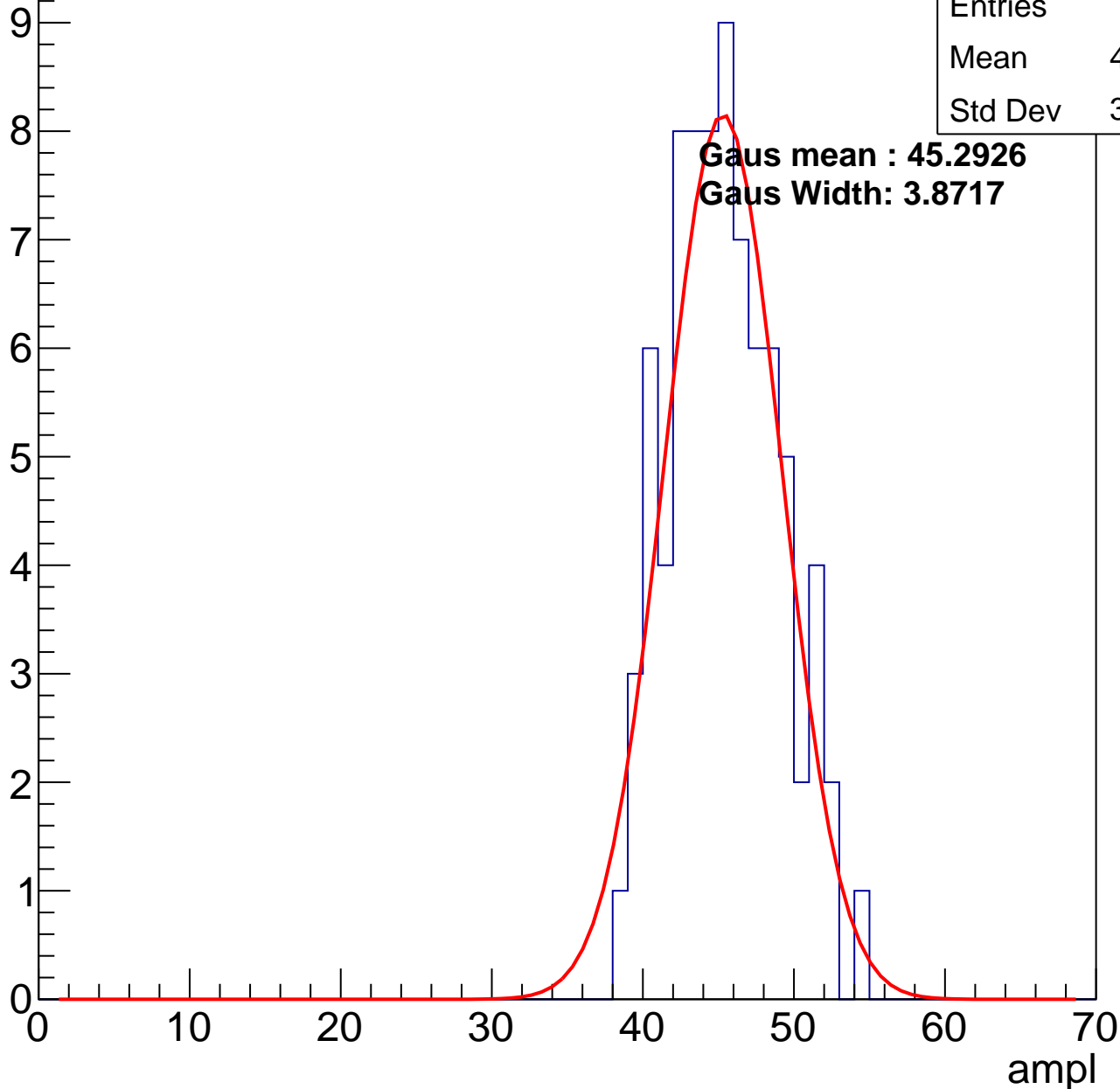
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	44.94
Std Dev	3.572

**Gaus mean : 45.2926**

**Gaus Width: 3.8717**

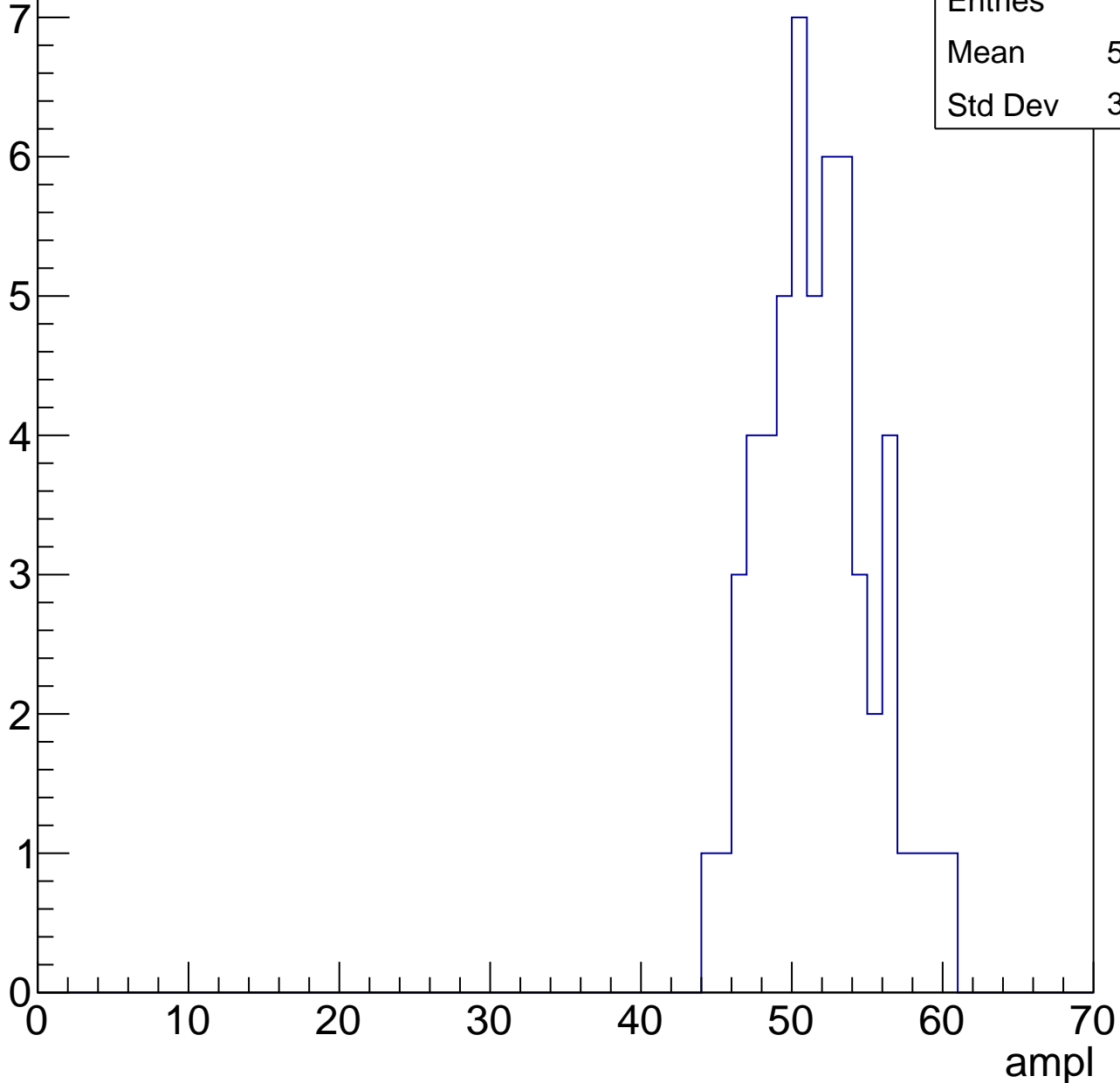


# B0L000S, U18-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	51.22
Std Dev	3.576

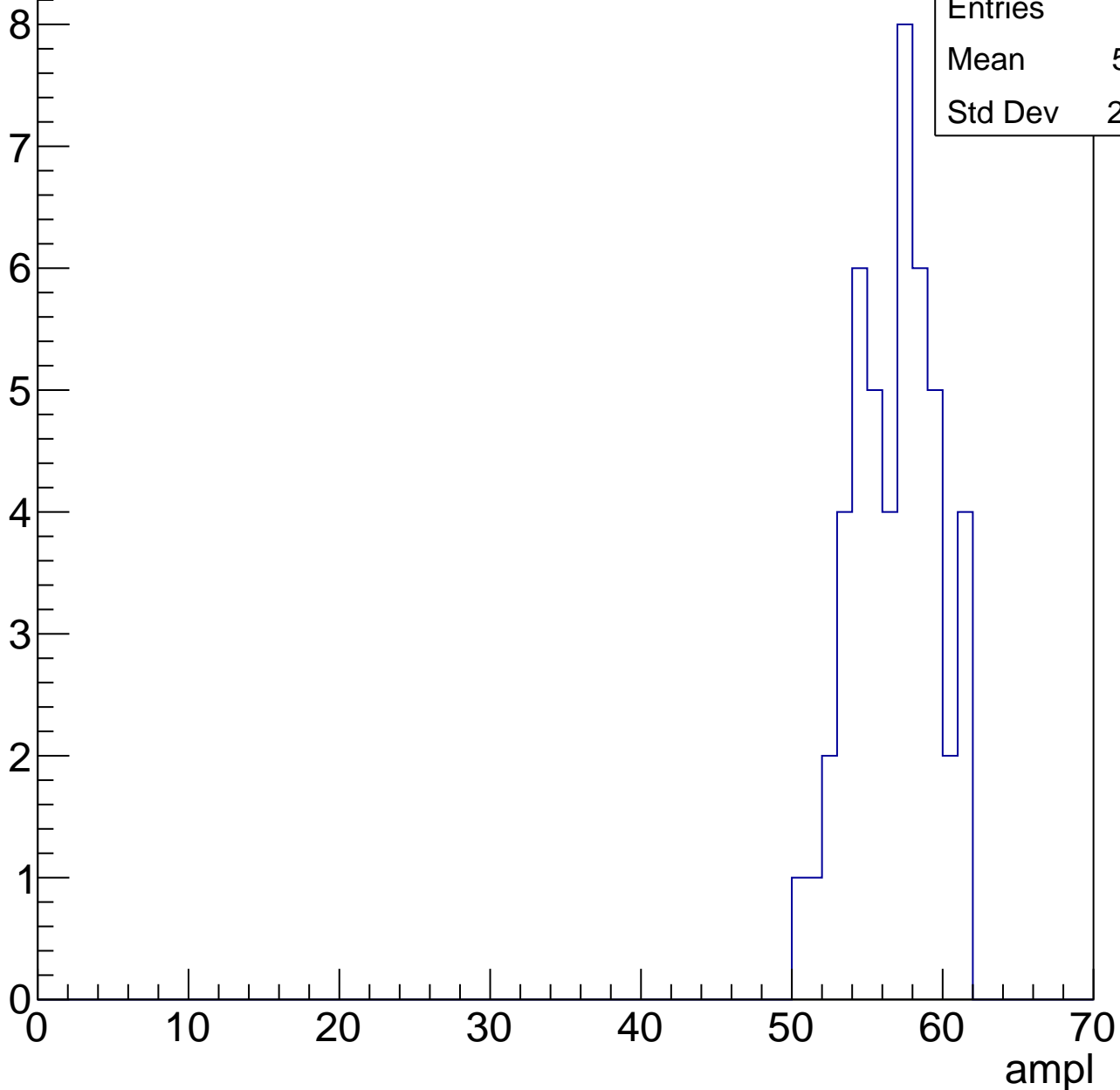


# B0L000S, U18-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.31
Std Dev	2.747

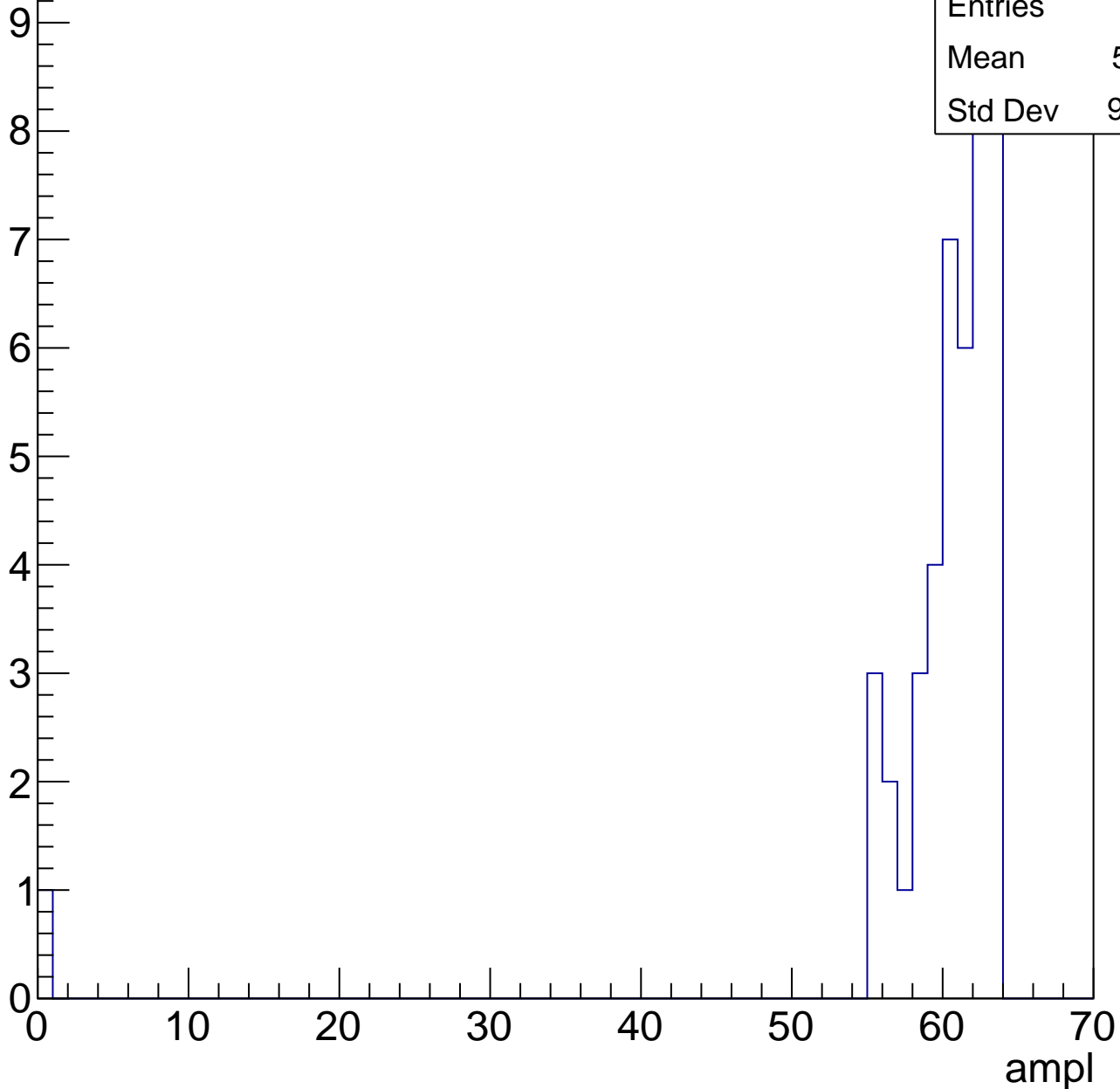


# B0L000S, U18-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.91
Std Dev	9.285



# B0L000S, U18-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

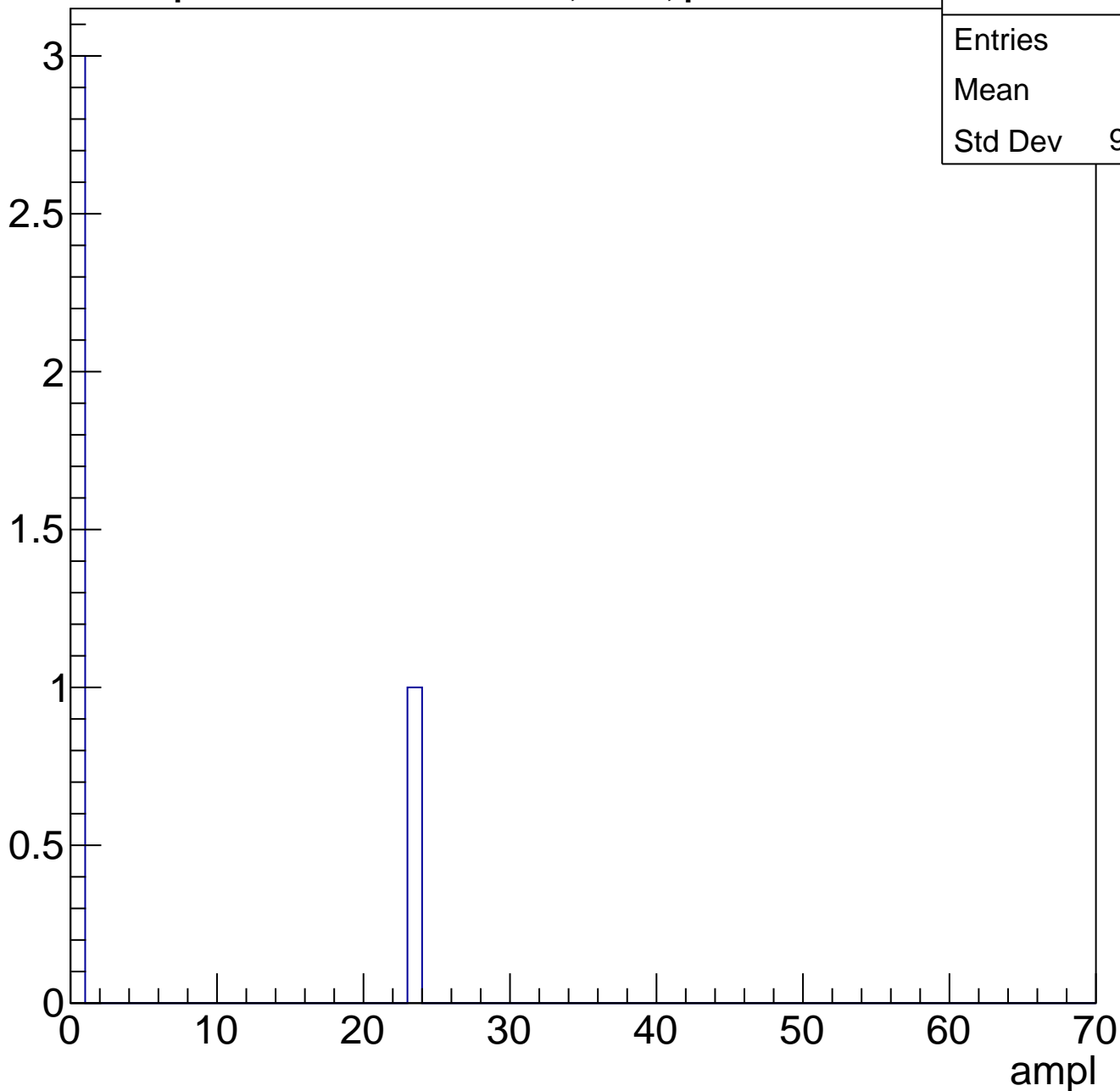




# B0L000S, U18-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch18, adc0

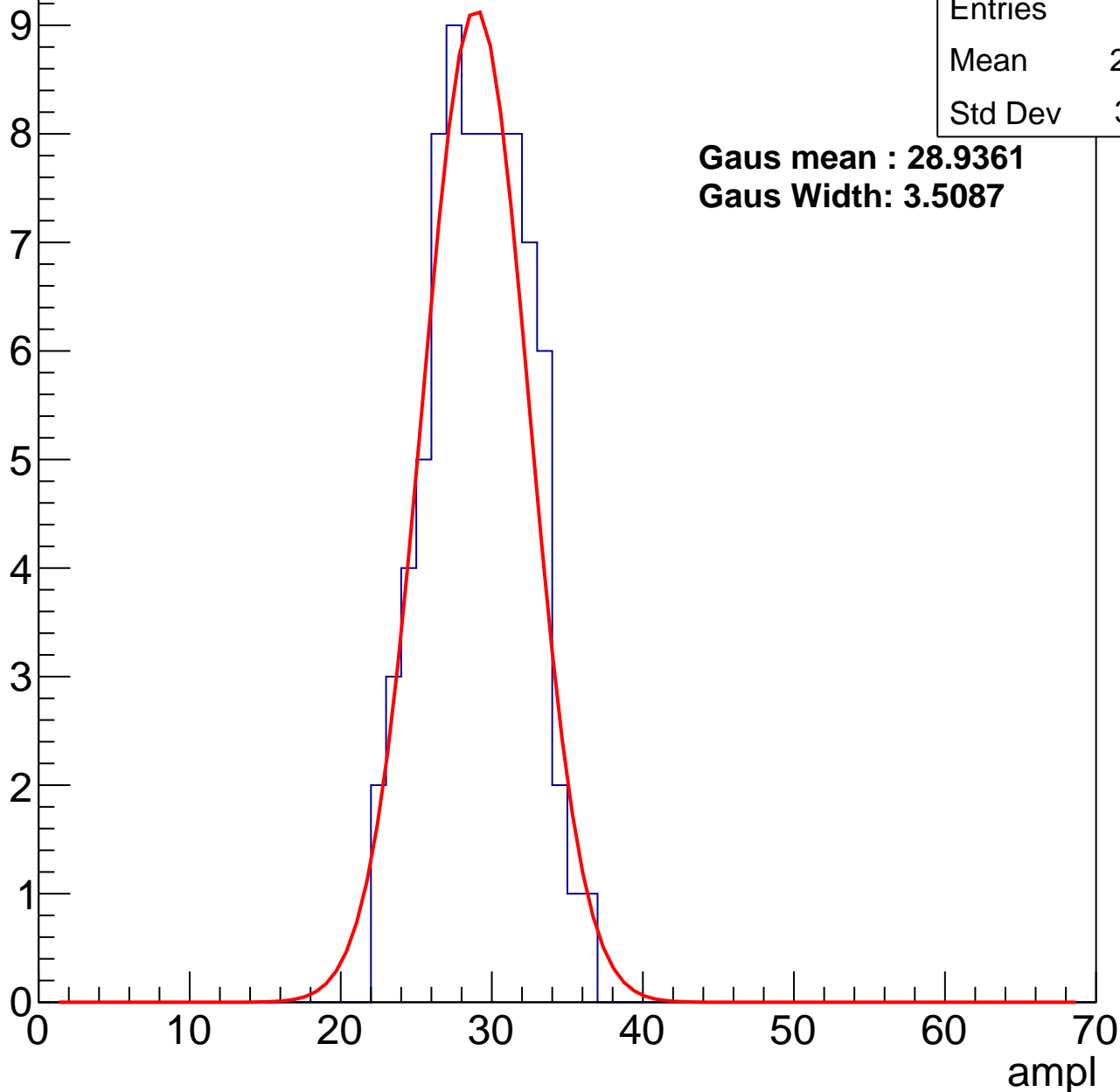
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	28.62
Std Dev	3.211

**Gaus mean : 28.9361**

**Gaus Width: 3.5087**



# B0L000S, U18-ch18, adc1

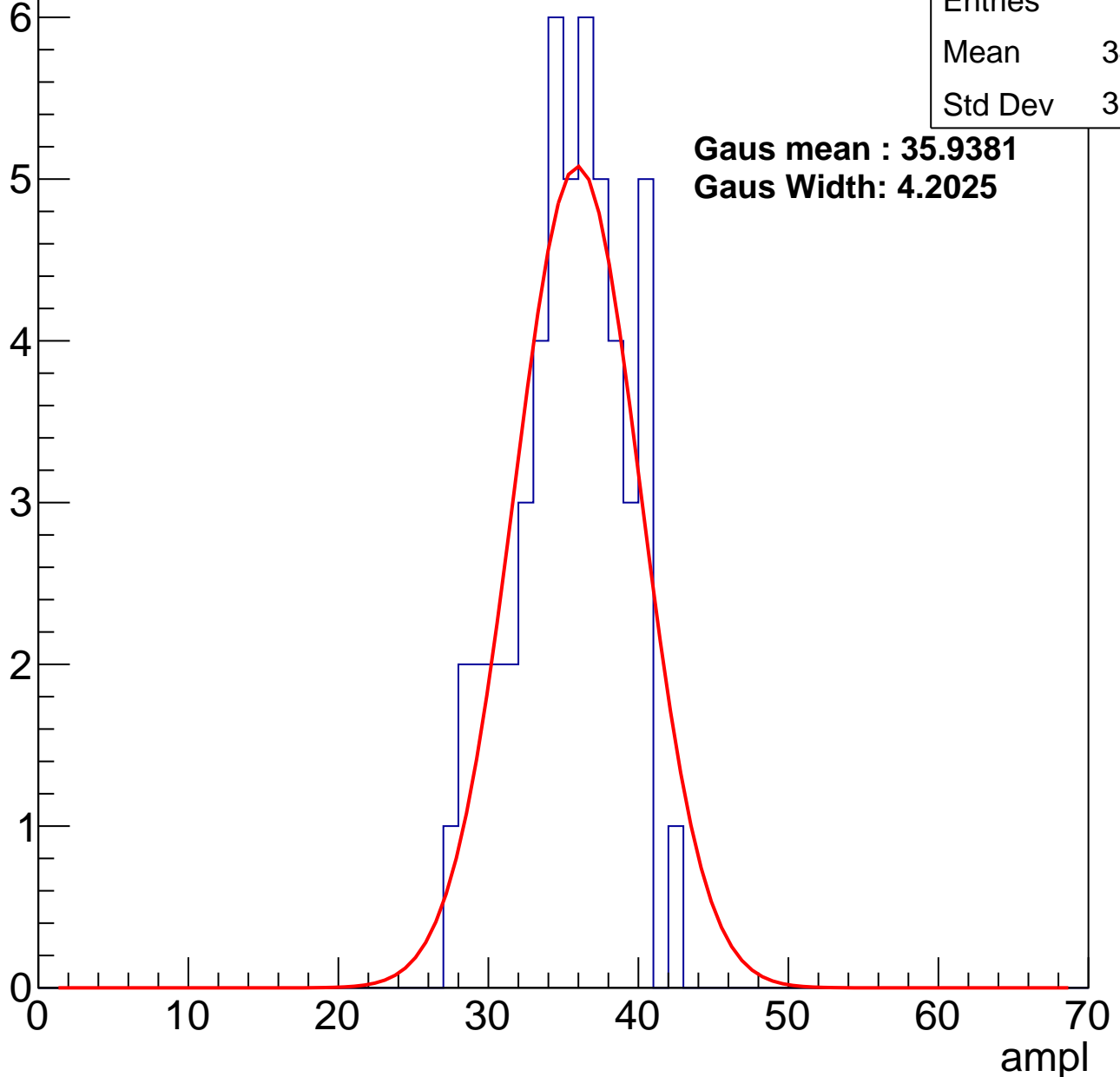
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	34.94
Std Dev	3.567

**Gaus mean : 35.9381**

**Gaus Width: 4.2025**



# B0L000S, U18-ch18, adc2

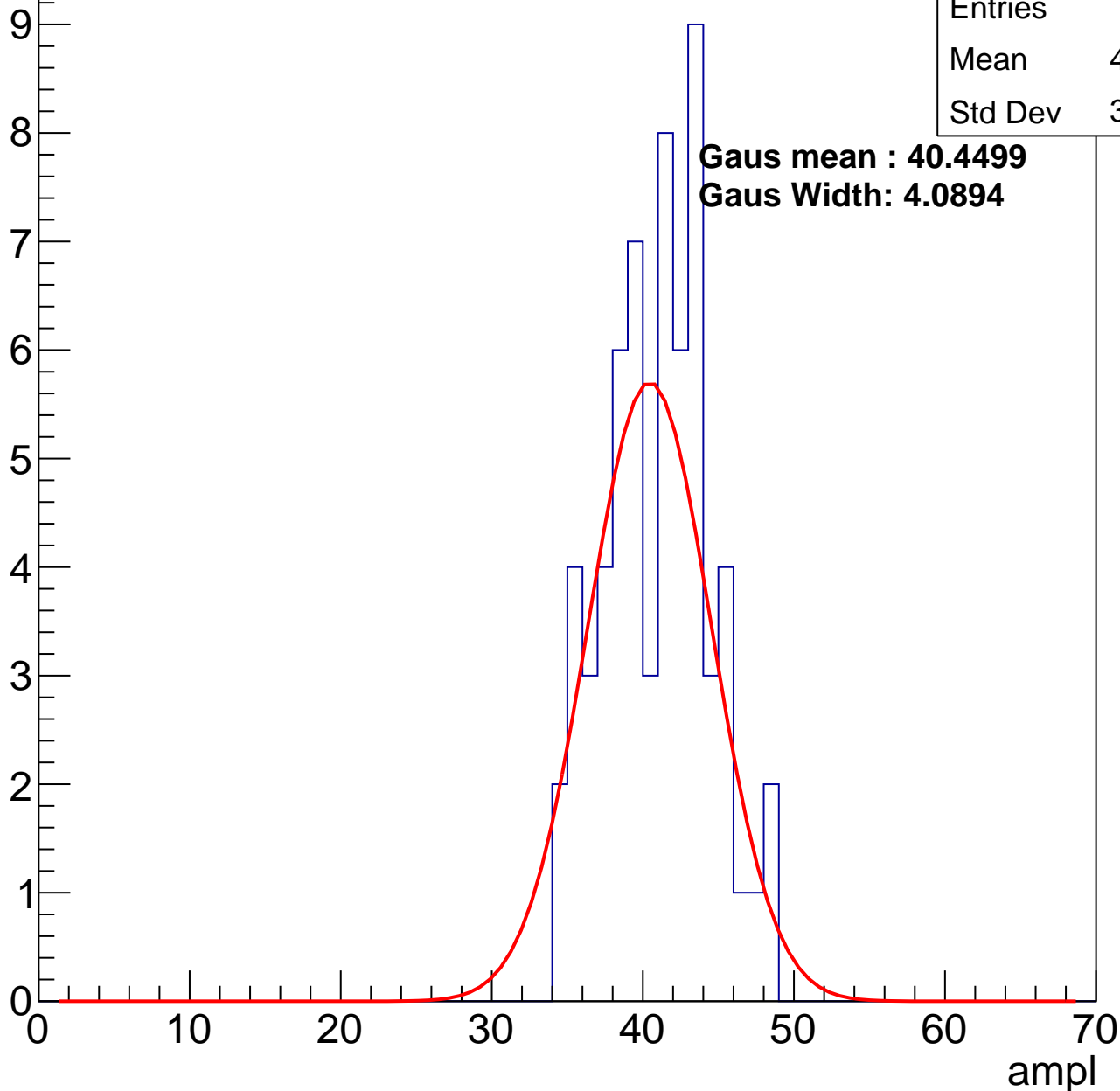
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	40.52
Std Dev	3.436

**Gaus mean : 40.4499**

**Gaus Width: 4.0894**

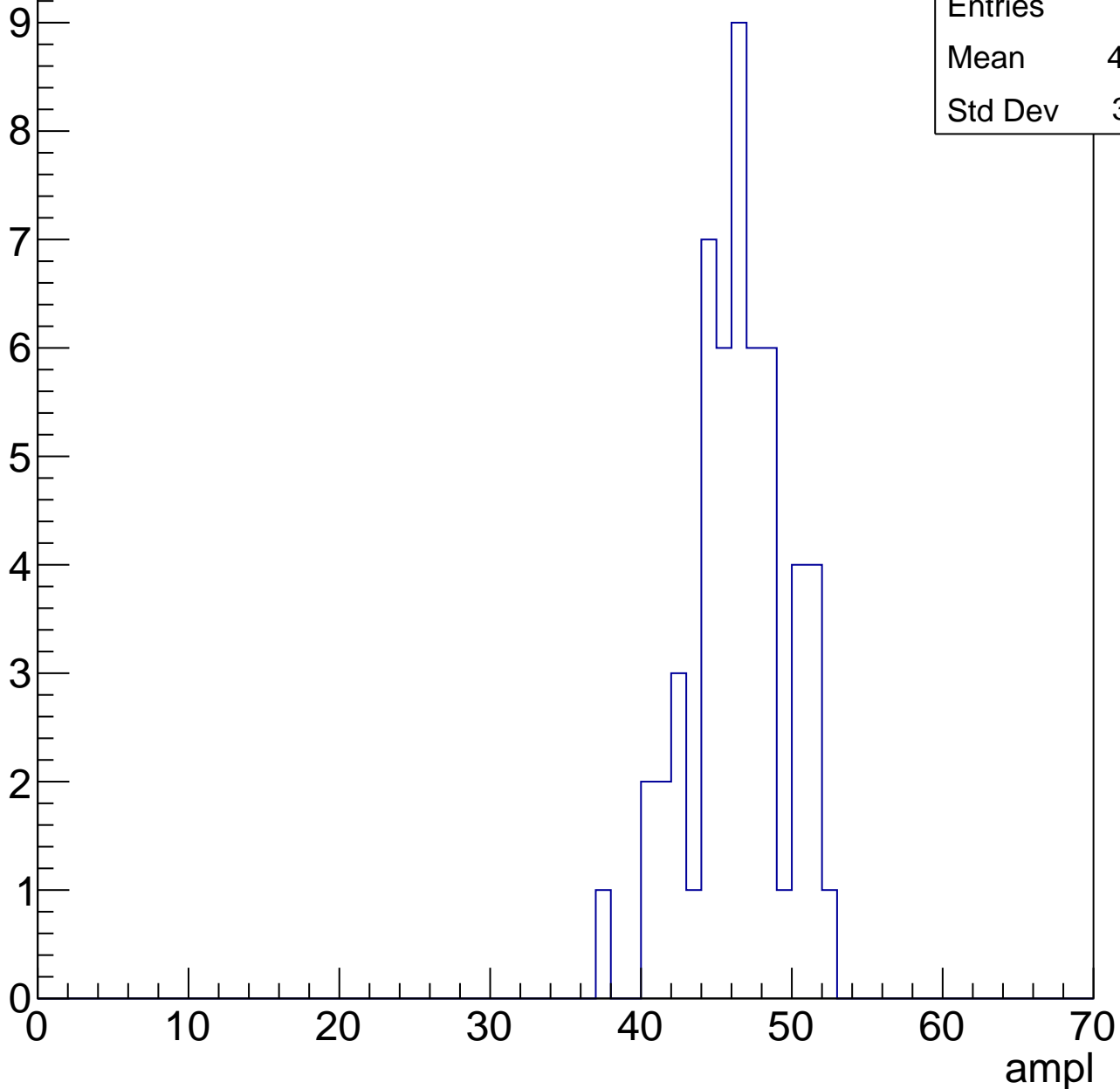


# B0L000S, U18-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	45.94
Std Dev	3.171

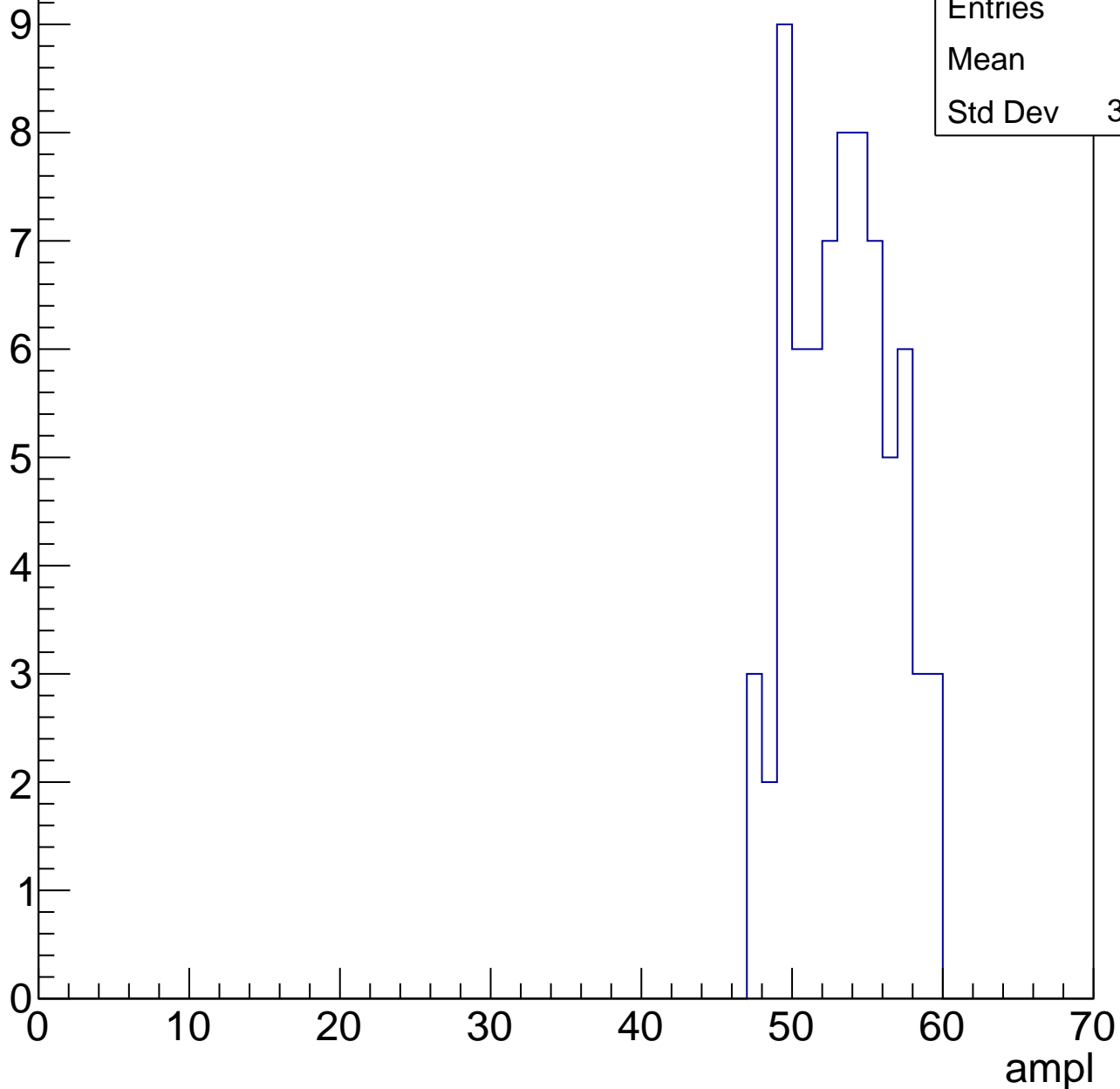


# B0L000S, U18-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	52.9
Std Dev	3.197

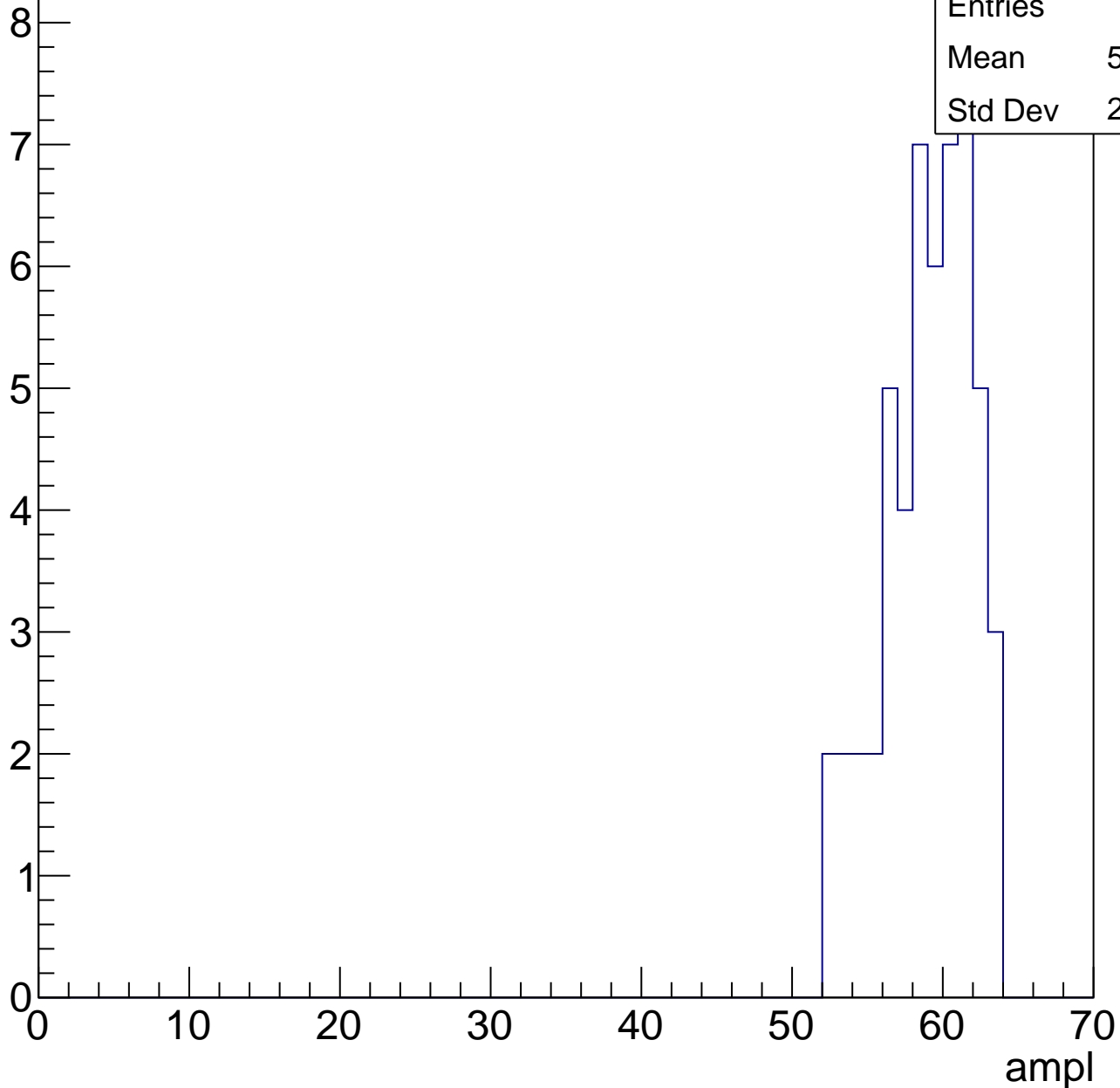


# B0L000S, U18-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.55
Std Dev	2.885

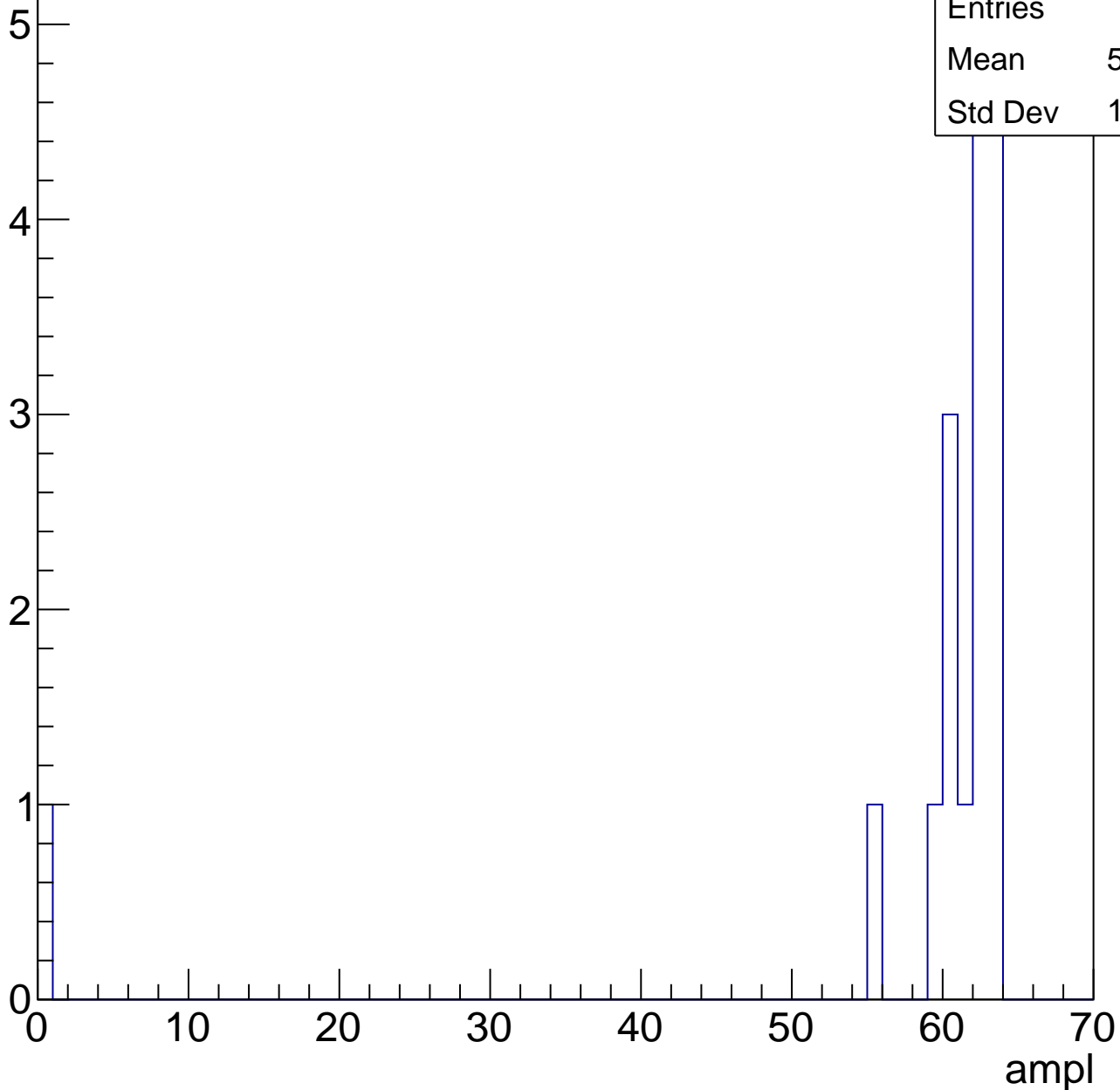


# B0L000S, U18-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.65
Std Dev	14.55

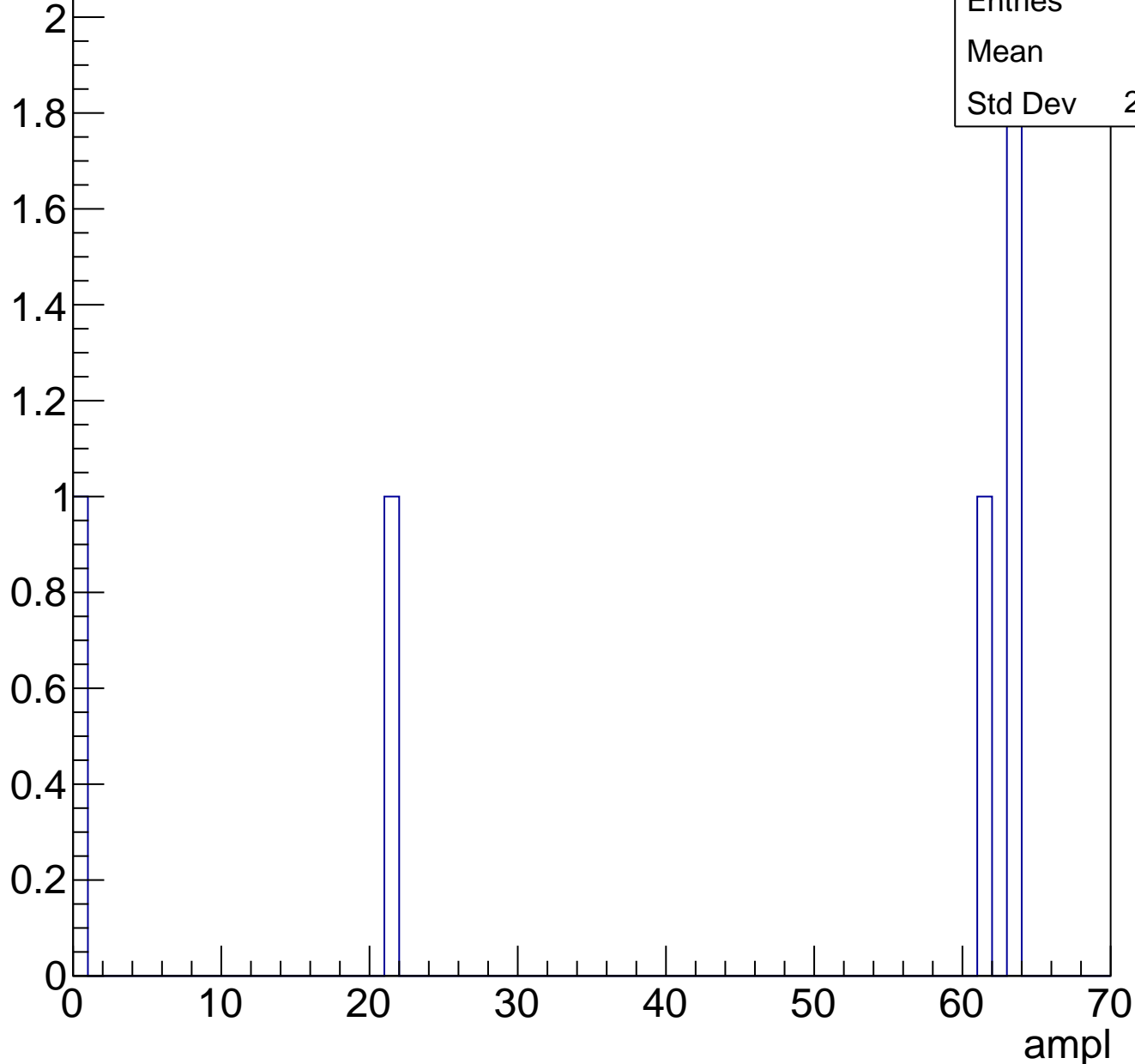




# B0L000S, U18-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	66
Mean	29.65
Std Dev	2.9

**Gaus mean : 30.2160**

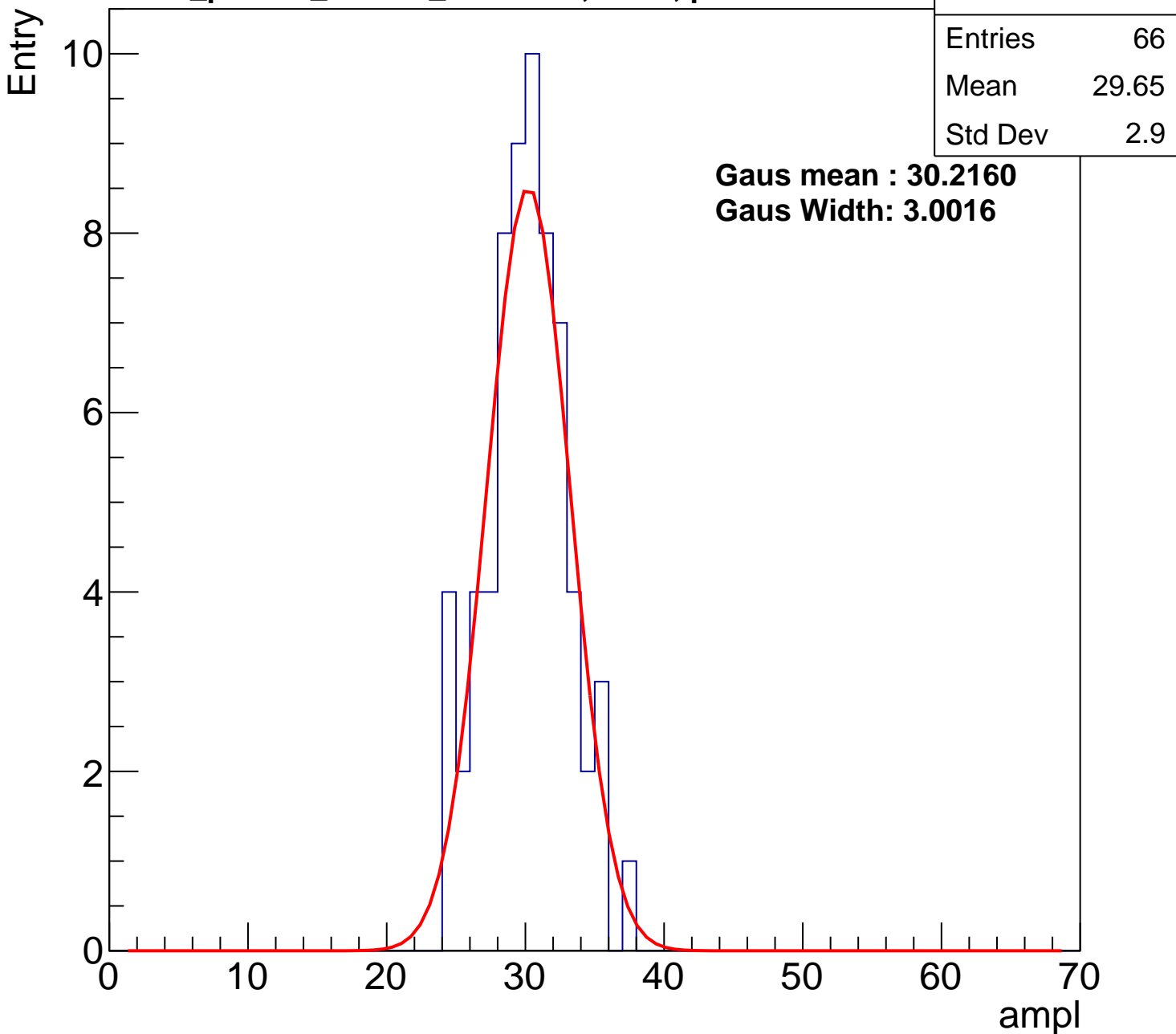
**Gaus Width: 3.0016**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch19, adc1

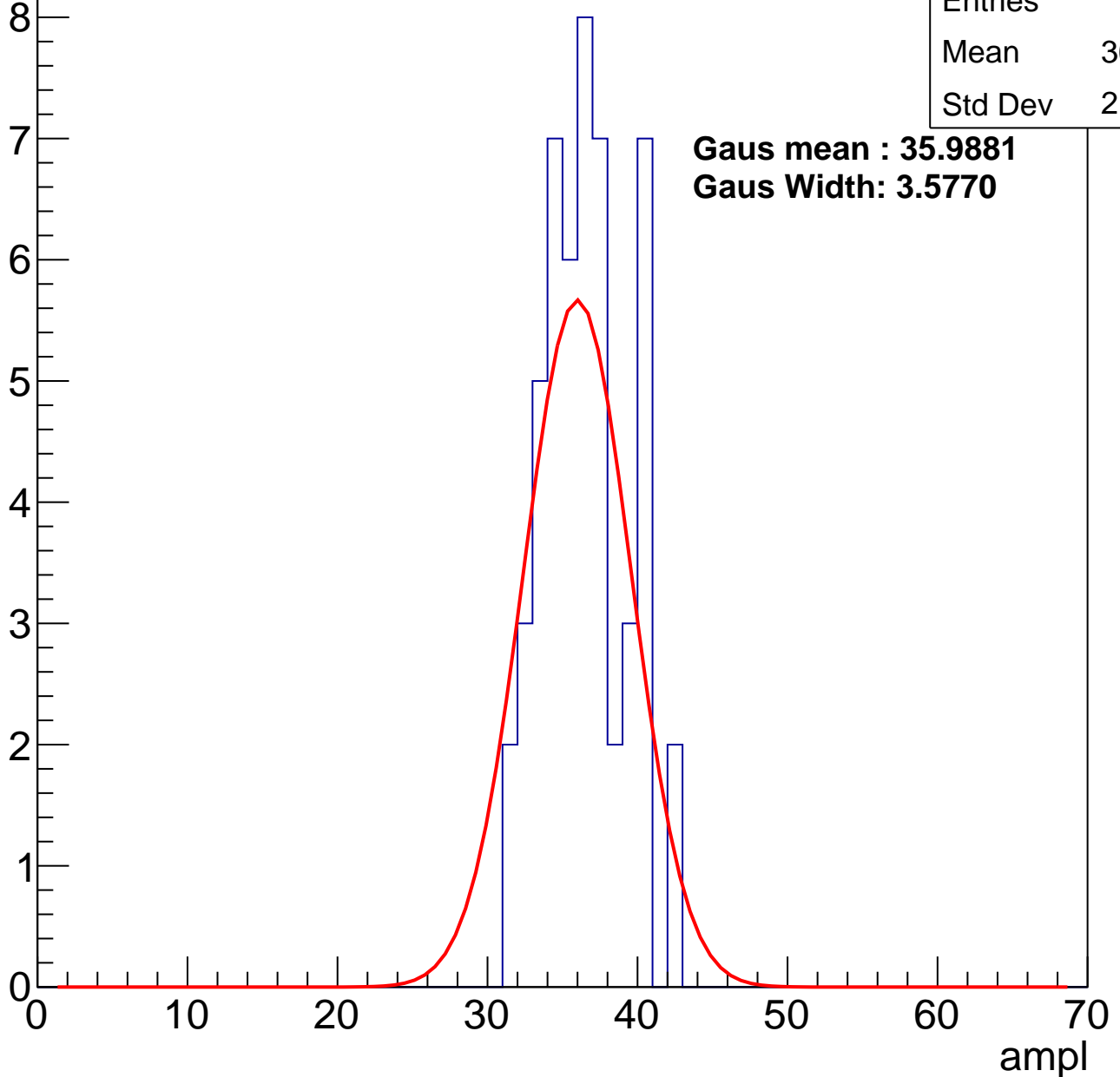
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	36.06
Std Dev	2.783

**Gaus mean : 35.9881**

**Gaus Width: 3.5770**



# B0L000S, U18-ch19, adc2

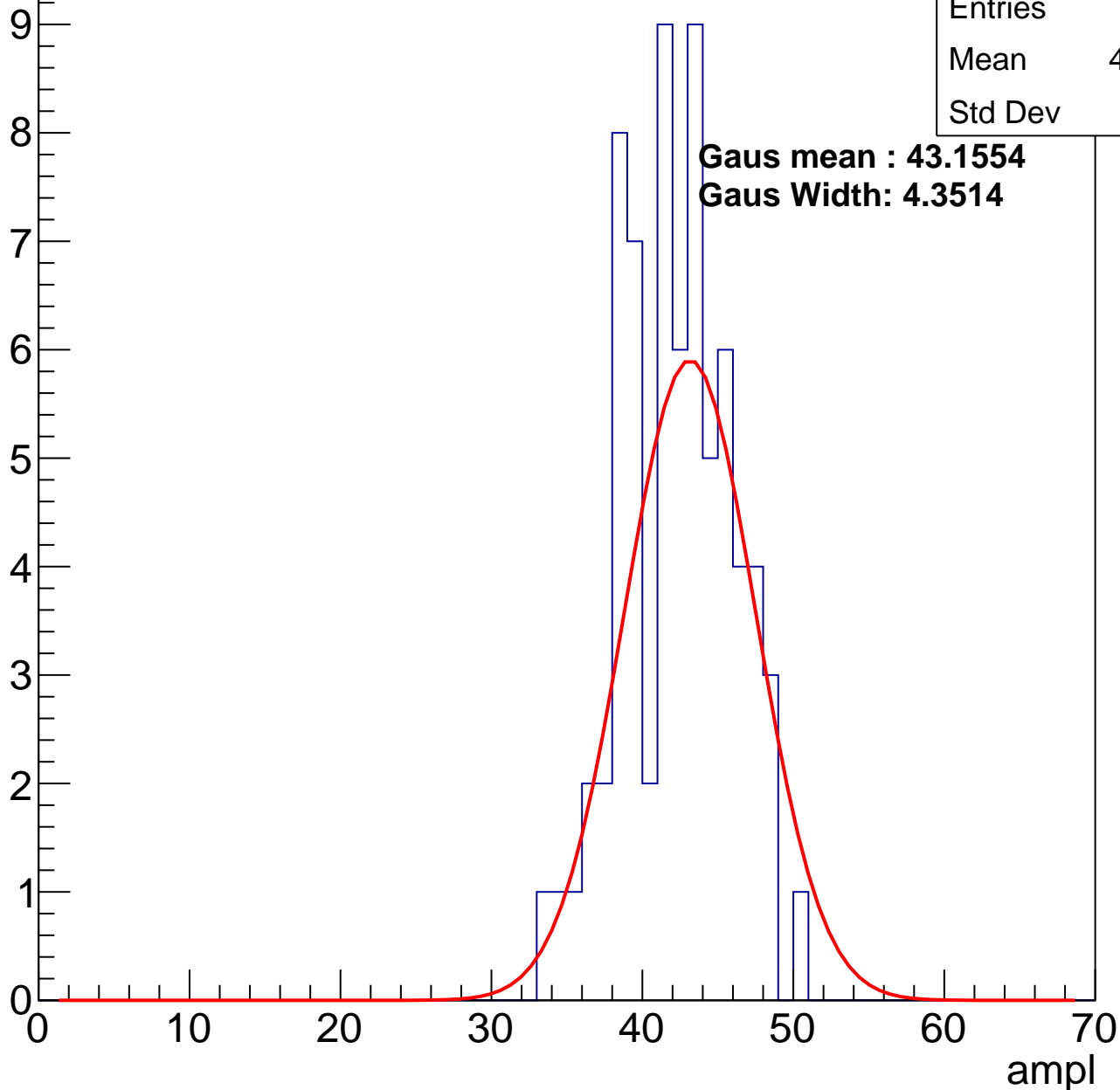
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	41.82
Std Dev	3.64

**Gaus mean : 43.1554**

**Gaus Width: 4.3514**

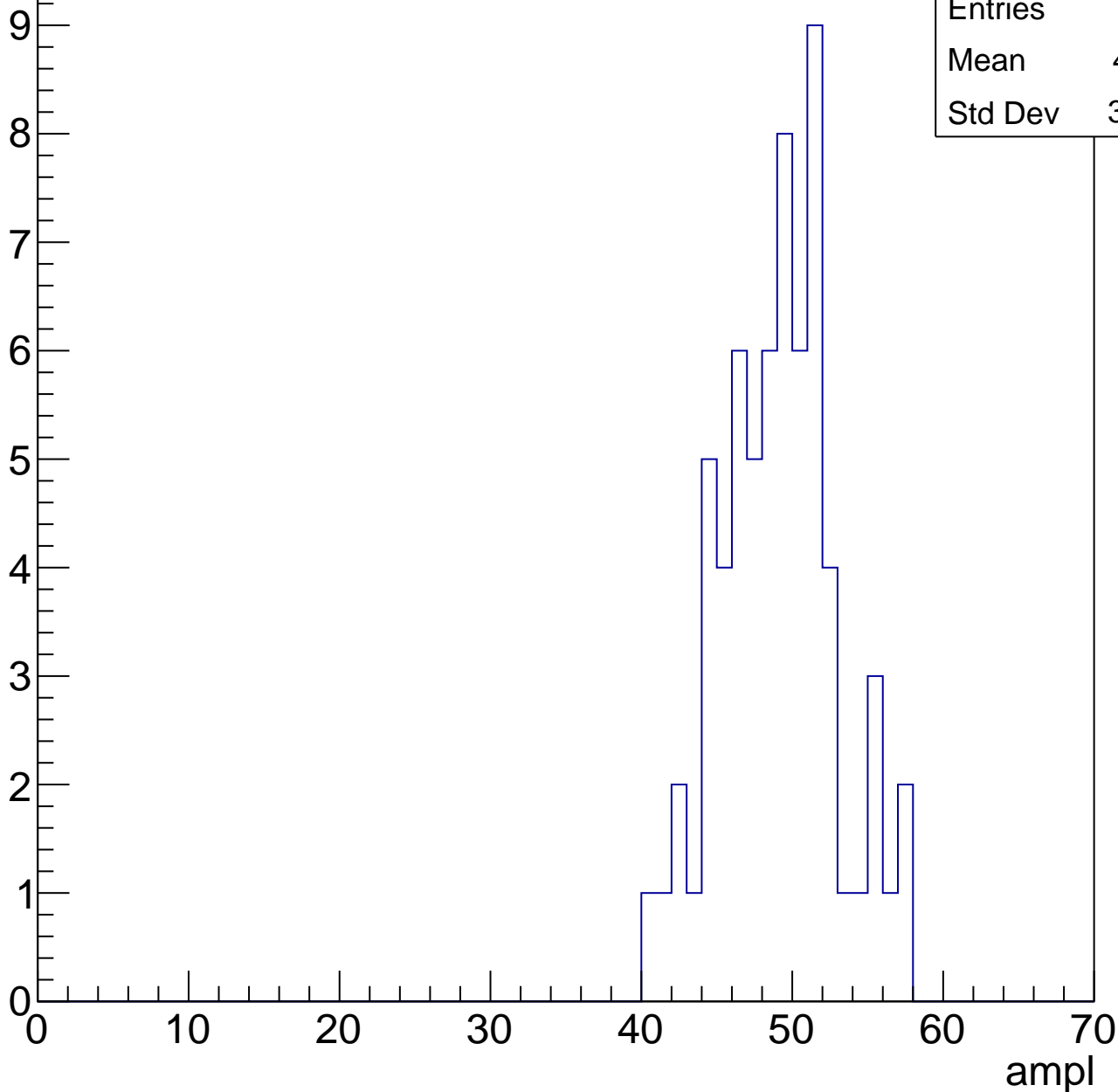


# B0L000S, U18-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.61
Std Dev	3.777

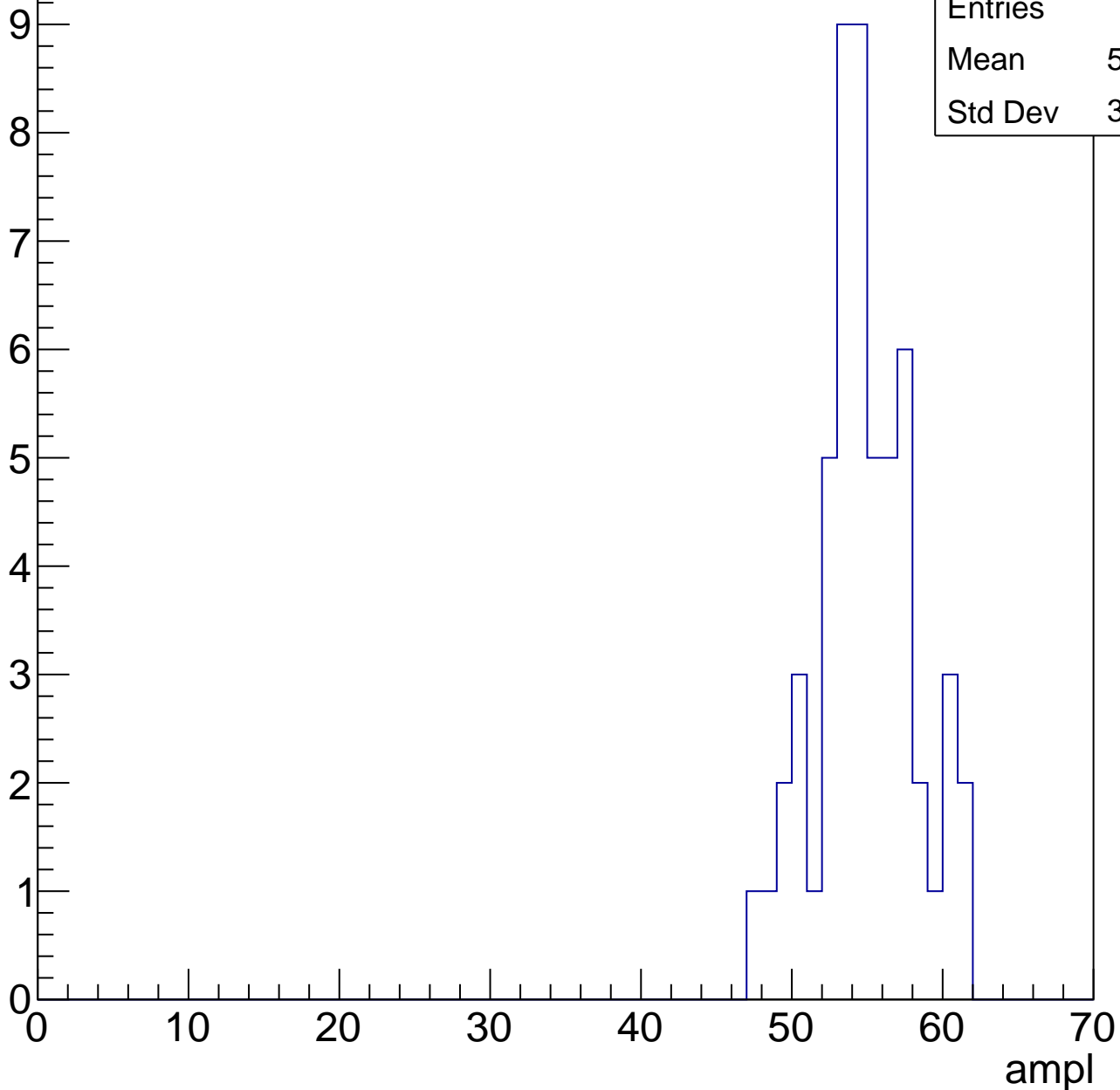


# B0L000S, U18-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

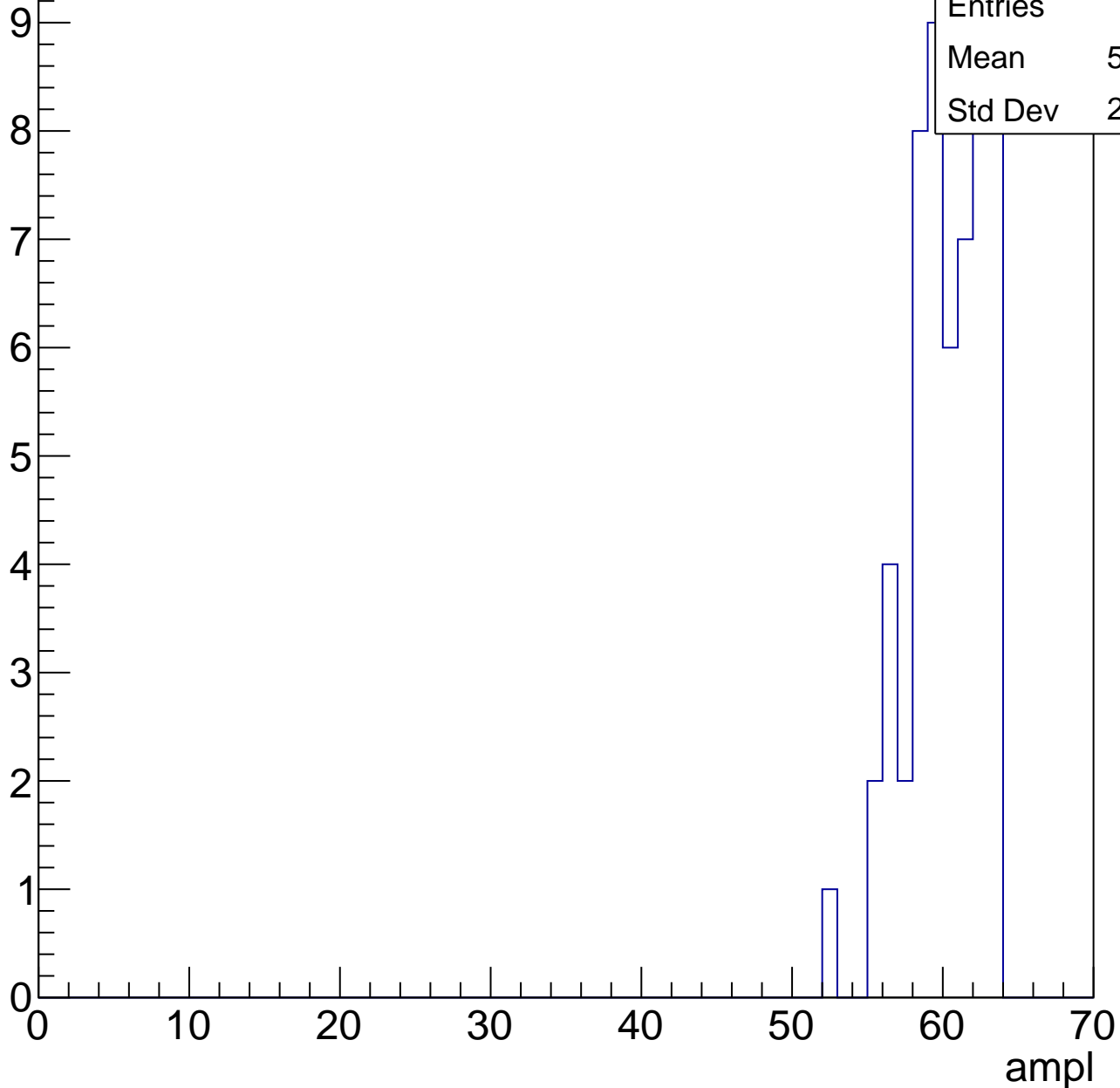
Entries	55
Mean	54.38
Std Dev	3.177



# B0L000S, U18-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

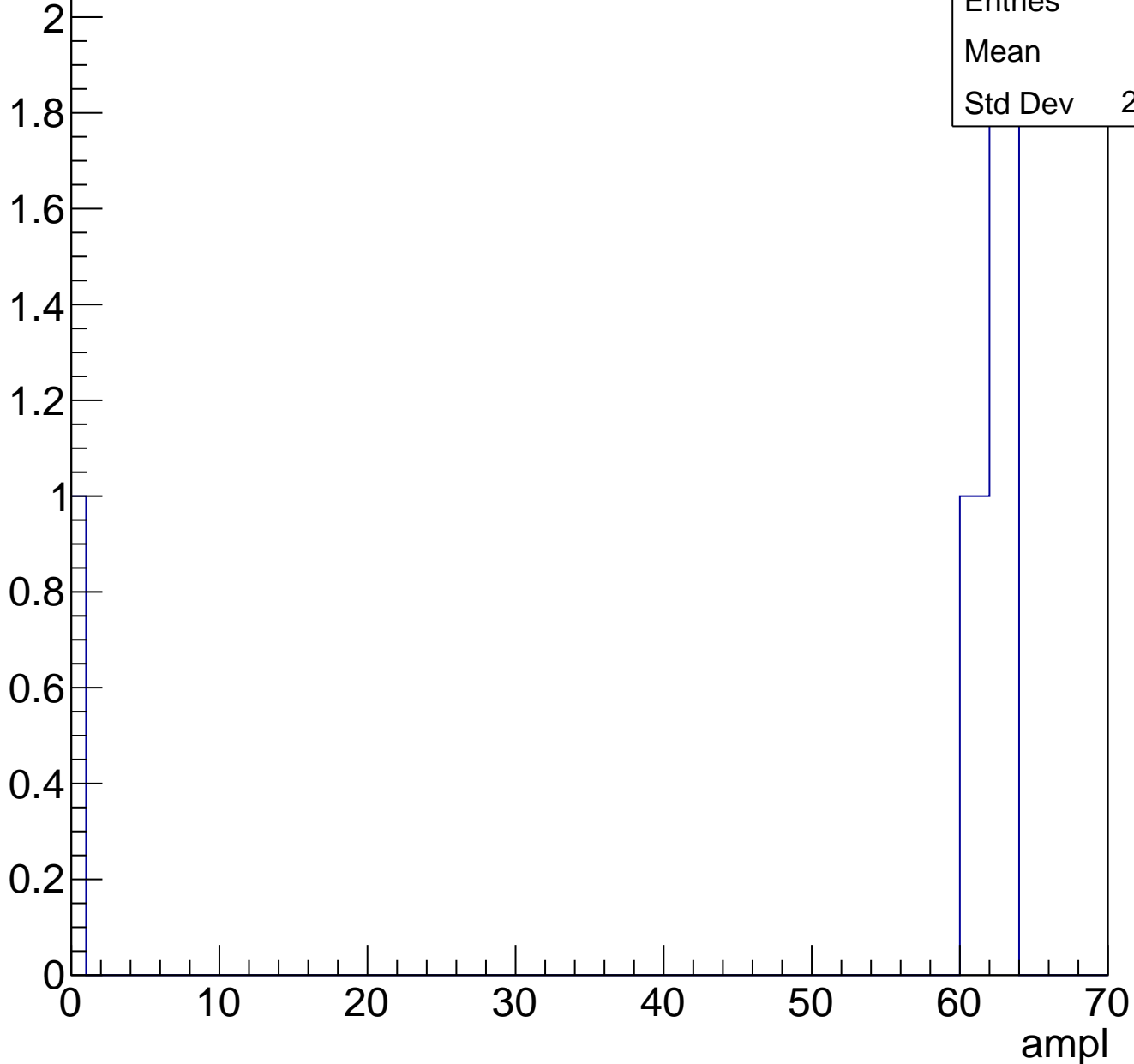
Entry



# B0L000S, U18-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch20, adc0

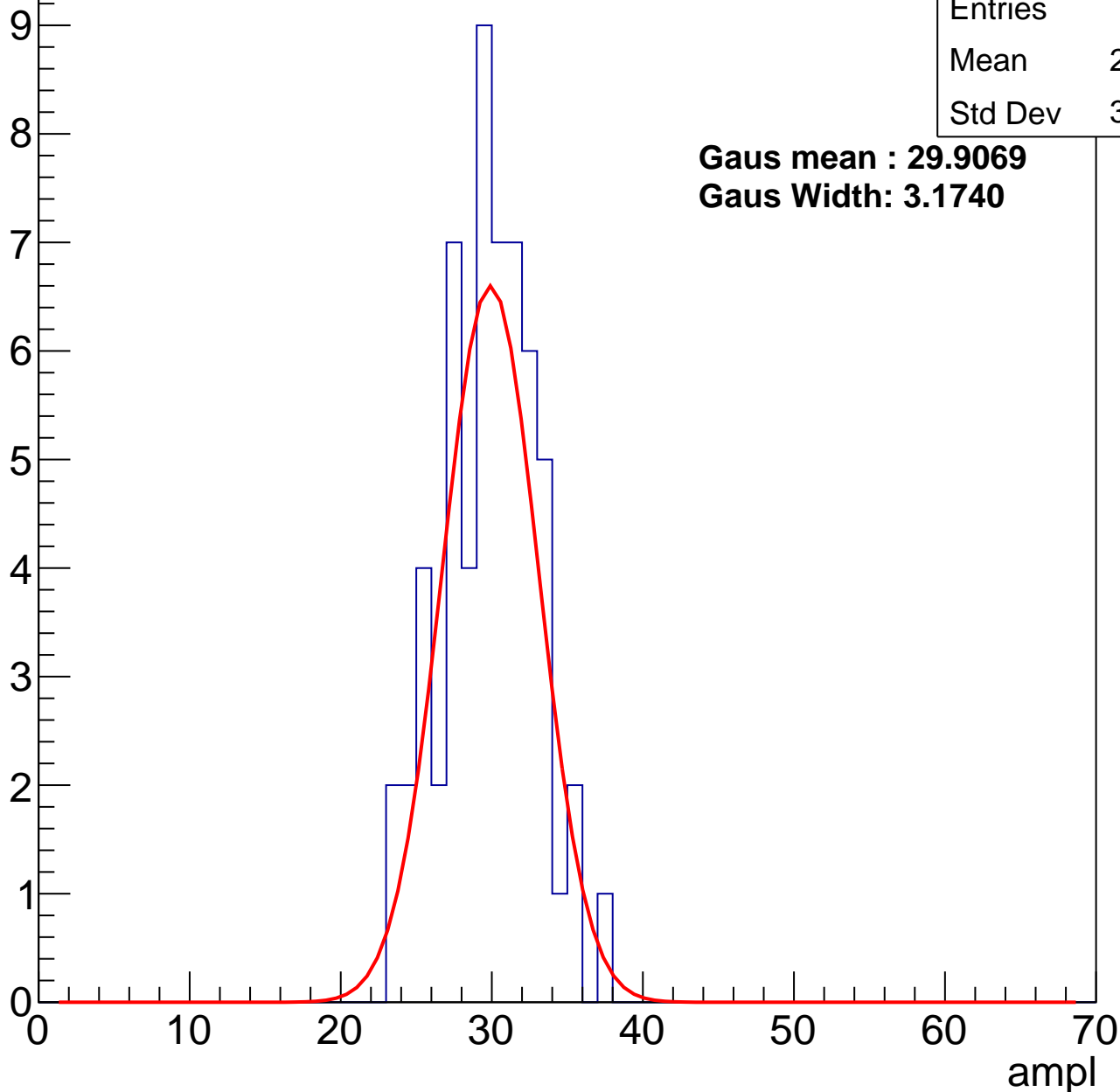
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.37
Std Dev	3.075

**Gaus mean : 29.9069**

**Gaus Width: 3.1740**



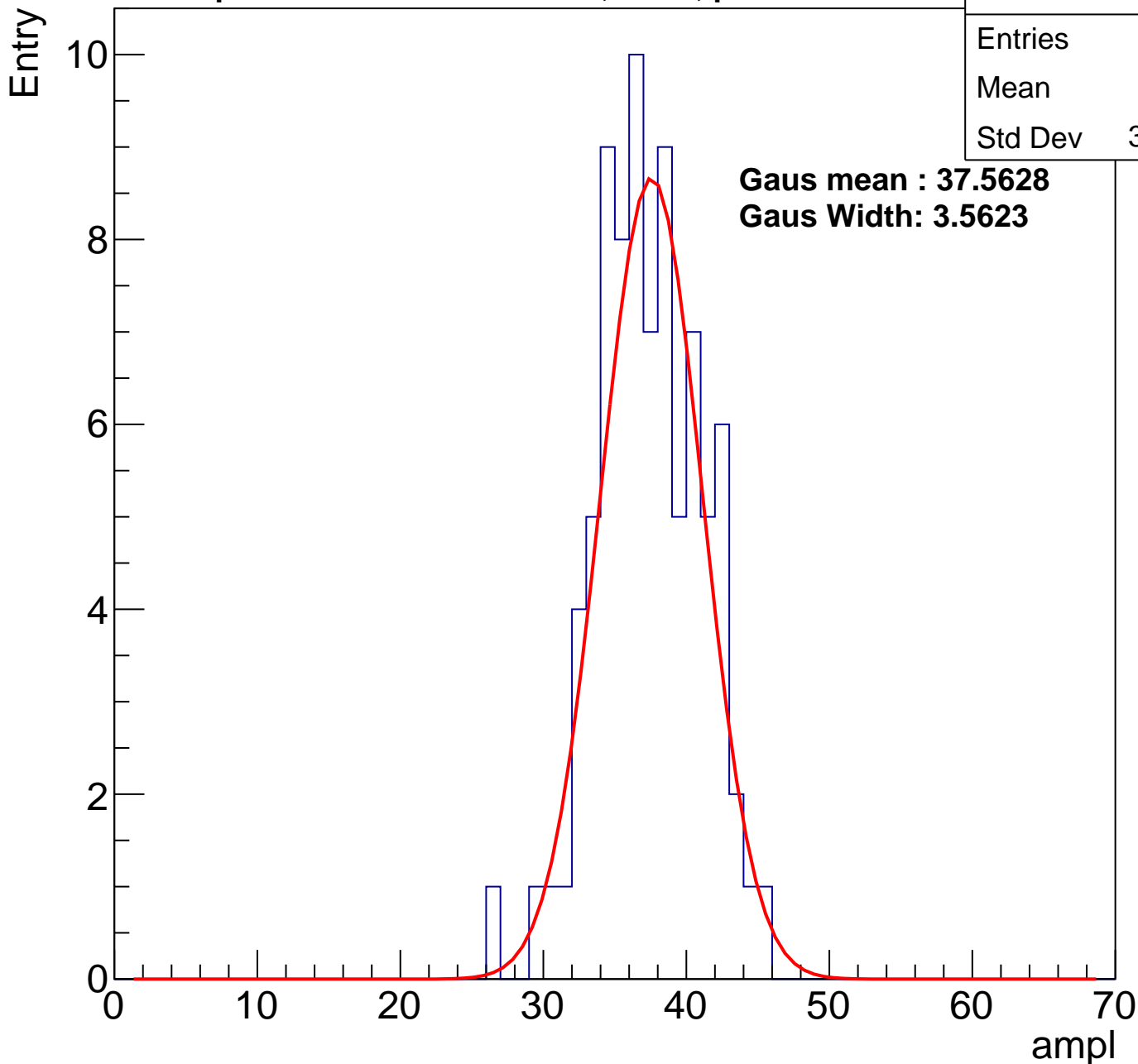
# B0L000S, U18-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	36.9
Std Dev	3.606

**Gaus mean : 37.5628**

**Gaus Width: 3.5623**



# B0L000S, U18-ch20, adc2

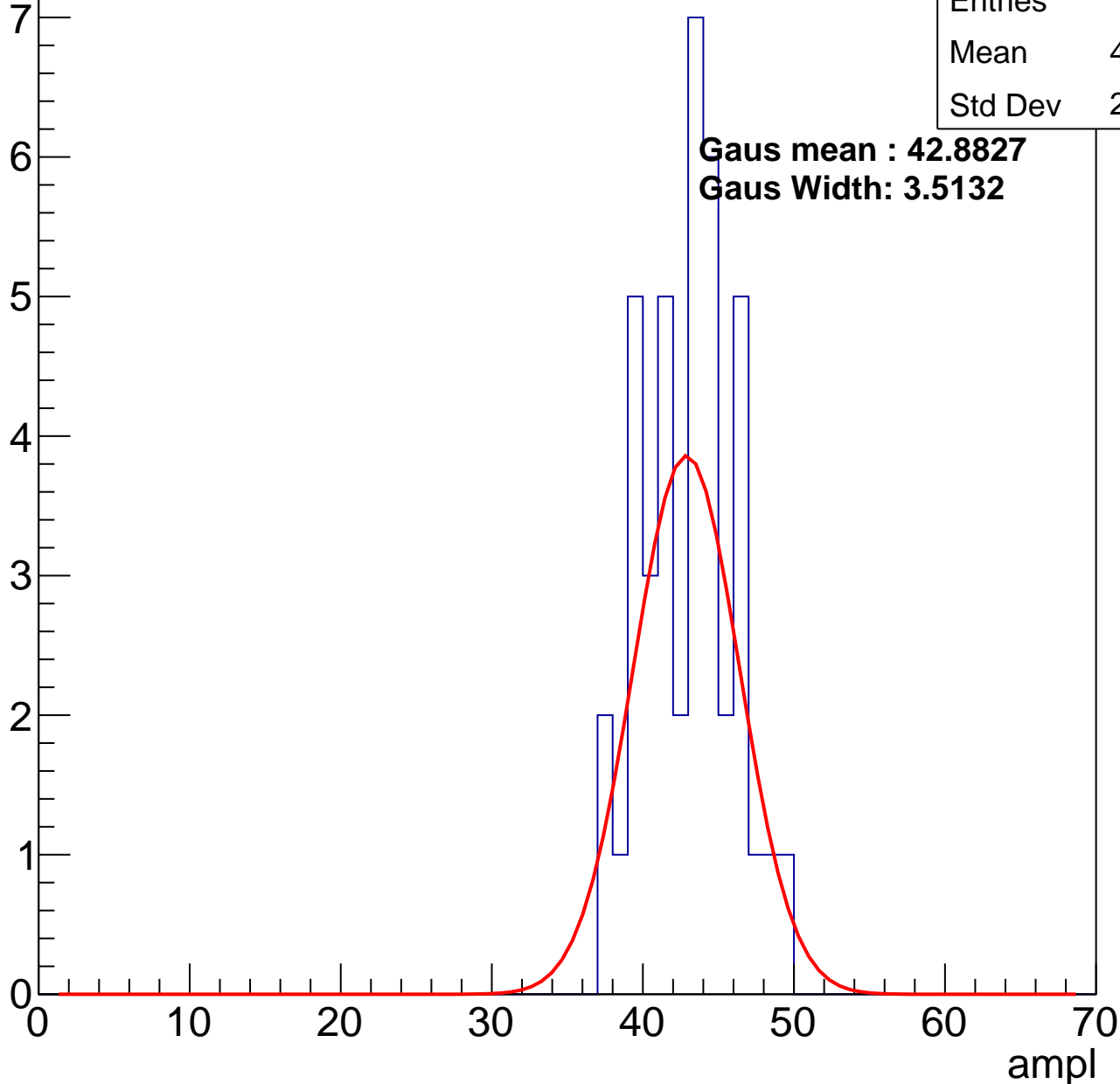
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	42.56
Std Dev	2.939

**Gaus mean : 42.8827**

**Gaus Width: 3.5132**

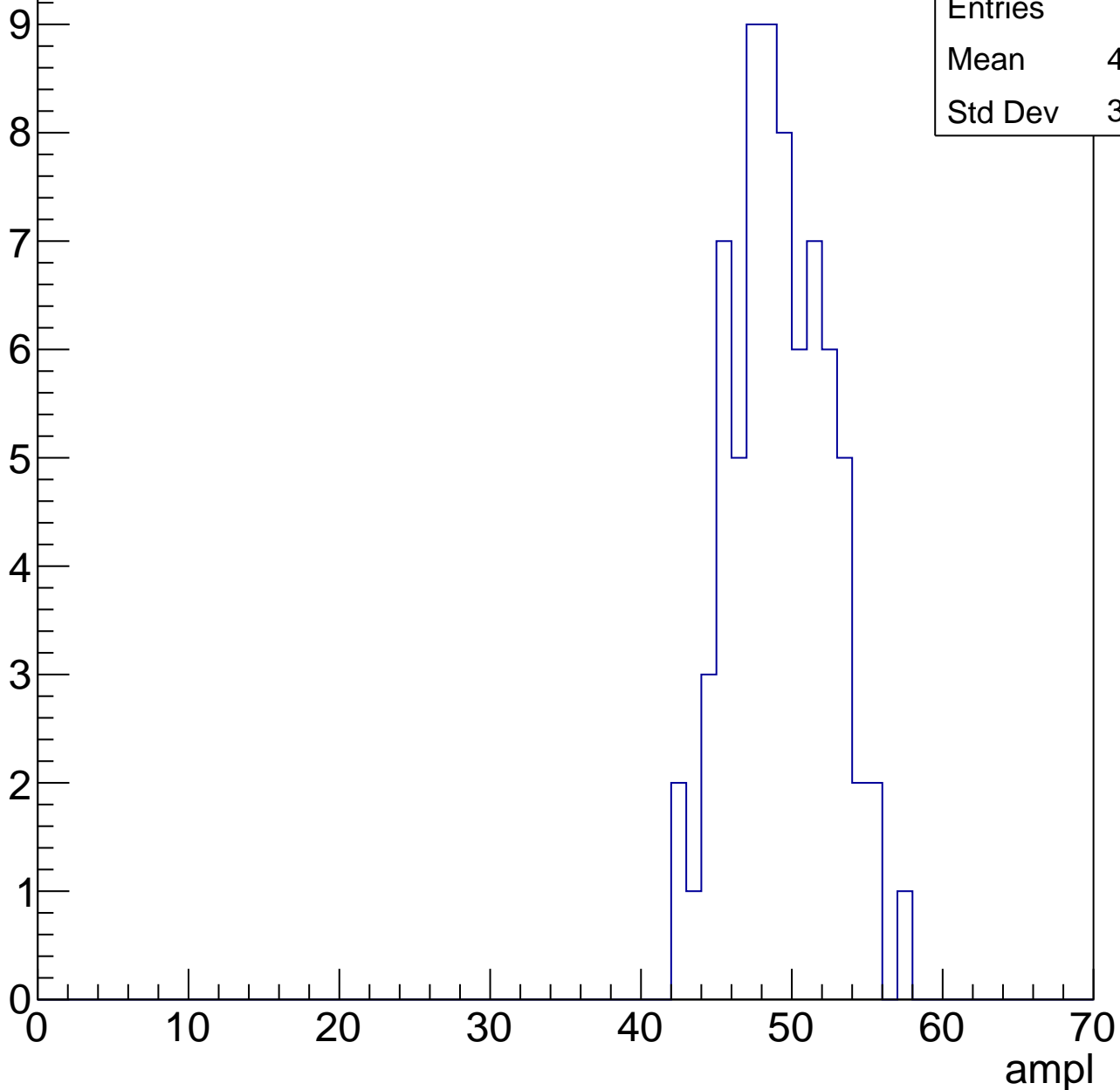


# B0L000S, U18-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	48.77
Std Dev	3.229

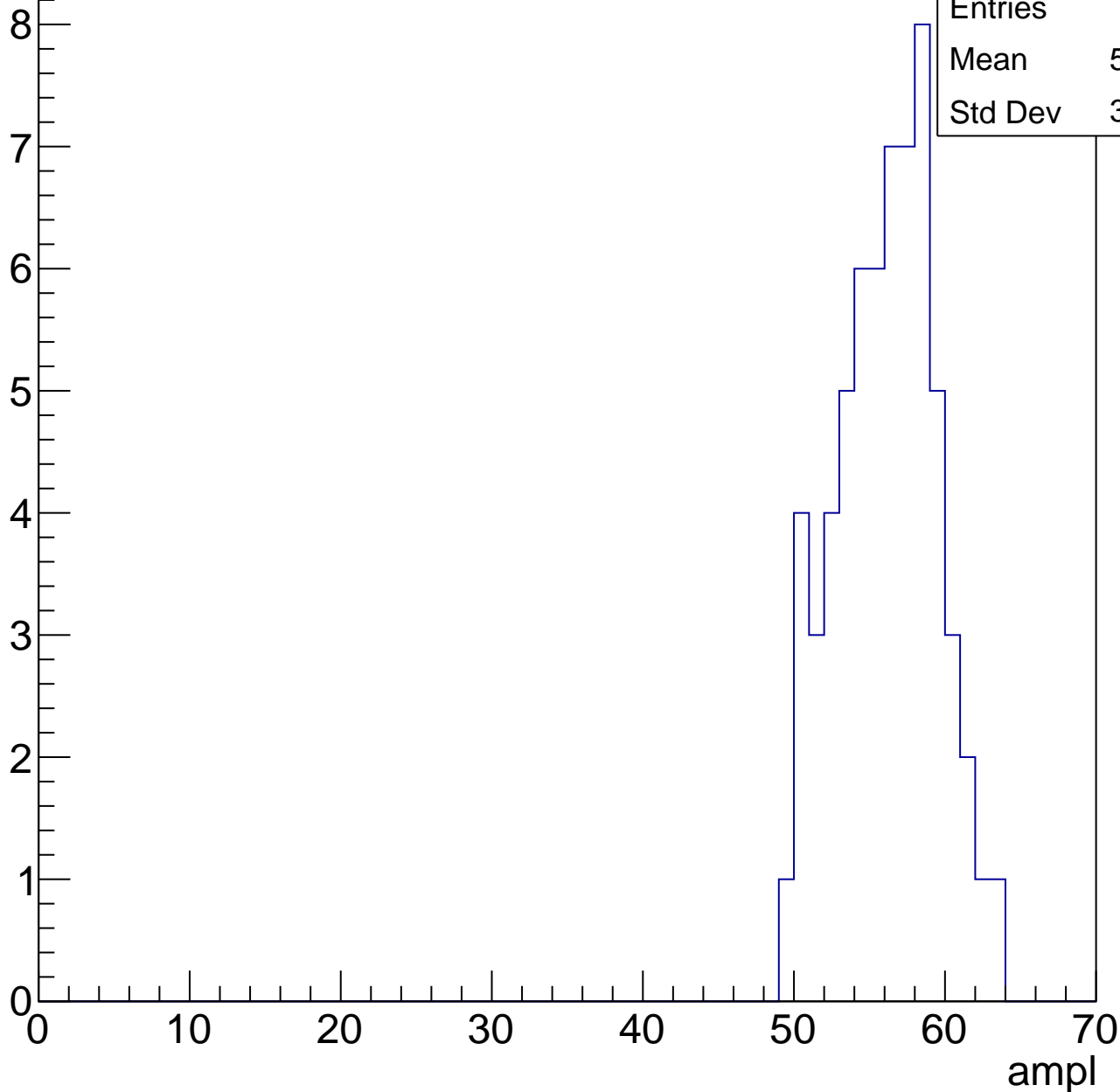


# B0L000S, U18-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	55.65
Std Dev	3.252



# B0L000S, U18-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 41

Mean 59.61

Std Dev 2.546

0

10

20

30

40

50

60

70

ampl

0

10

20

30

40

50

60

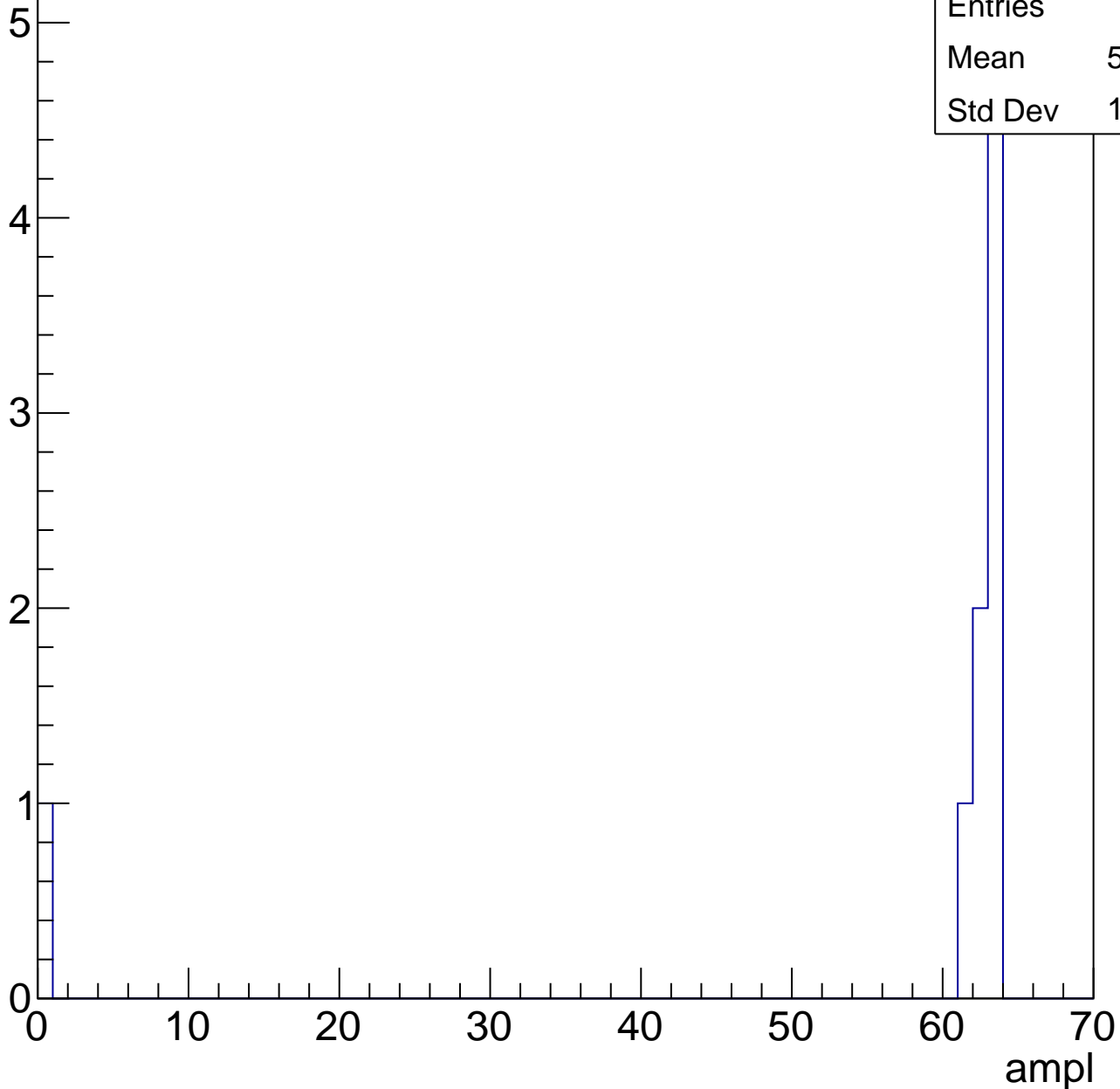
70

# B0L000S, U18-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	55.56
Std Dev	19.65





# B0L000S, U18-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

# B0L000S, U18-ch21, adc0

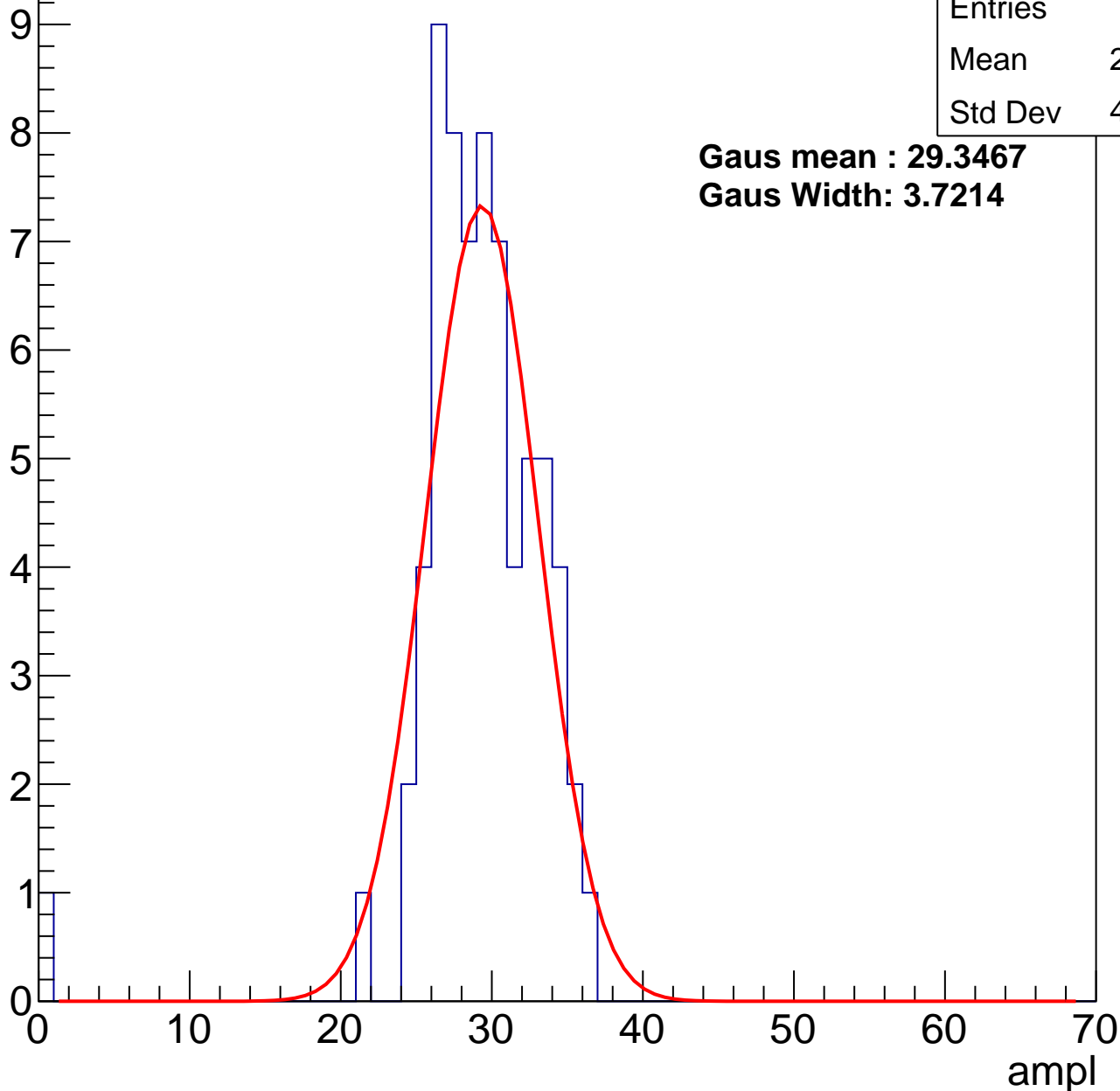
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28.65
Std Dev	4.696

**Gaus mean : 29.3467**

**Gaus Width: 3.7214**



# B0L000S, U18-ch21, adc1

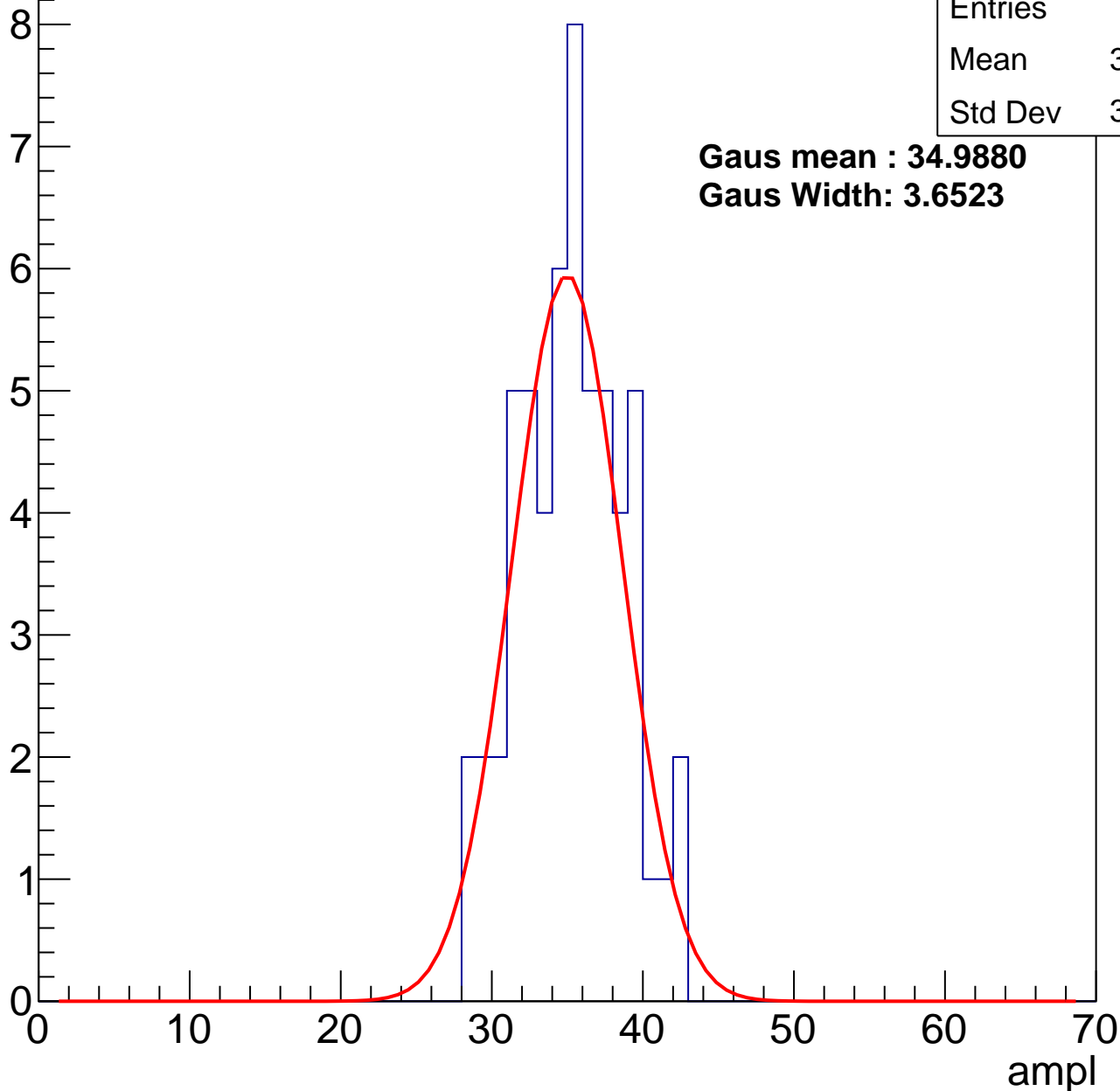
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	34.77
Std Dev	3.413

**Gaus mean : 34.9880**

**Gaus Width: 3.6523**



# B0L000S, U18-ch21, adc2

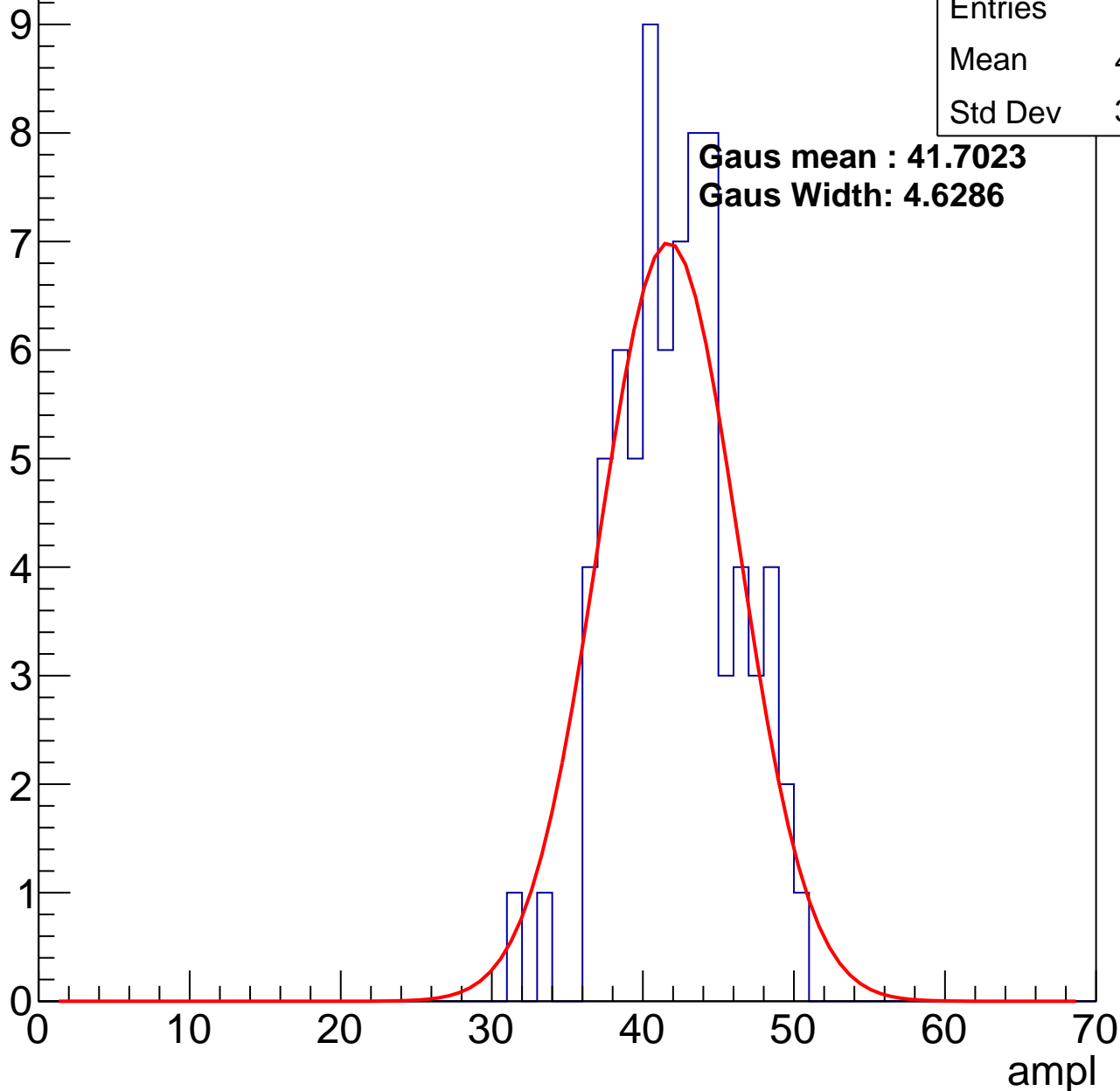
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	41.71
Std Dev	3.881

**Gaus mean : 41.7023**

**Gaus Width: 4.6286**

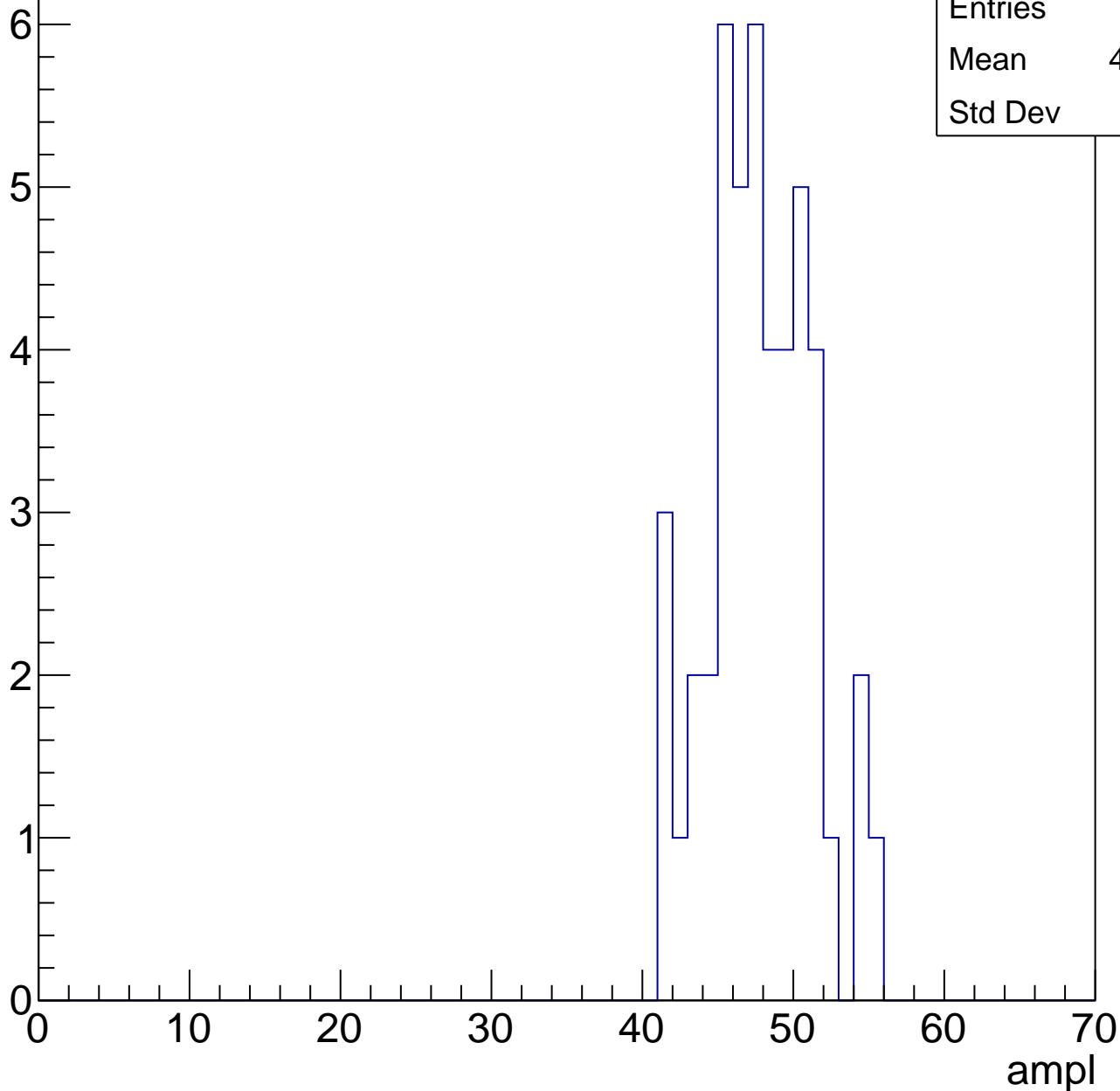


# B0L000S, U18-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	47.35
Std Dev	3.37

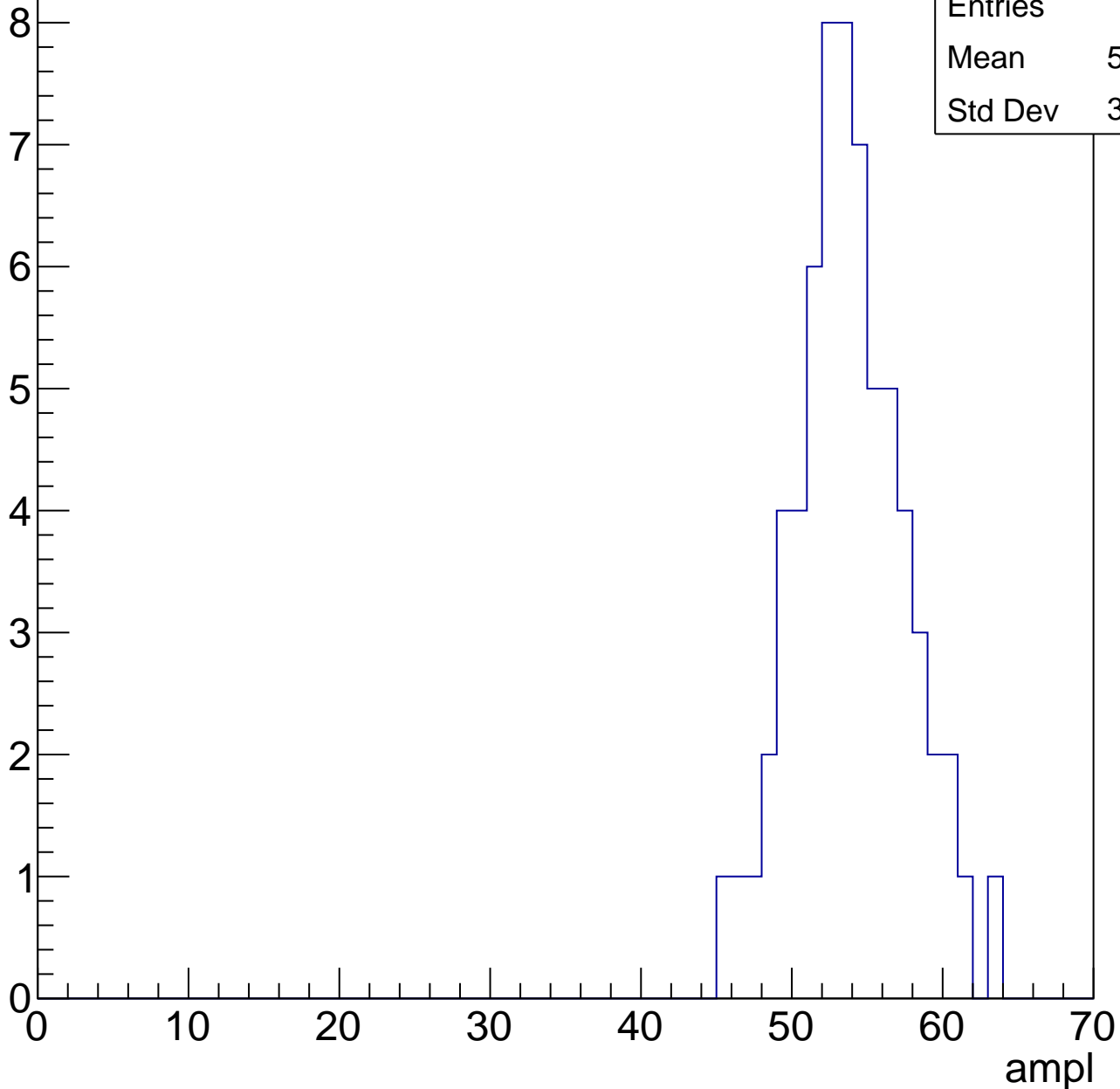


# B0L000S, U18-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

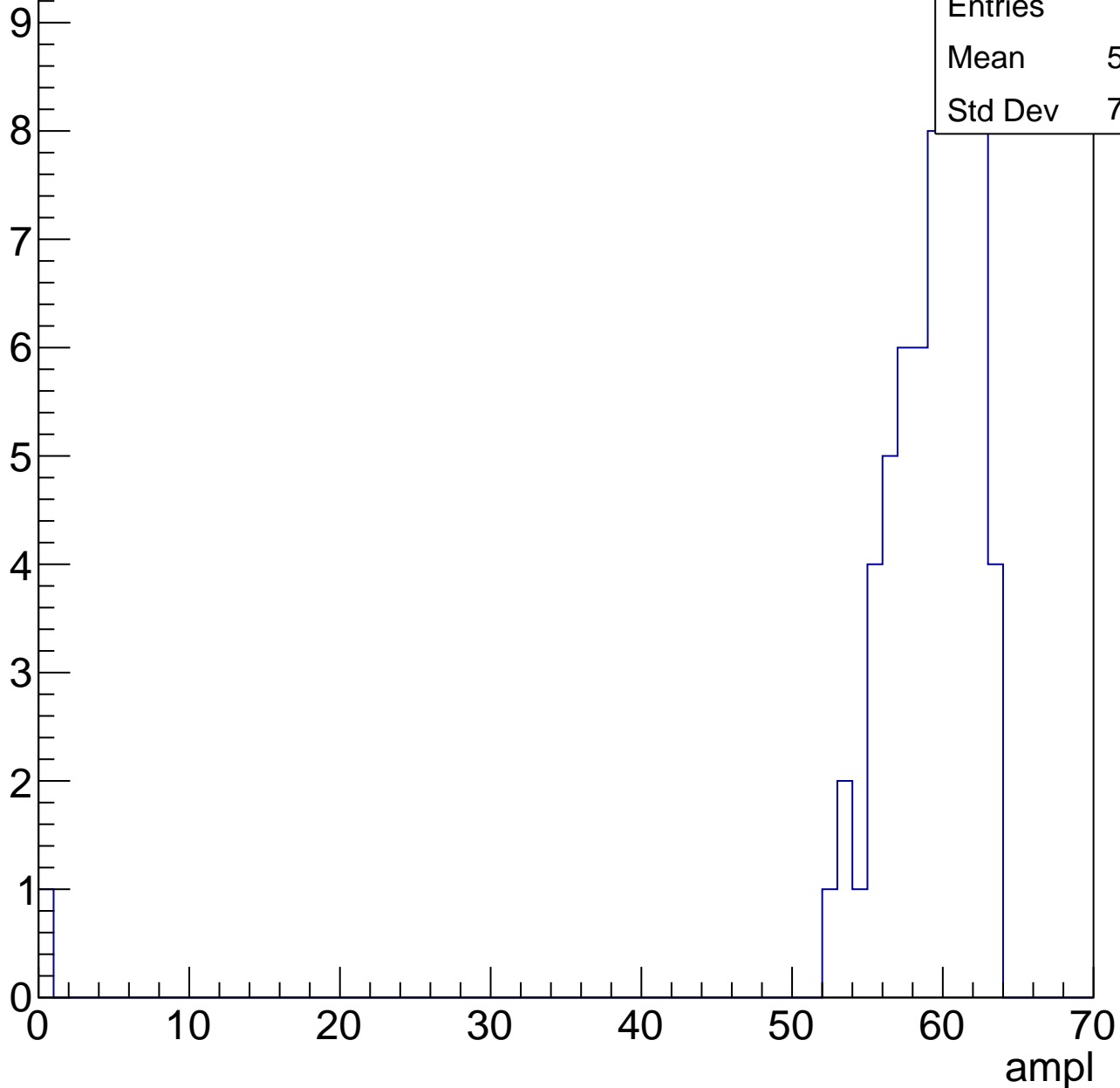
Entries	65
Mean	53.43
Std Dev	3.646



# B0L000S, U18-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

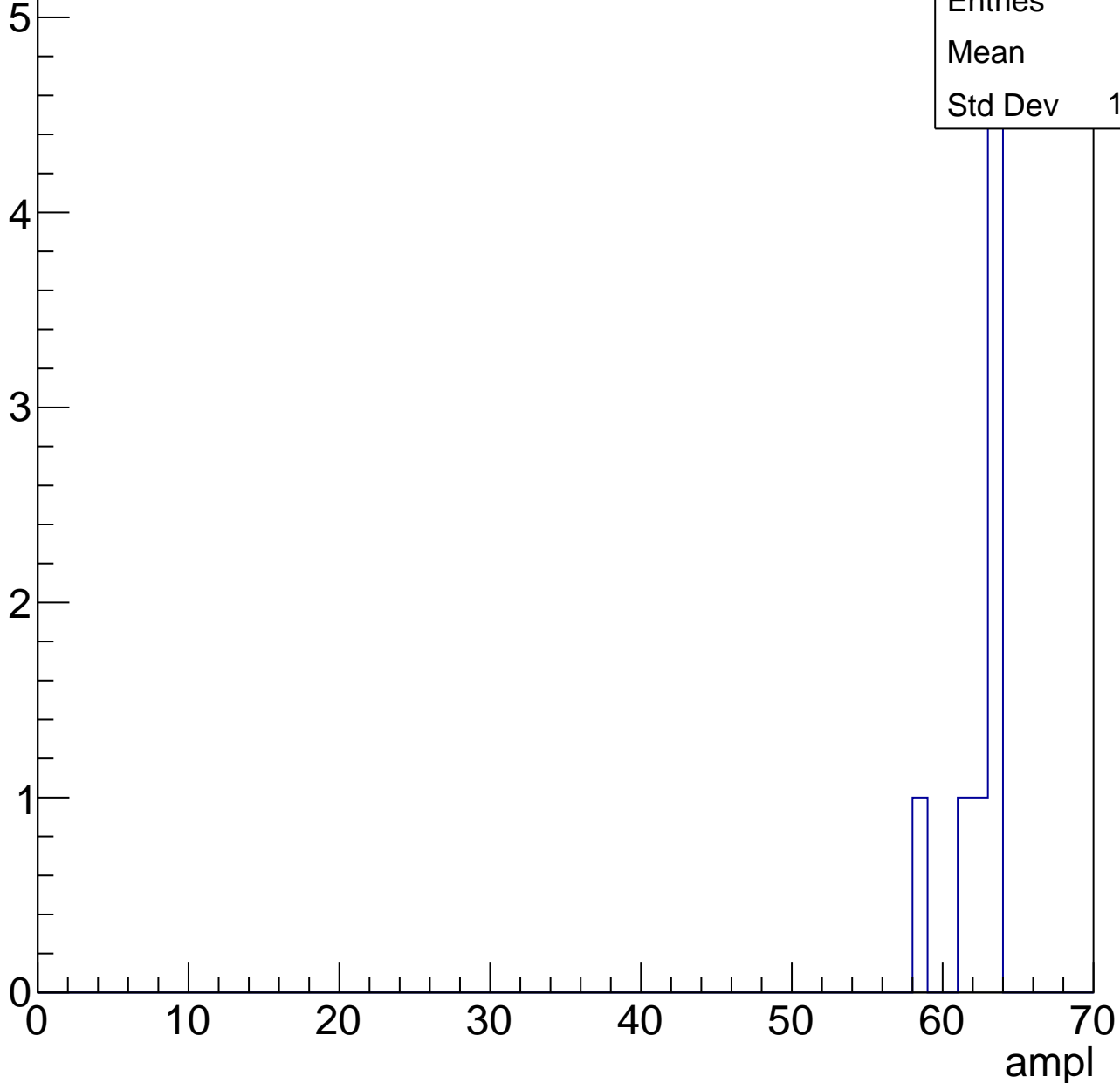


# B0L000S, U18-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	62
Std Dev	1.658





# B0L000S, U18-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U18-ch22, adc0

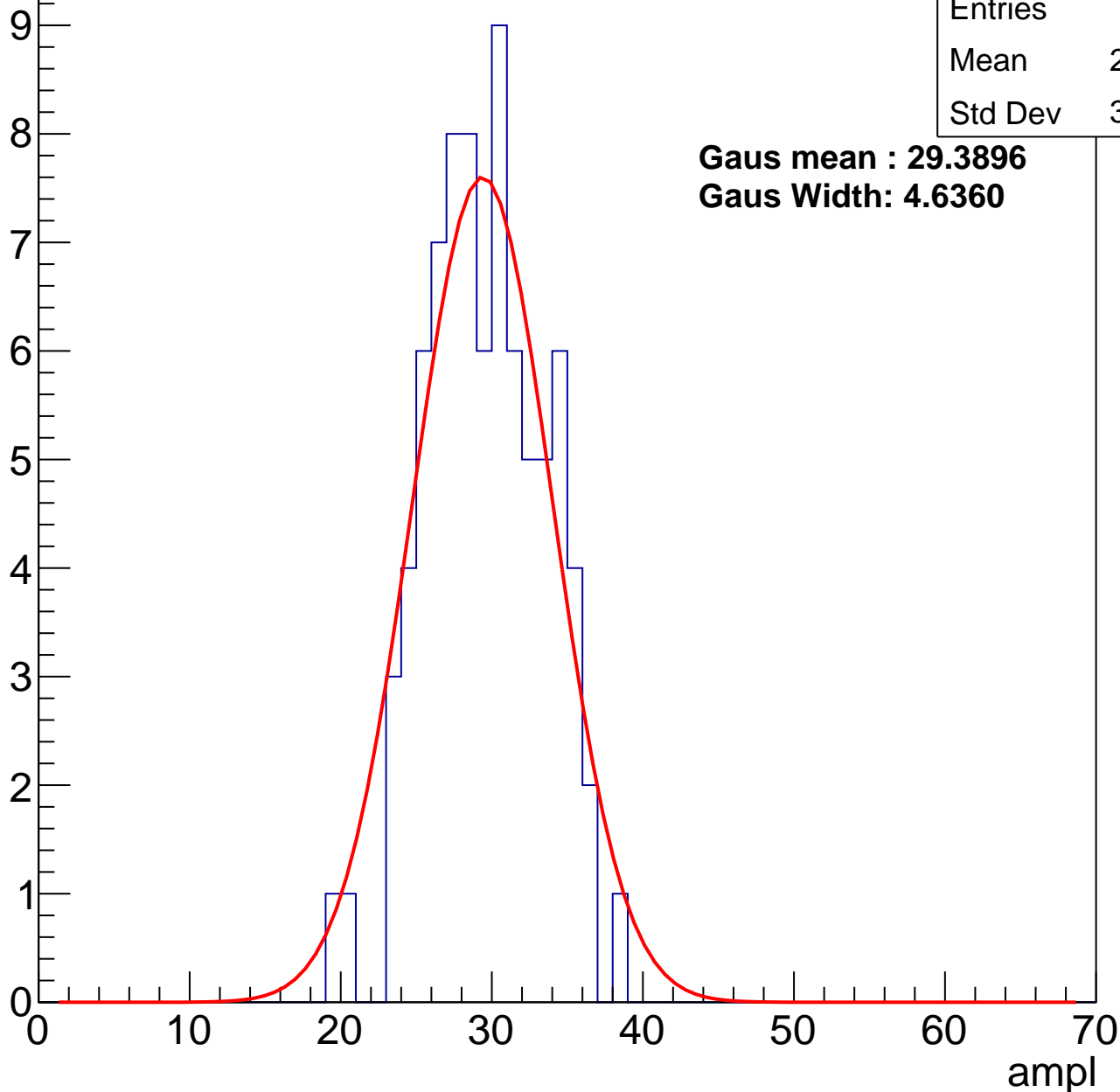
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	29.09
Std Dev	3.855

**Gaus mean : 29.3896**

**Gaus Width: 4.6360**



# B0L000S, U18-ch22, adc1

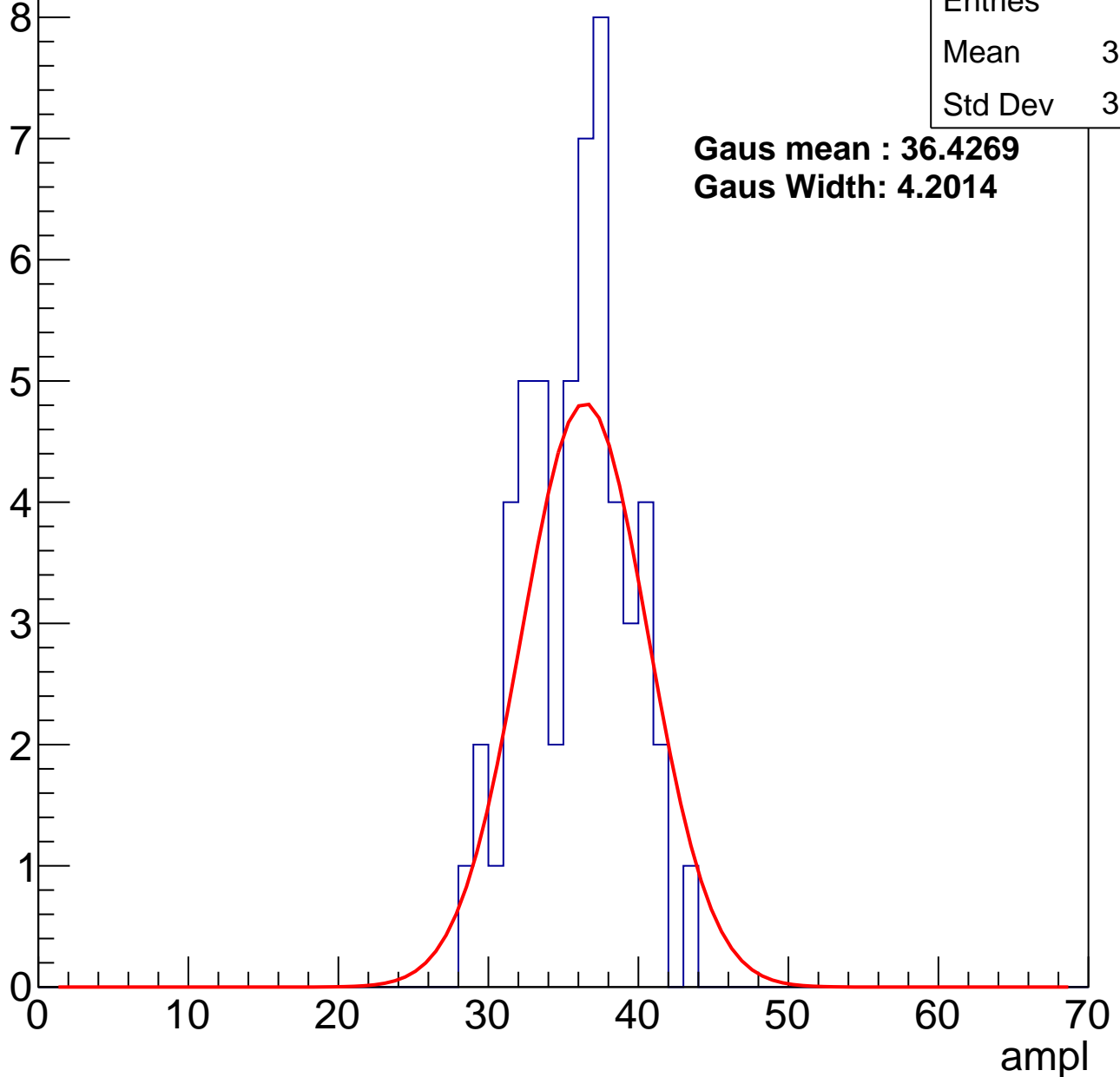
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	35.37
Std Dev	3.412

**Gaus mean : 36.4269**

**Gaus Width: 4.2014**



# B0L000S, U18-ch22, adc2

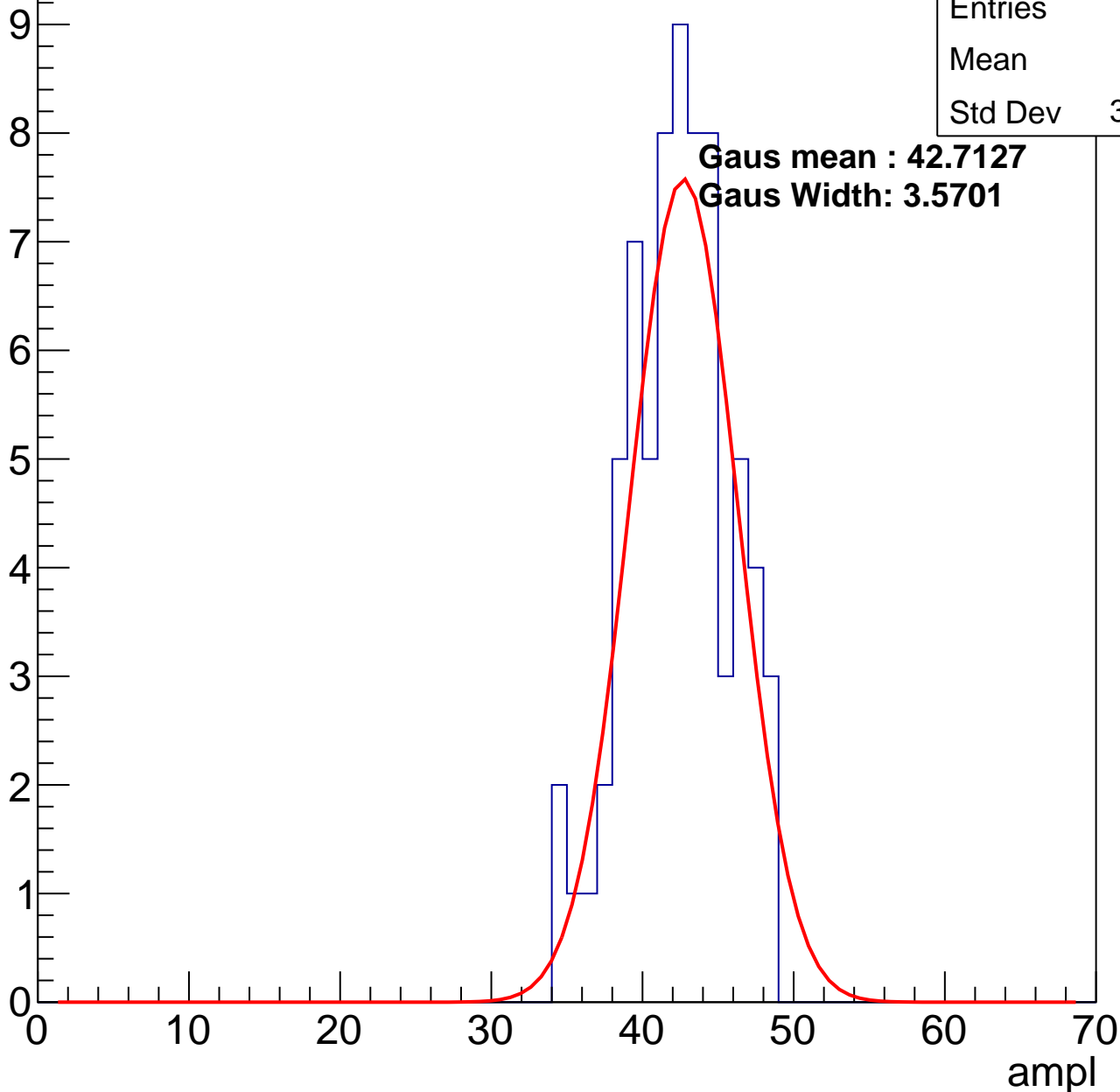
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	41.9
Std Dev	3.332

**Gaus mean : 42.7127**

**Gaus Width: 3.5701**

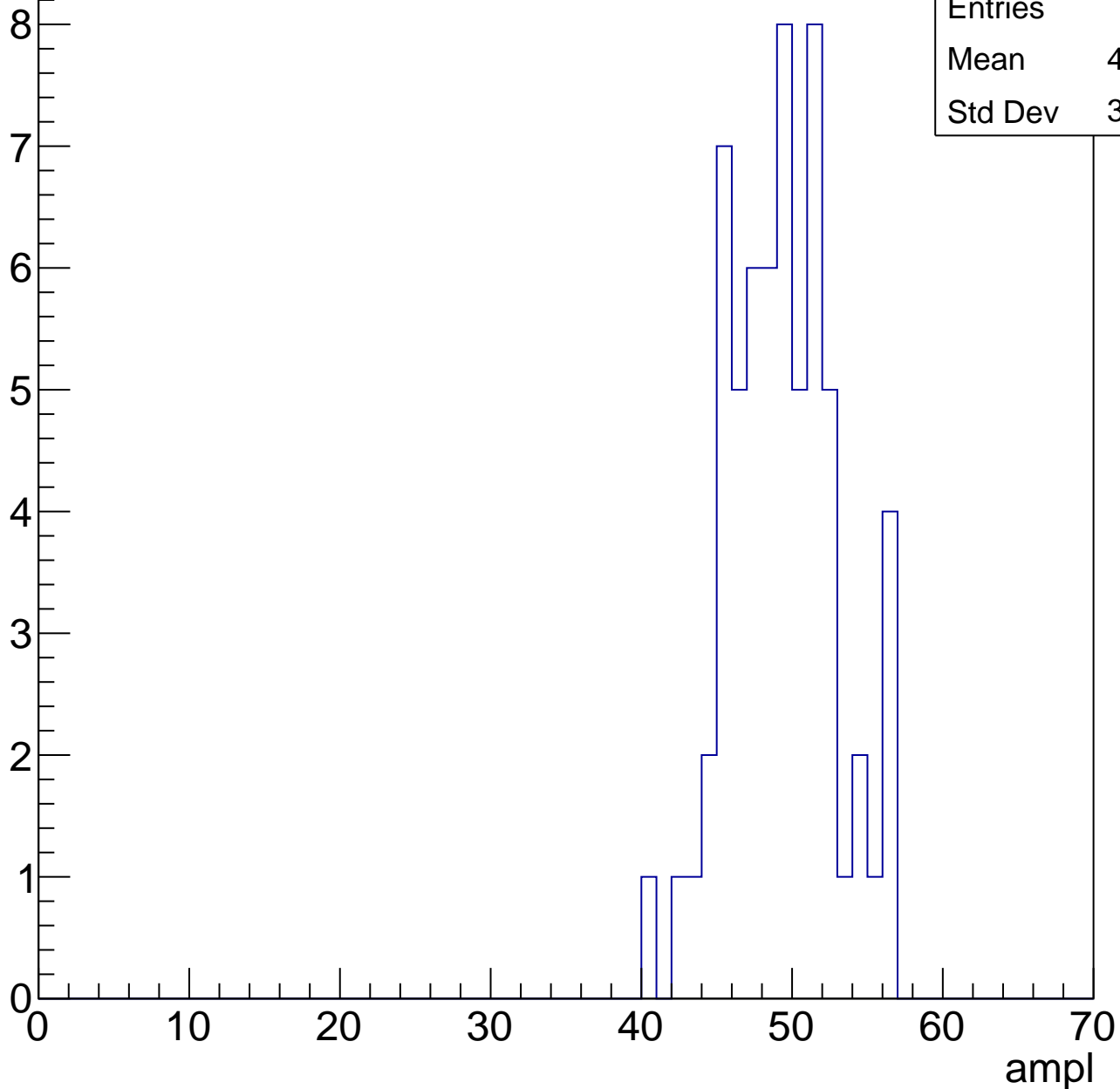


# B0L000S, U18-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	48.86
Std Dev	3.523

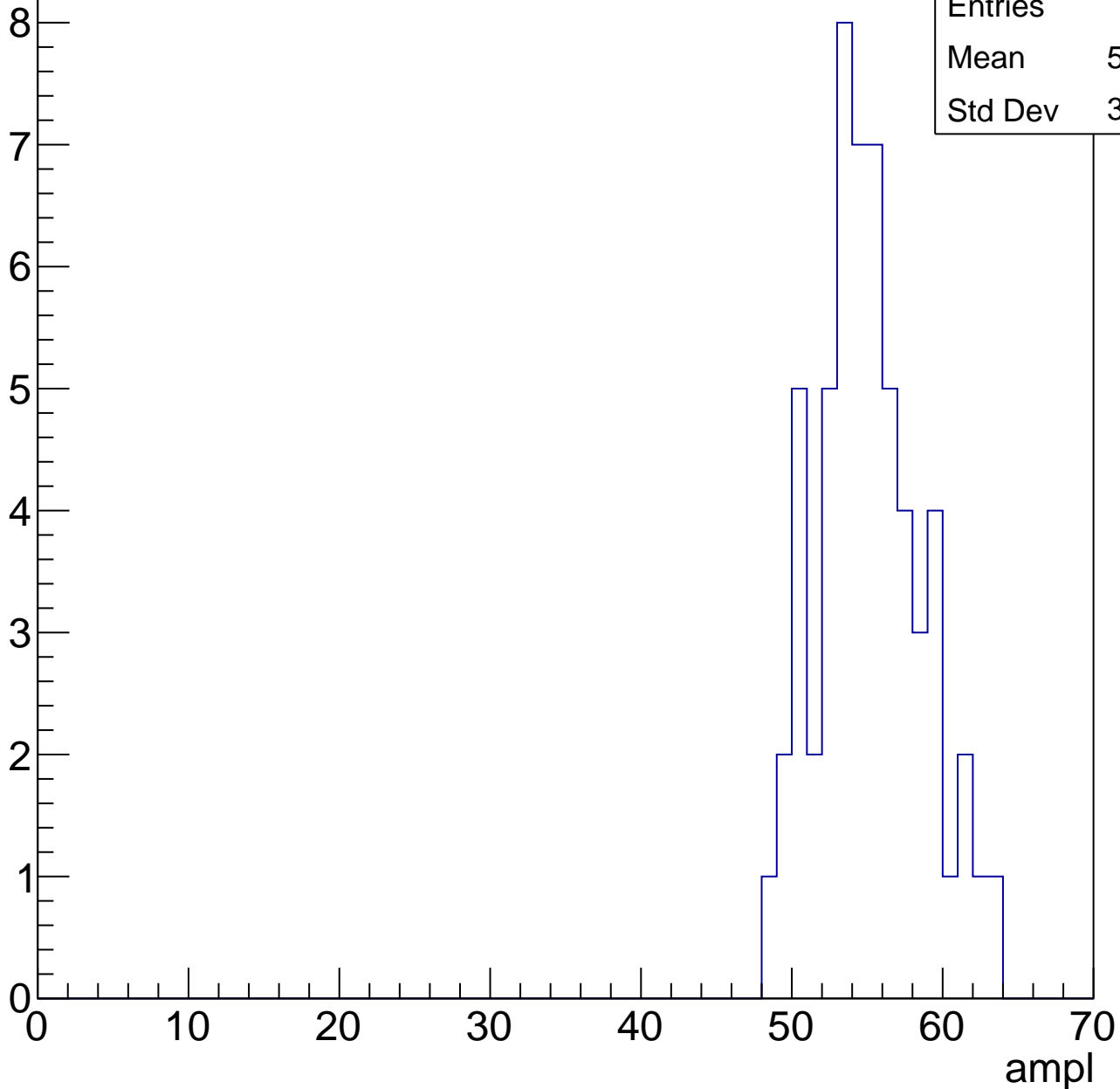


# B0L000S, U18-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	54.66
Std Dev	3.417

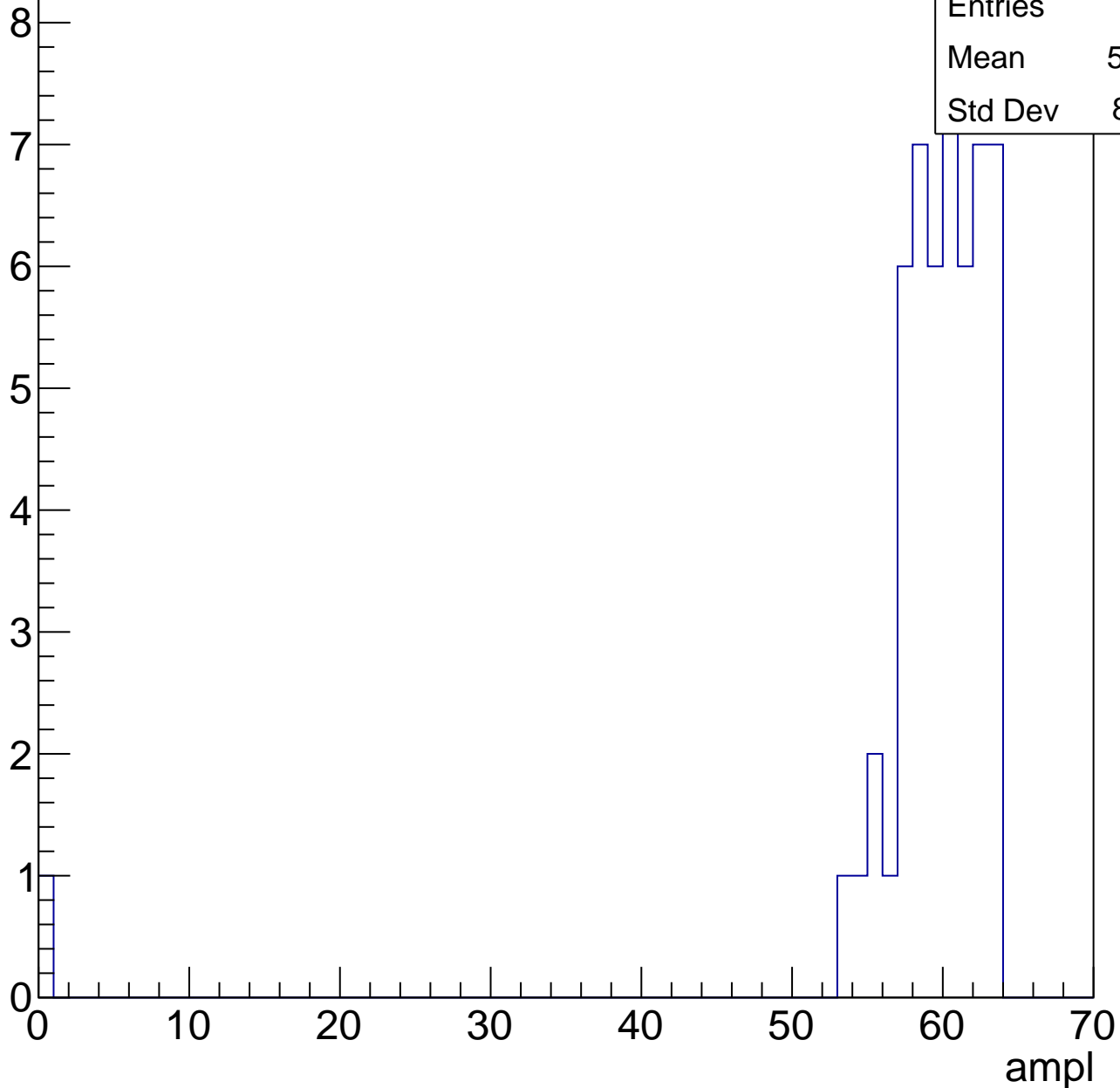


# B0L000S, U18-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.42
Std Dev	8.471



# B0L000S, U18-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.5
Std Dev	0.9574

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch23, adc0

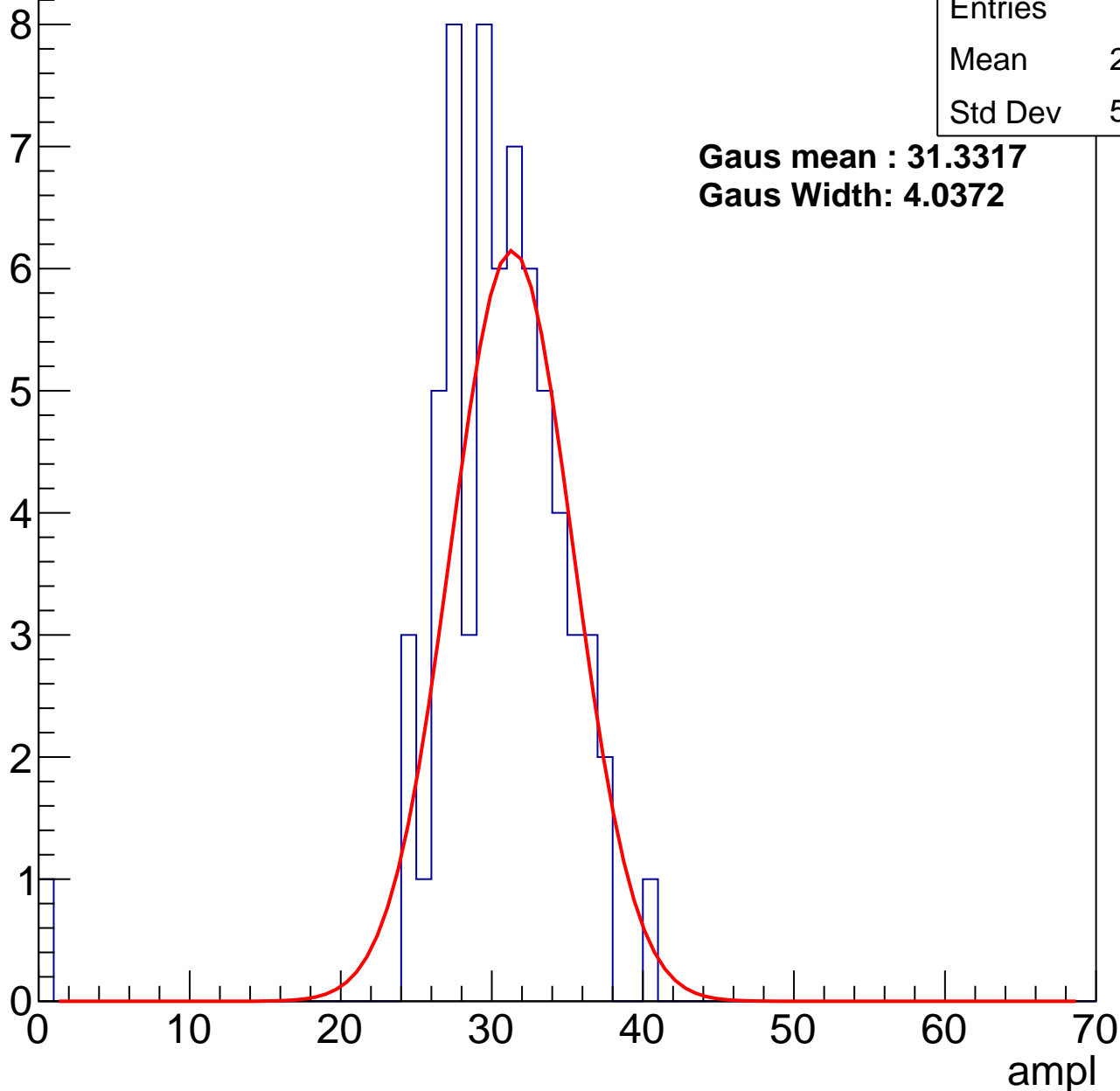
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.94
Std Dev	5.113

**Gaus mean : 31.3317**

**Gaus Width: 4.0372**



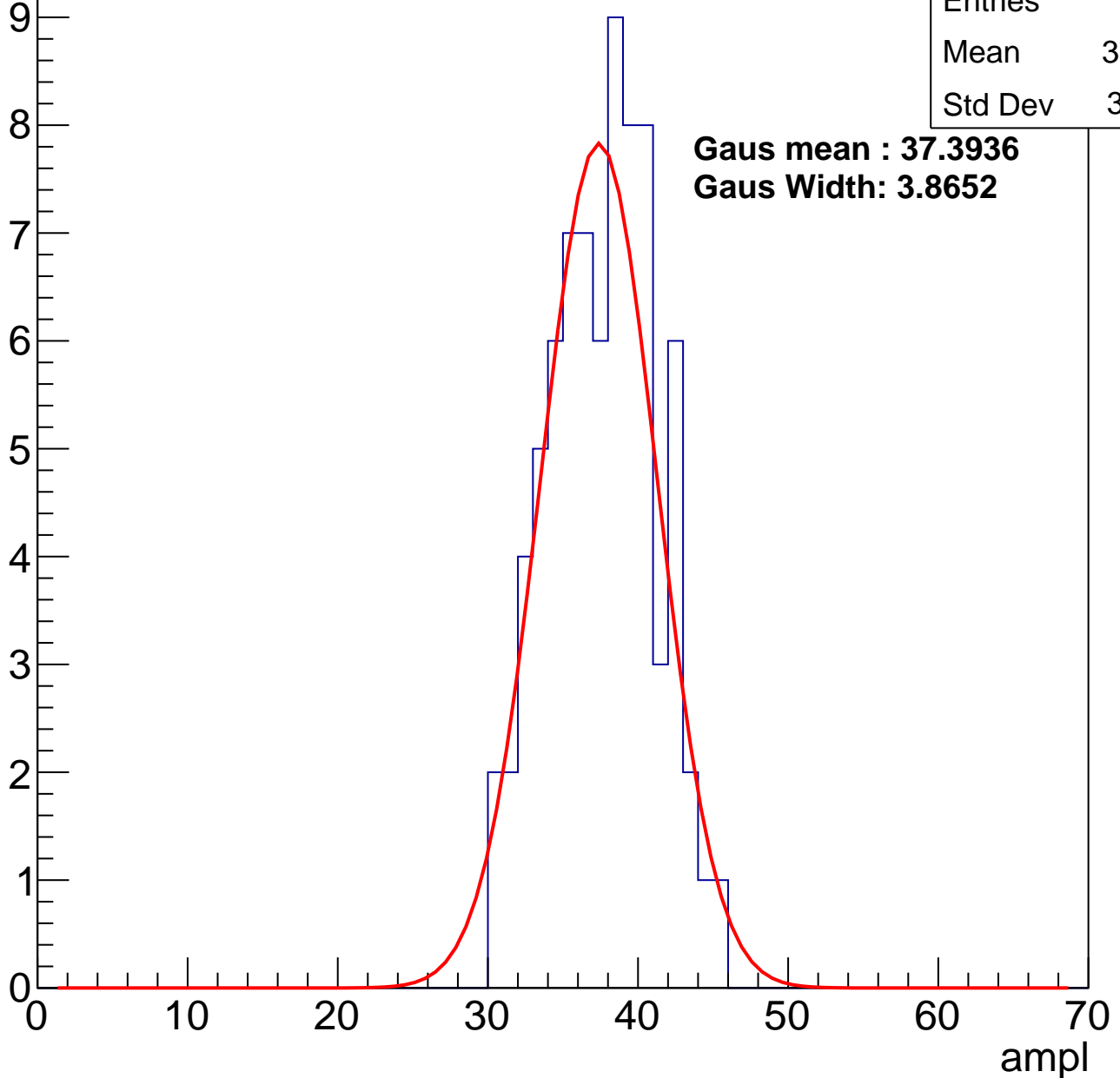
# B0L000S, U18-ch23, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	37.17
Std Dev	3.481

**Gaus mean : 37.3936**  
**Gaus Width: 3.8652**



# B0L000S, U18-ch23, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	45.23
Std Dev	3.444

**Gaus mean : 46.1841**

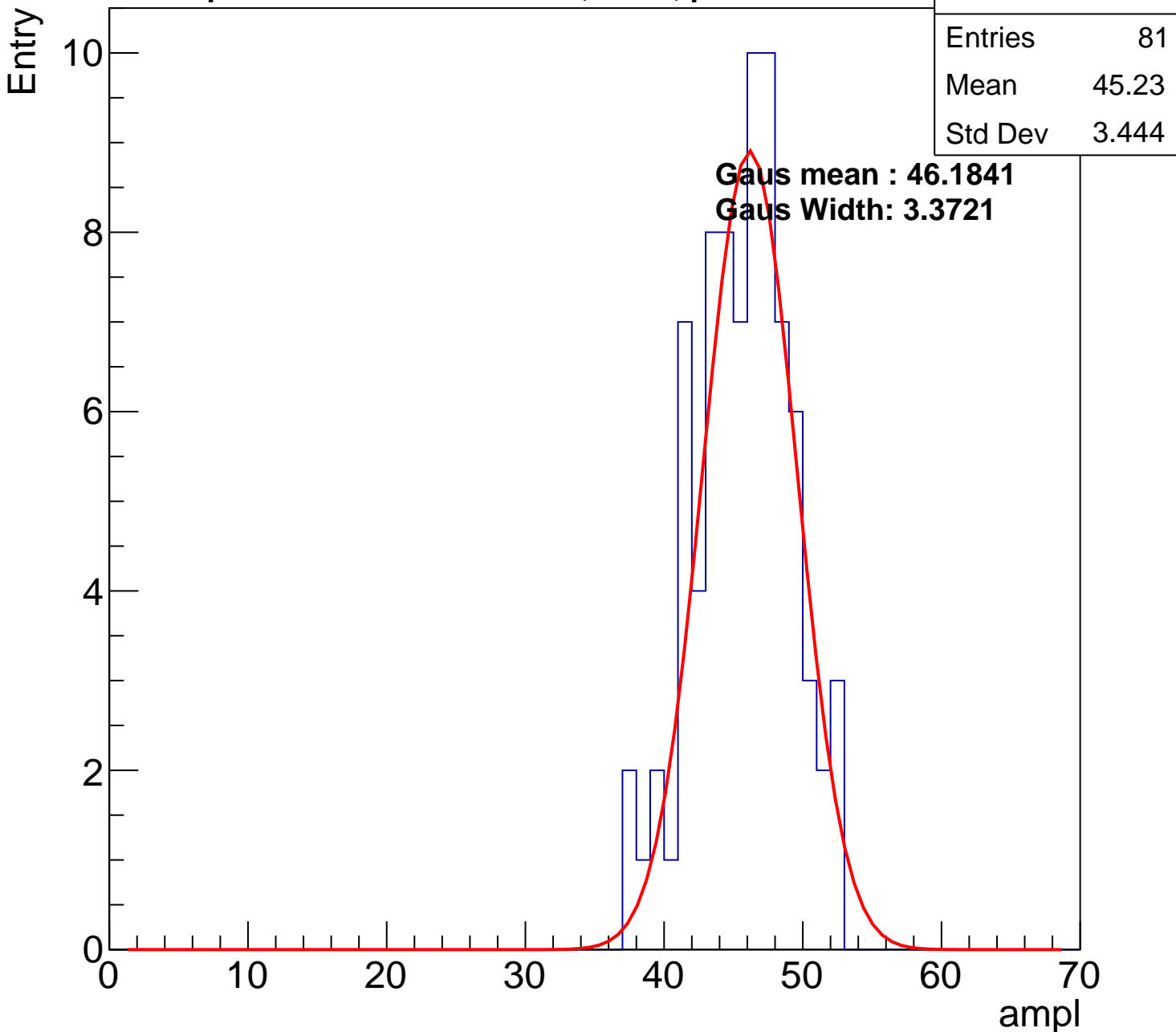
**Gaus Width: 3.3721**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

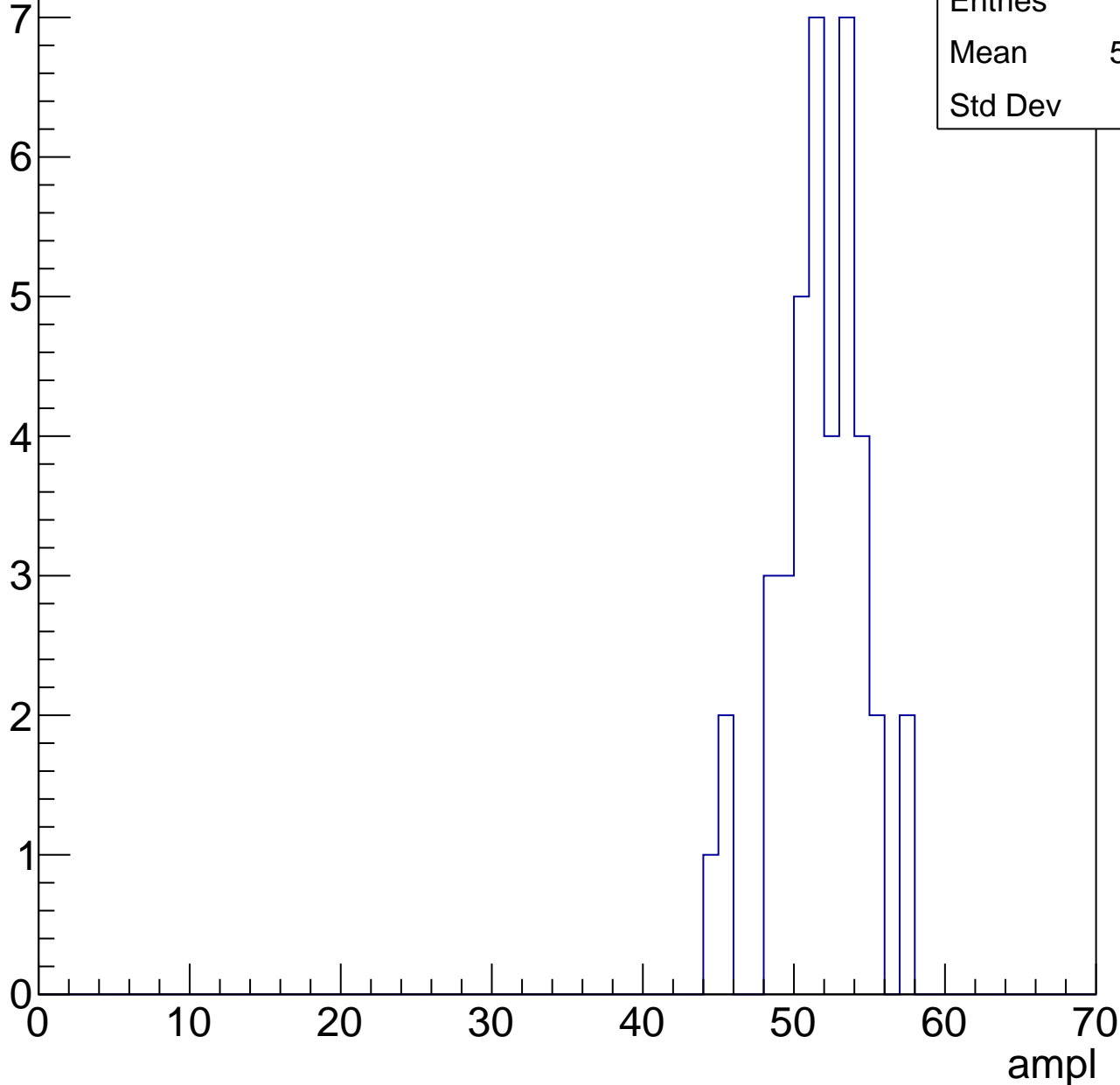


# B0L000S, U18-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

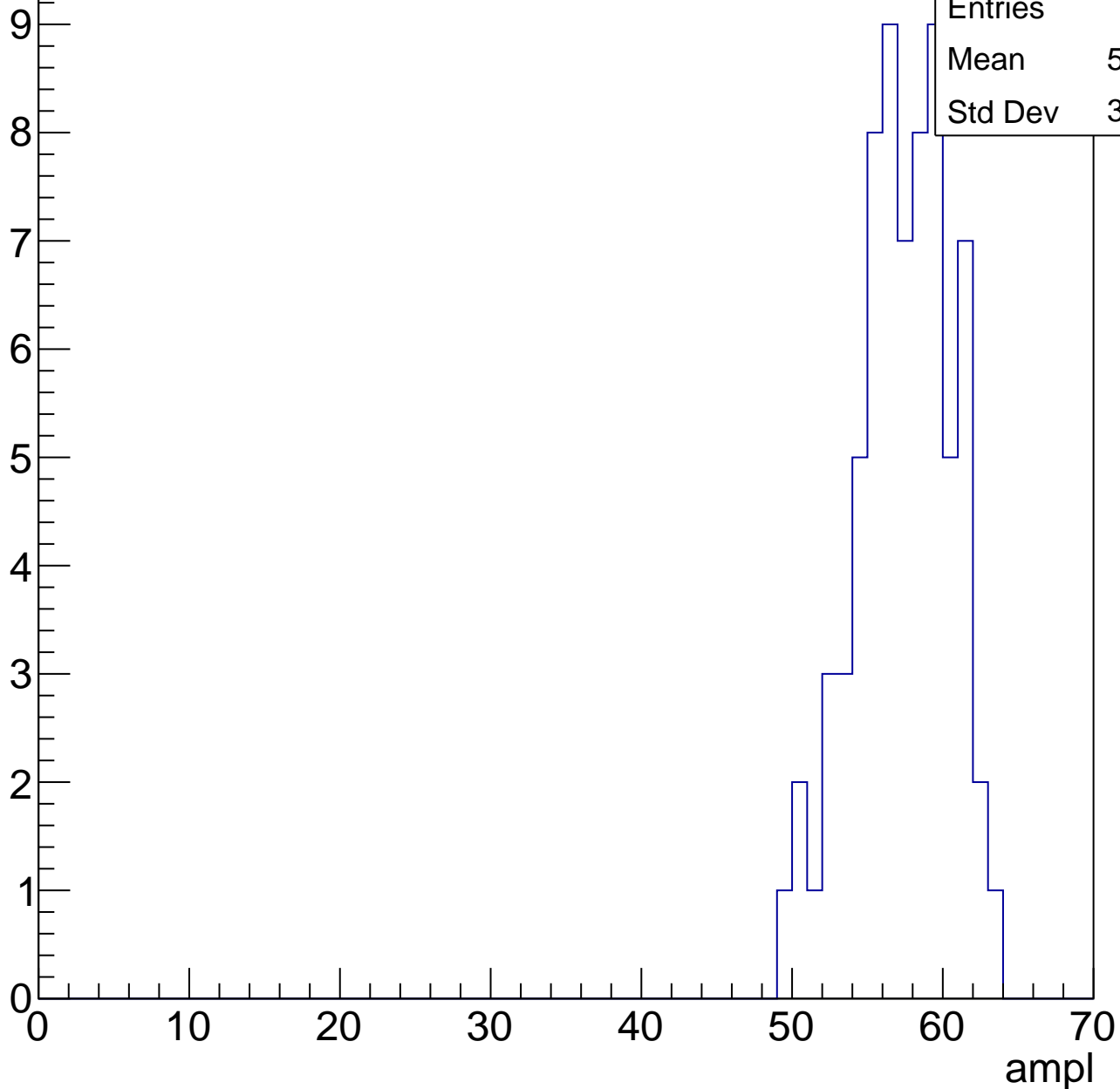
Entries	40
Mean	51.27
Std Dev	2.89



# B0L000S, U18-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

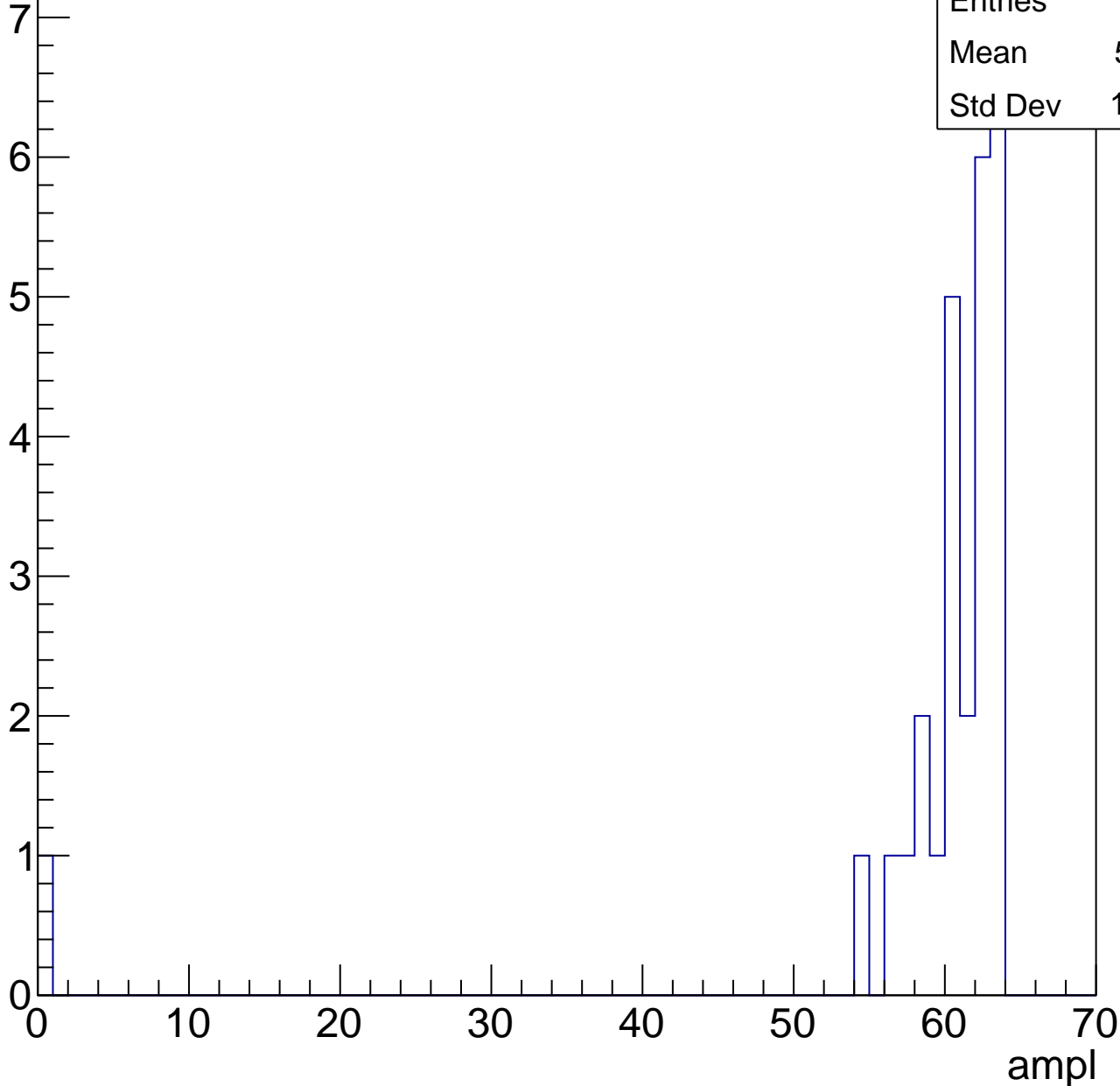


# B0L000S, U18-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	27
Mean	58.41
Std Dev	11.69



# B0L000S, U18-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch24, adc0

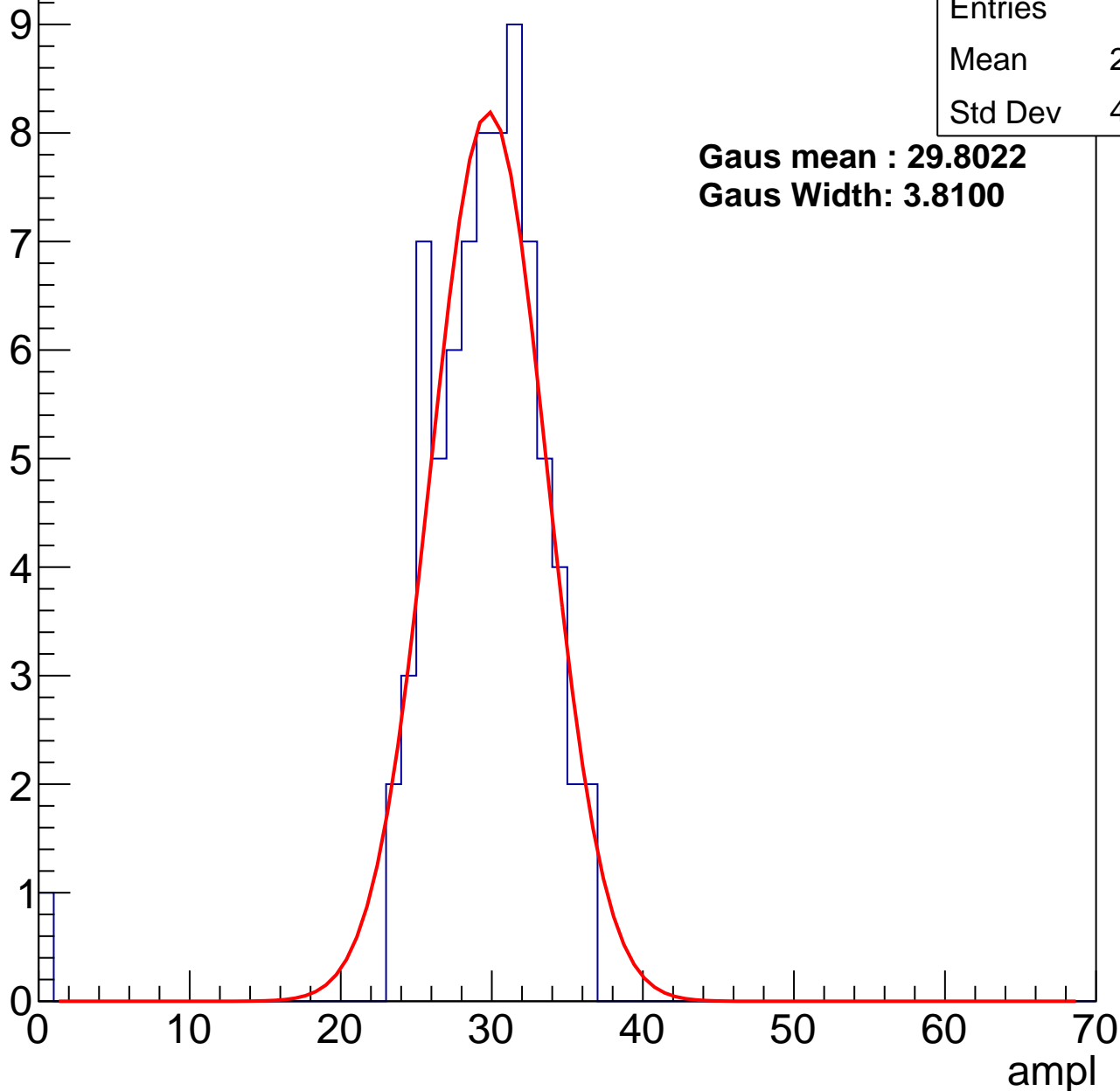
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	28.93
Std Dev	4.635

**Gaus mean : 29.8022**

**Gaus Width: 3.8100**



# B0L000S, U18-ch24, adc1

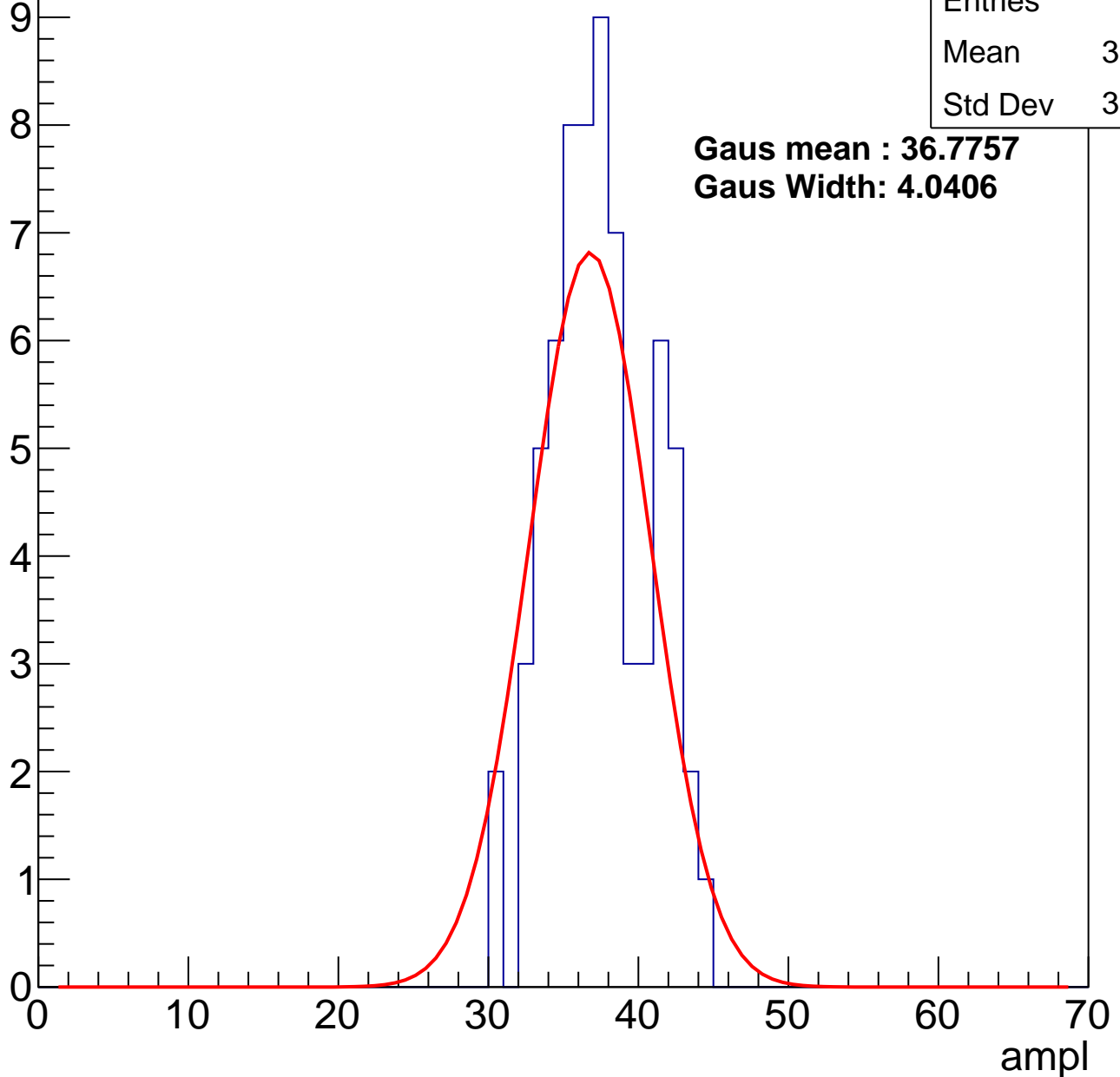
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.99
Std Dev	3.288

**Gaus mean : 36.7757**

**Gaus Width: 4.0406**



# B0L000S, U18-ch24, adc2

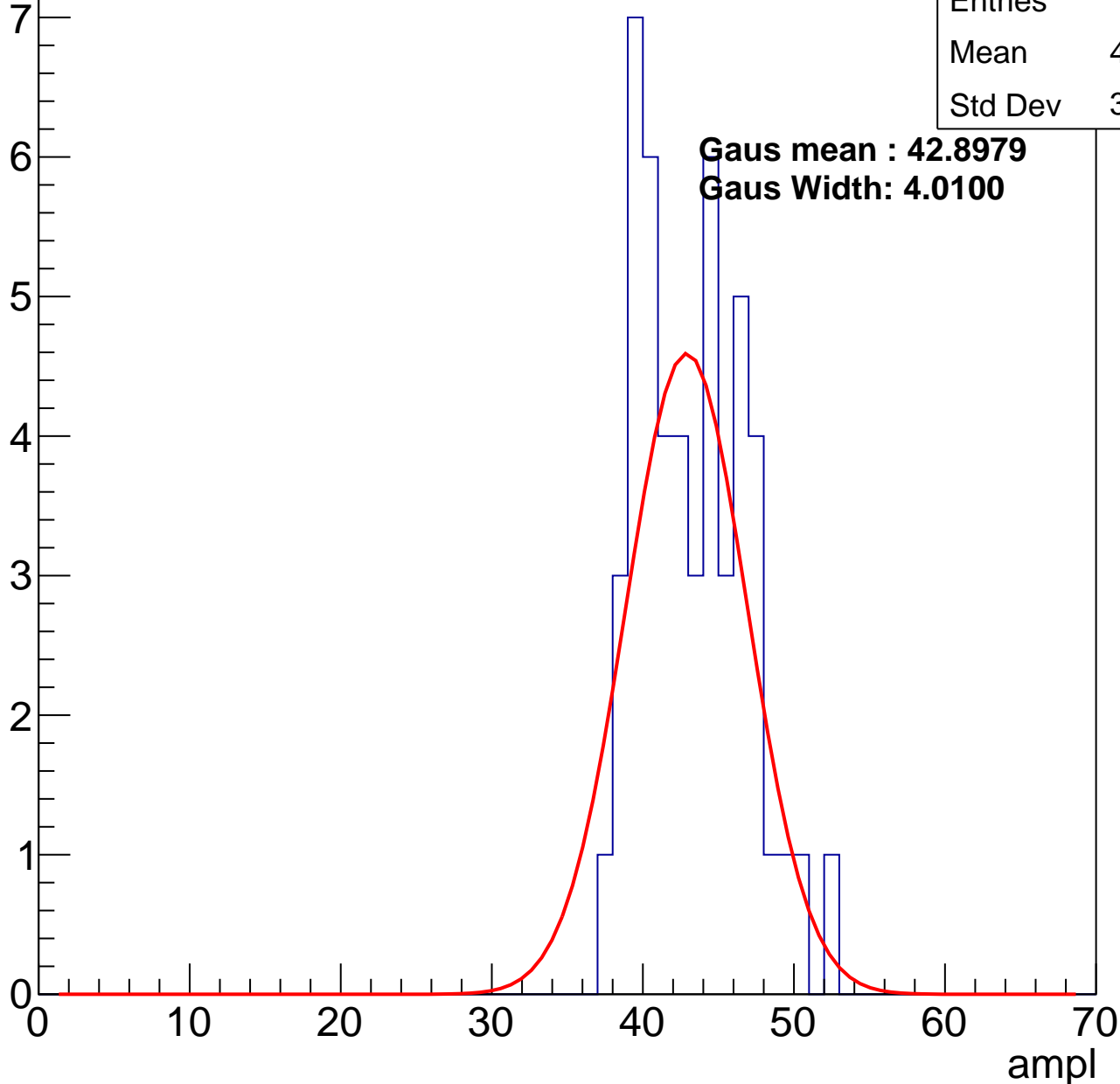
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	42.82
Std Dev	3.503

**Gaus mean : 42.8979**

**Gaus Width: 4.0100**

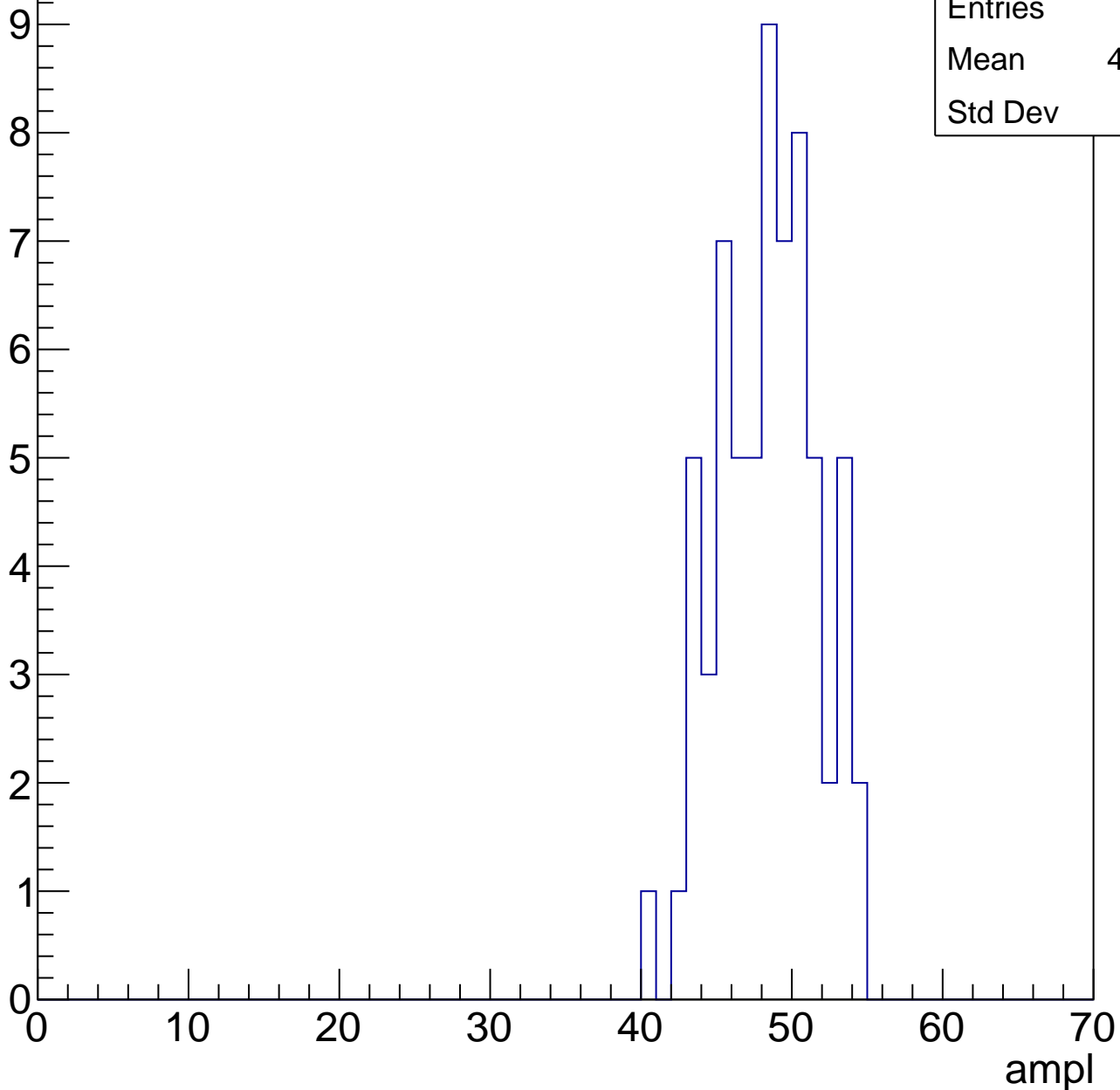


# B0L000S, U18-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	47.94
Std Dev	3.22



# B0L000S, U18-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

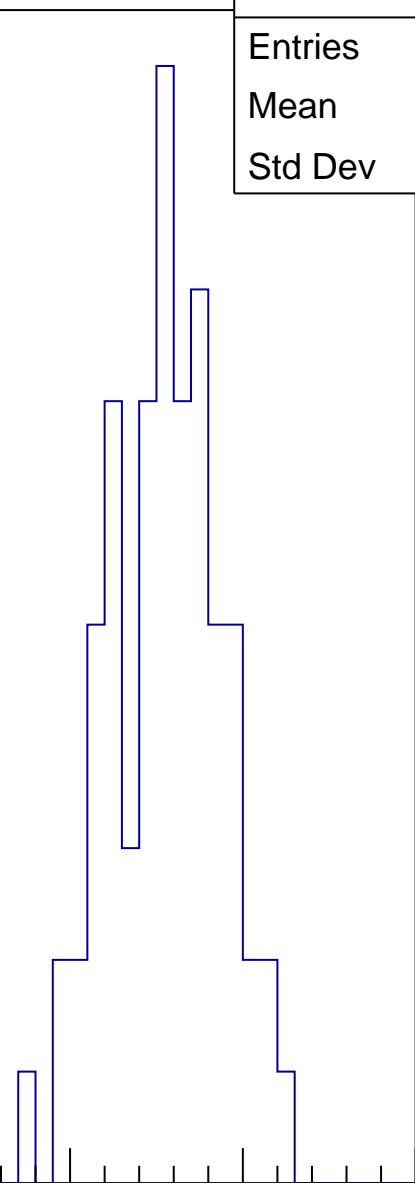
Entries	67
Mean	55.04
Std Dev	3.192

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

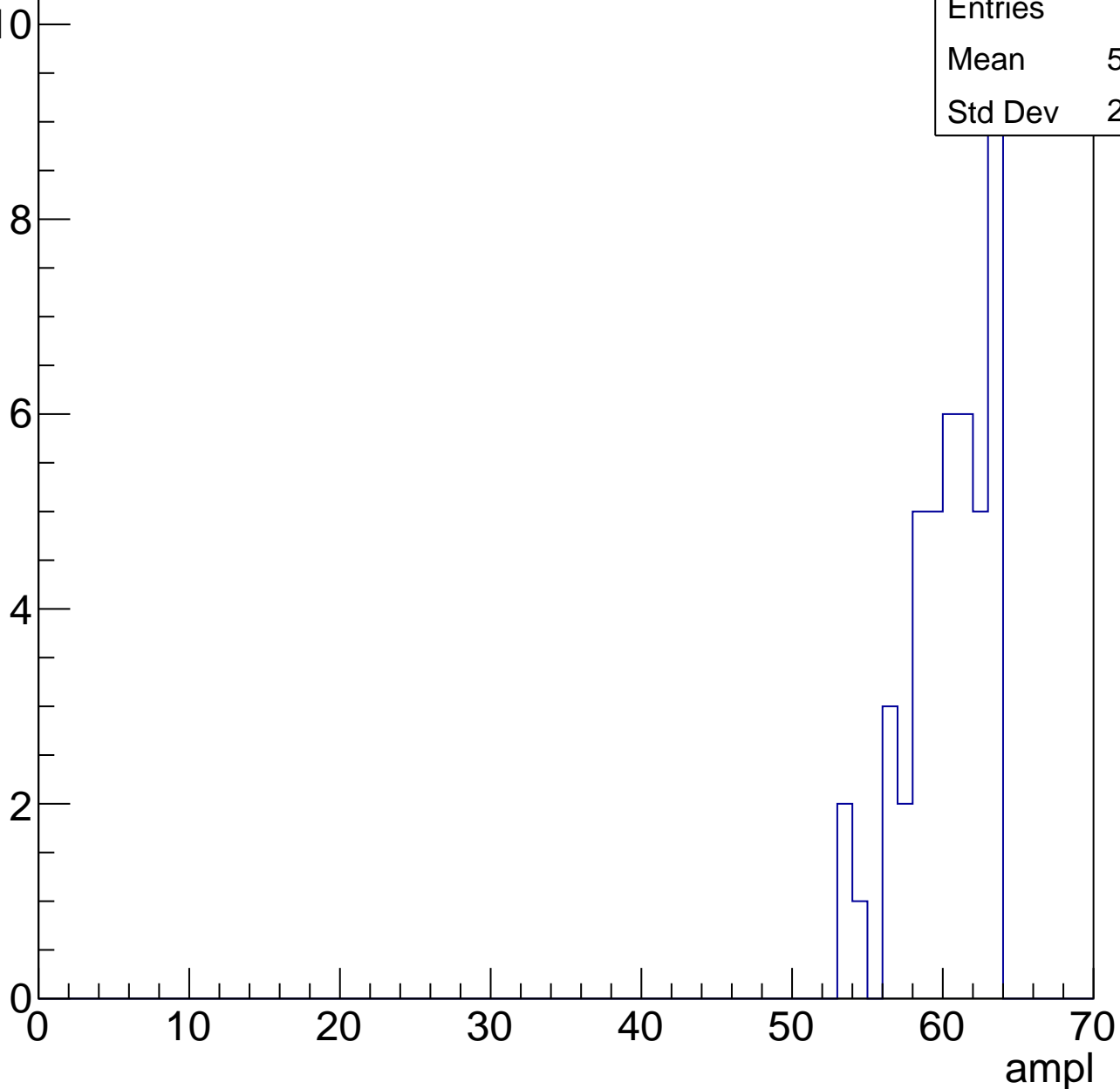


# B0L000S, U18-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

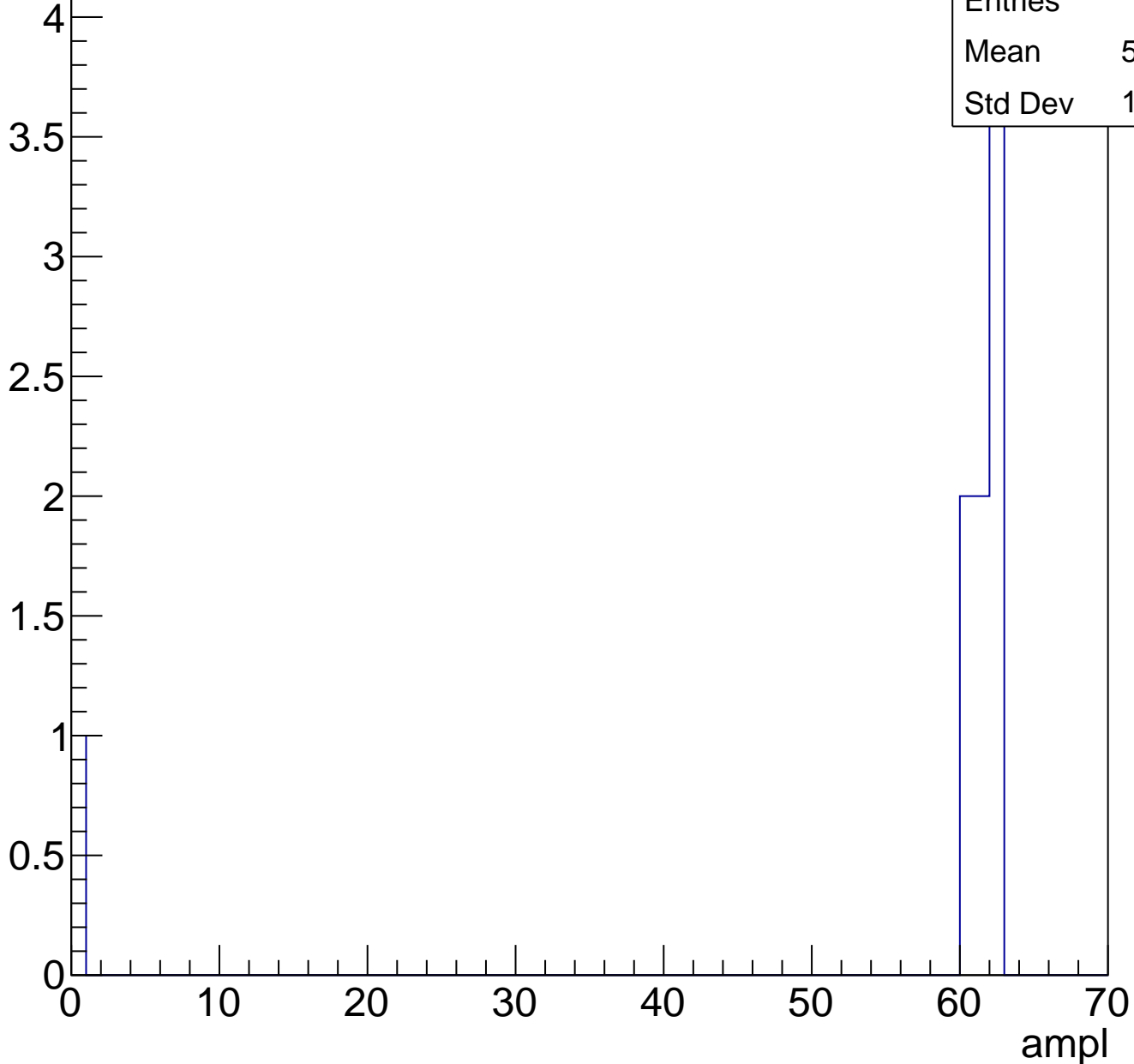
Entries	45
Mean	59.84
Std Dev	2.748



# B0L000S, U18-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U18-ch25, adc0

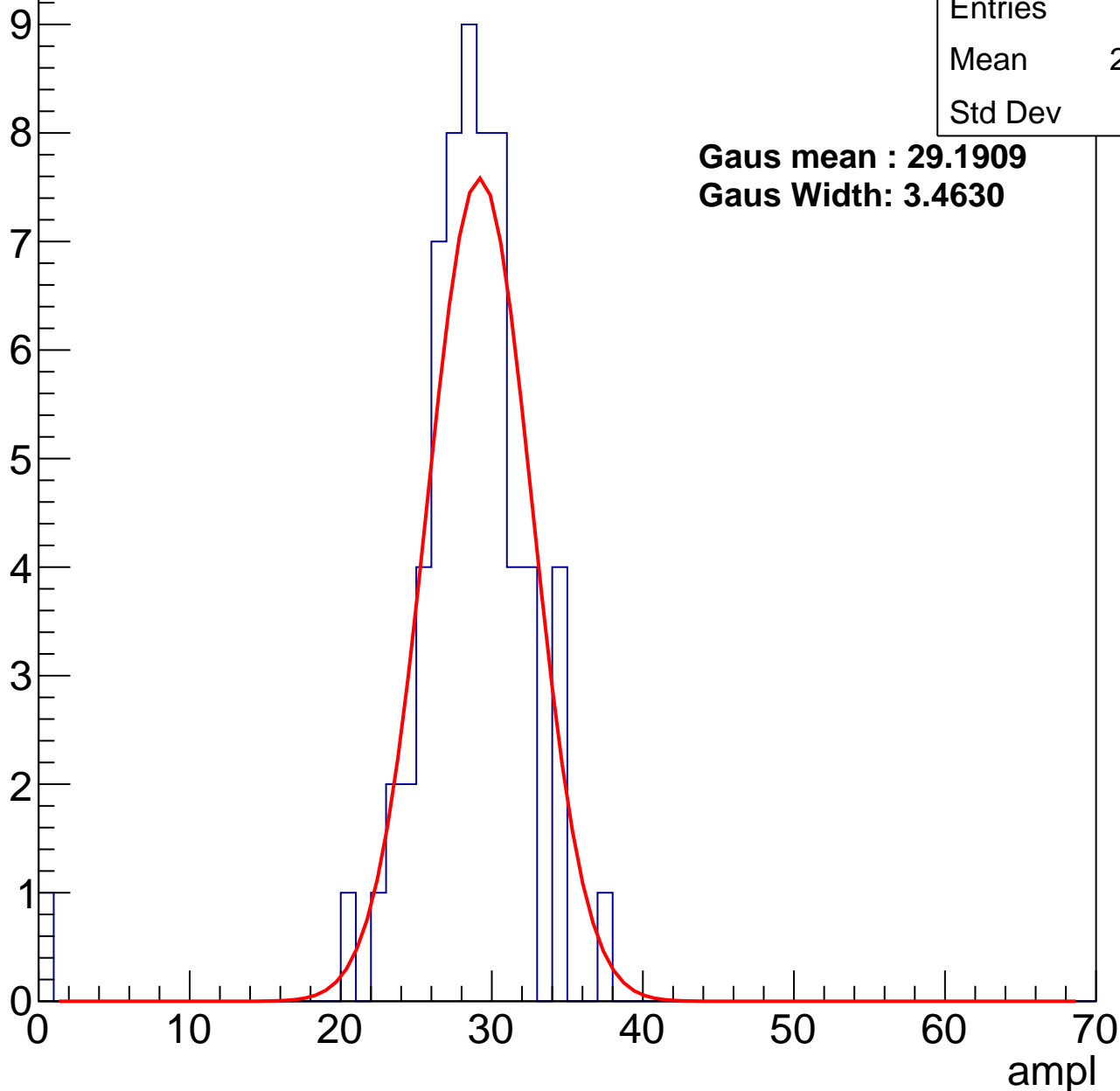
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	27.86
Std Dev	4.68

**Gaus mean : 29.1909**

**Gaus Width: 3.4630**



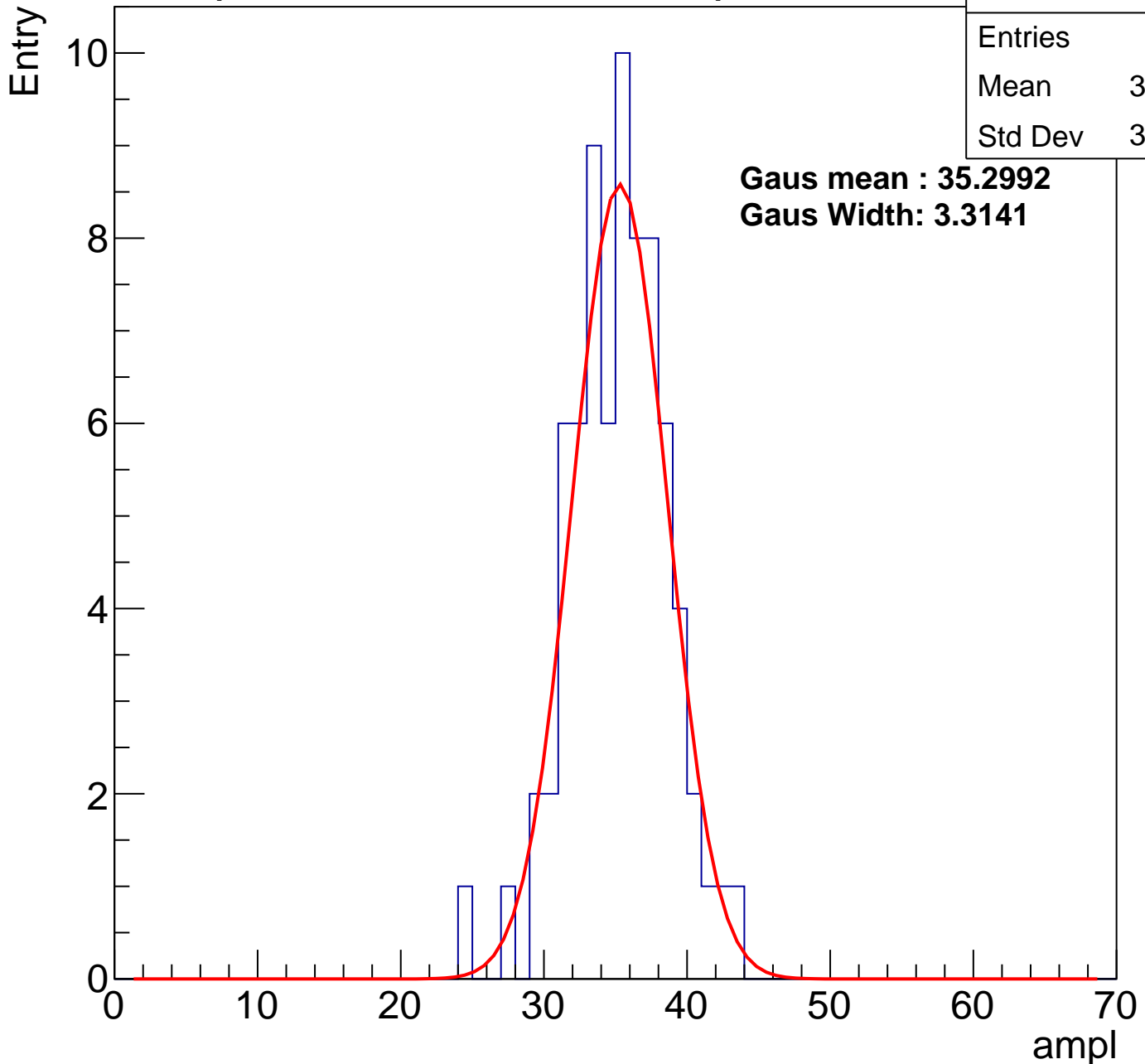
# B0L000S, U18-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	34.76
Std Dev	3.392

**Gaus mean : 35.2992**

**Gaus Width: 3.3141**



# B0L000S, U18-ch25, adc2

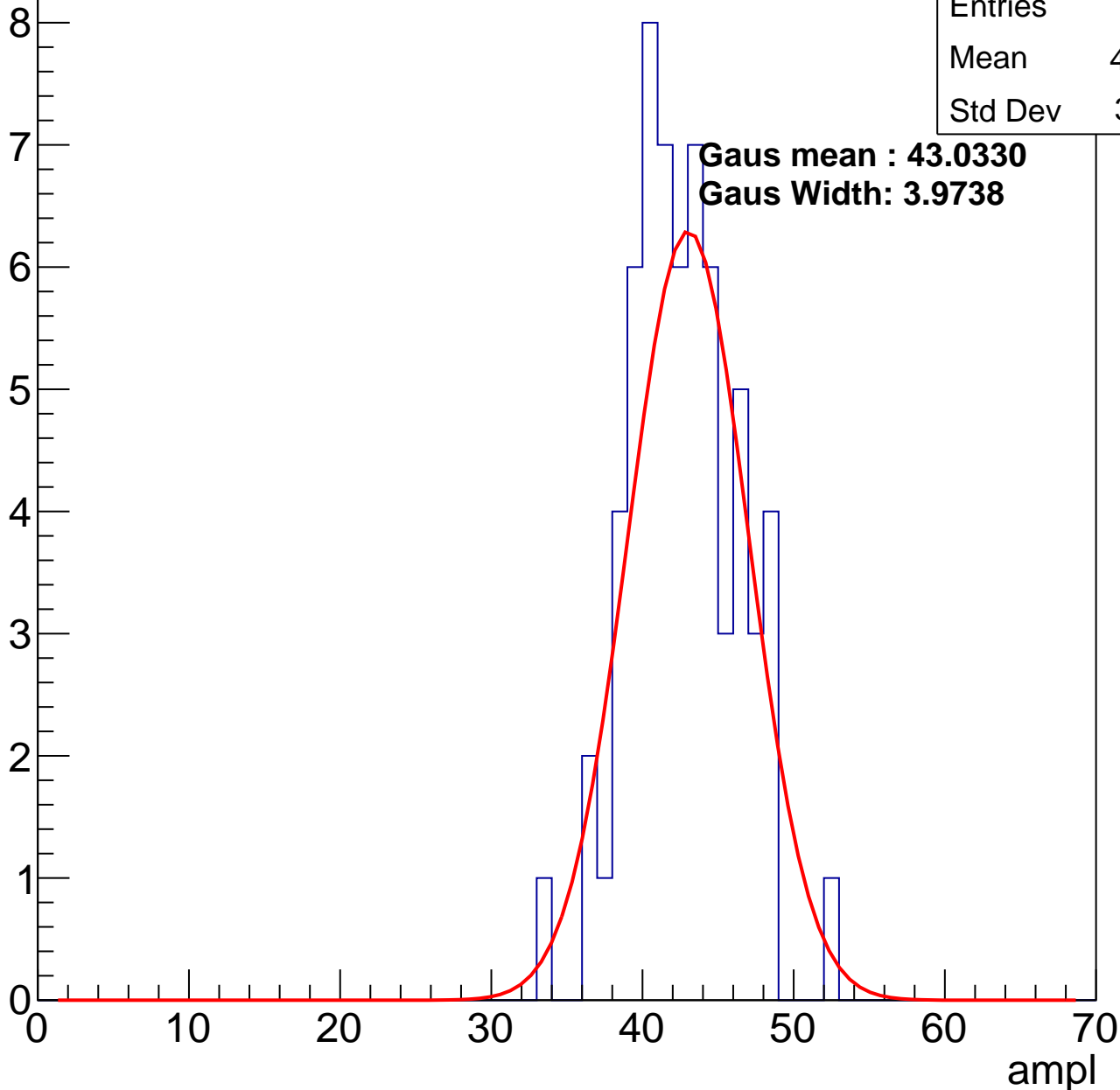
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.22
Std Dev	3.511

**Gaus mean : 43.0330**

**Gaus Width: 3.9738**

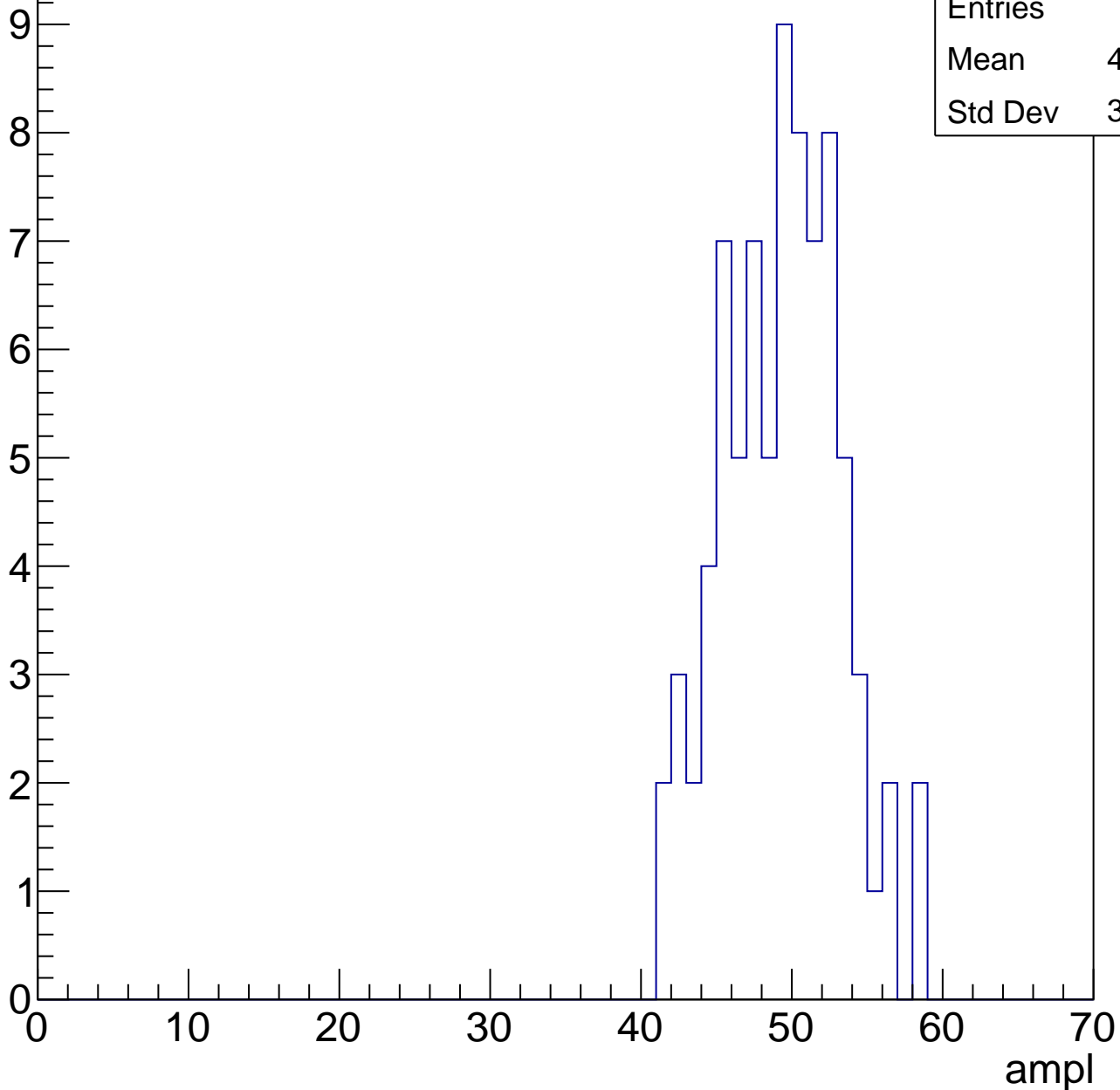


# B0L000S, U18-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	48.85
Std Dev	3.864

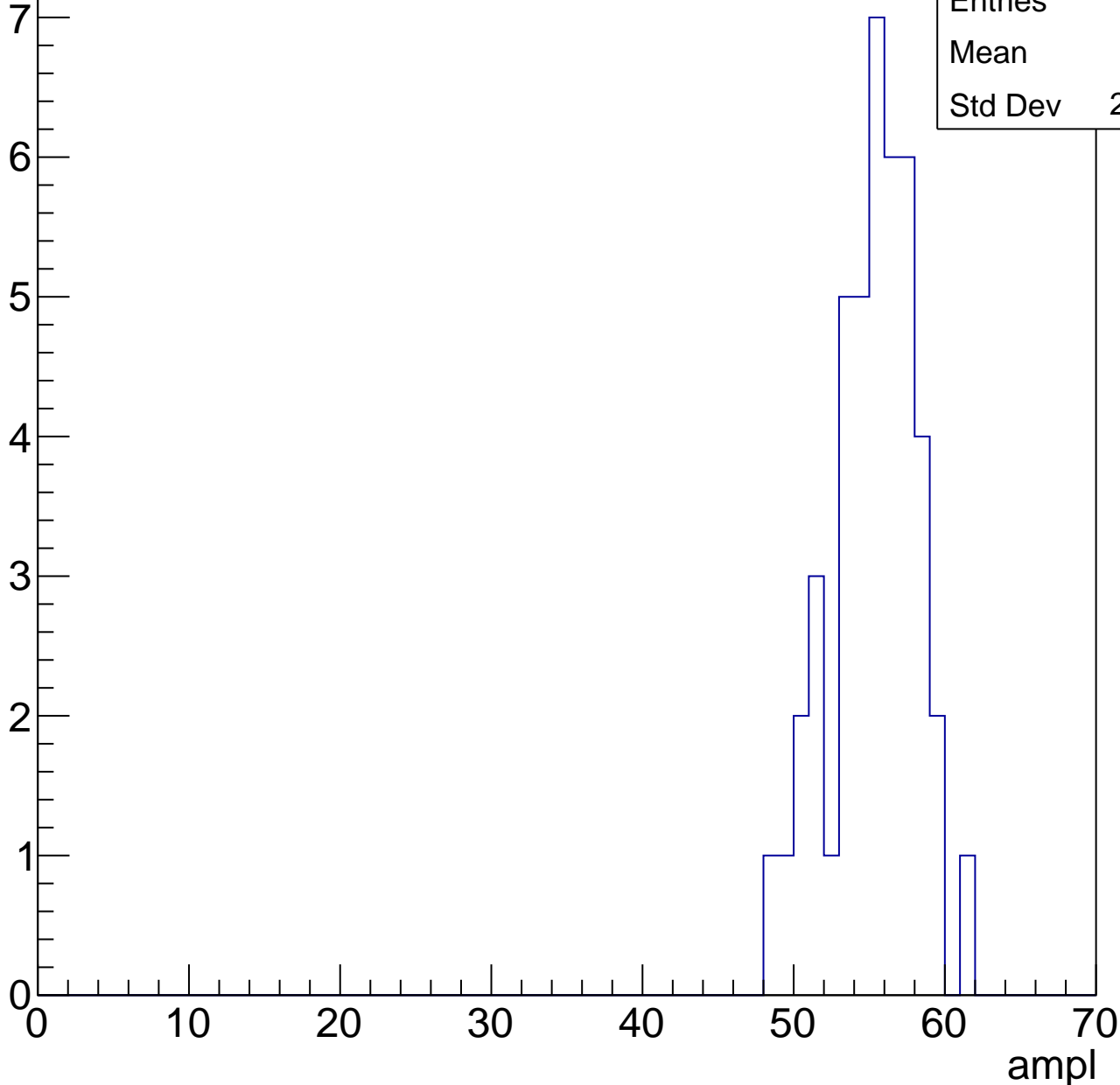


# B0L000S, U18-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	54.8
Std Dev	2.817



# B0L000S, U18-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

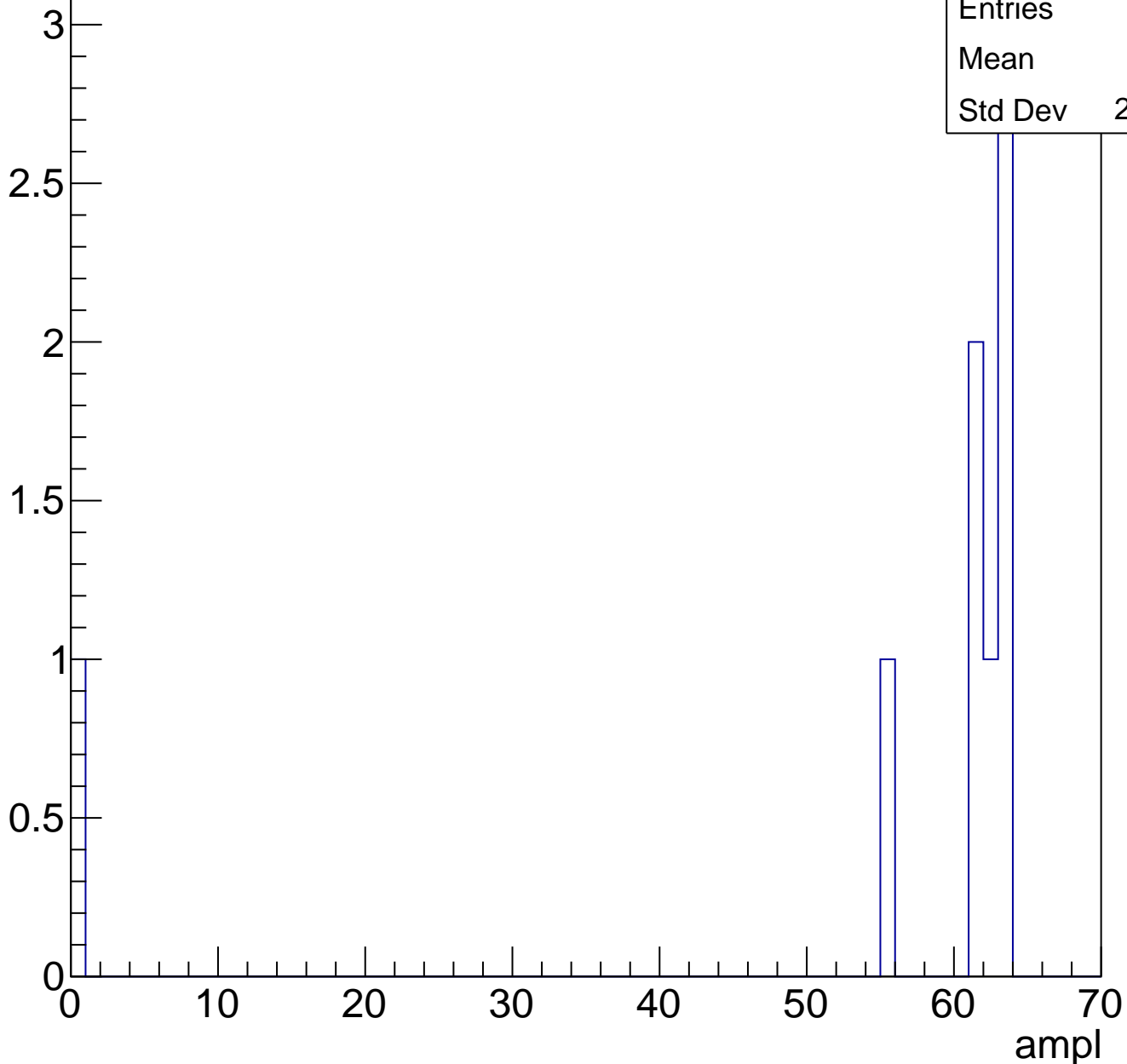
ampl

Entries	53
Mean	59.81
Std Dev	2.315

# B0L000S, U18-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch26, adc0

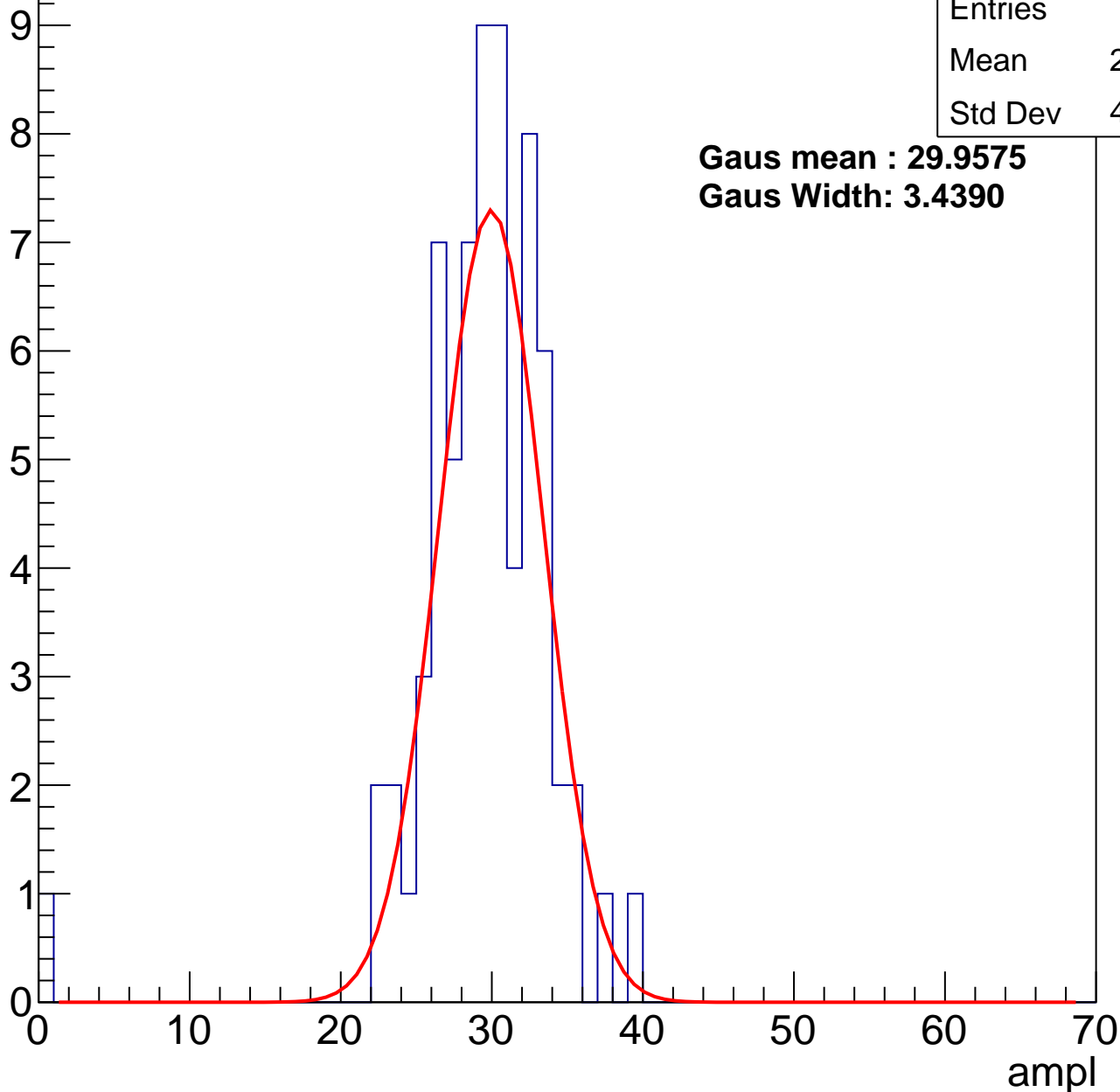
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.93
Std Dev	4.859

**Gaus mean : 29.9575**

**Gaus Width: 3.4390**



# B0L000S, U18-ch26, adc1

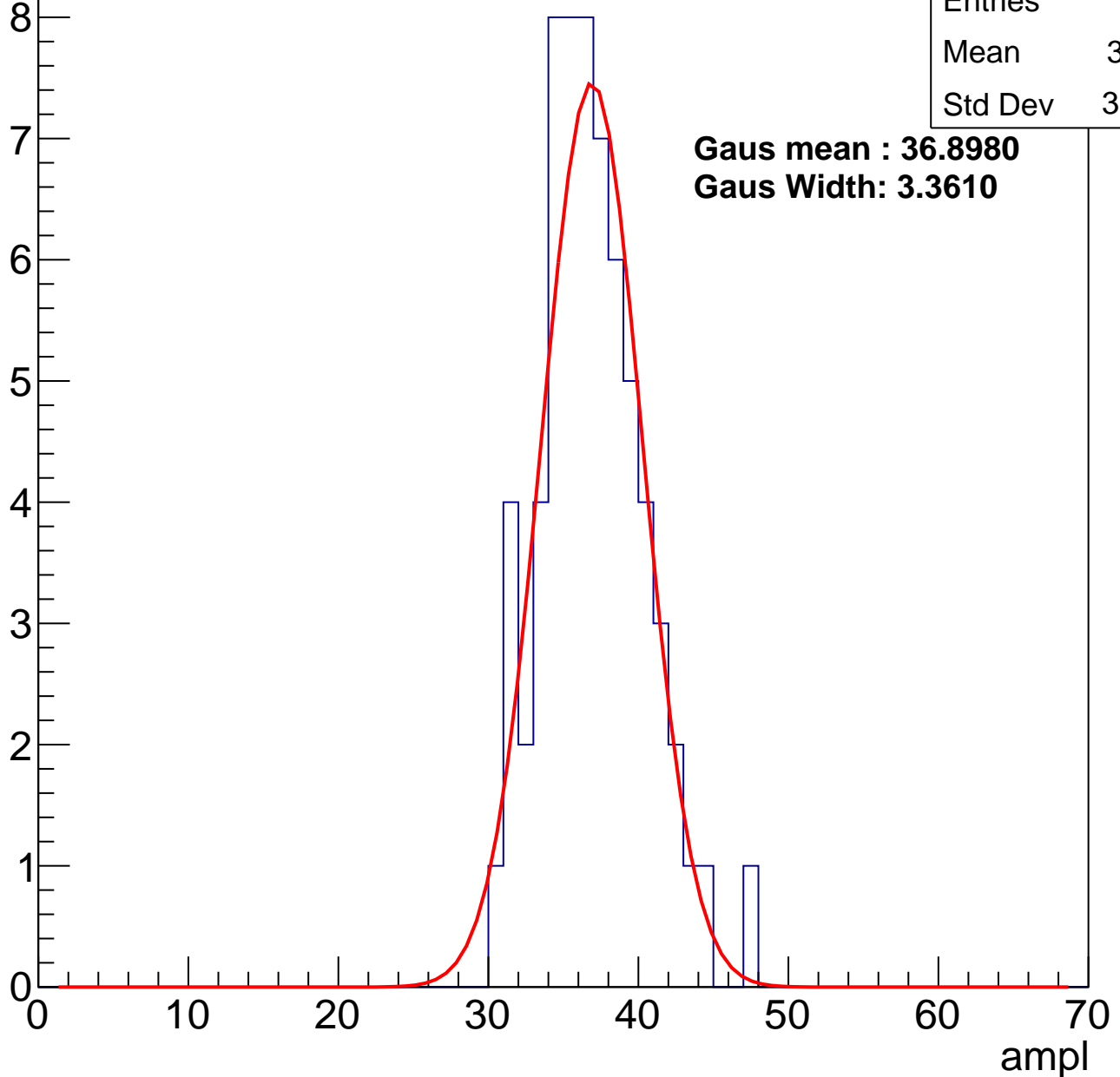
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.51
Std Dev	3.393

**Gaus mean : 36.8980**

**Gaus Width: 3.3610**

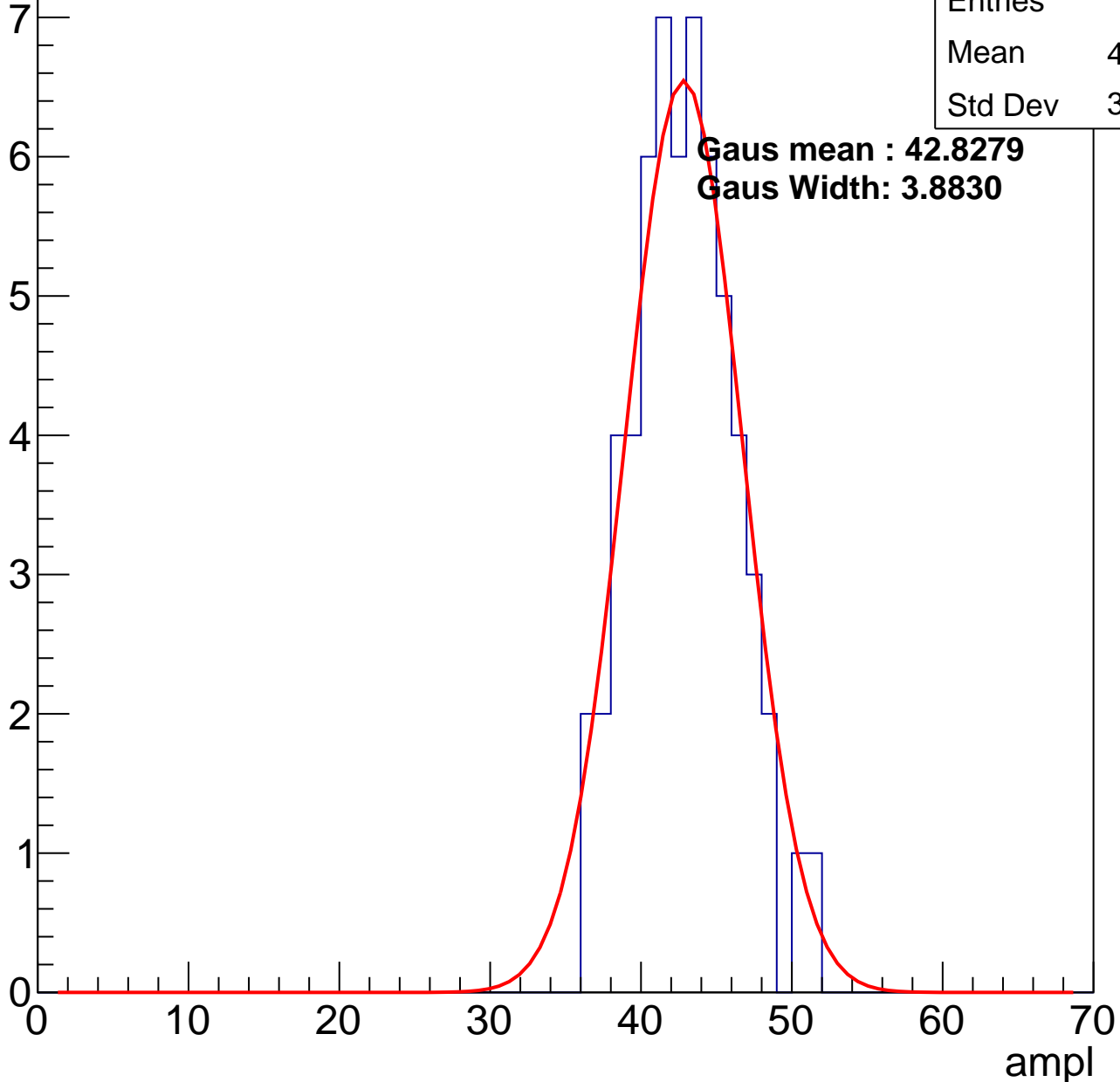


# B0L000S, U18-ch26, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.42
Std Dev	3.353

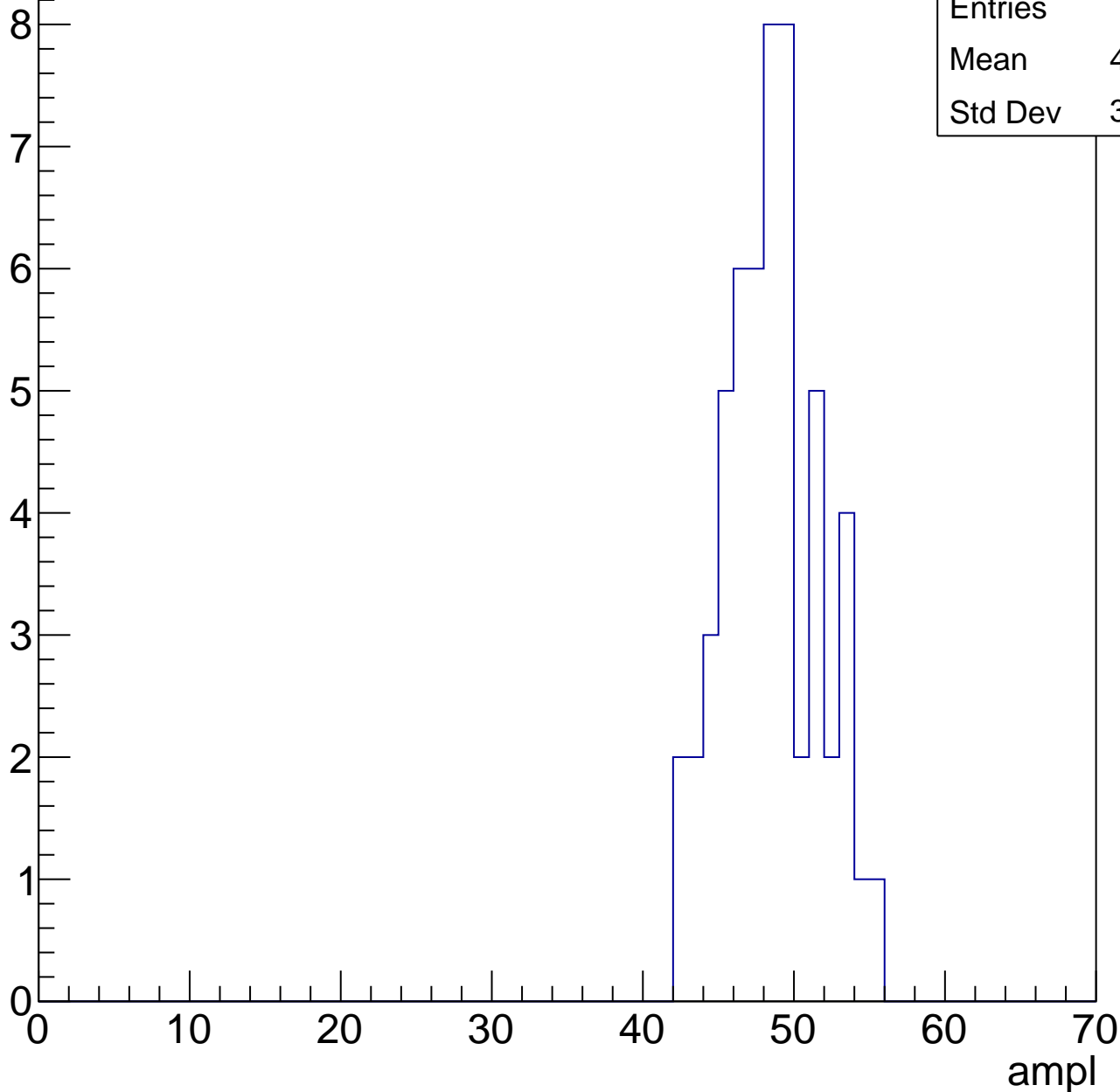


# B0L000S, U18-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

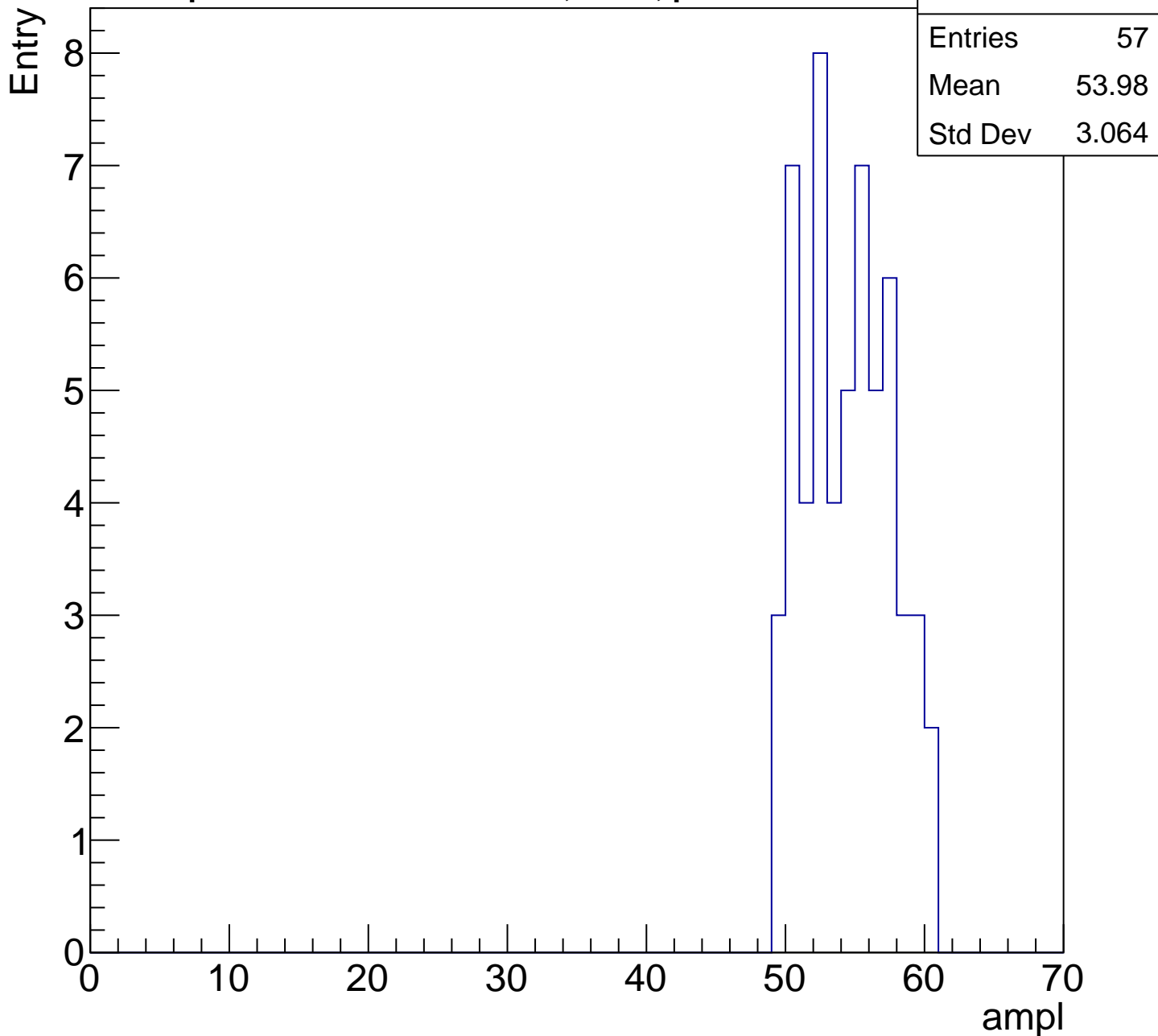
Entry

Entries	55
Mean	48.02
Std Dev	3.084



# B0L000S, U18-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

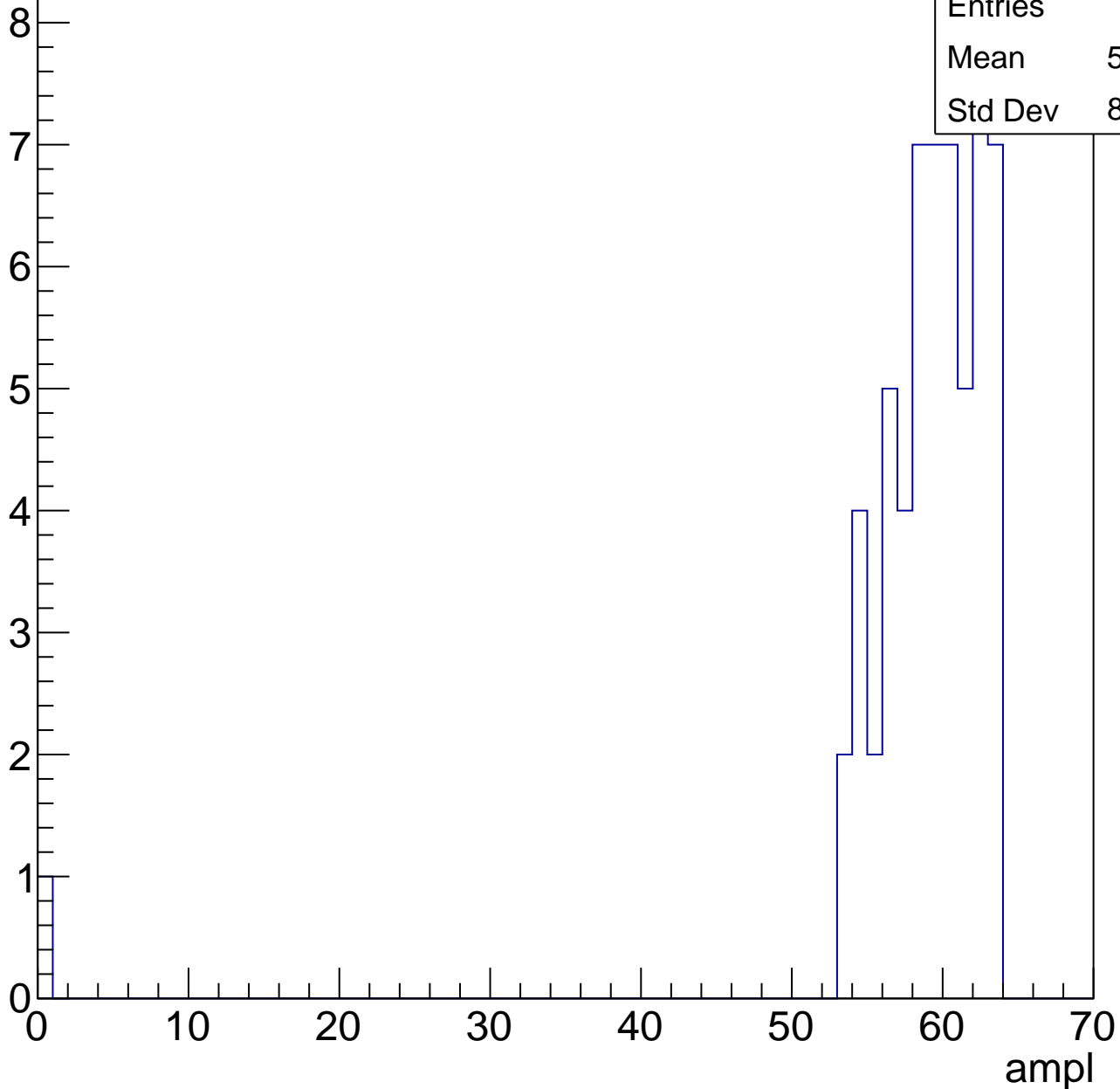


# B0L000S, U18-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	57.98
Std Dev	8.133

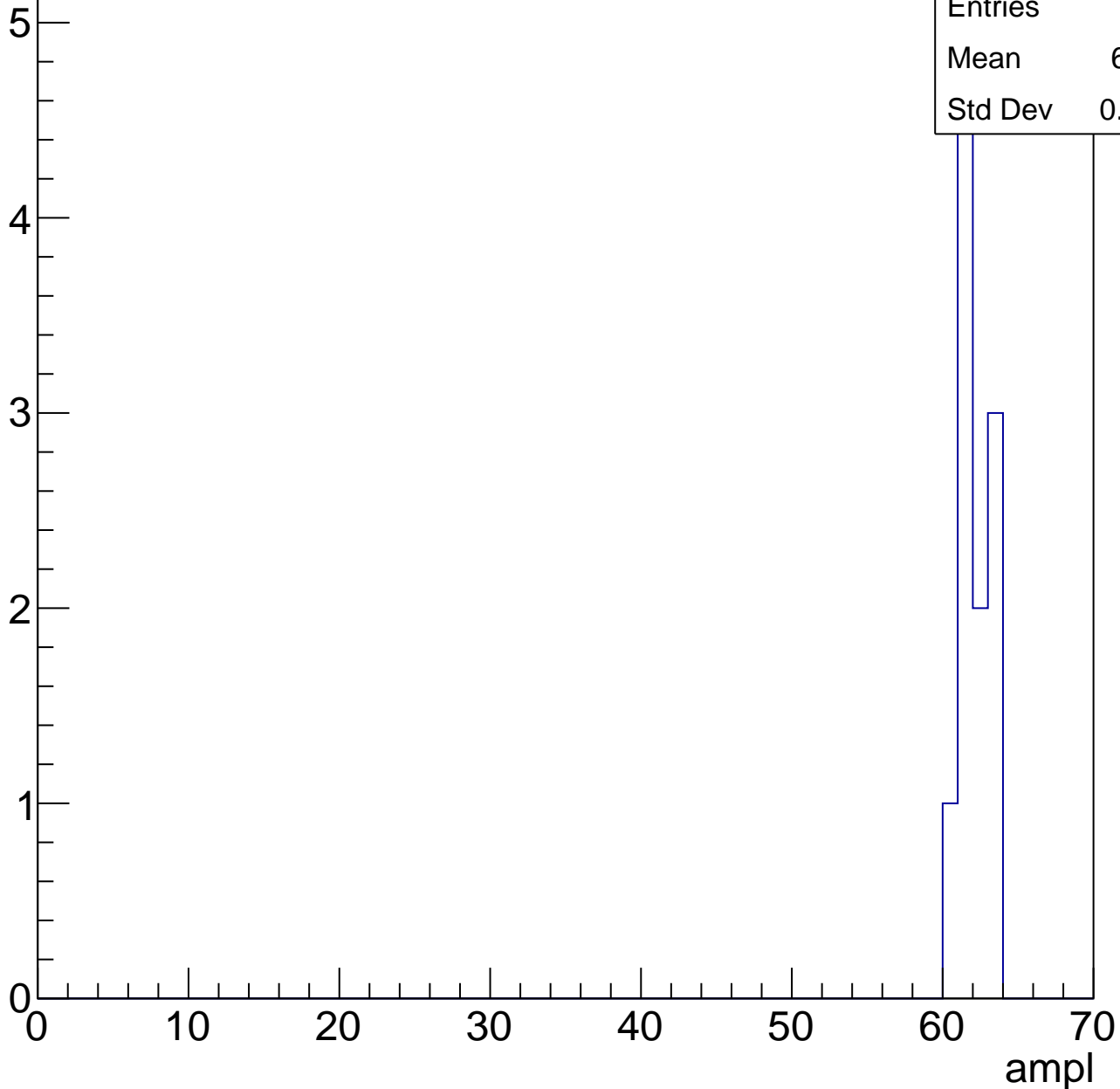


# B0L000S, U18-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	61.64
Std Dev	0.9791





# B0L000S, U18-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch27, adc0

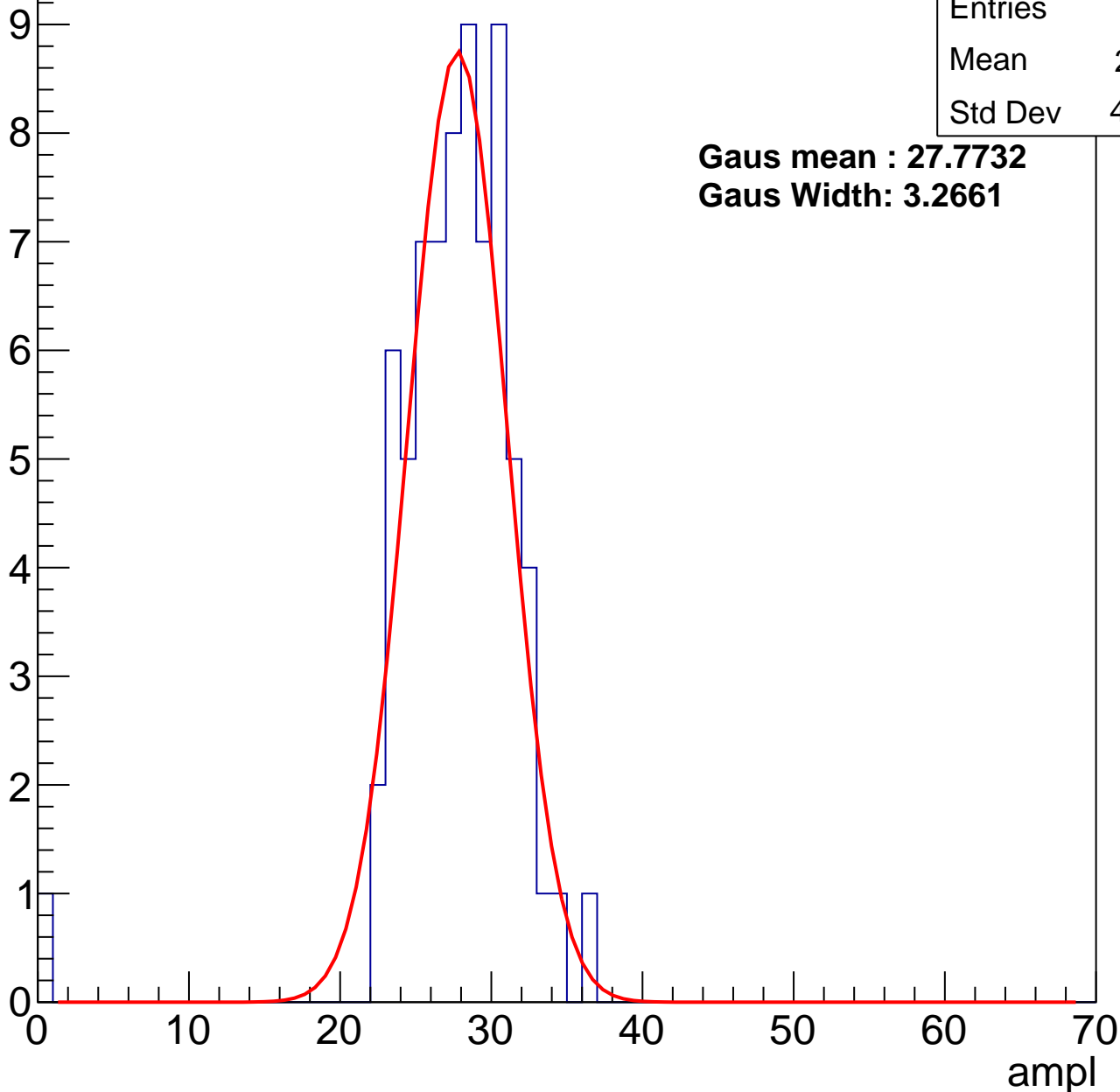
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	27.21
Std Dev	4.404

**Gaus mean : 27.7732**

**Gaus Width: 3.2661**

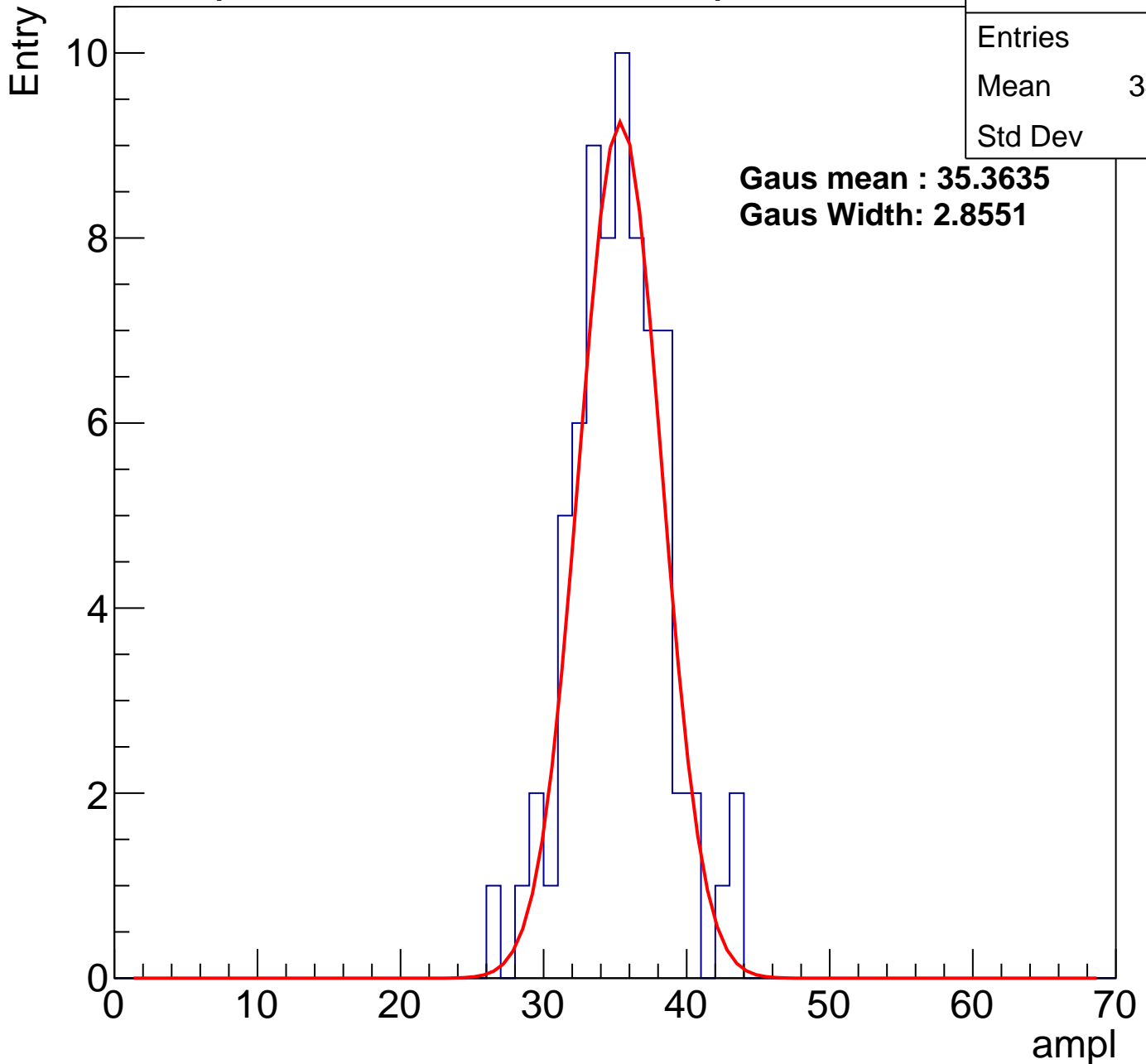


# B0L000S, U18-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	34.82
Std Dev	3.25

**Gaus mean : 35.3635**  
**Gaus Width: 2.8551**



# B0L000S, U18-ch27, adc2

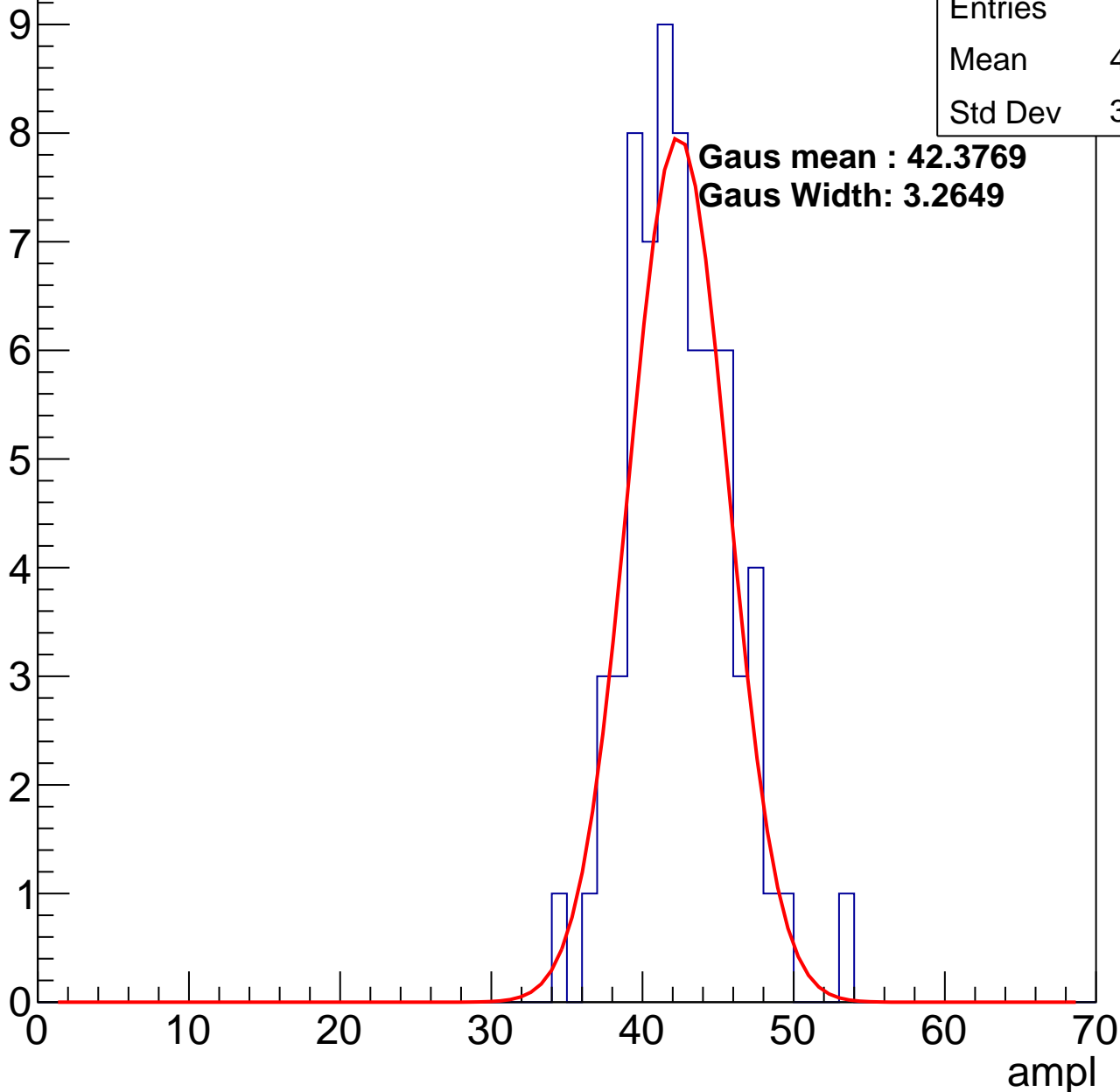
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.06
Std Dev	3.365

**Gaus mean : 42.3769**

**Gaus Width: 3.2649**

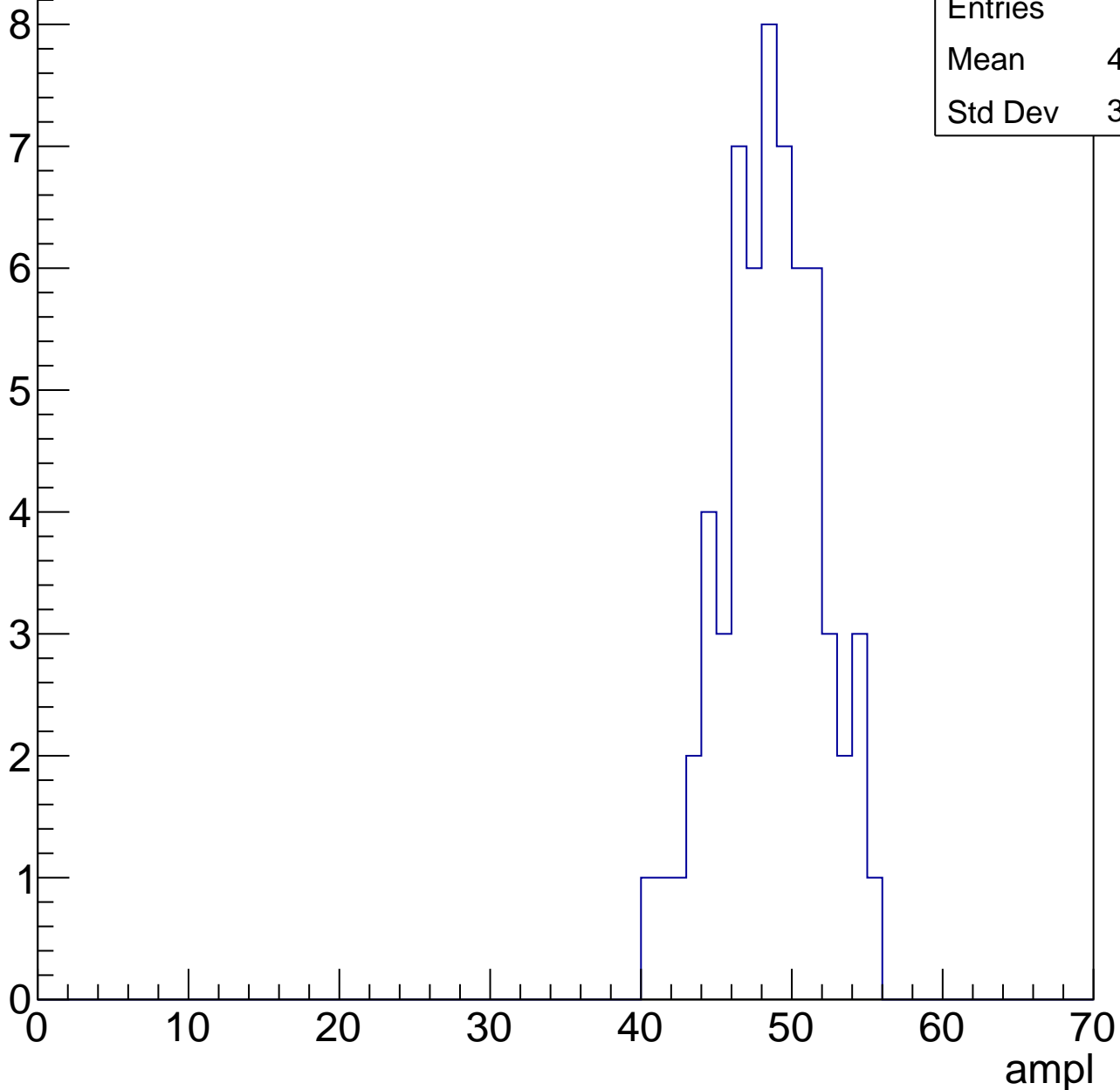


# B0L000S, U18-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.13
Std Dev	3.297

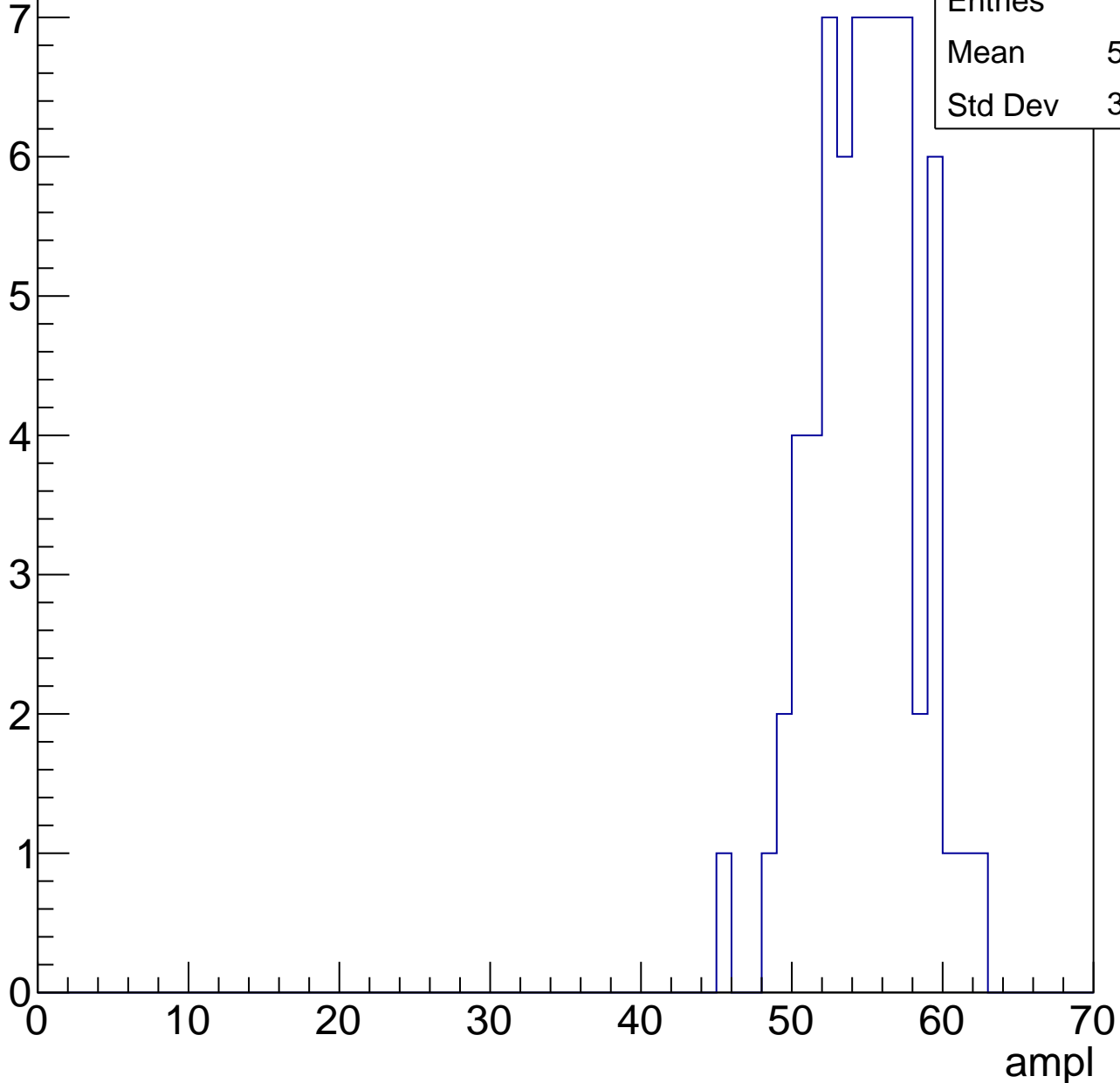


# B0L000S, U18-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.44
Std Dev	3.349

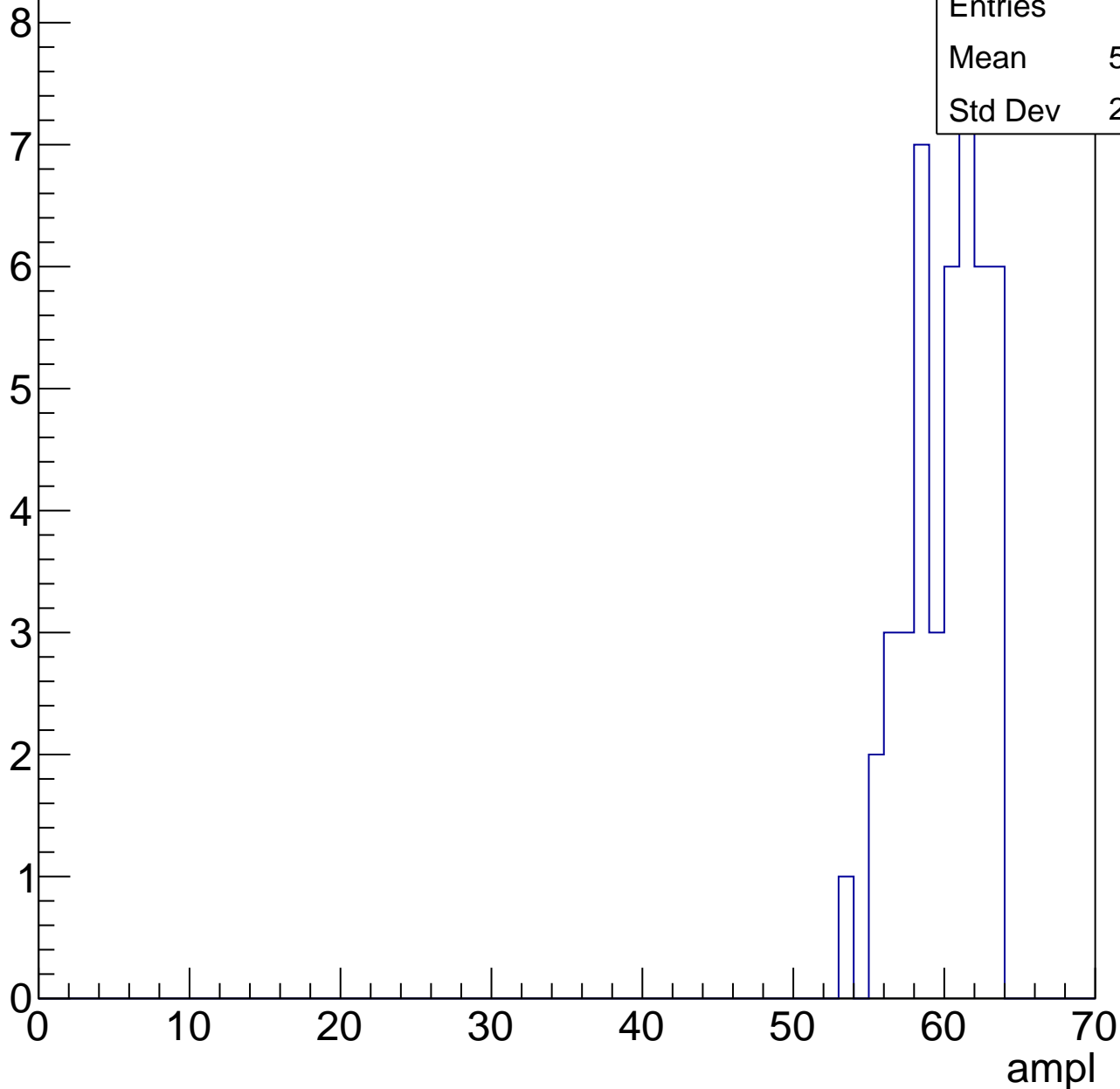


# B0L000S, U18-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

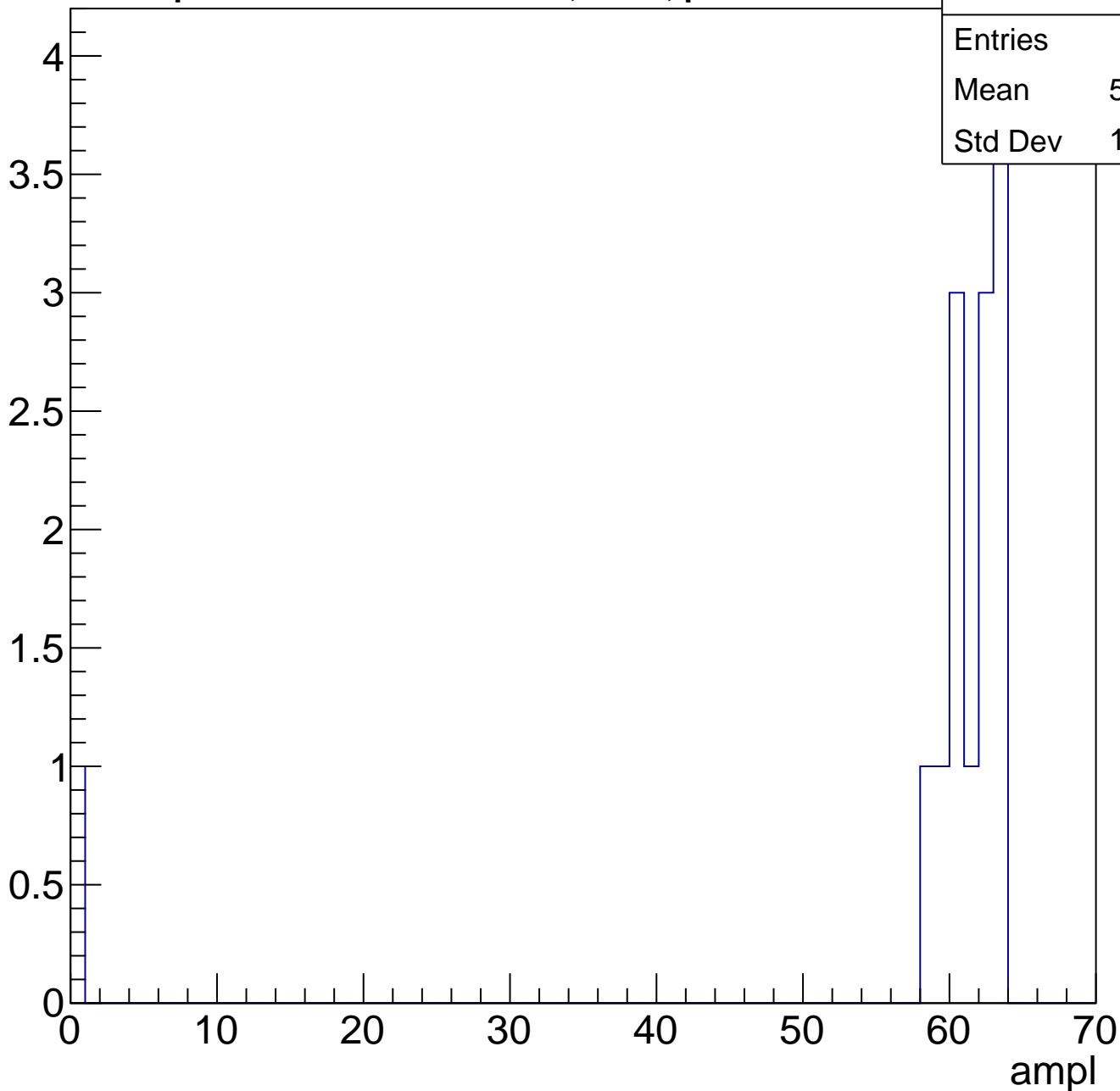
Entries	45
Mean	59.62
Std Dev	2.515



# B0L000S, U18-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	21
Std Dev	0

ampl

# B0L000S, U18-ch28, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	29.53
Std Dev	3.442

**Gaus mean : 29.7308**

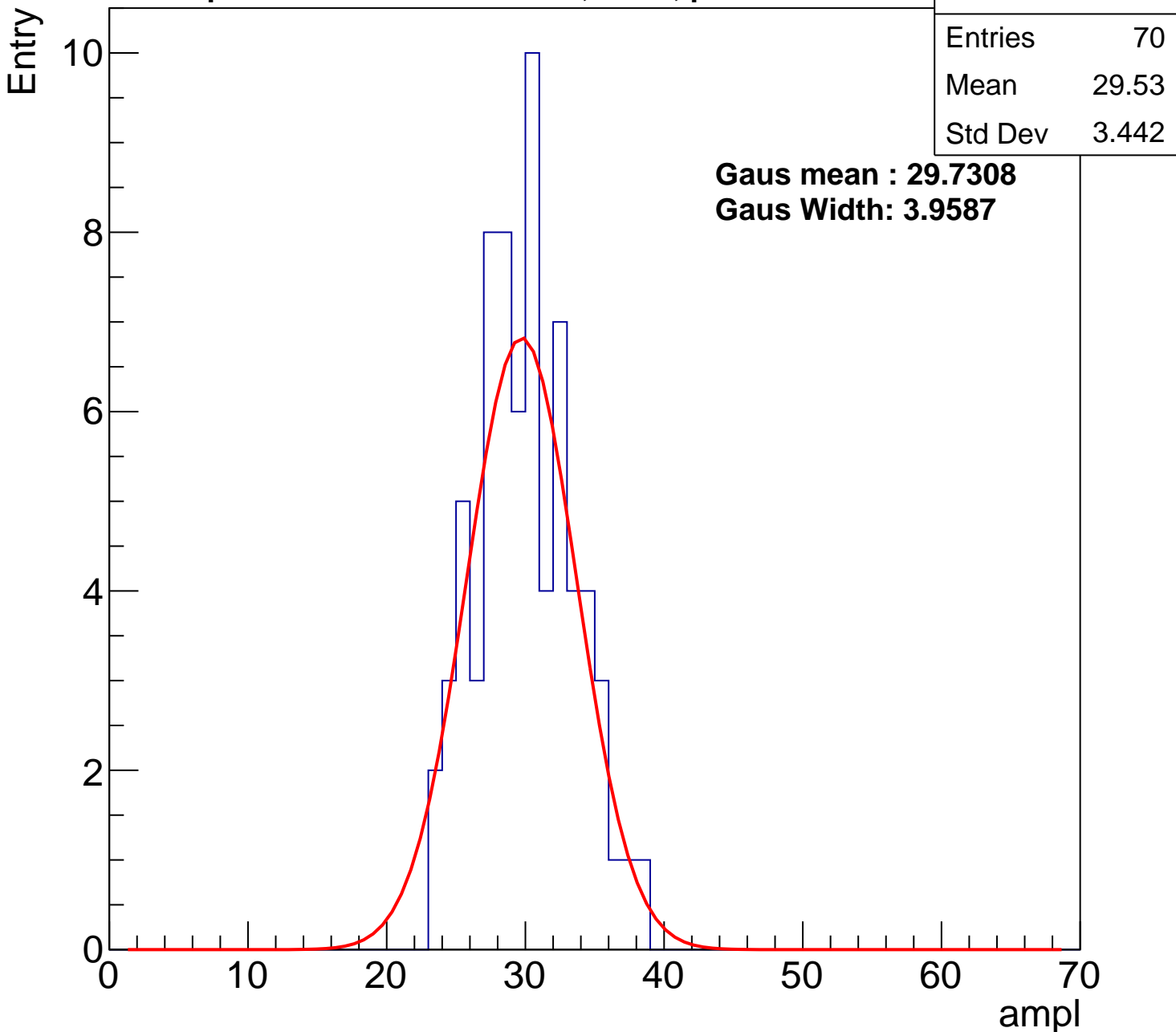
**Gaus Width: 3.9587**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



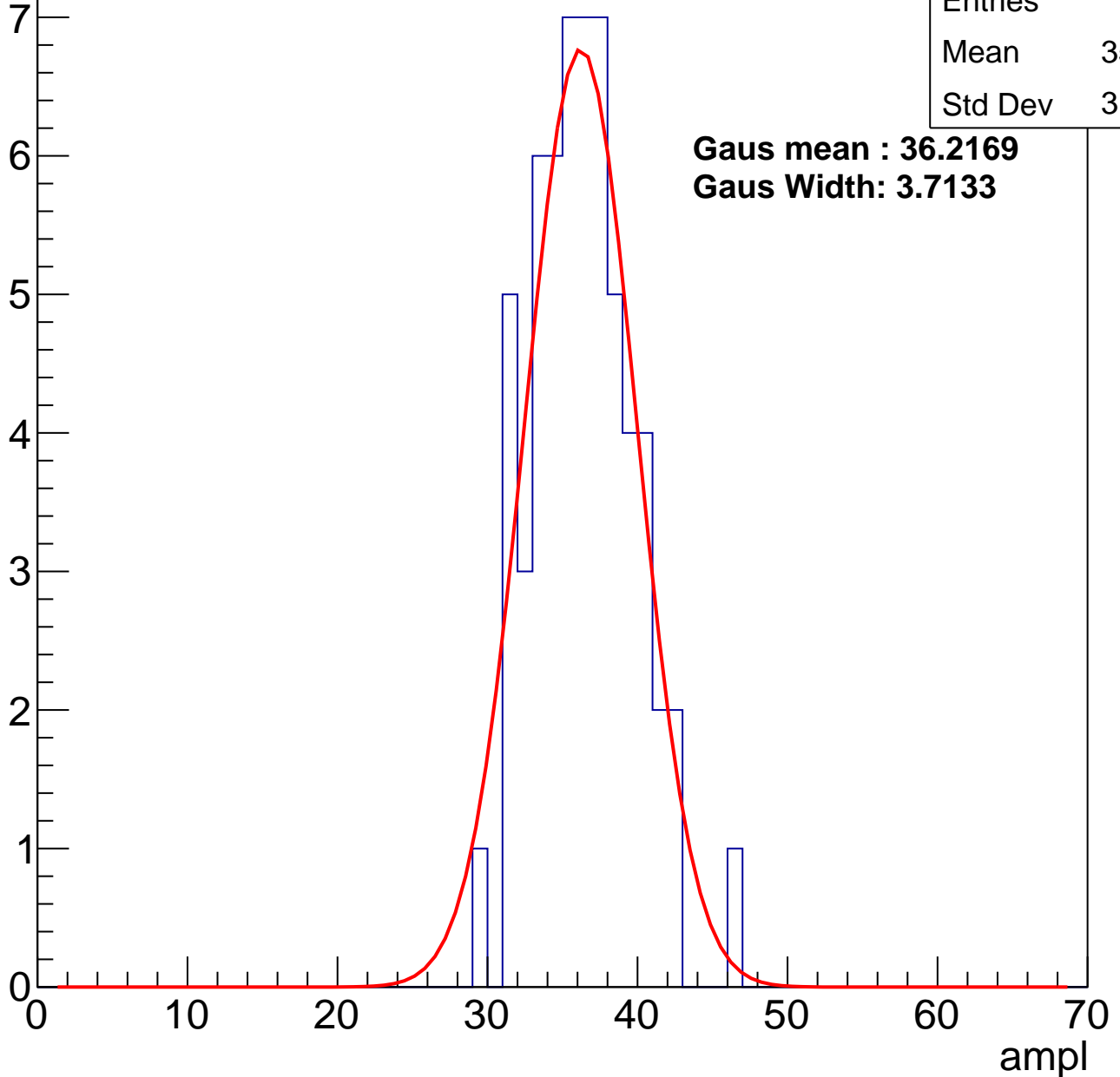
# B0L000S, U18-ch28, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	35.93
Std Dev	3.306

**Gaus mean : 36.2169**  
**Gaus Width: 3.7133**



# B0L000S, U18-ch28, adc2

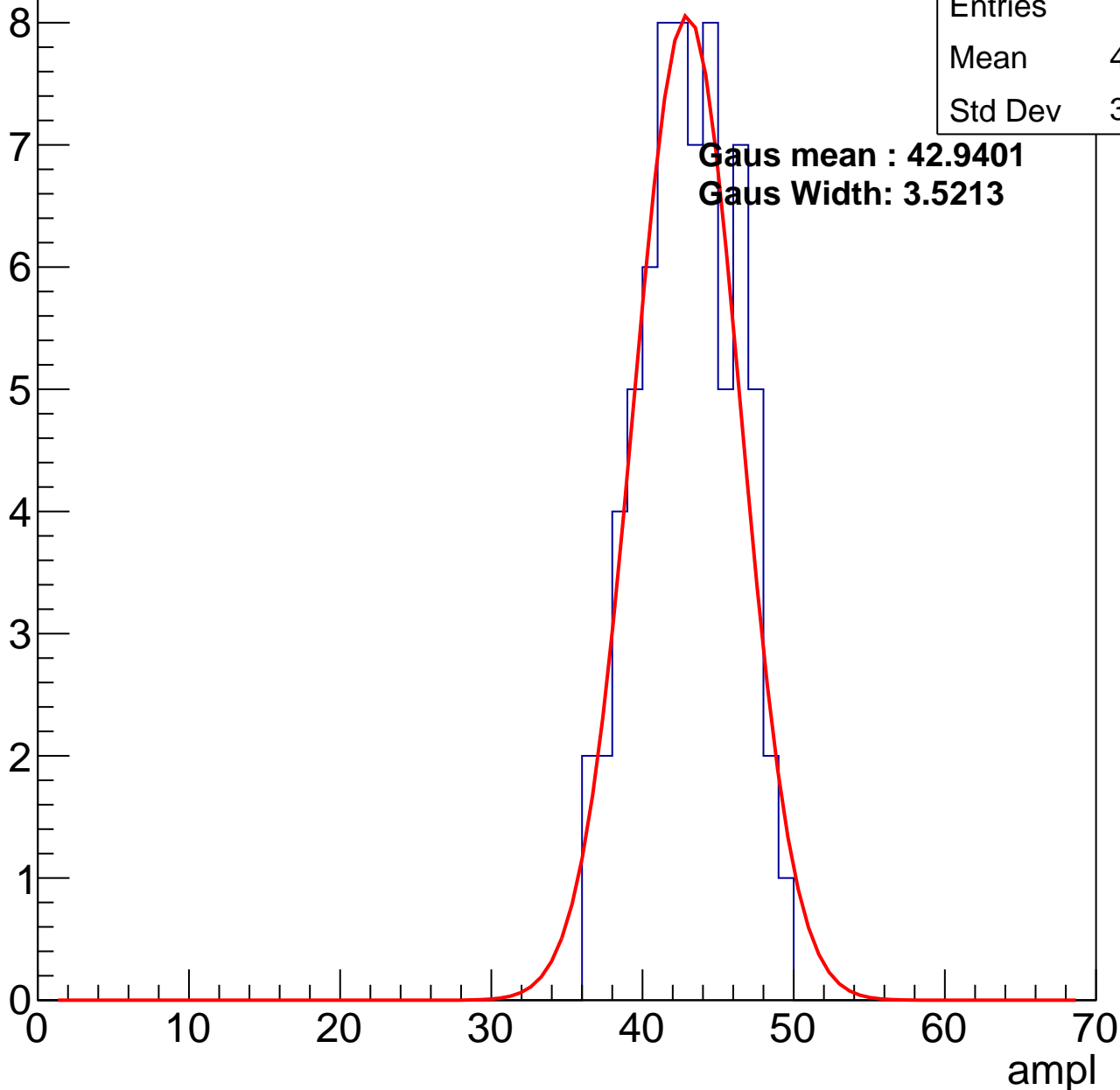
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.53
Std Dev	3.129

**Gaus mean : 42.9401**

**Gaus Width: 3.5213**

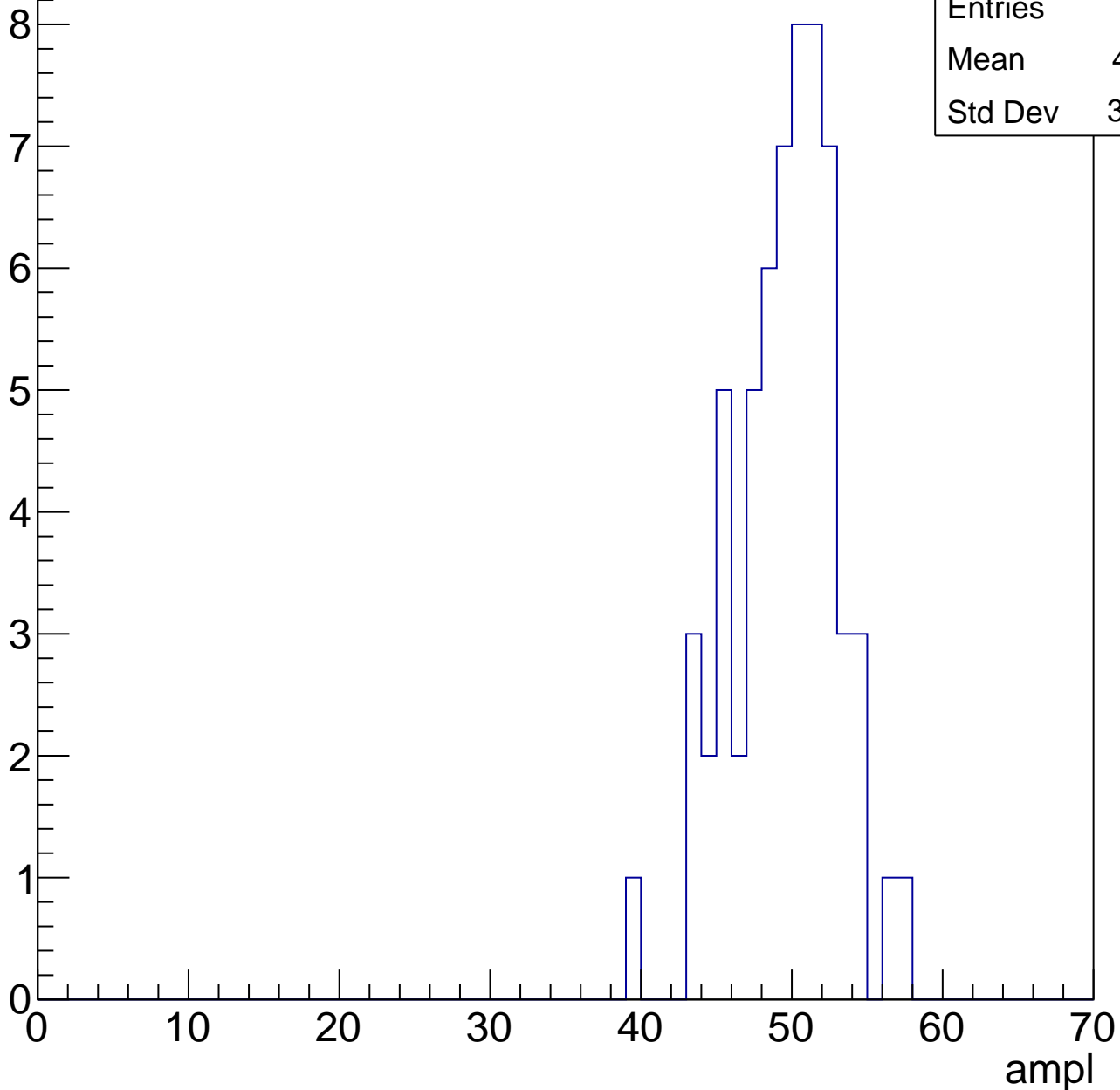


# B0L000S, U18-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

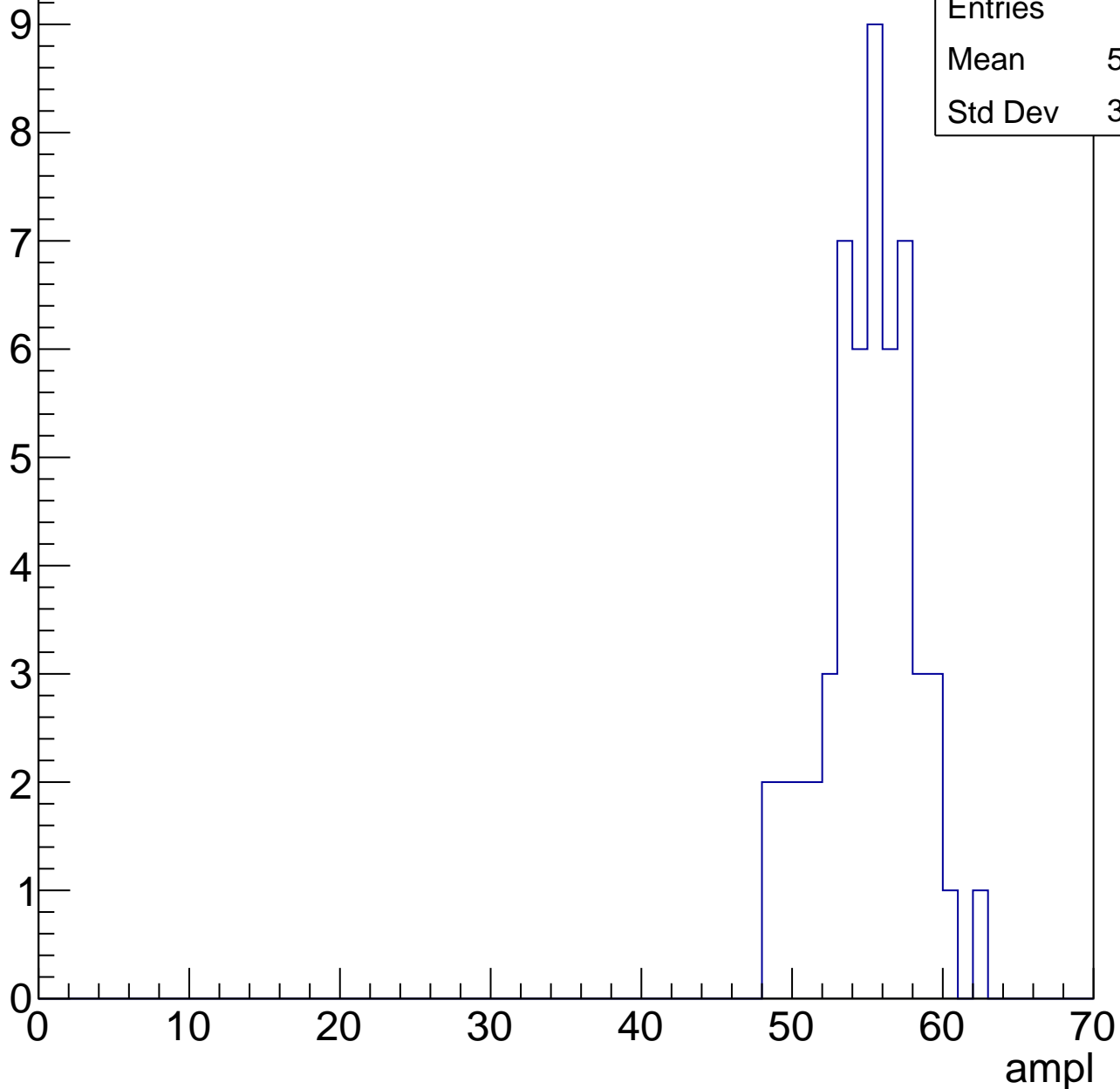
Entries	62
Mean	49.11
Std Dev	3.408



# B0L000S, U18-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

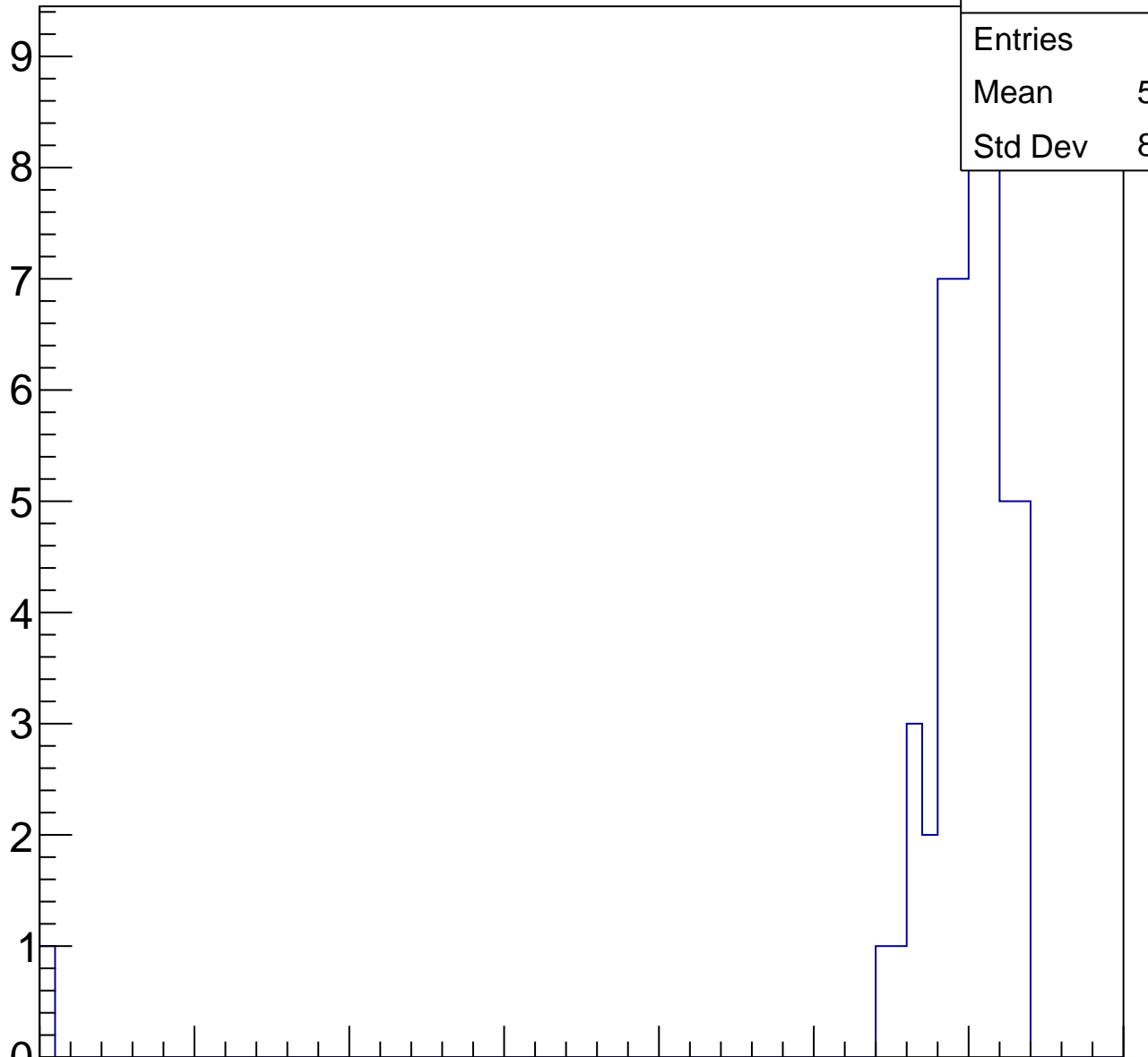
Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	58.48
Std Dev	8.626

ampl

0 10 20 30 40 50 60 70

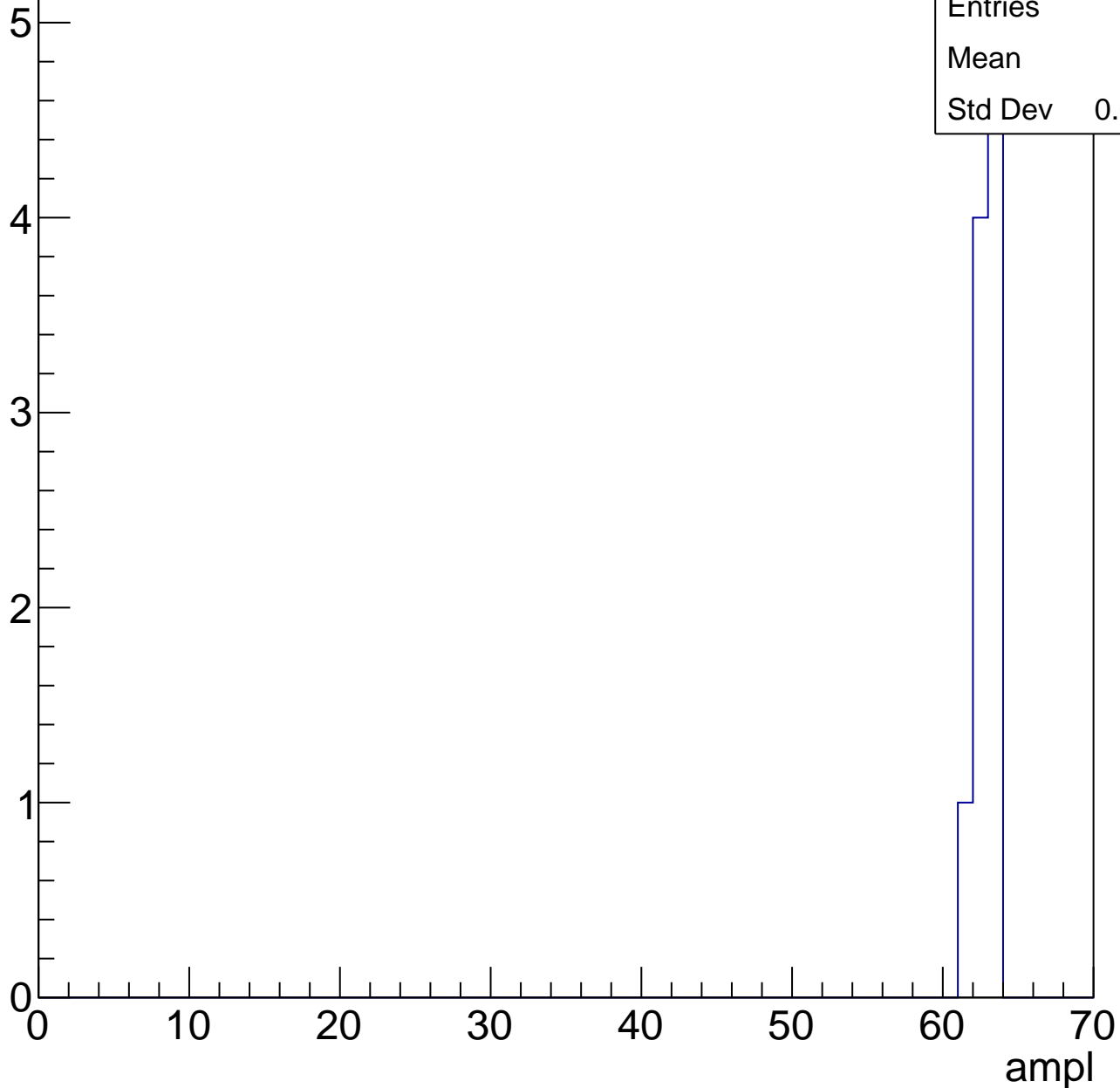


# B0L000S, U18-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	62.4
Std Dev	0.6633





# B0L000S, U18-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch29, adc0

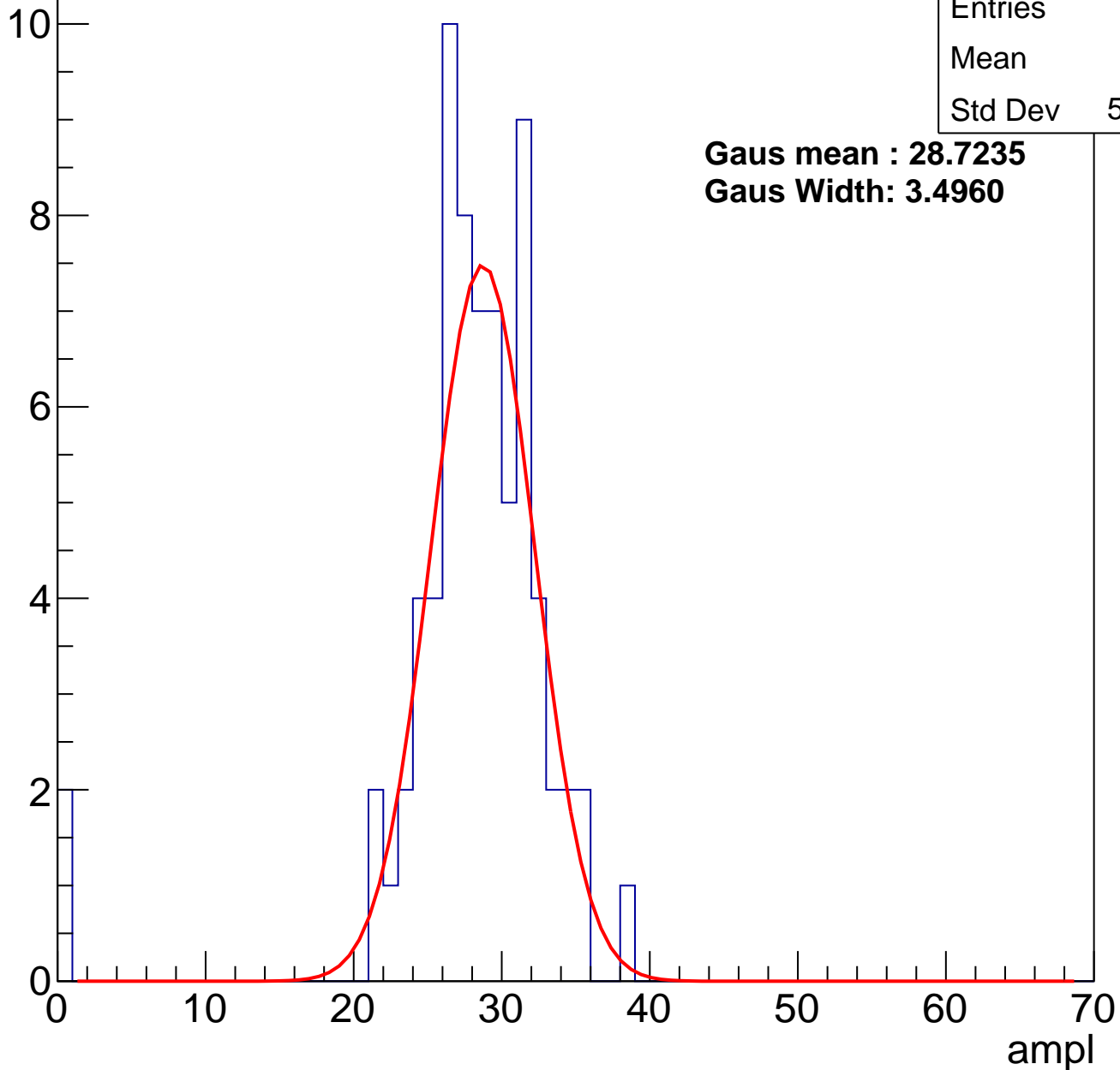
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	27.5
Std Dev	5.742

**Gaus mean : 28.7235**

**Gaus Width: 3.4960**

Entry



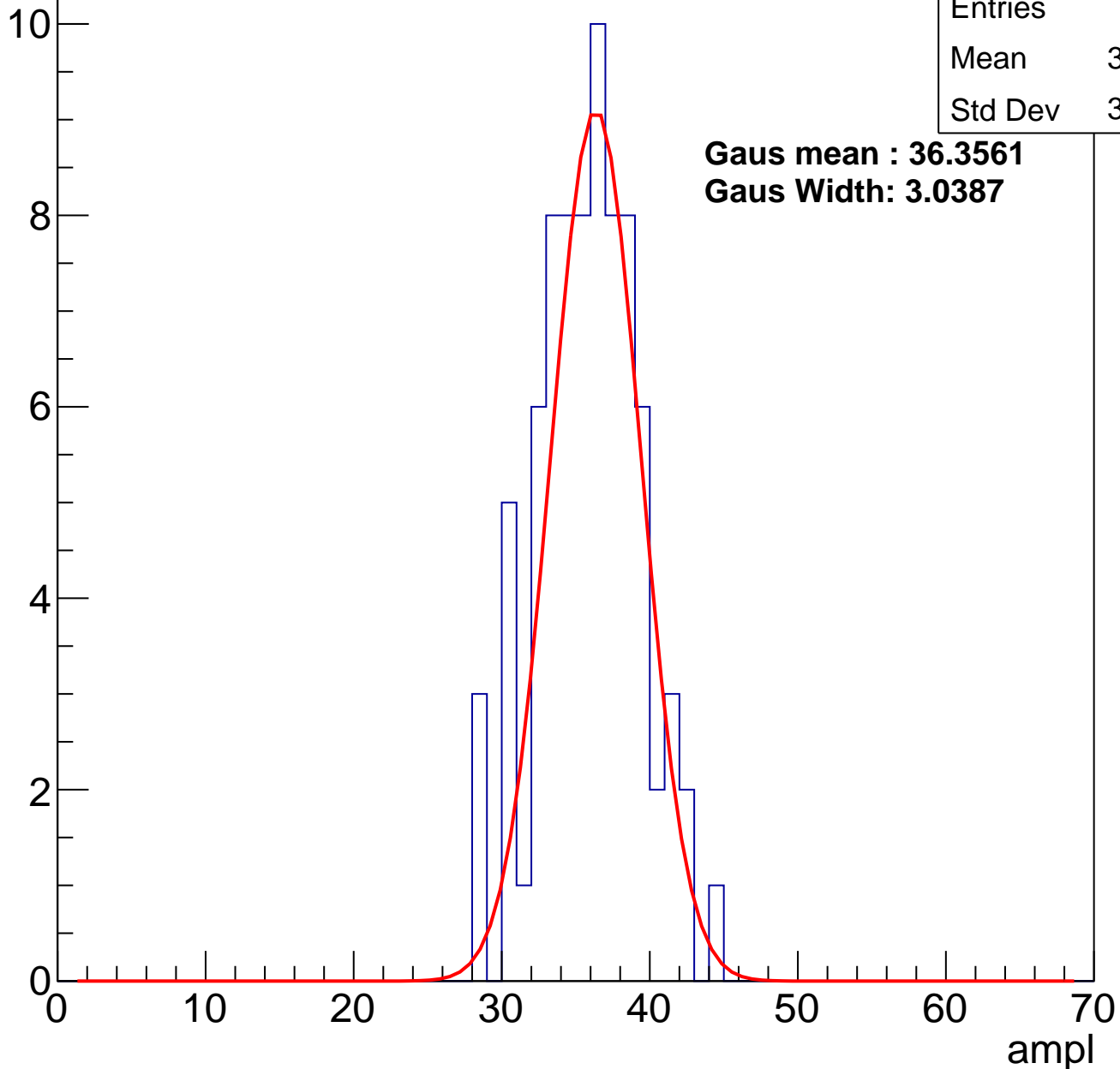
# B0L000S, U18-ch29, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	35.42
Std Dev	3.404

**Gaus mean : 36.3561**  
**Gaus Width: 3.0387**

Entry



# B0L000S, U18-ch29, adc2

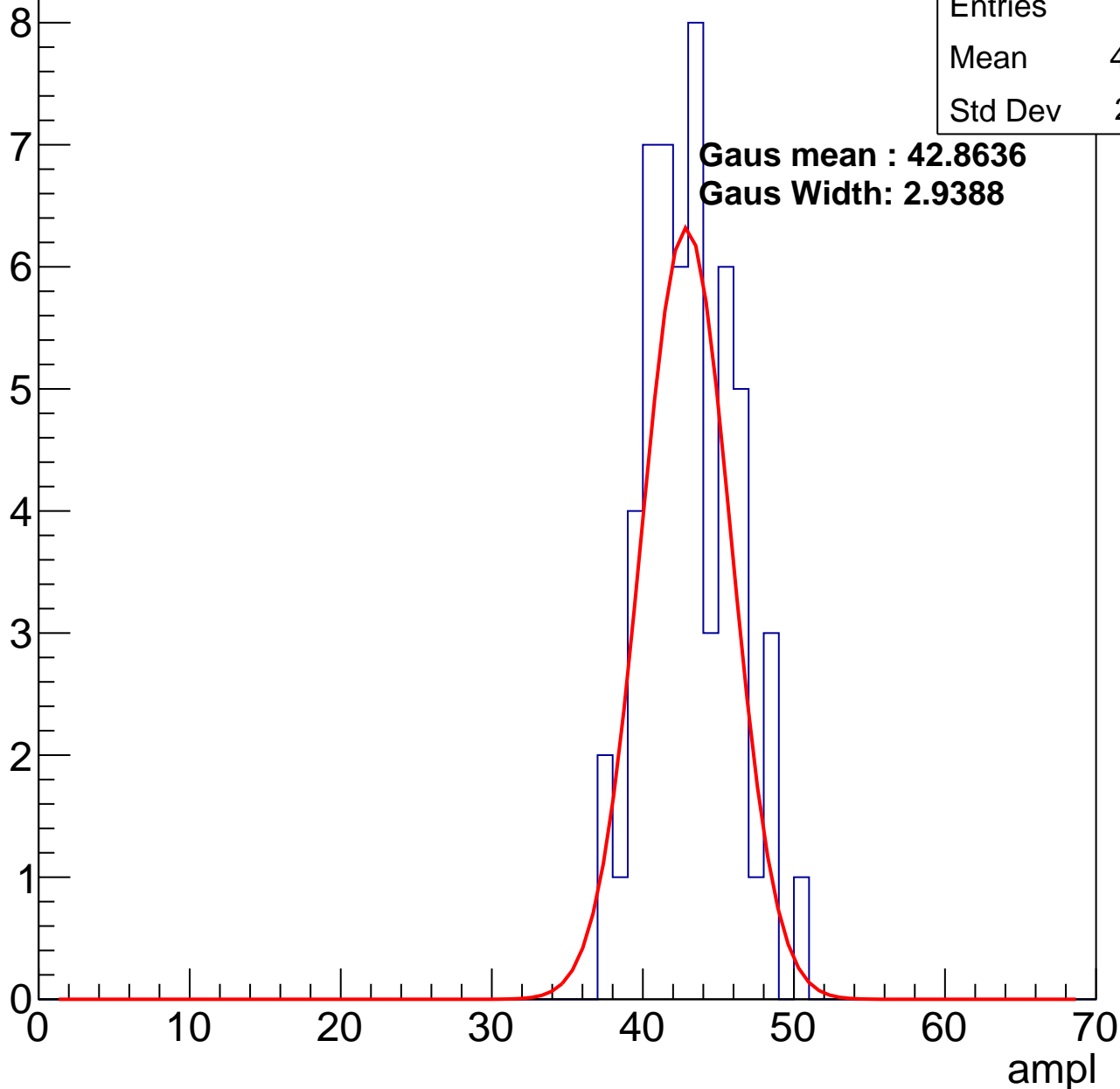
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	42.67
Std Dev	2.931

**Gaus mean : 42.8636**

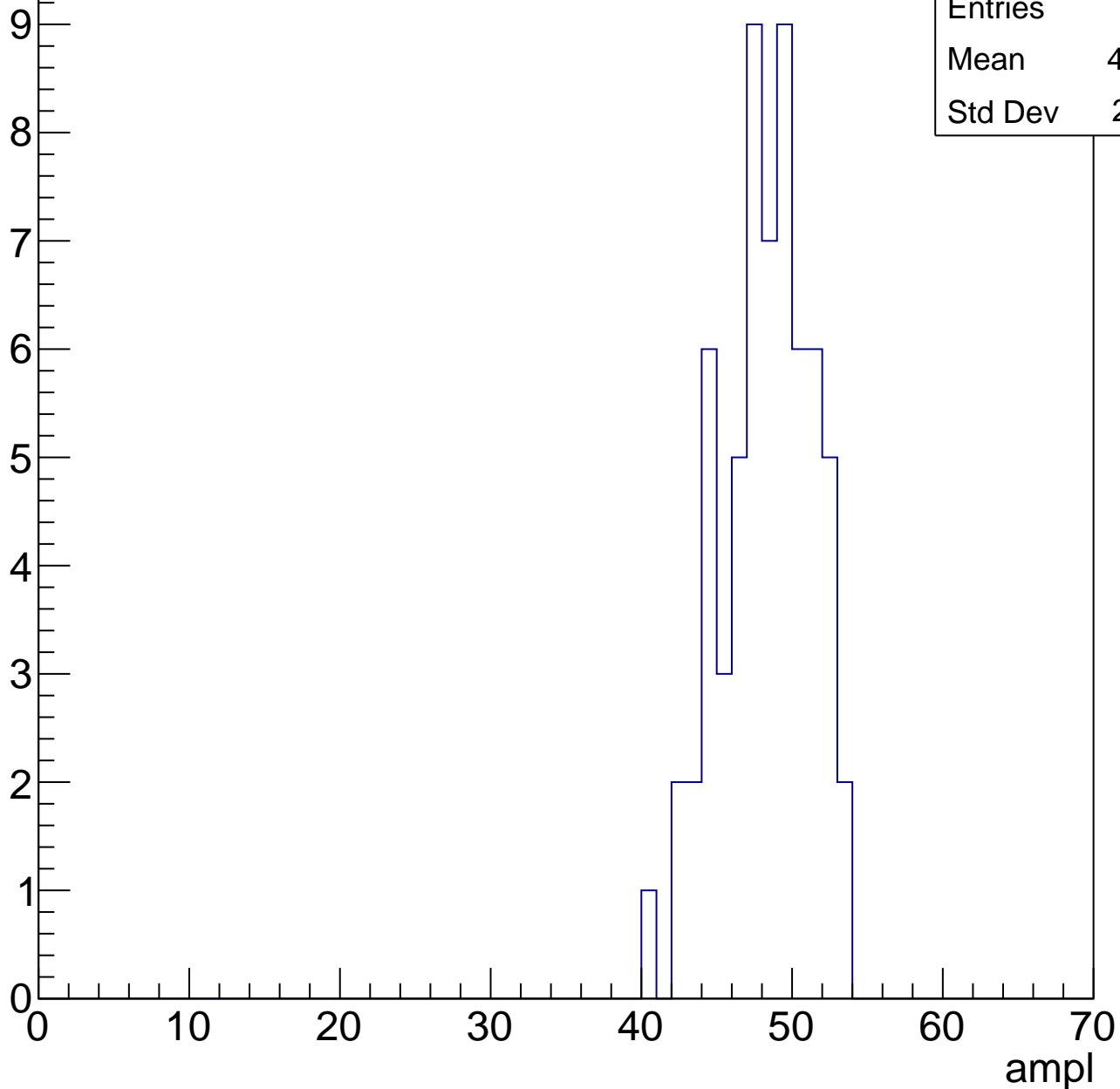
**Gaus Width: 2.9388**



# B0L000S, U18-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



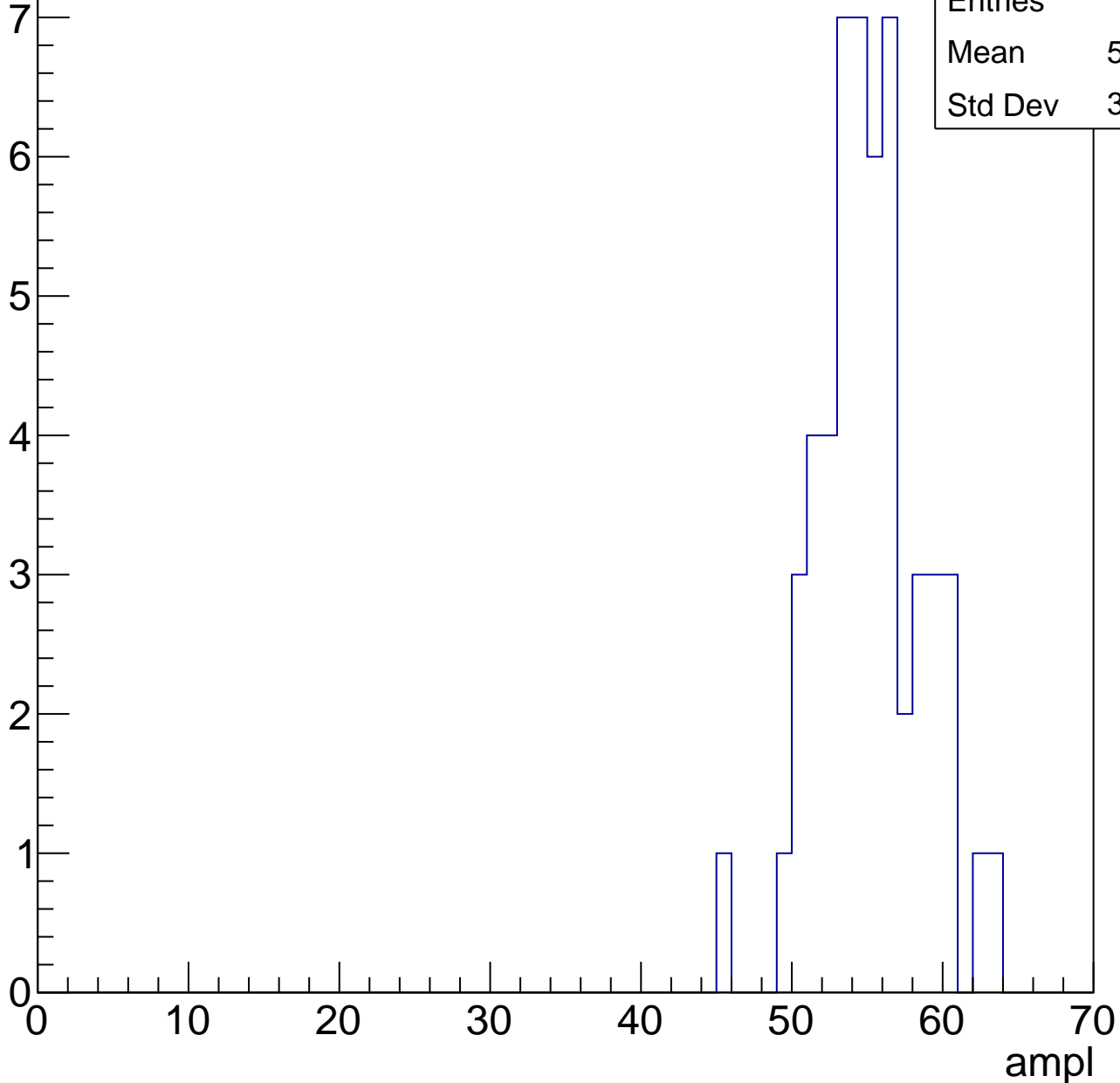
Entries	63
Mean	47.79
Std Dev	2.961

# B0L000S, U18-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	54.66
Std Dev	3.414

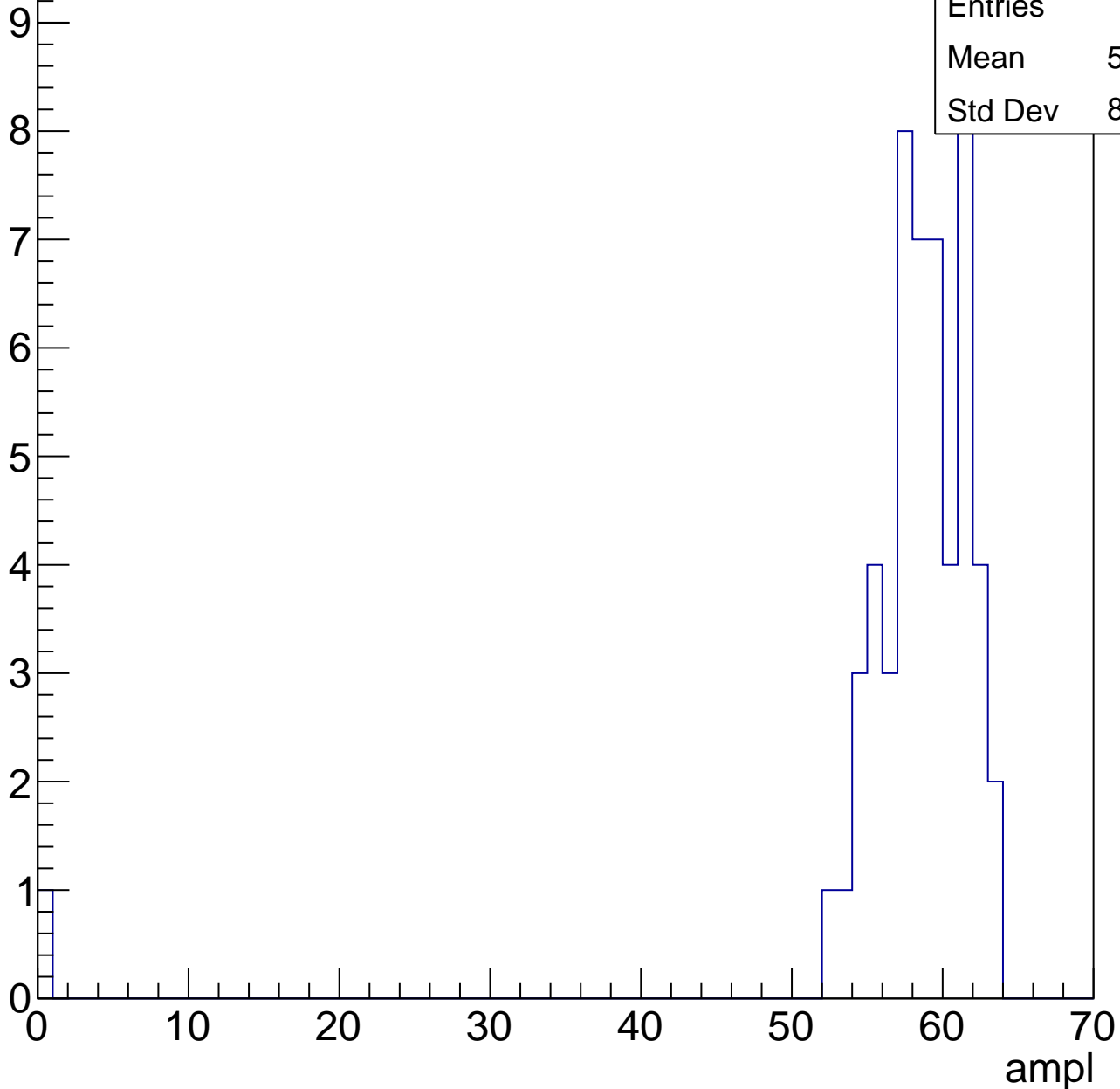


# B0L000S, U18-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

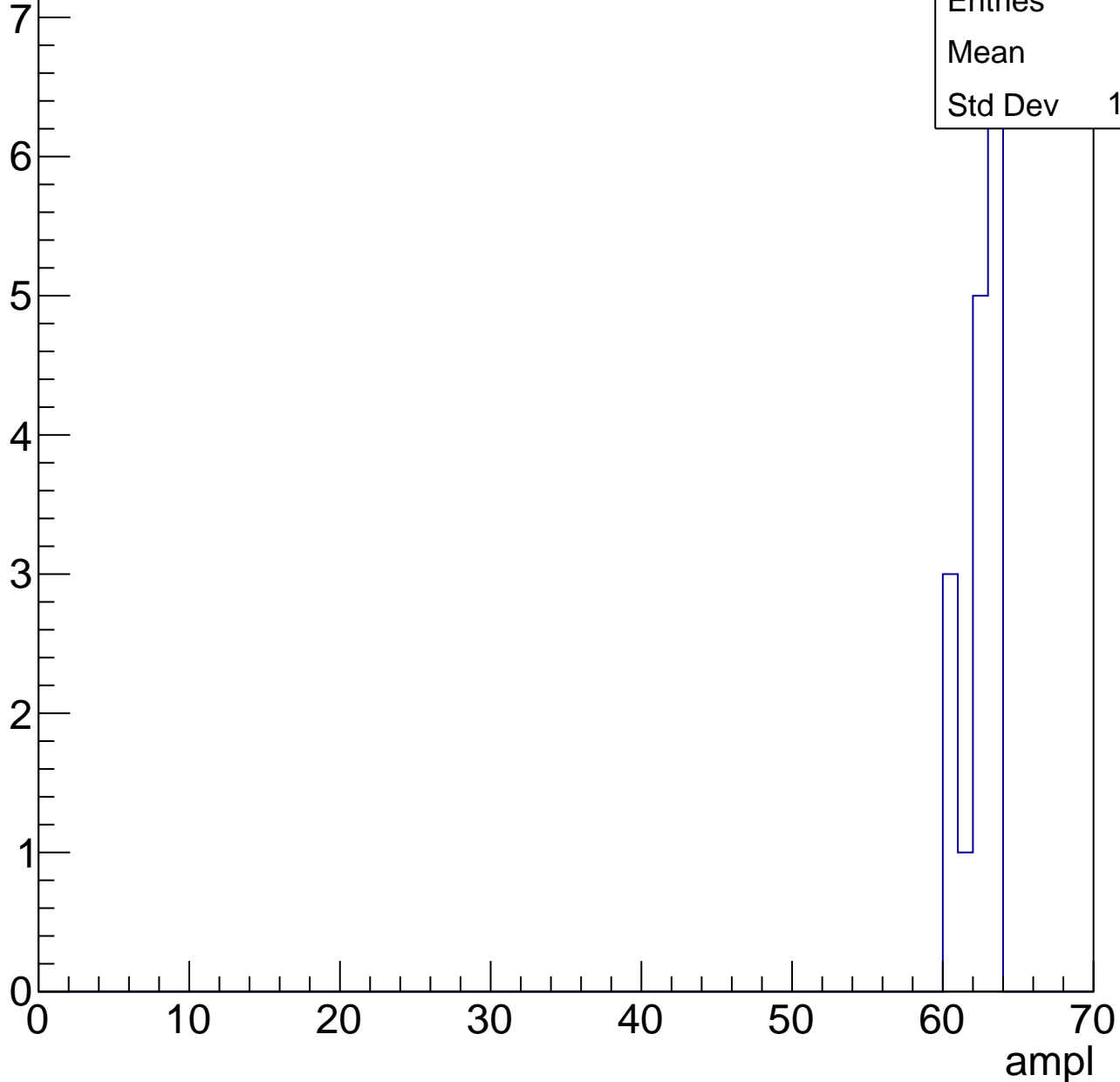
Entries	54
Mean	57.28
Std Dev	8.299



# B0L000S, U18-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

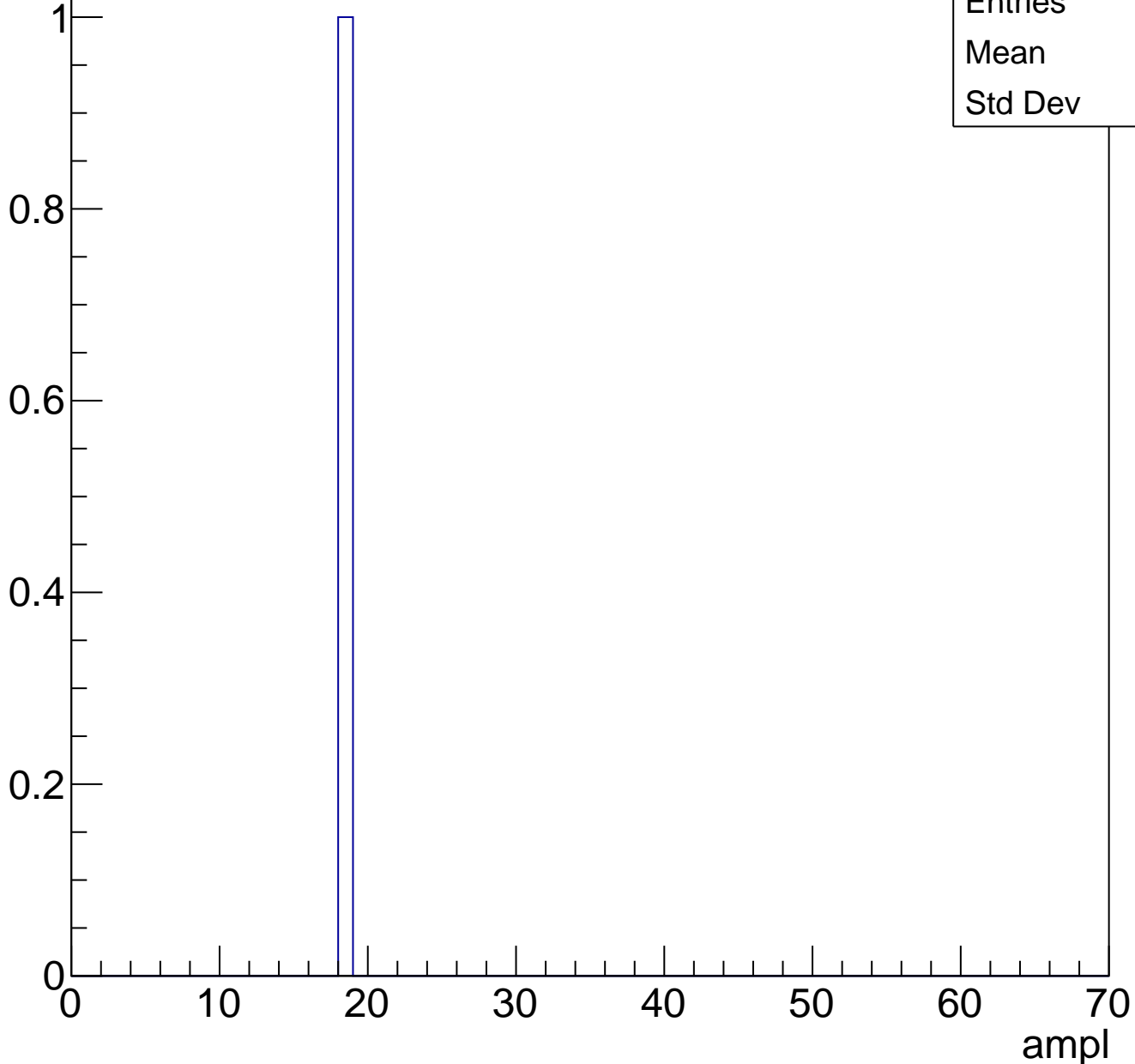




# B0L000S, U18-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	18
Std Dev	0

# B0L000S, U18-ch30, adc0

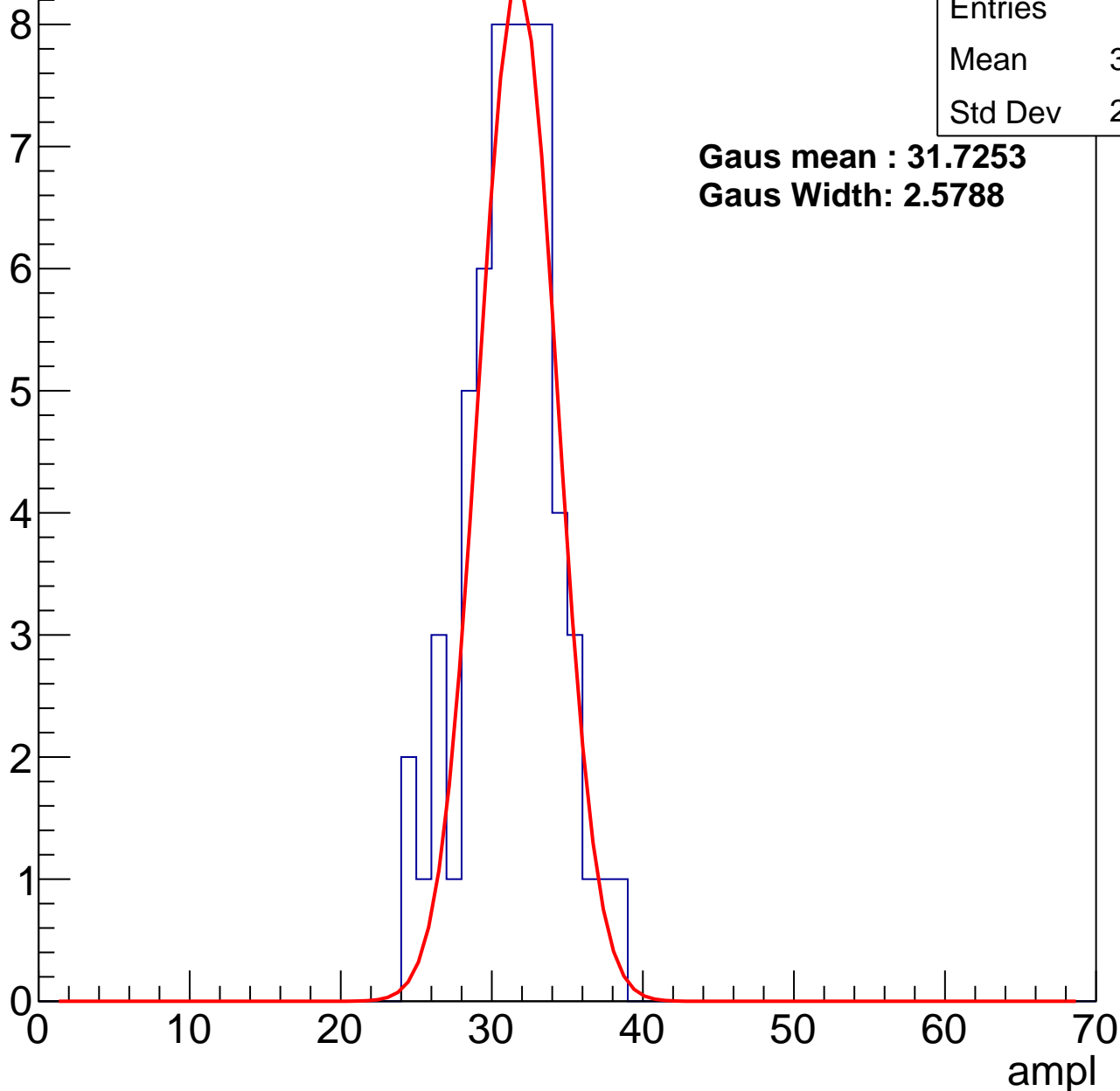
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.87
Std Dev	2.986

**Gaus mean : 31.7253**

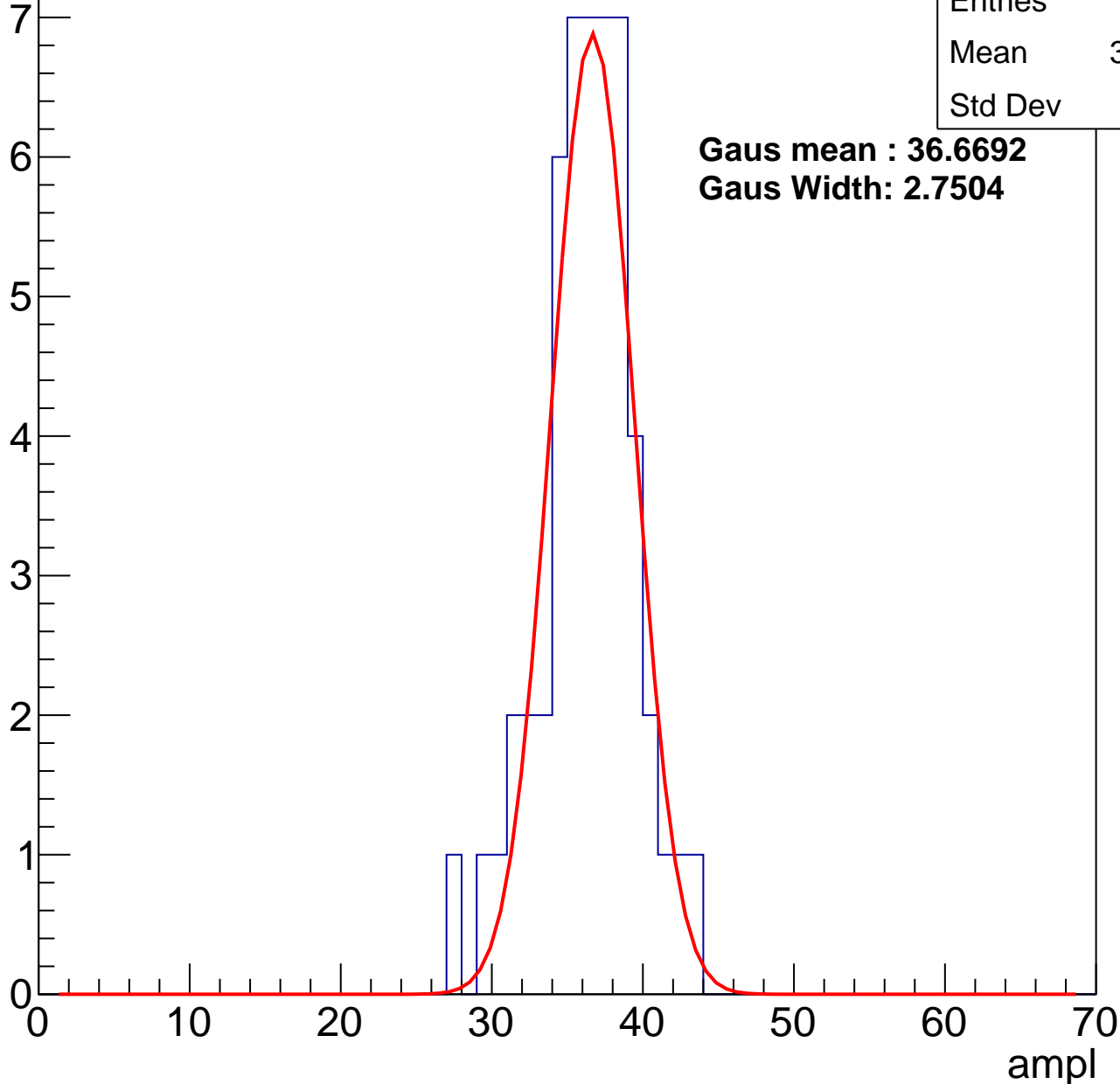
**Gaus Width: 2.5788**



# B0L000S, U18-ch30, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch30, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	42.98
Std Dev	3.174

**Gaus mean : 43.4953**

**Gaus Width: 3.2538**

Entry

10

8

6

4

2

0

0

10

20

30

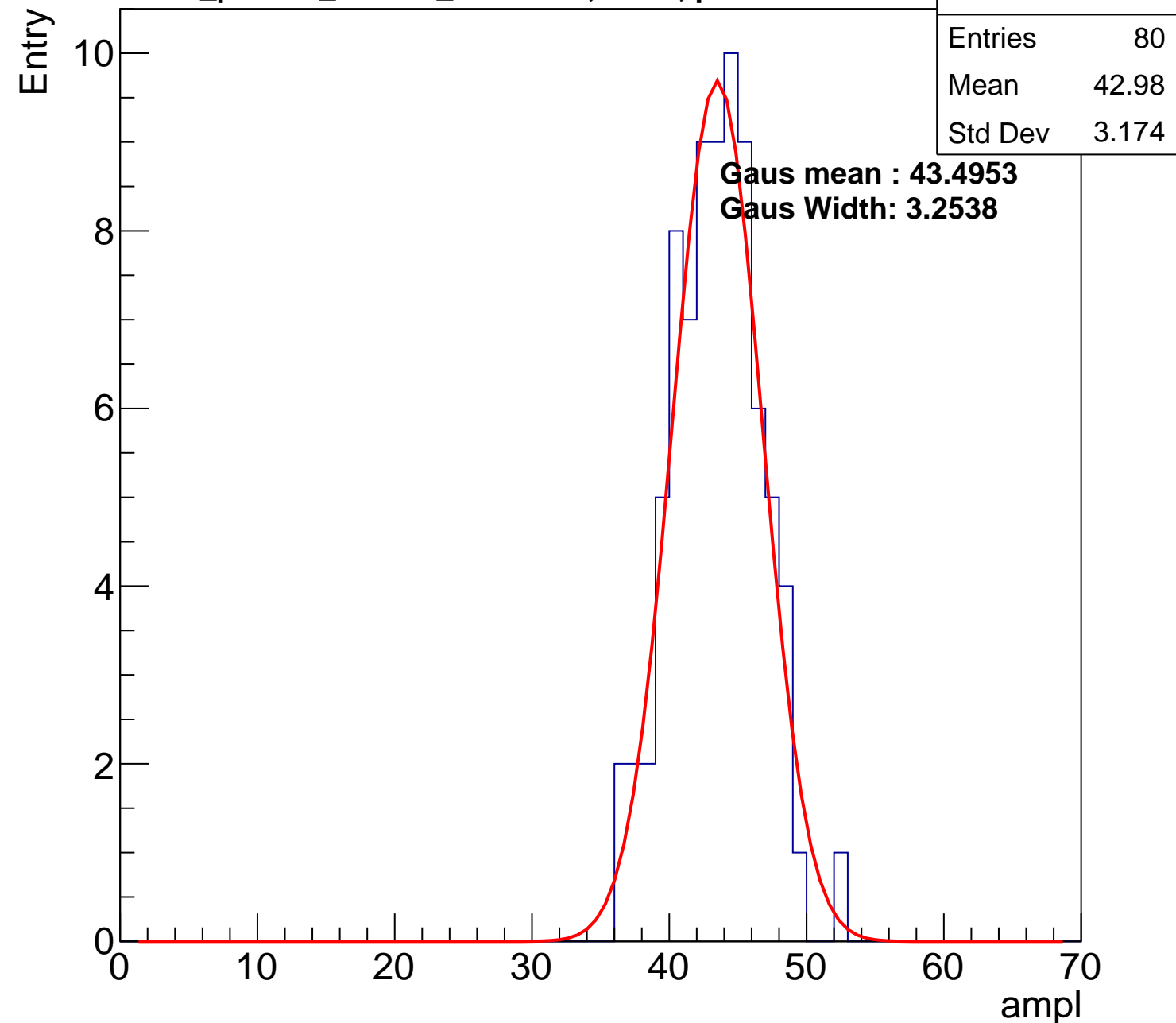
40

50

60

70

ampl

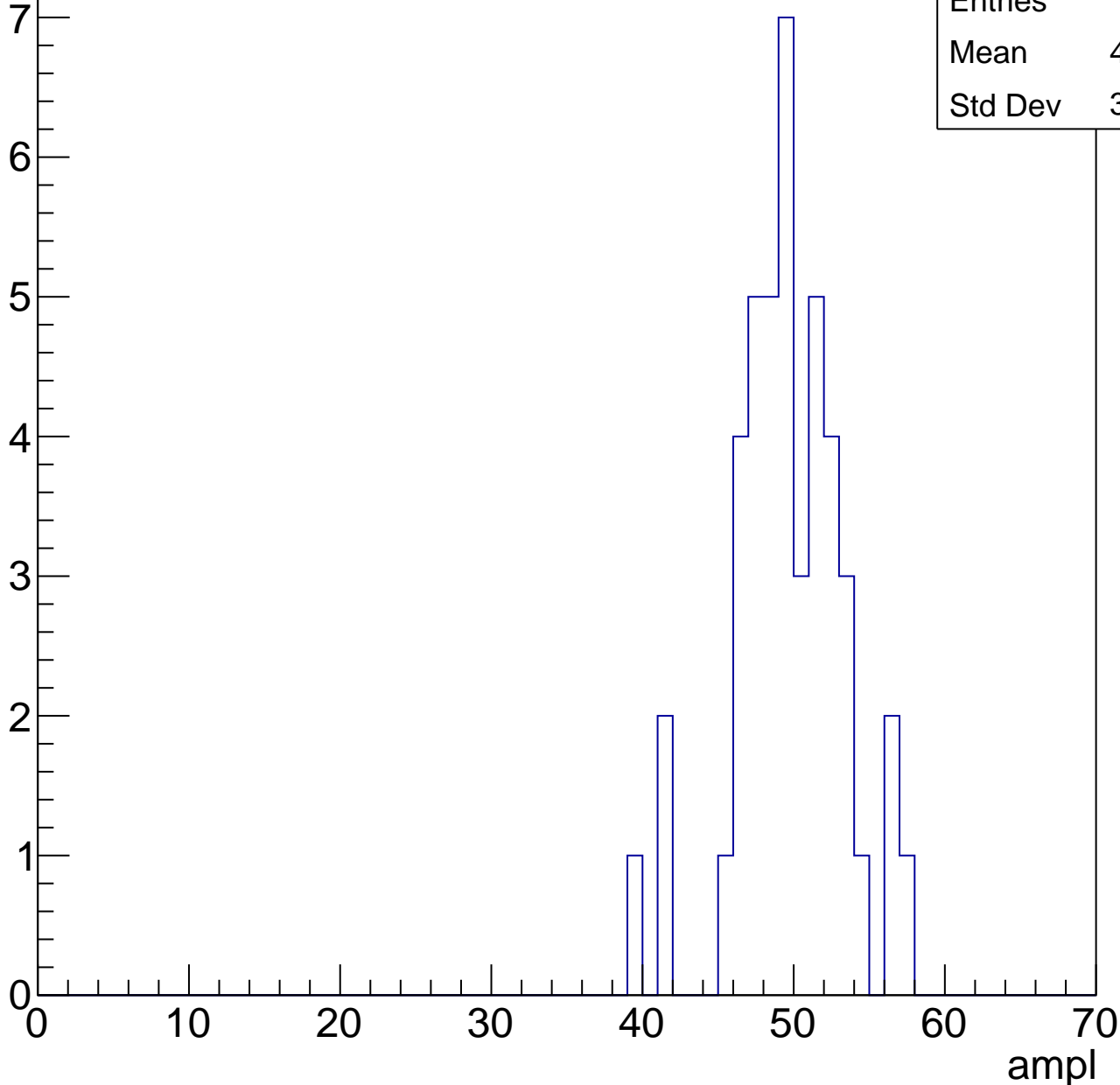


# B0L000S, U18-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	49.16
Std Dev	3.686

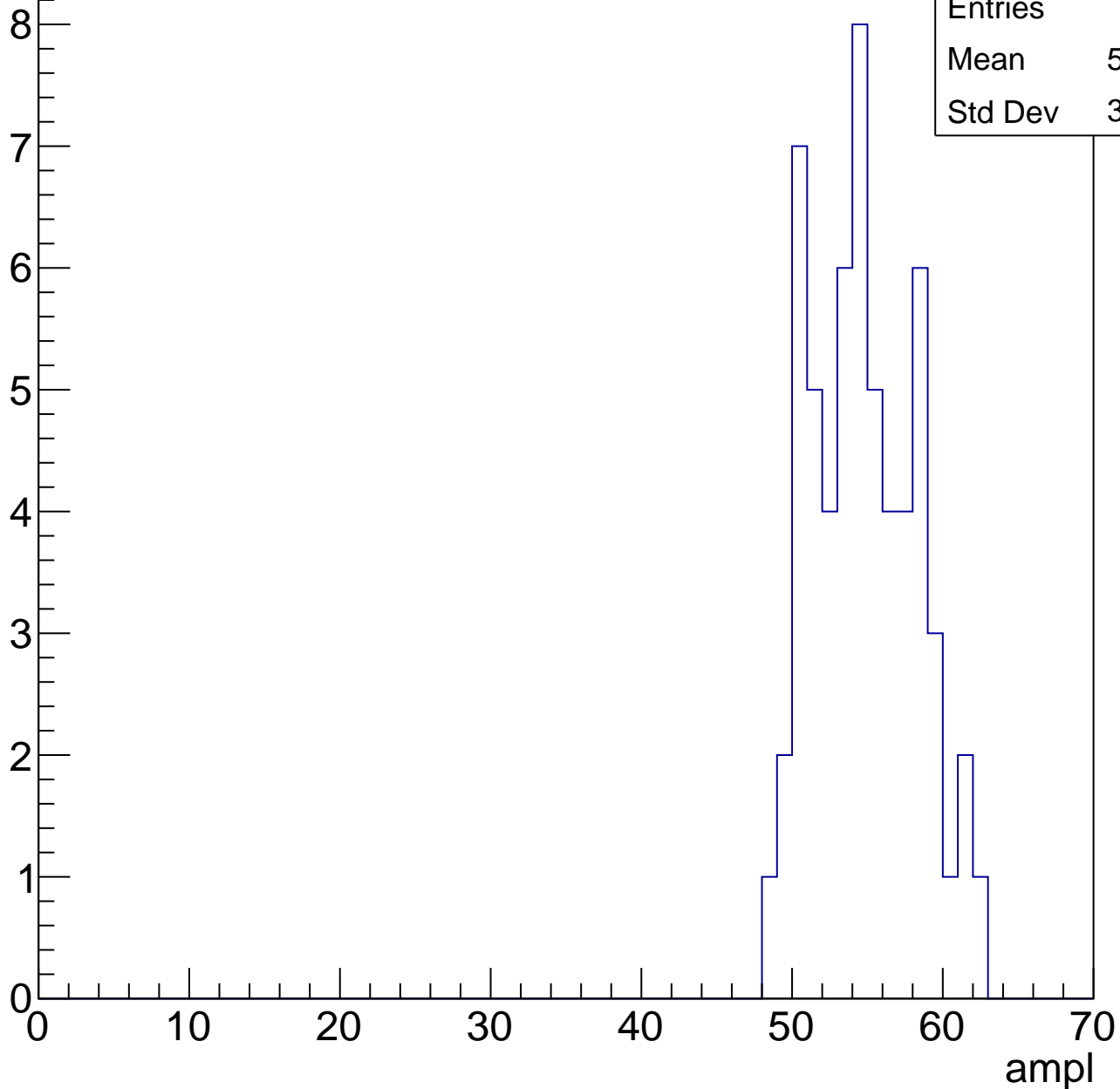


# B0L000S, U18-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.32
Std Dev	3.407

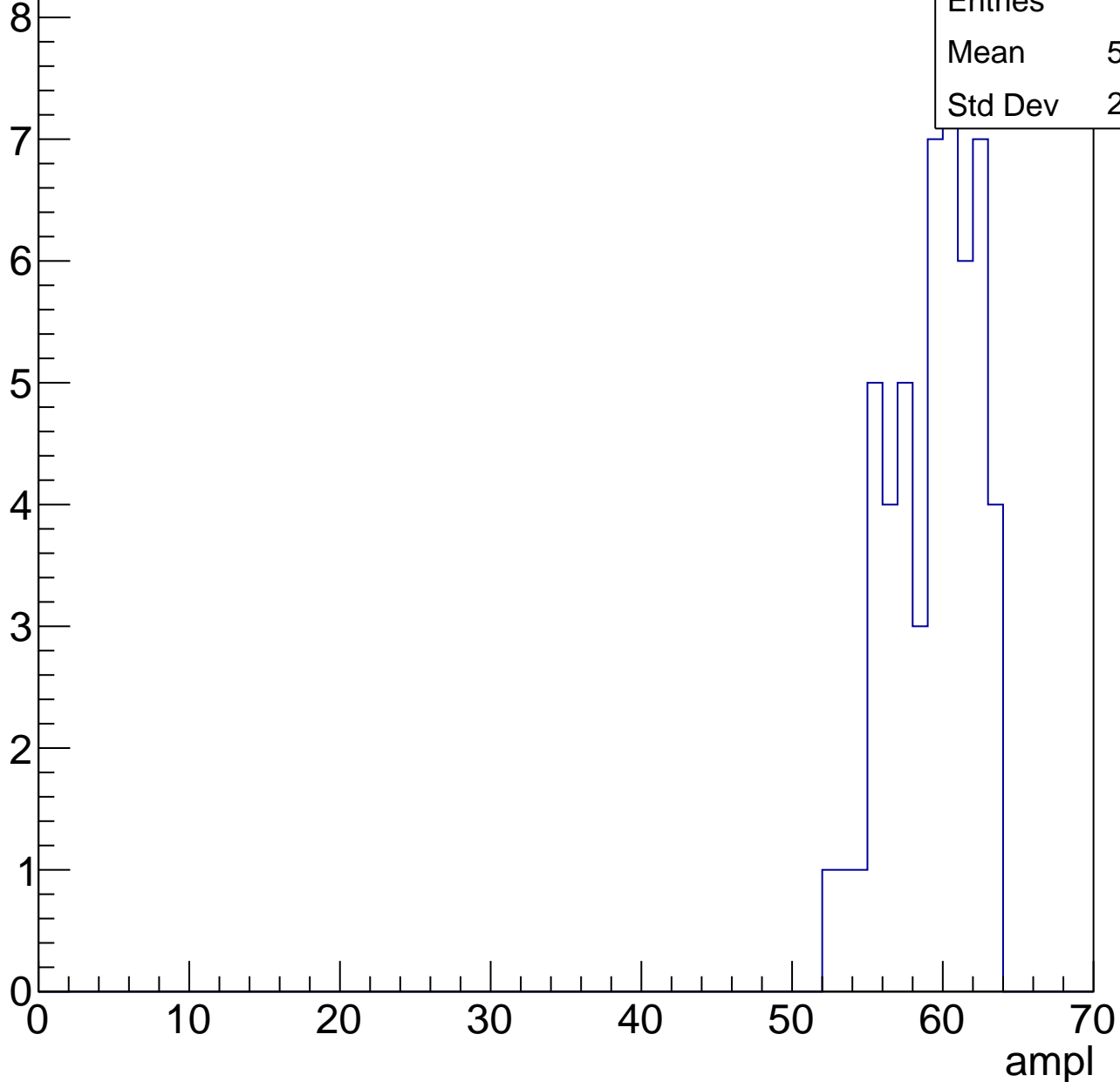


# B0L000S, U18-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.88
Std Dev	2.799

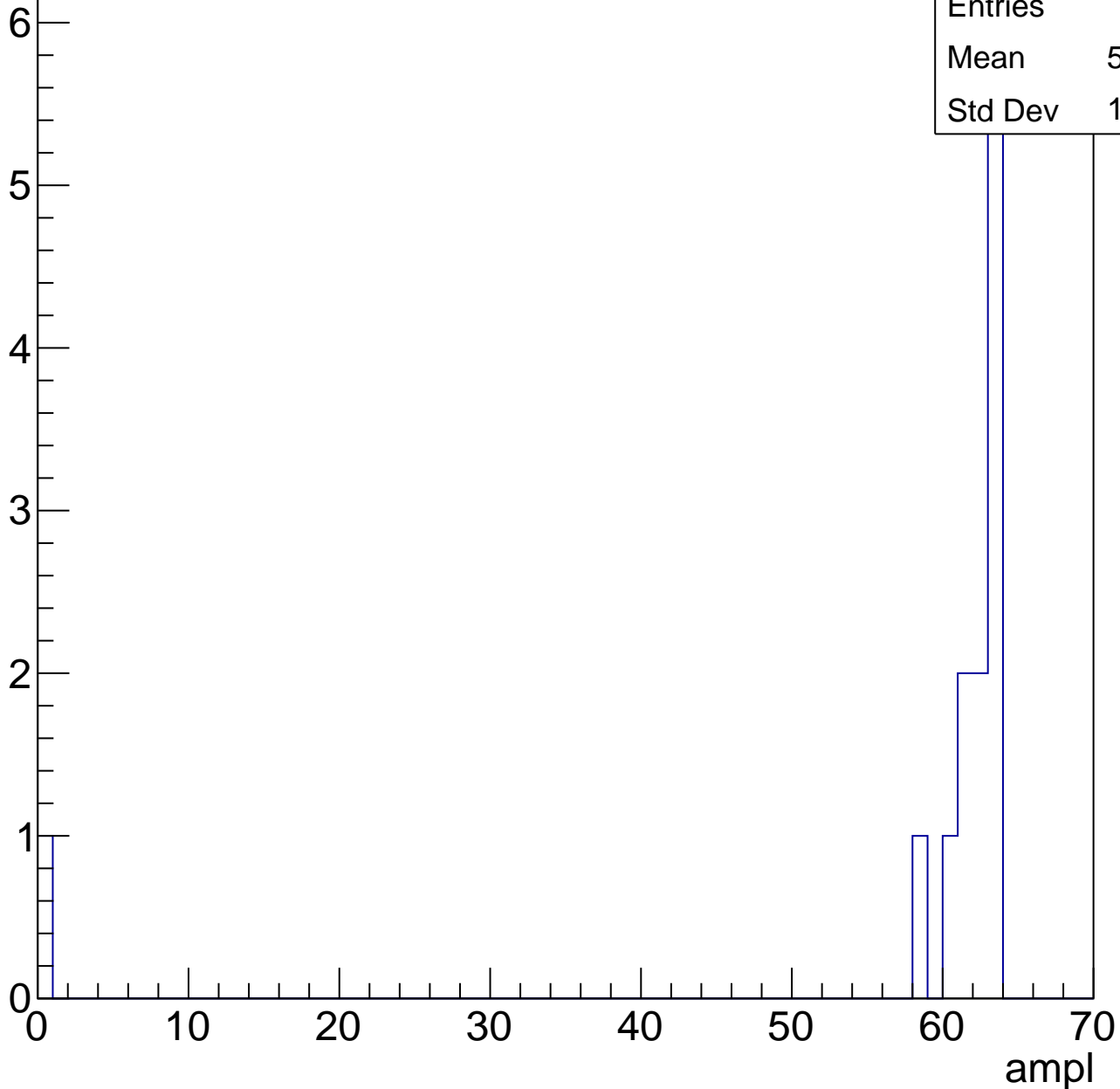


# B0L000S, U18-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	57.08
Std Dev	16.54





# B0L000S, U18-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch31, adc0

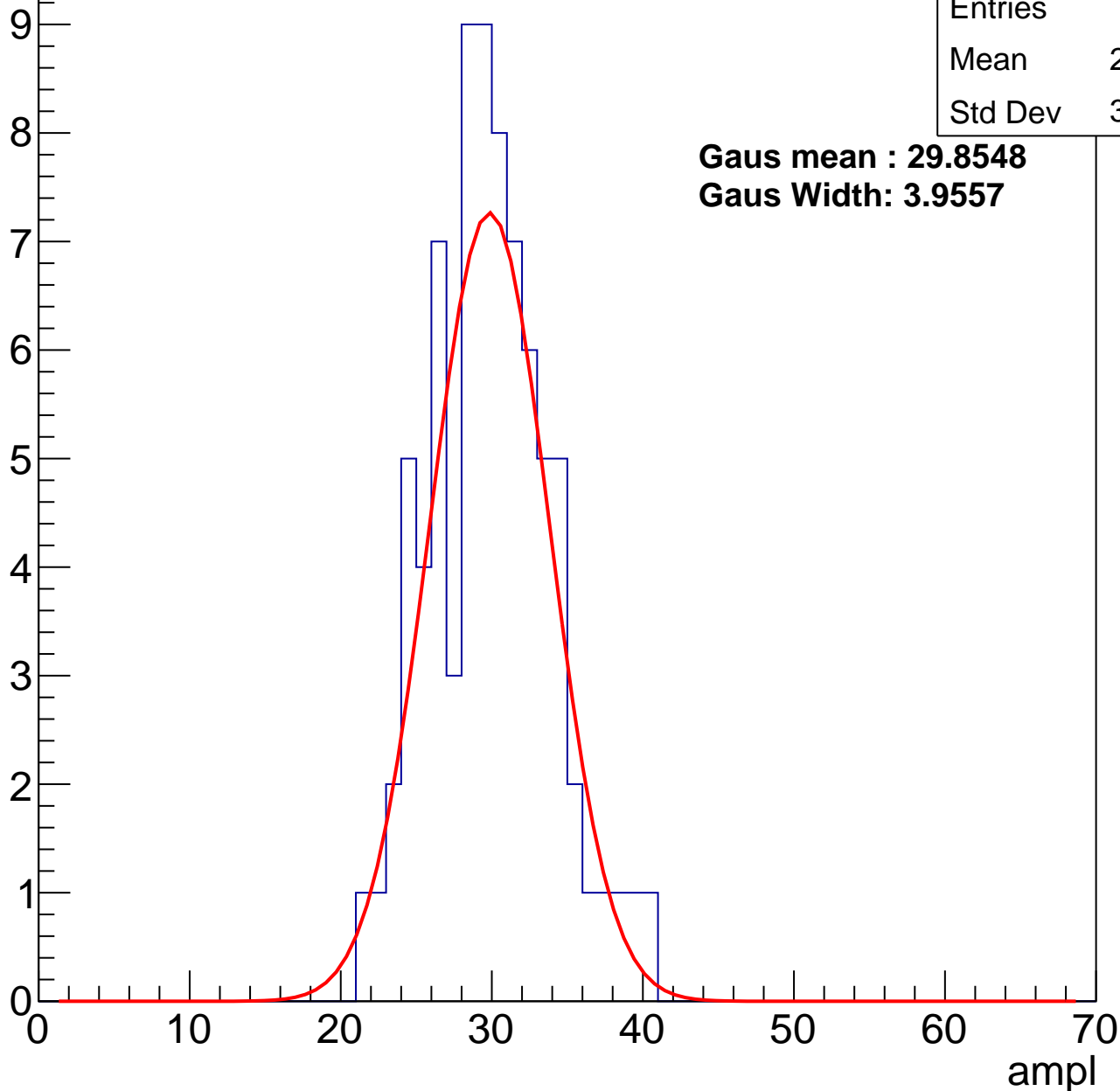
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	29.48
Std Dev	3.933

**Gaus mean : 29.8548**

**Gaus Width: 3.9557**



# B0L000S, U18-ch31, adc1

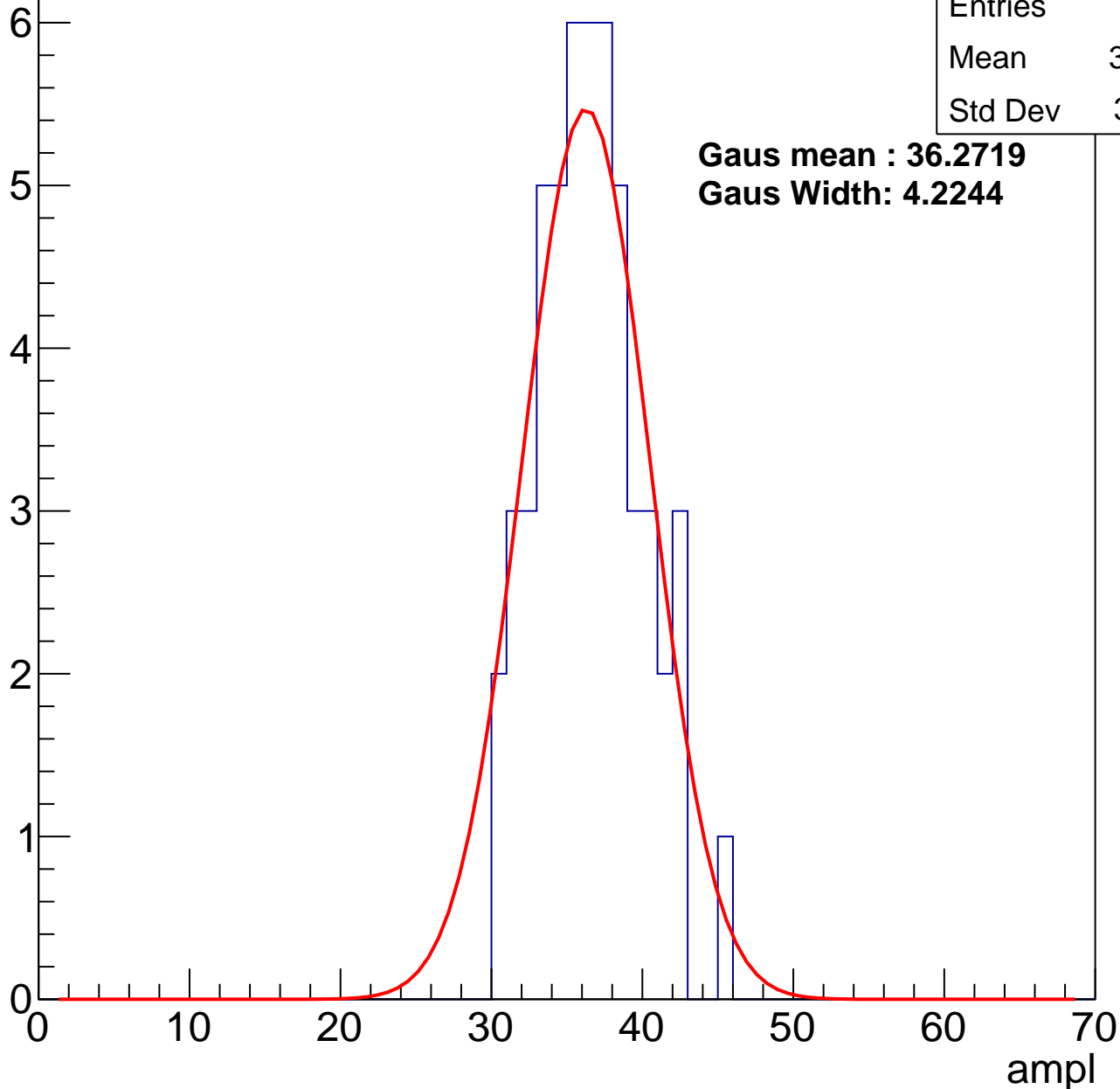
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	36.08
Std Dev	3.381

**Gaus mean : 36.2719**

**Gaus Width: 4.2244**



# B0L000S, U18-ch31, adc2

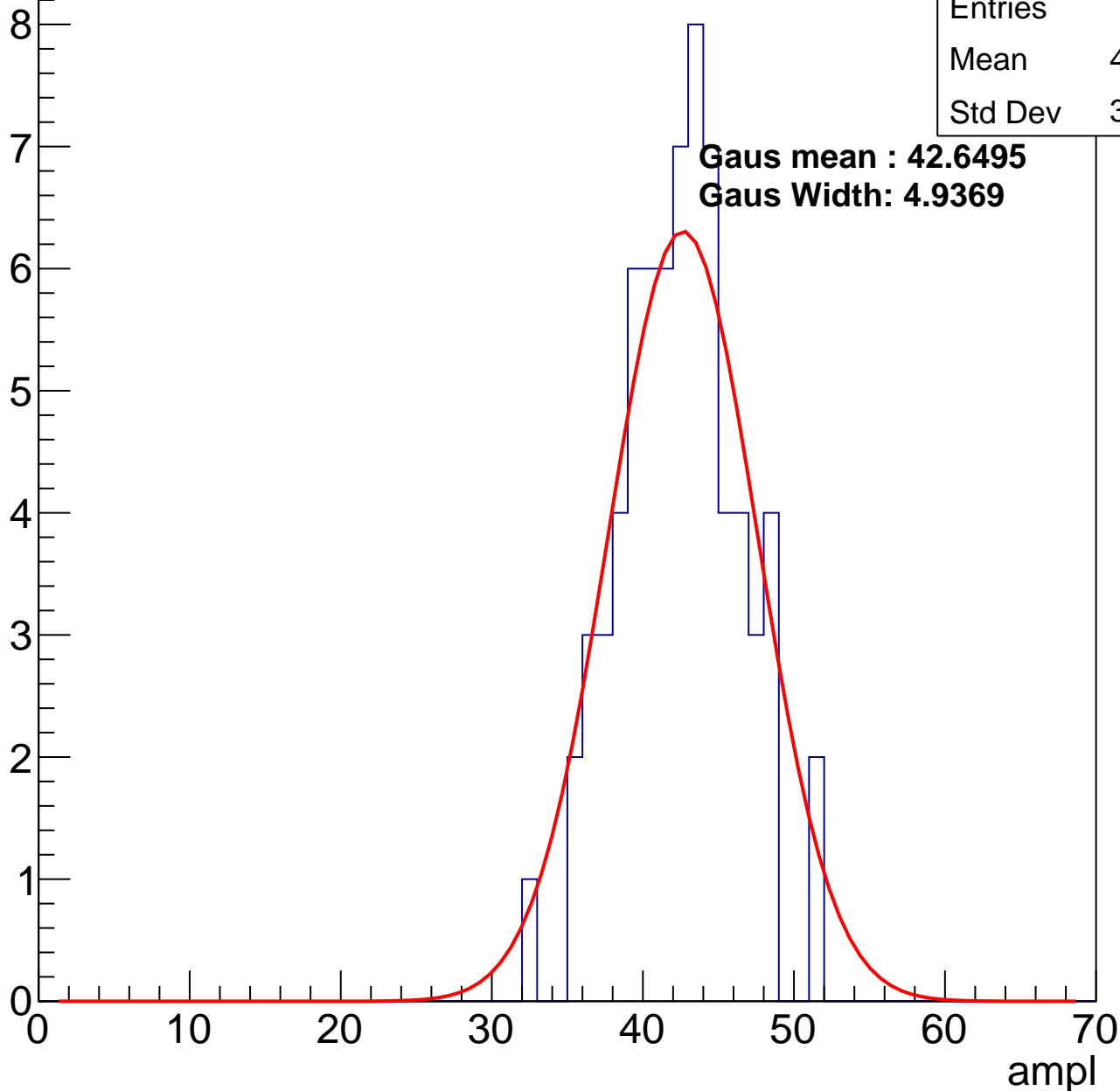
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.97
Std Dev	3.865

**Gaus mean : 42.6495**

**Gaus Width: 4.9369**

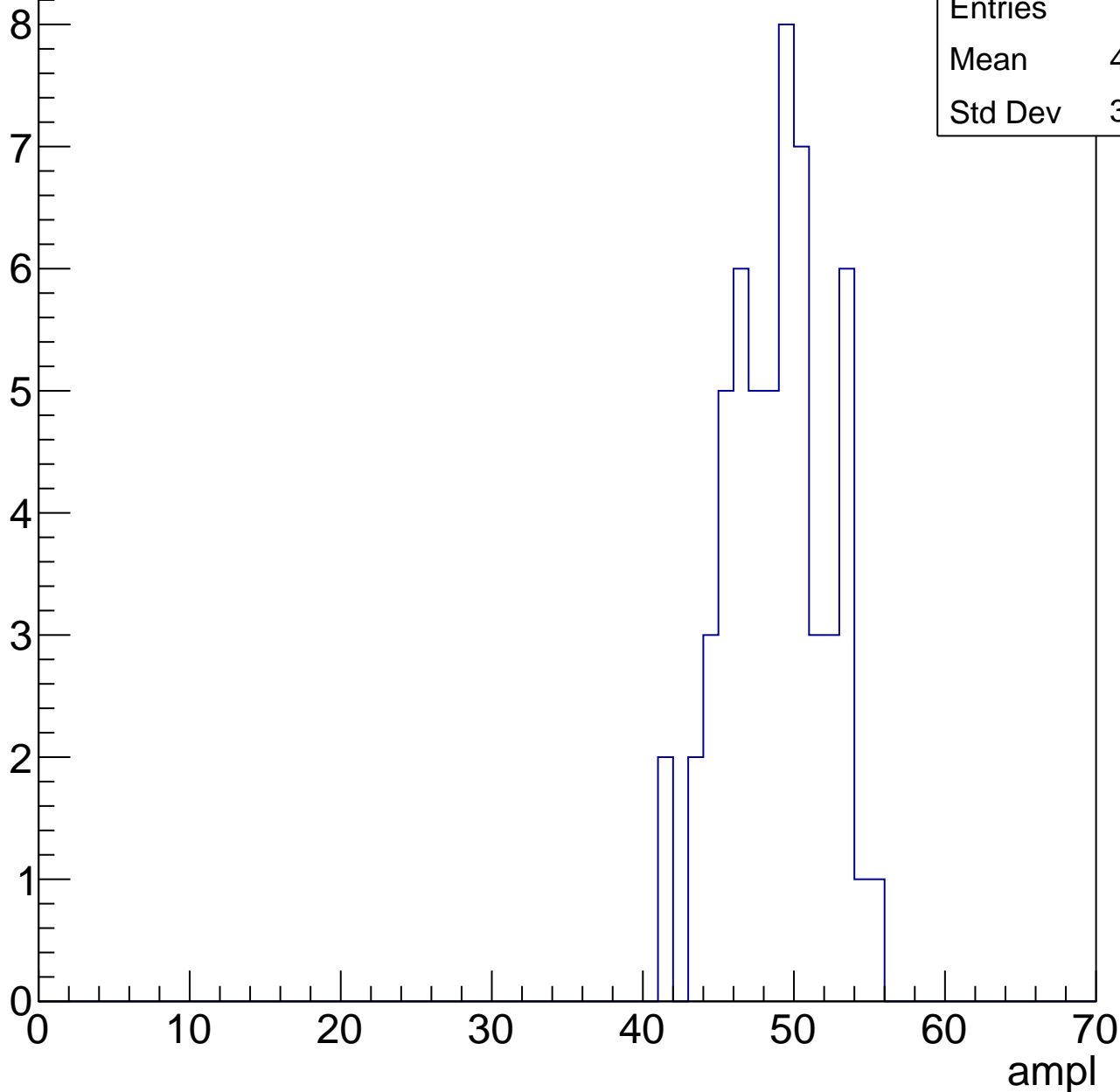


# B0L000S, U18-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	48.32
Std Dev	3.272

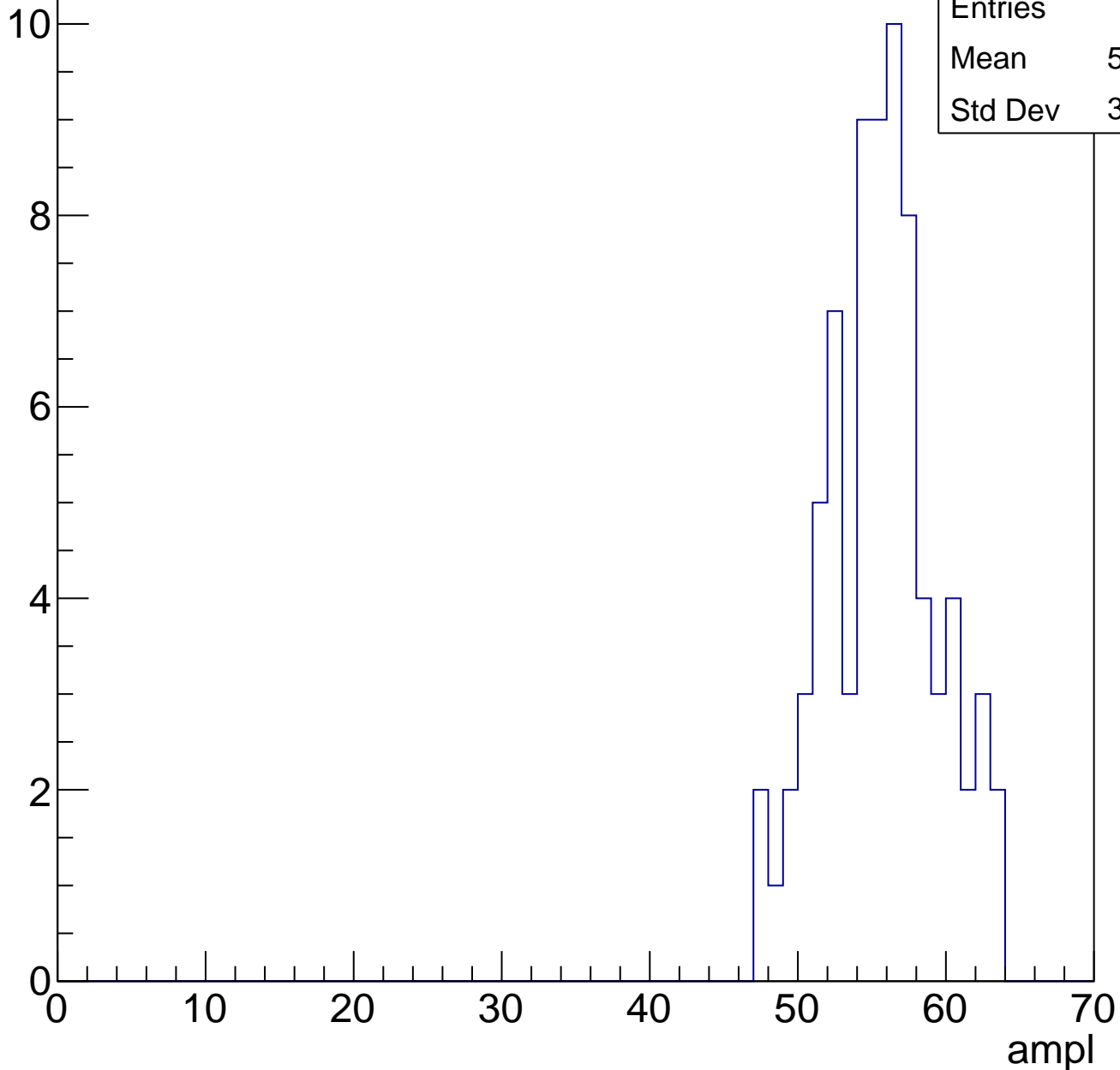


# B0L000S, U18-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	55.17
Std Dev	3.708

Entry

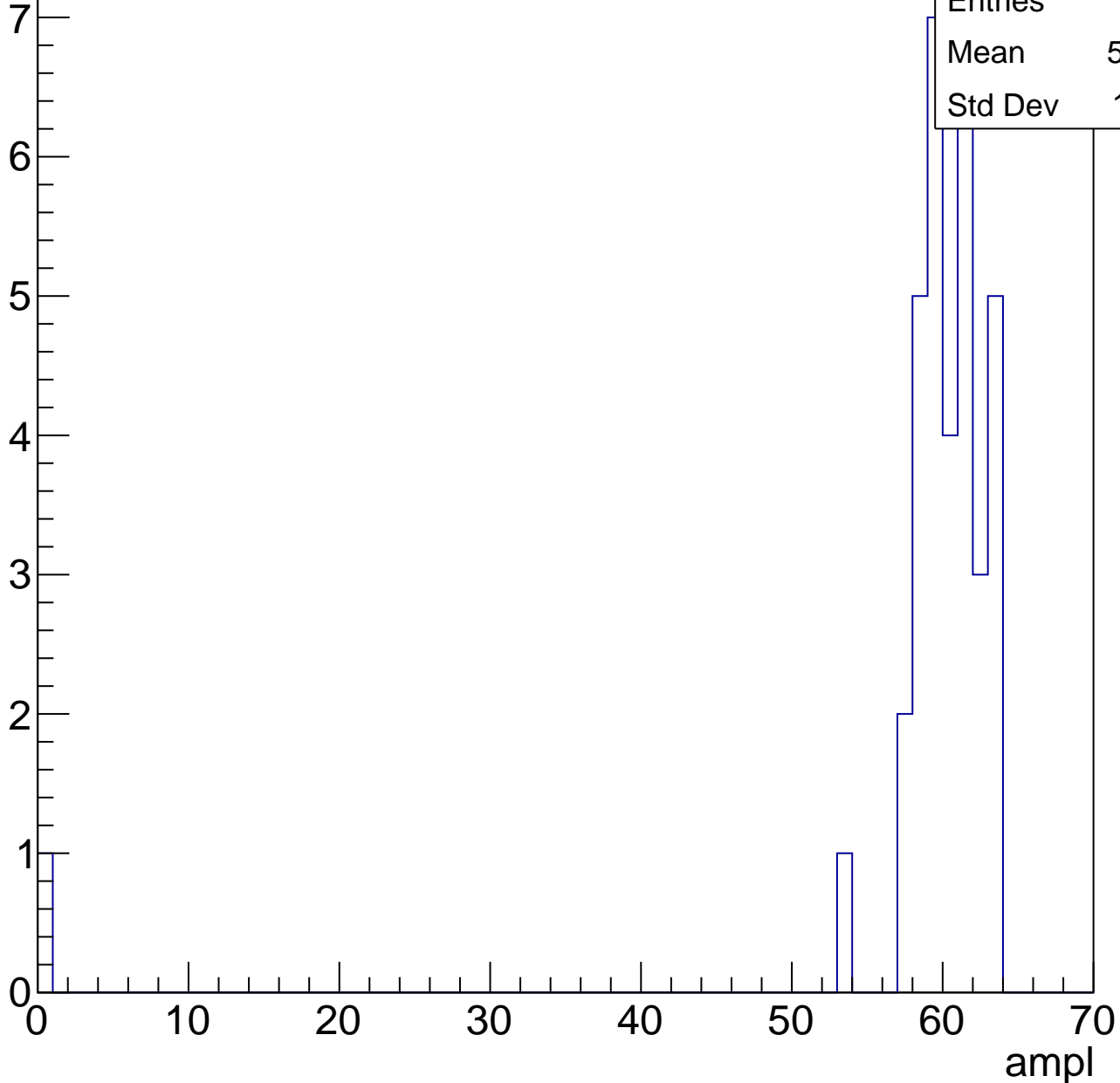


# B0L000S, U18-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

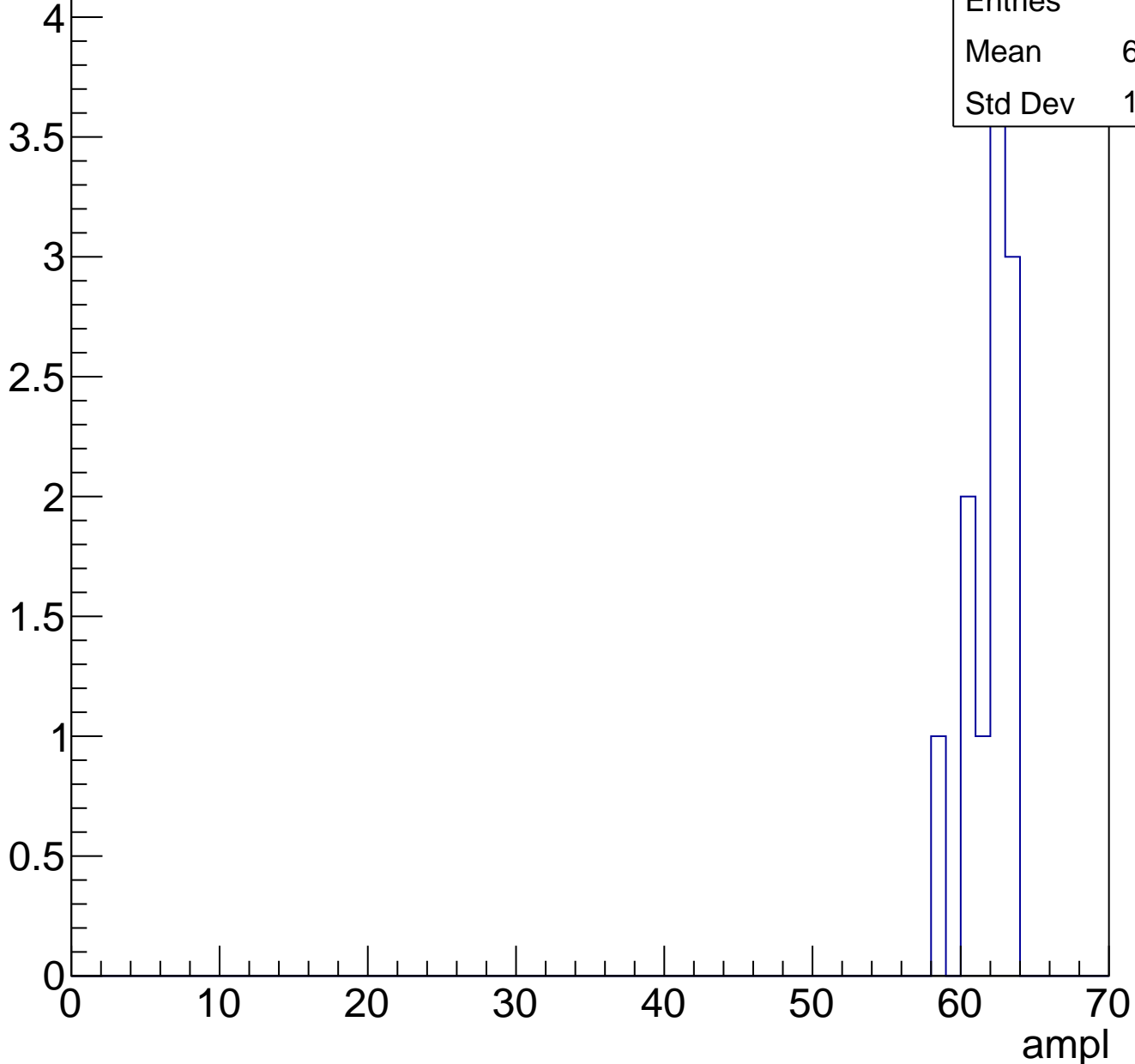
Entries	35
Mean	58.23
Std Dev	10.21



# B0L000S, U18-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	11
Mean	61.45
Std Dev	1.499



# B0L000S, U18-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch32, adc0

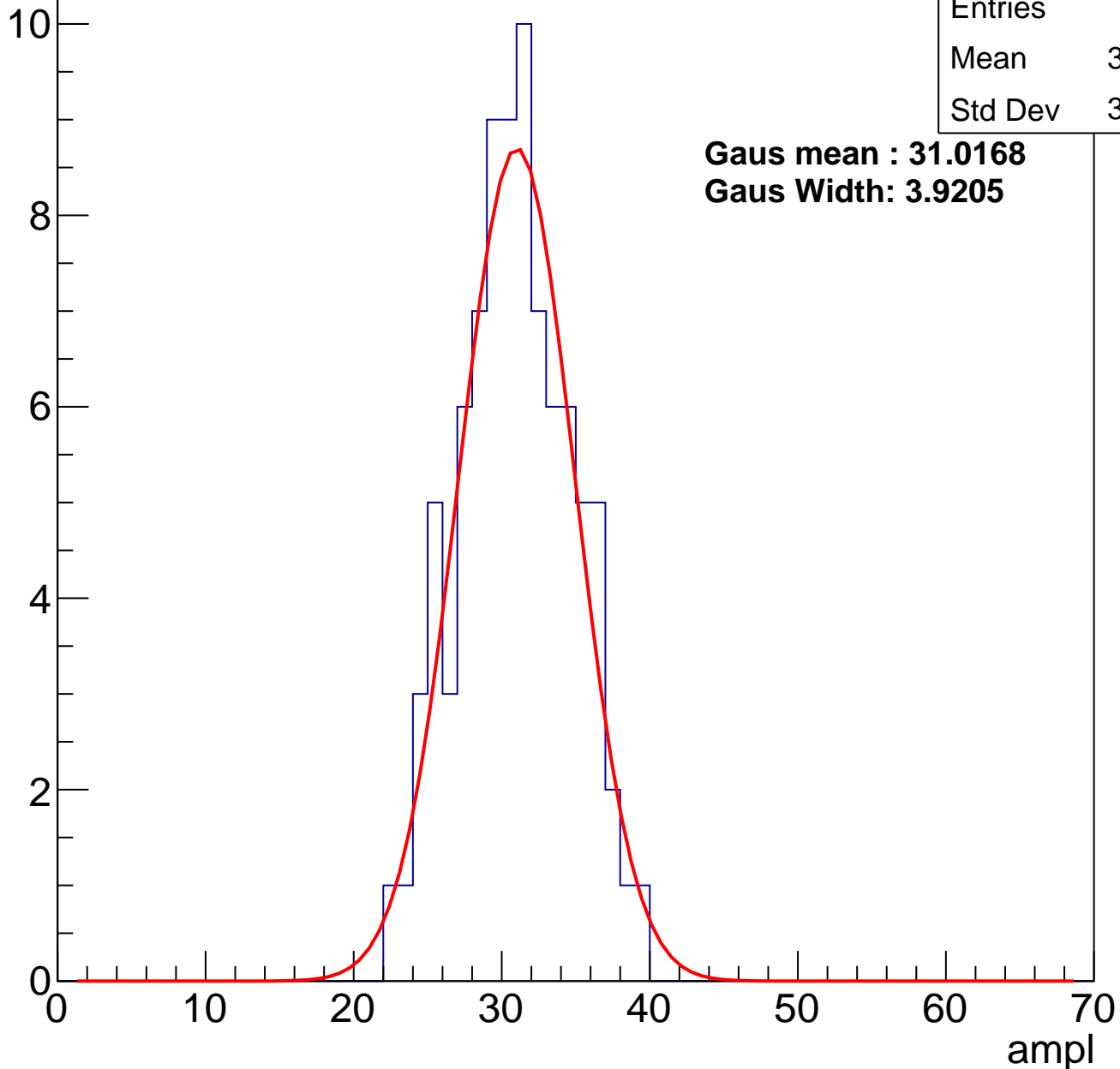
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	30.47
Std Dev	3.717

**Gaus mean : 31.0168**

**Gaus Width: 3.9205**

Entry



# B0L000S, U18-ch32, adc1

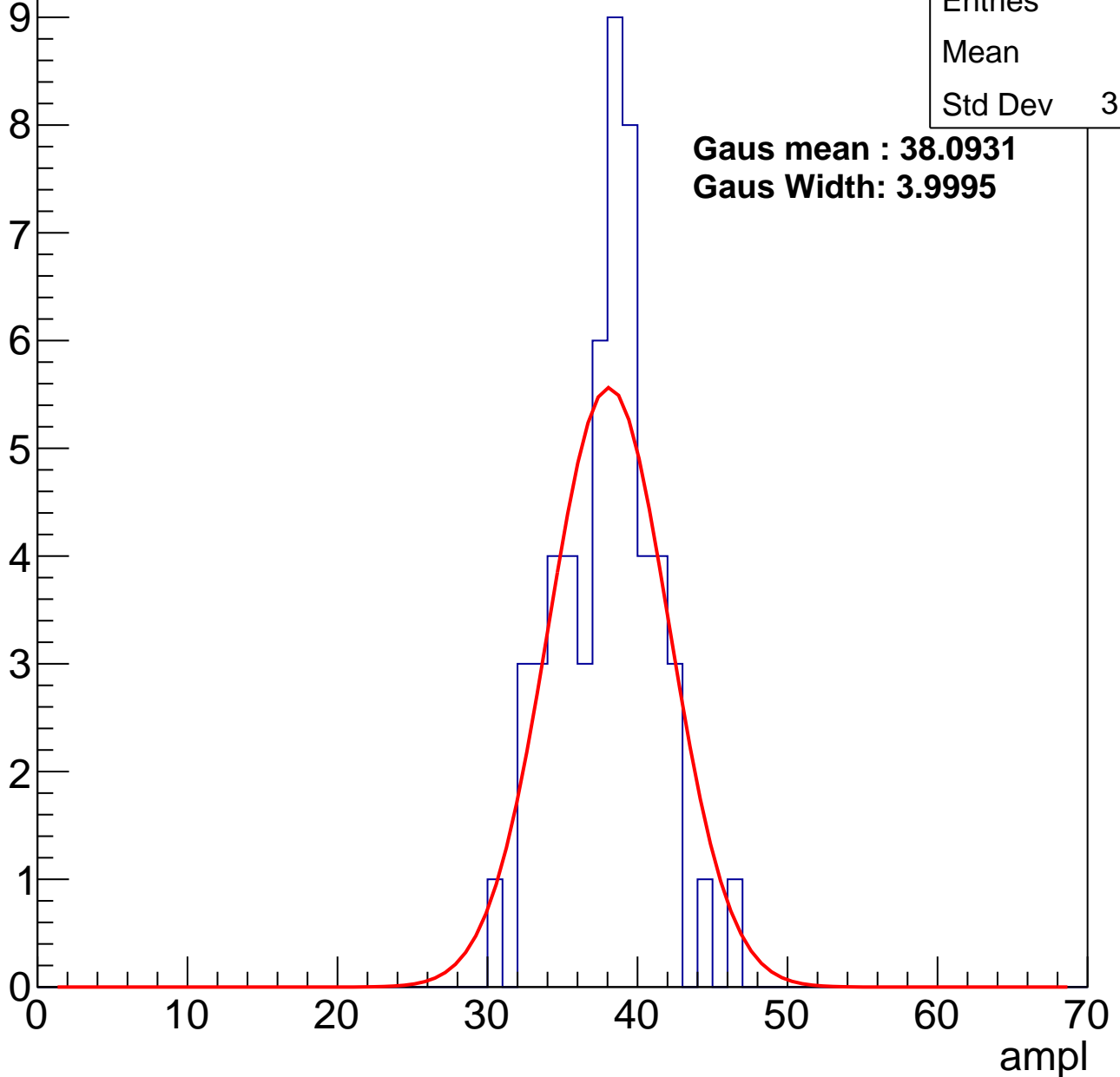
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	37.5
Std Dev	3.219

**Gaus mean : 38.0931**

**Gaus Width: 3.9995**



# B0L000S, U18-ch32, adc2

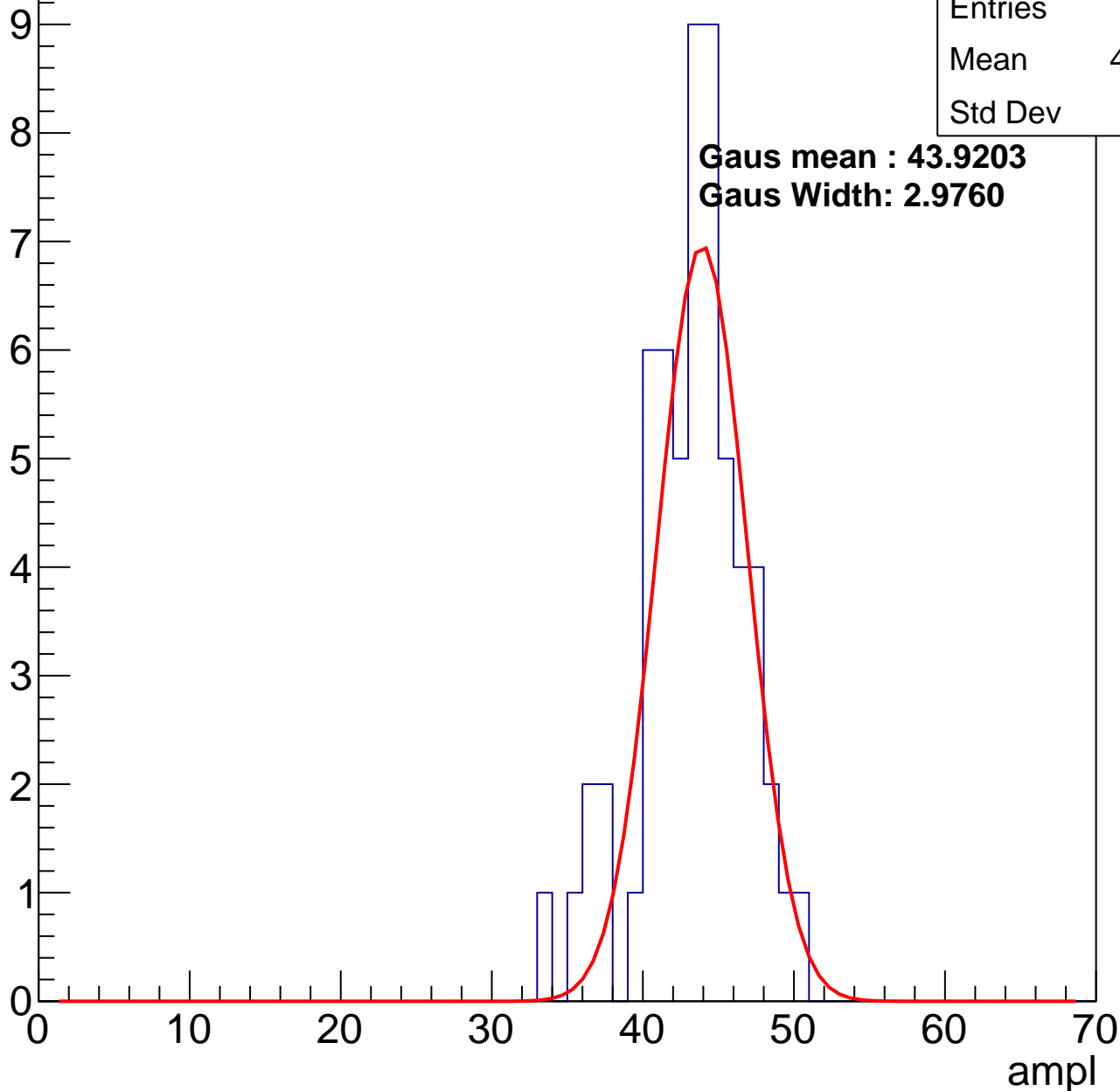
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.78
Std Dev	3.43

**Gaus mean : 43.9203**

**Gaus Width: 2.9760**

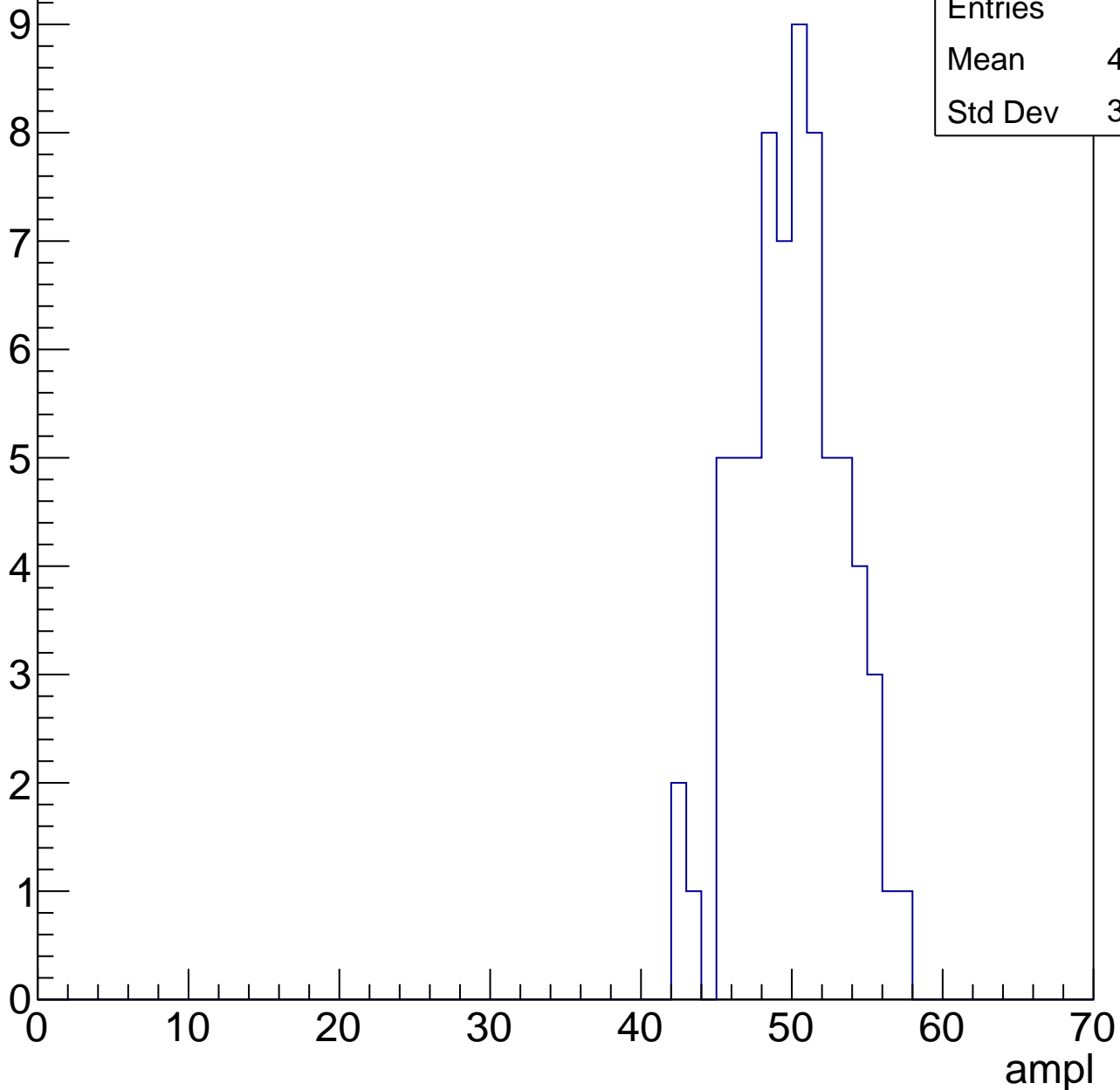


# B0L000S, U18-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.58
Std Dev	3.299

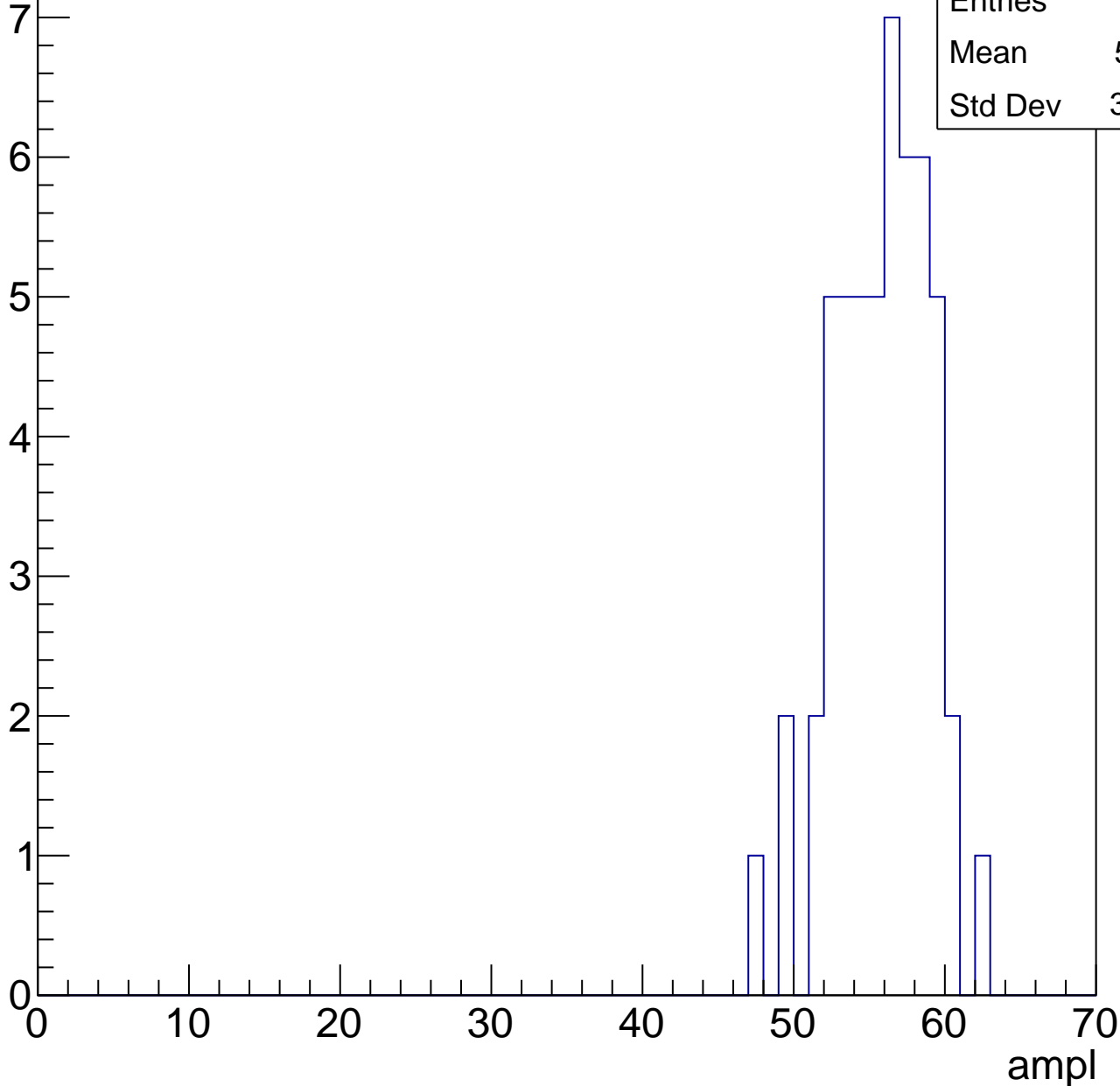


# B0L000S, U18-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	55.31
Std Dev	3.092



# B0L000S, U18-ch32, adc5

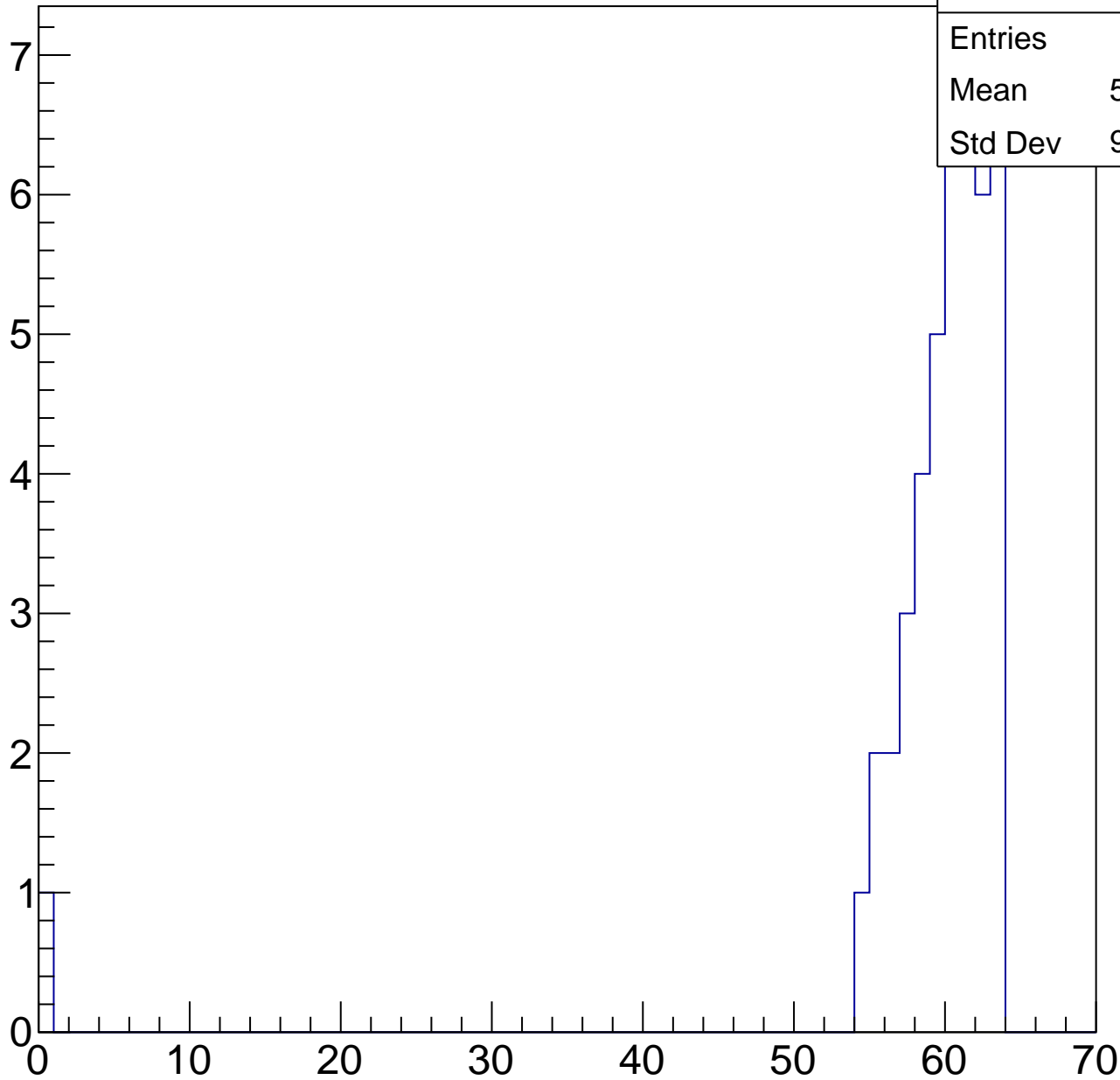
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.53
Std Dev	9.145

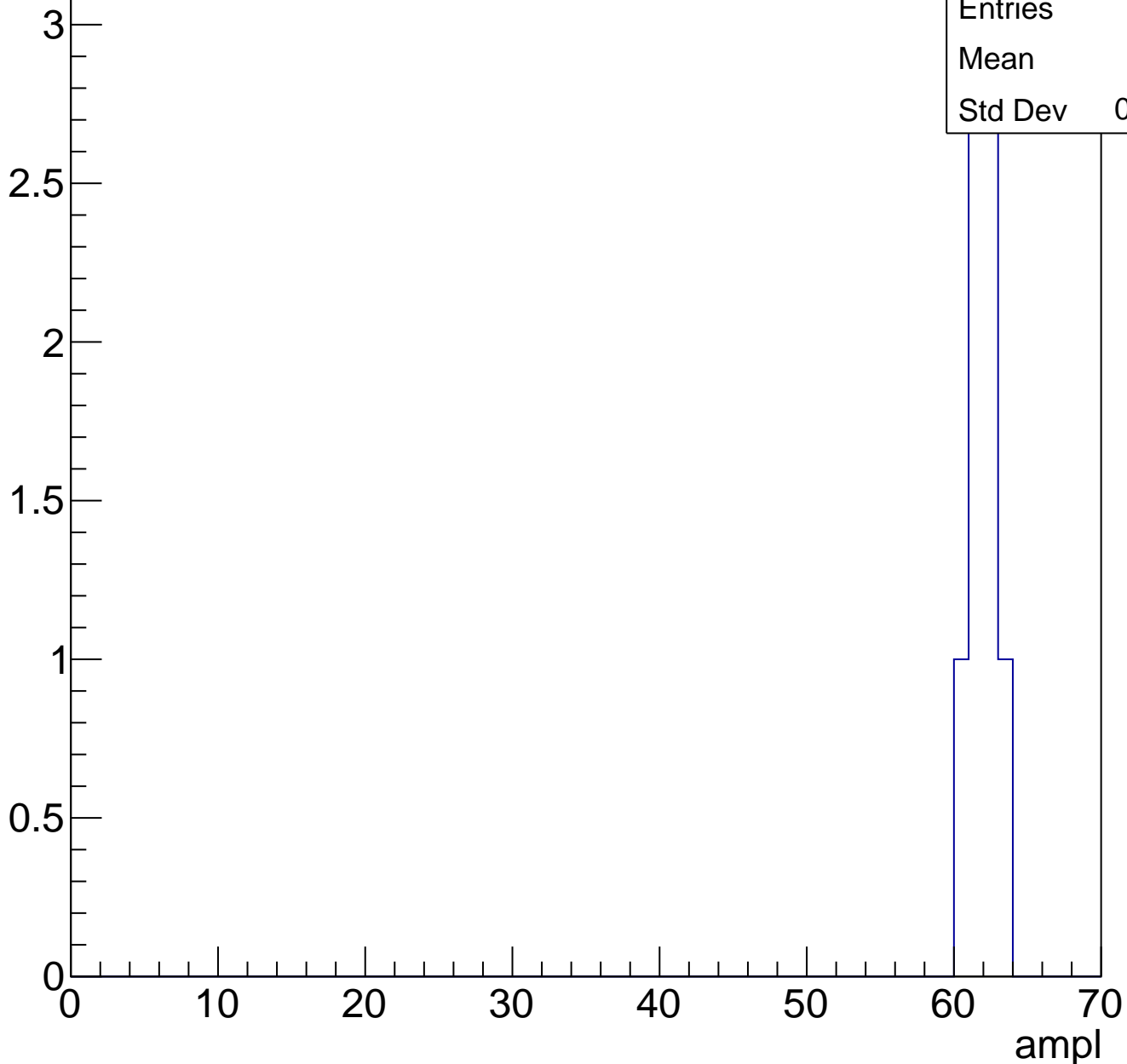
ampl



# B0L000S, U18-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch33, adc0

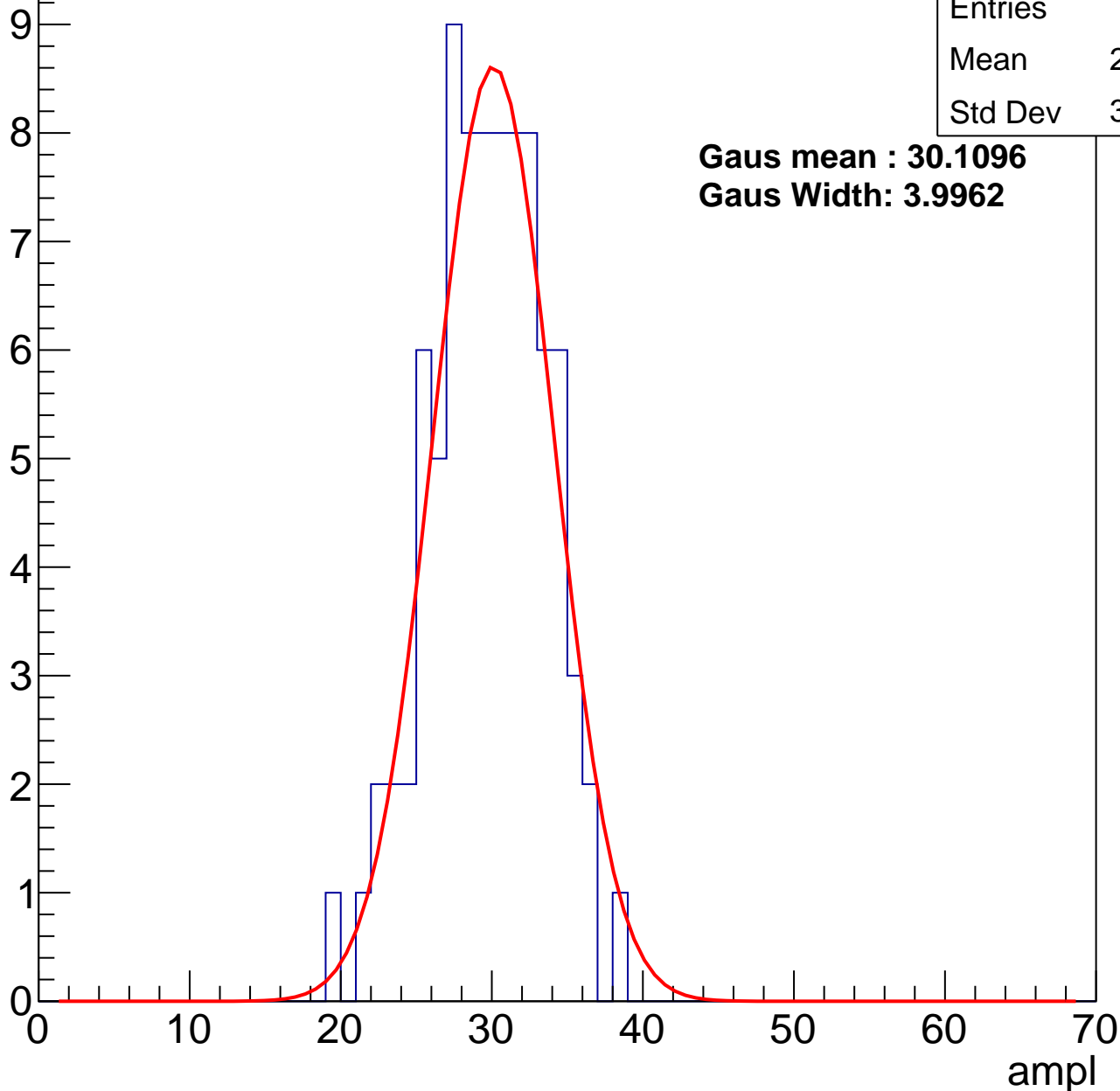
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	29.28
Std Dev	3.747

**Gaus mean : 30.1096**

**Gaus Width: 3.9962**



# B0L000S, U18-ch33, adc1

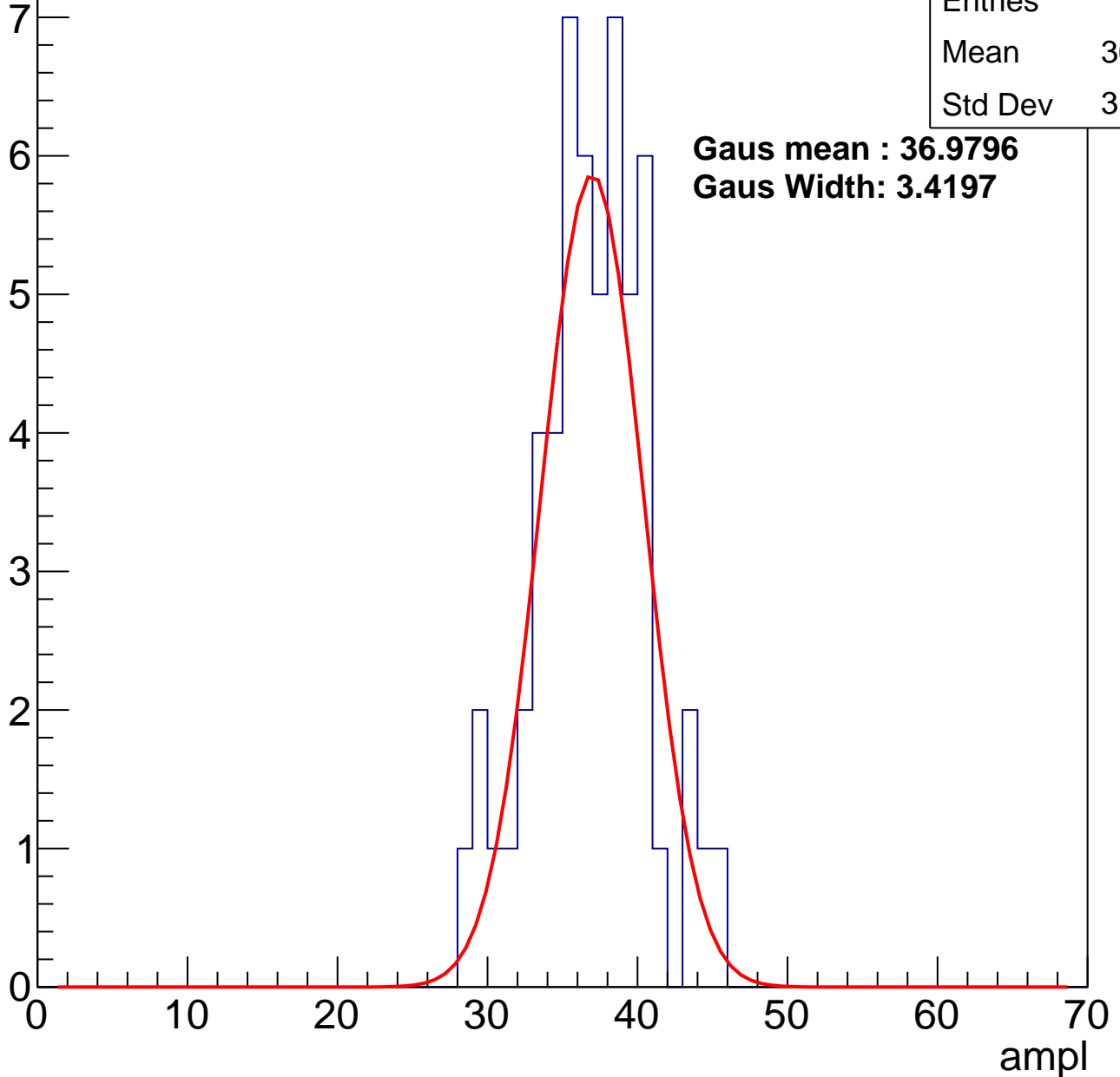
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.46
Std Dev	3.645

**Gaus mean : 36.9796**

**Gaus Width: 3.4197**



# B0L000S, U18-ch33, adc2

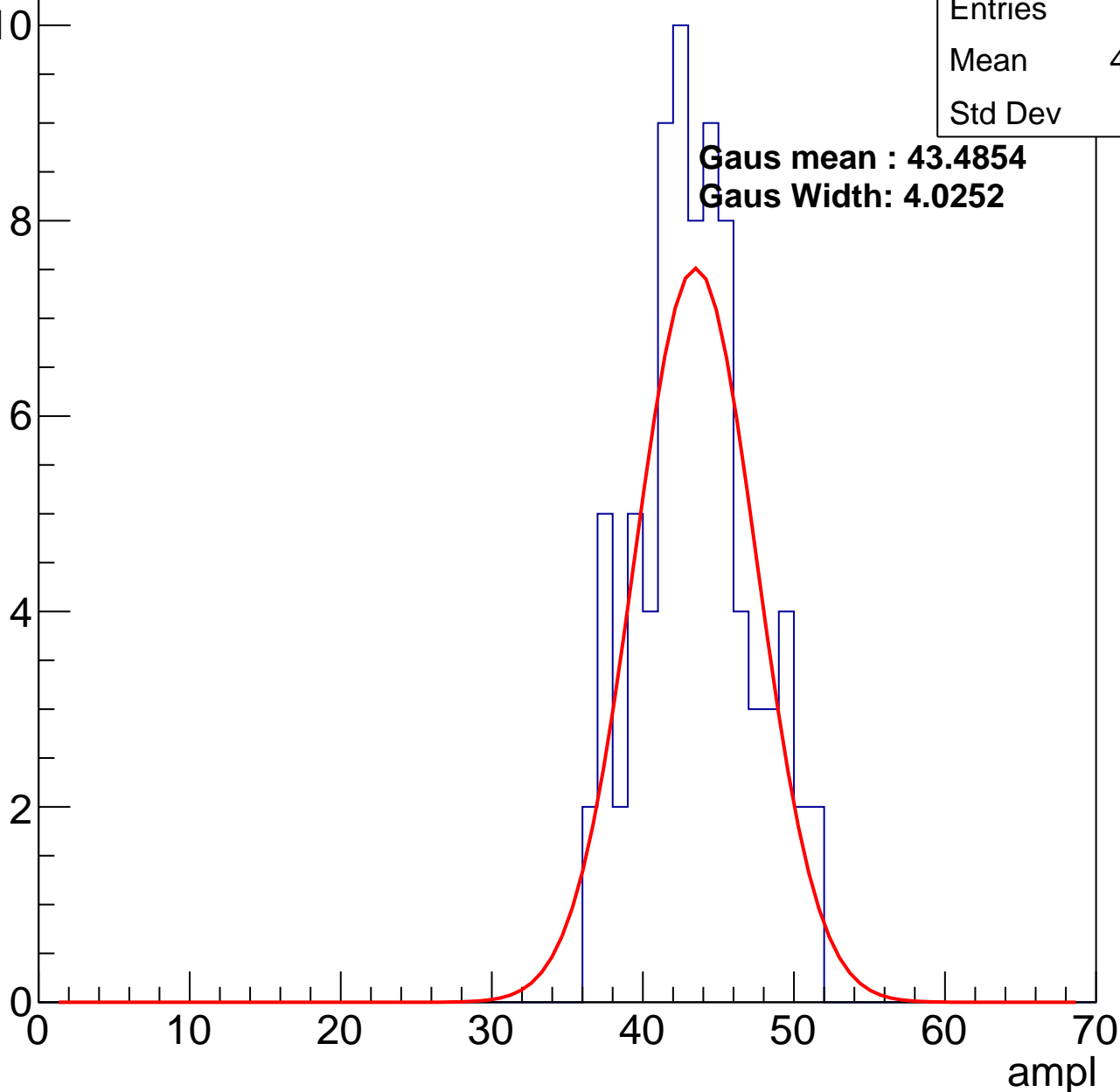
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	43.05
Std Dev	3.64

**Gaus mean : 43.4854**

**Gaus Width: 4.0252**

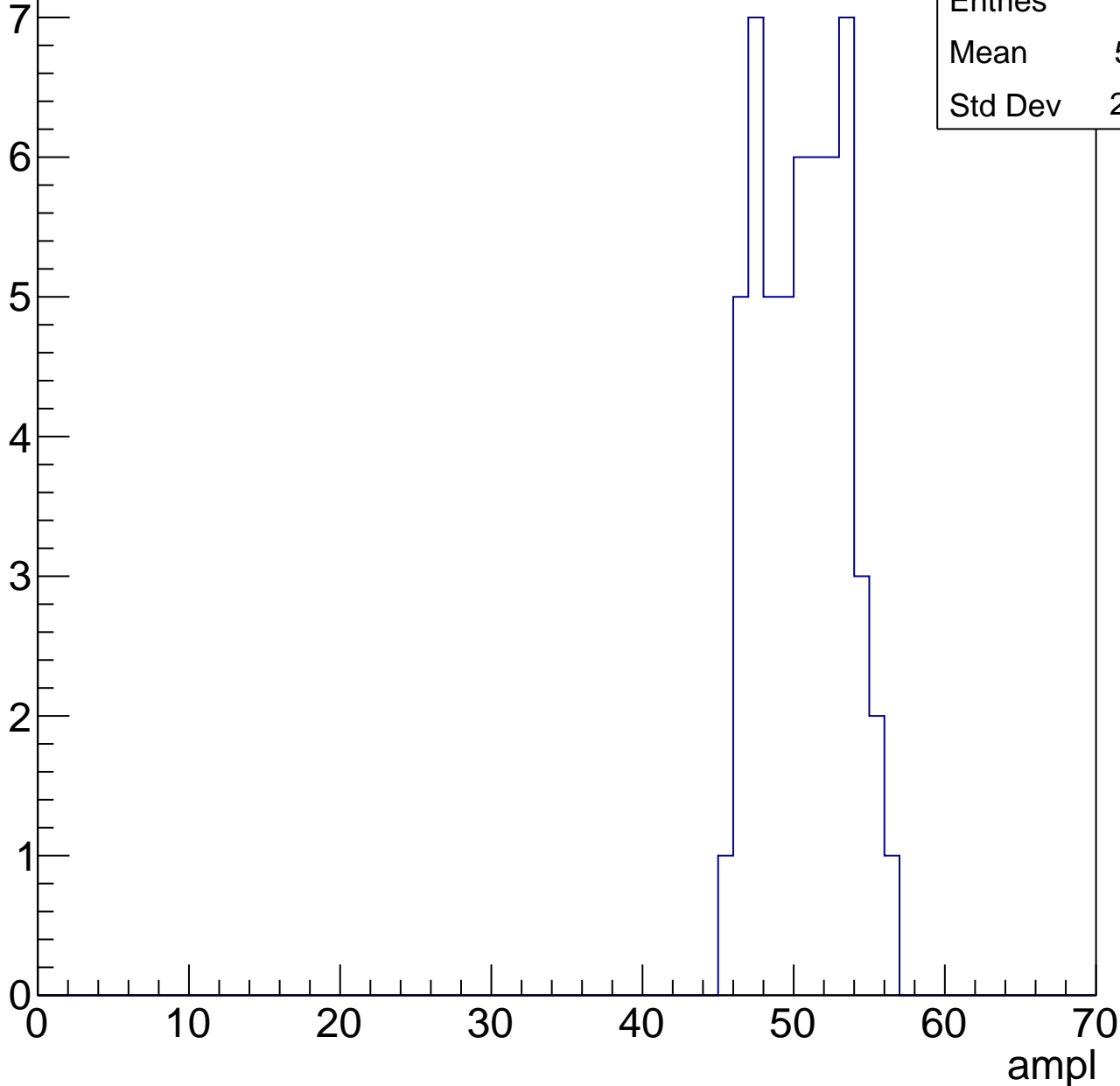


# B0L000S, U18-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	50.11
Std Dev	2.787

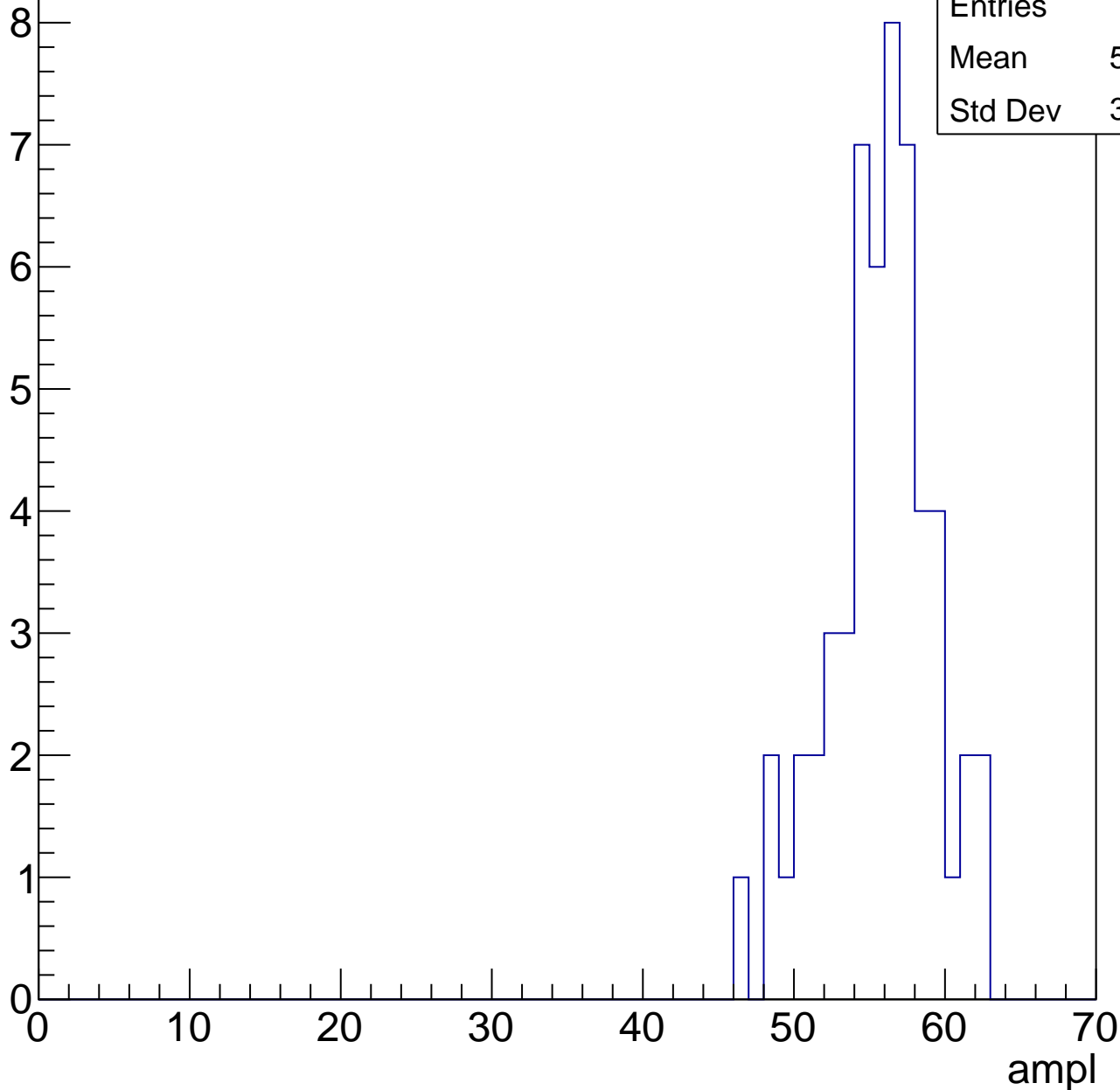


# B0L000S, U18-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

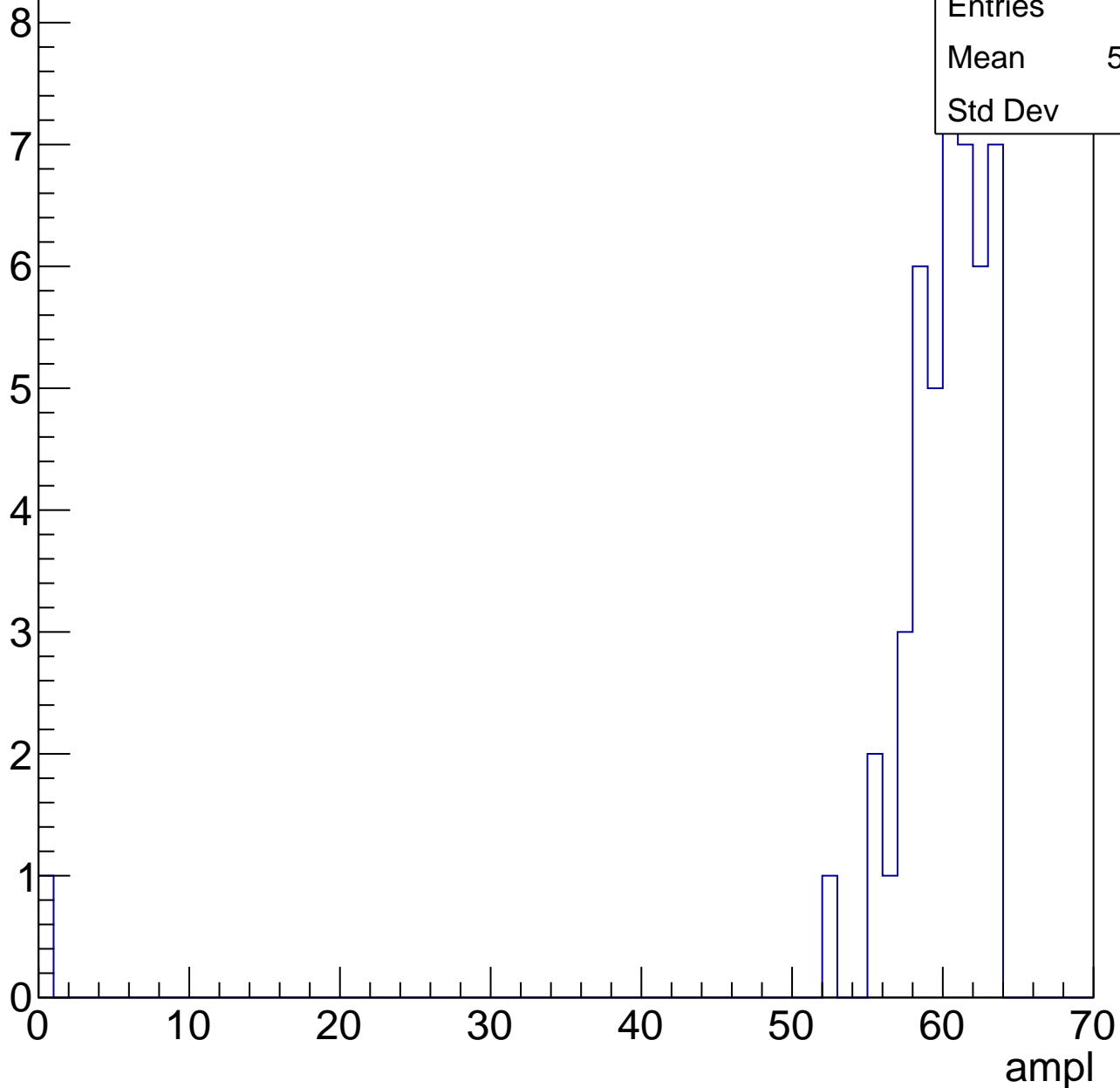
Entries	55
Mean	55.22
Std Dev	3.494



# B0L000S, U18-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

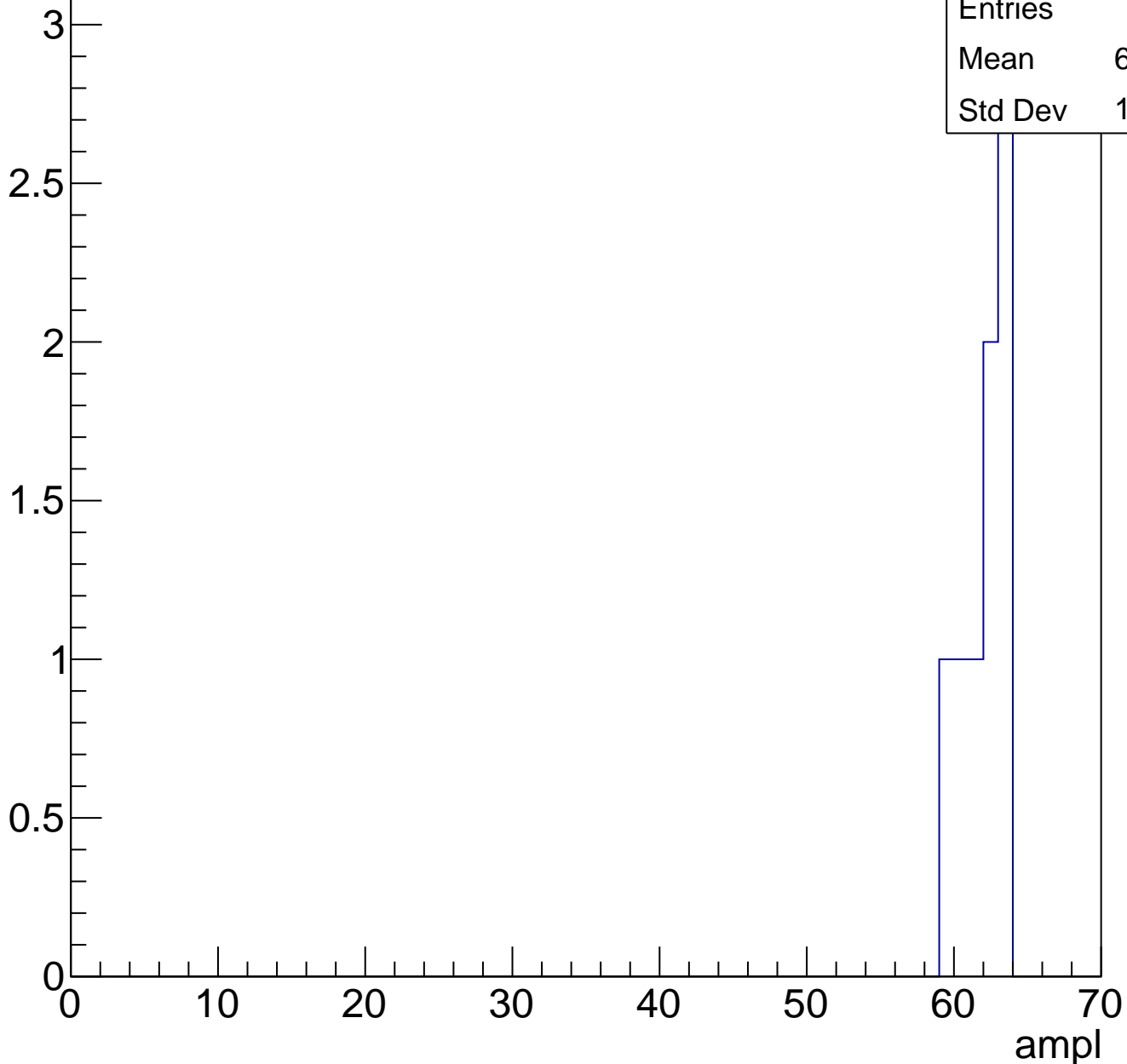
Entry



# B0L000S, U18-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch34, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	28.49
Std Dev	2.949

**Gaus mean : 28.9282**

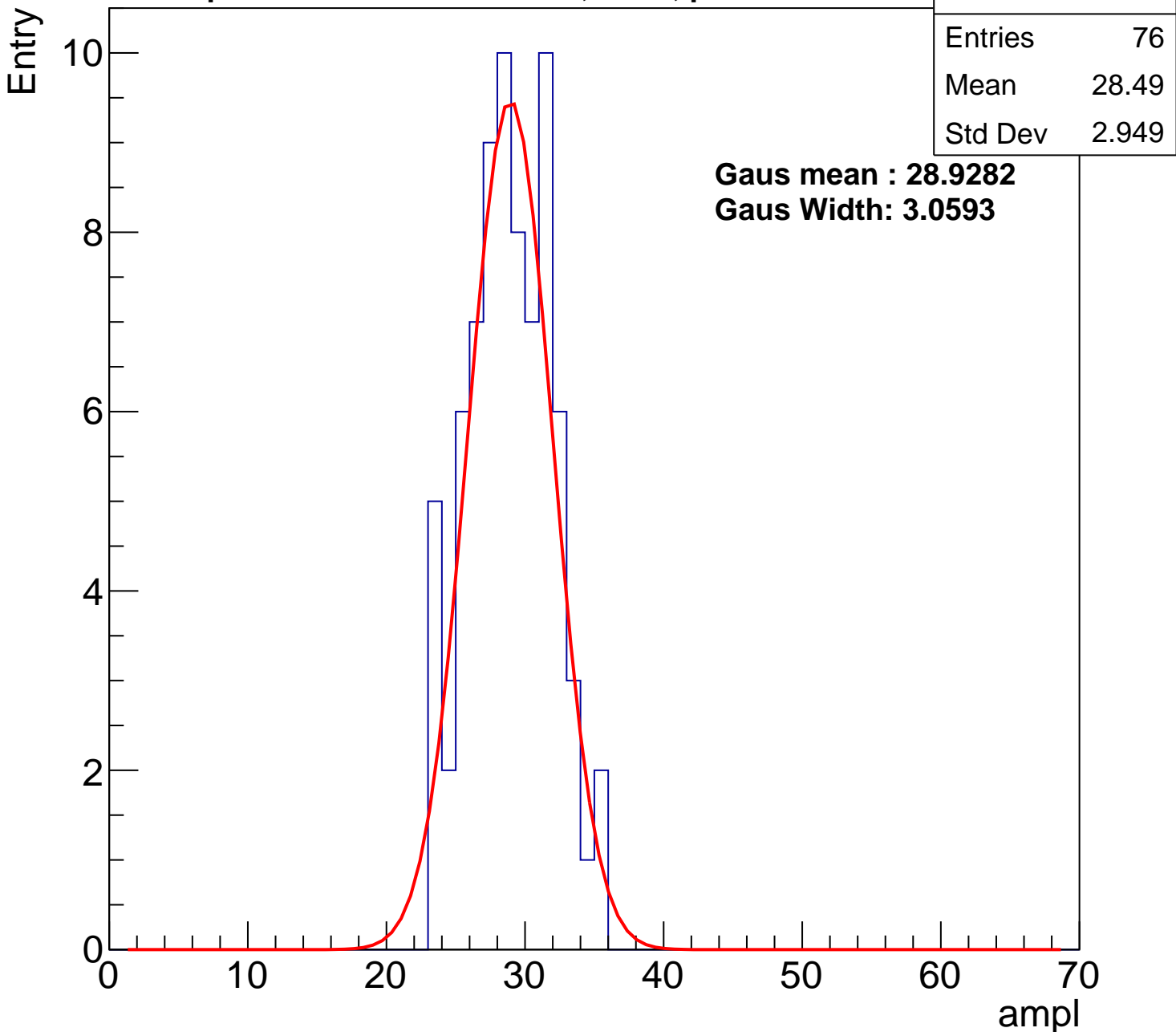
**Gaus Width: 3.0593**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch34, adc1

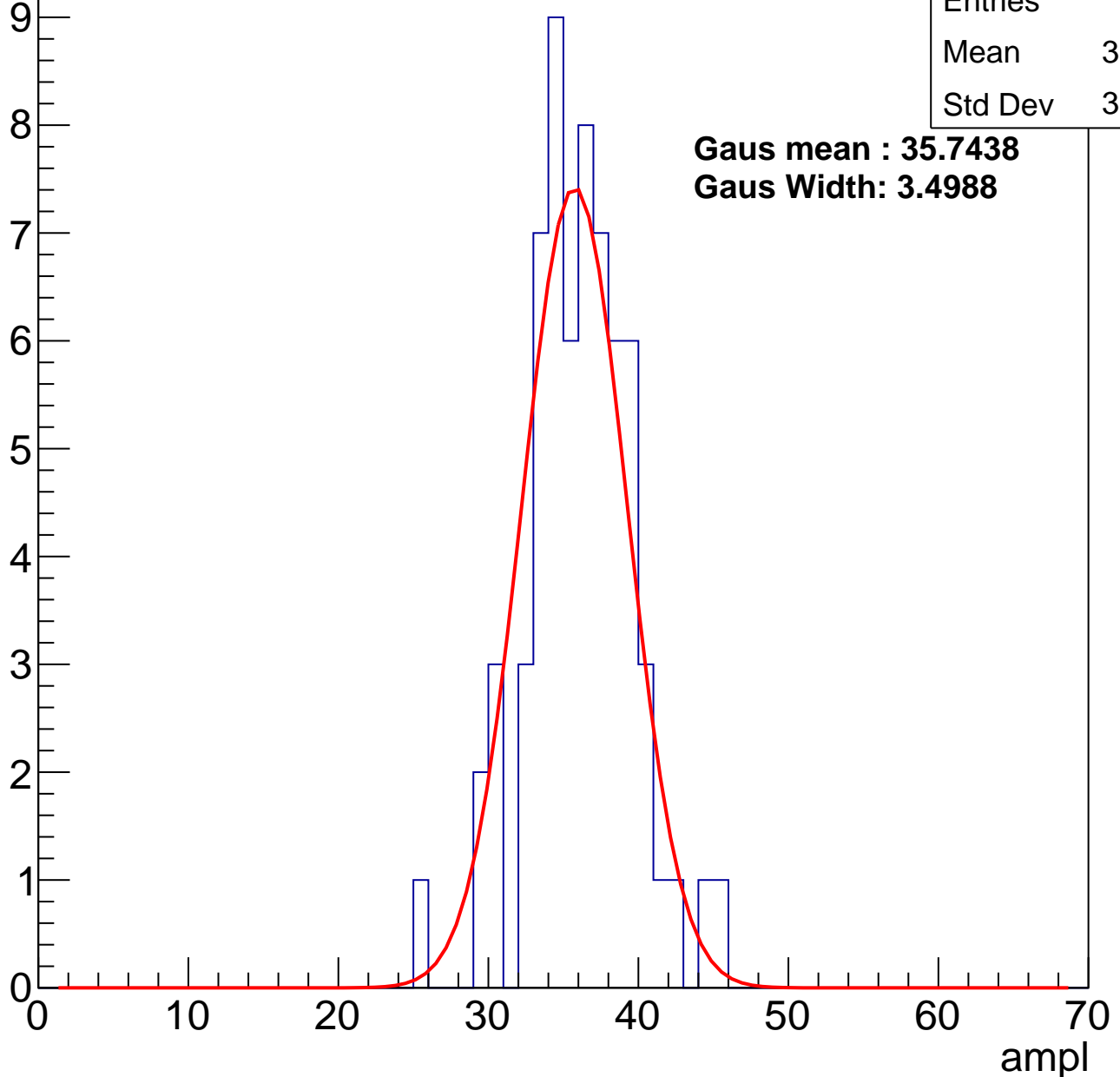
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.65
Std Dev	3.532

**Gaus mean : 35.7438**

**Gaus Width: 3.4988**



# B0L000S, U18-ch34, adc2

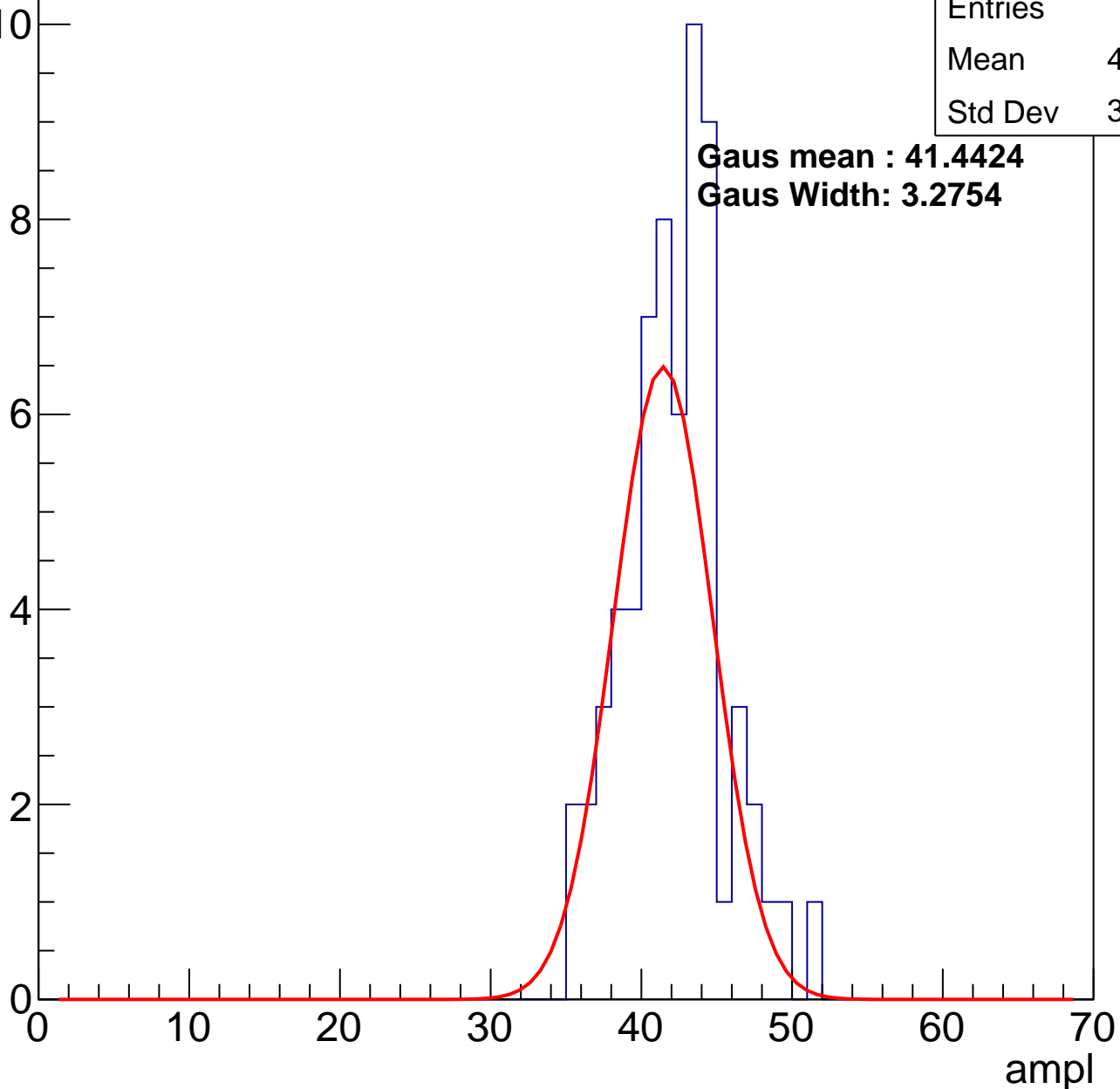
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	41.75
Std Dev	3.298

**Gaus mean : 41.4424**

**Gaus Width: 3.2754**

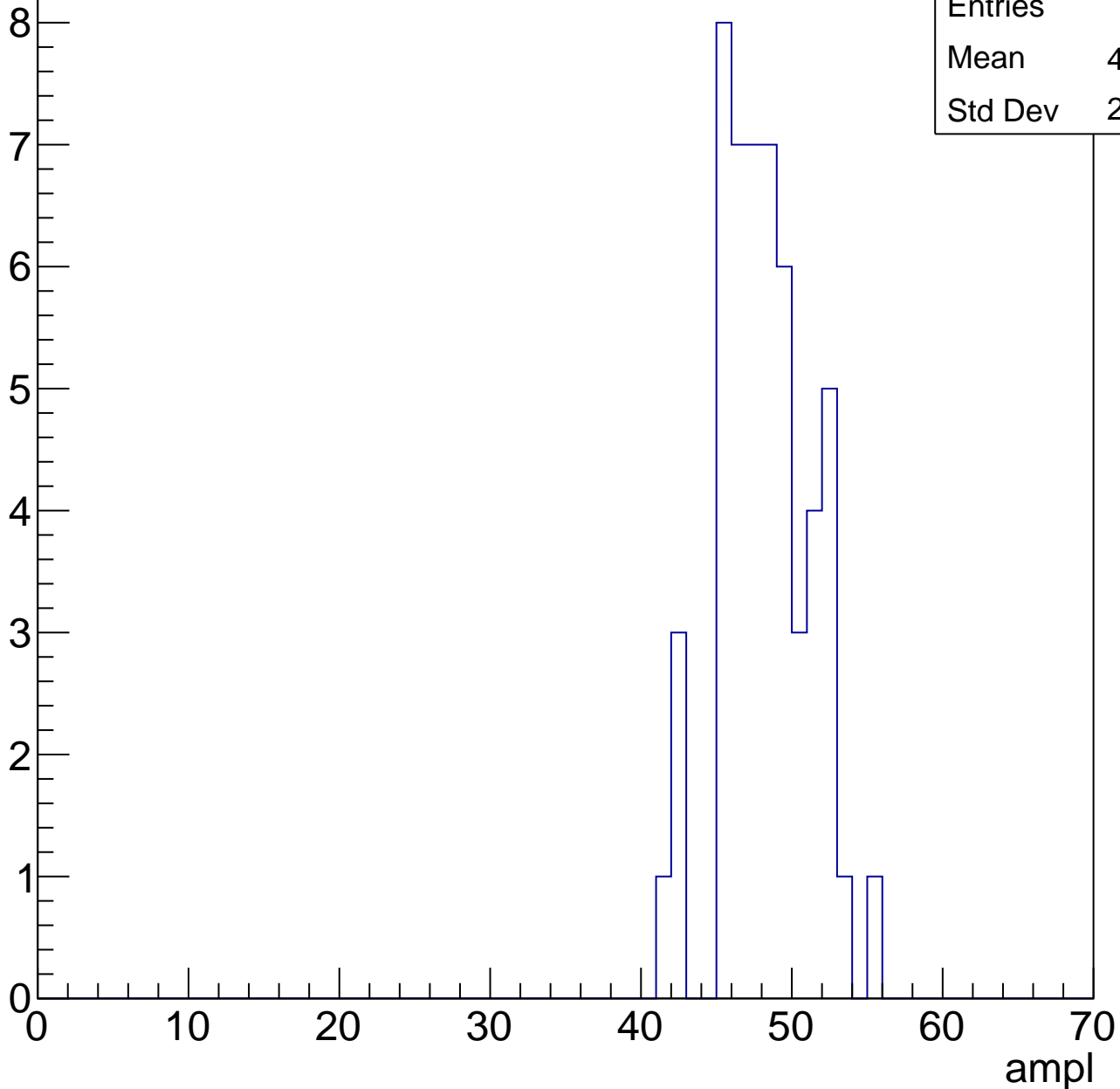


# B0L000S, U18-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	47.74
Std Dev	2.973

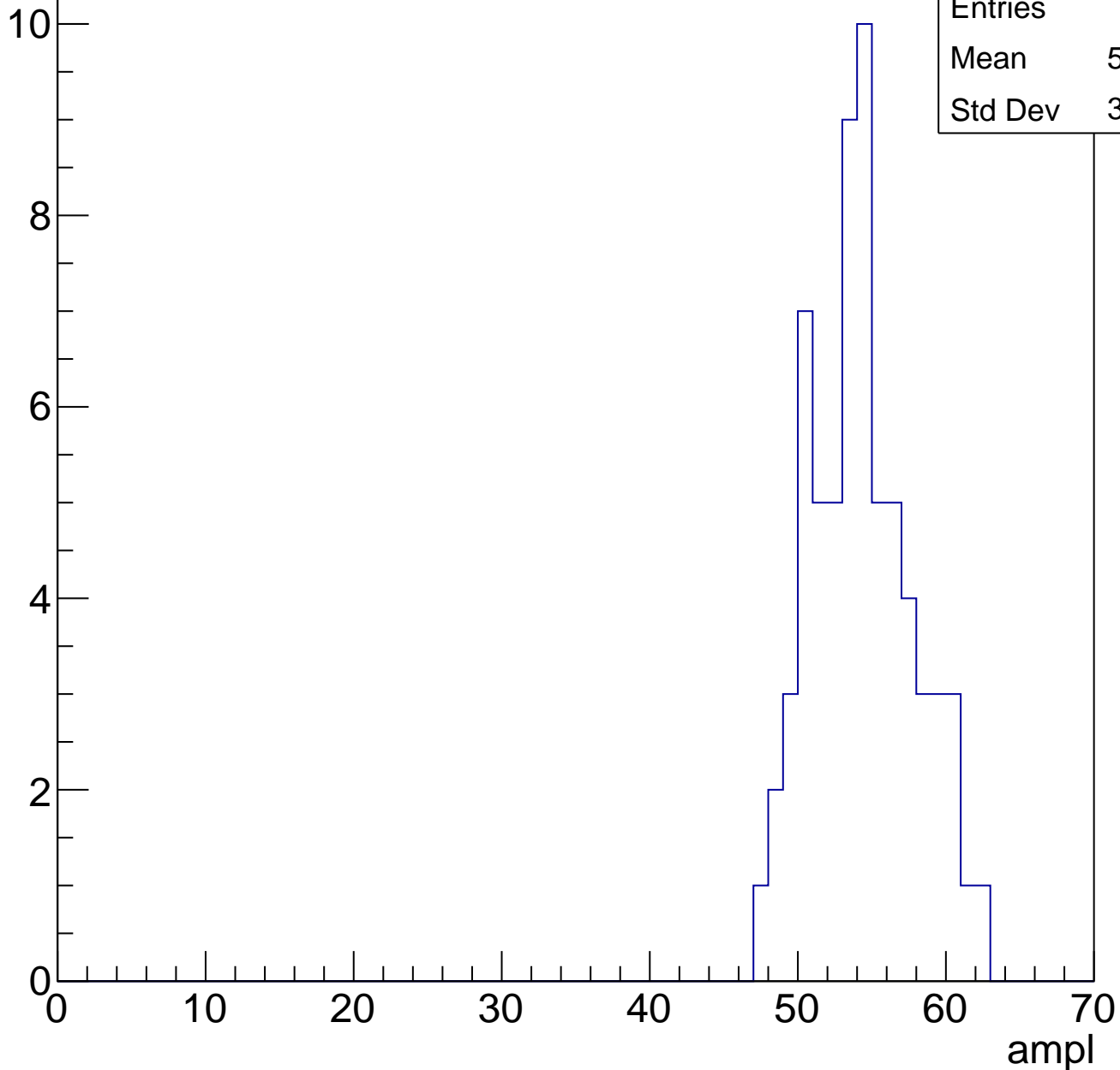


# B0L000S, U18-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	53.87
Std Dev	3.425

Entry

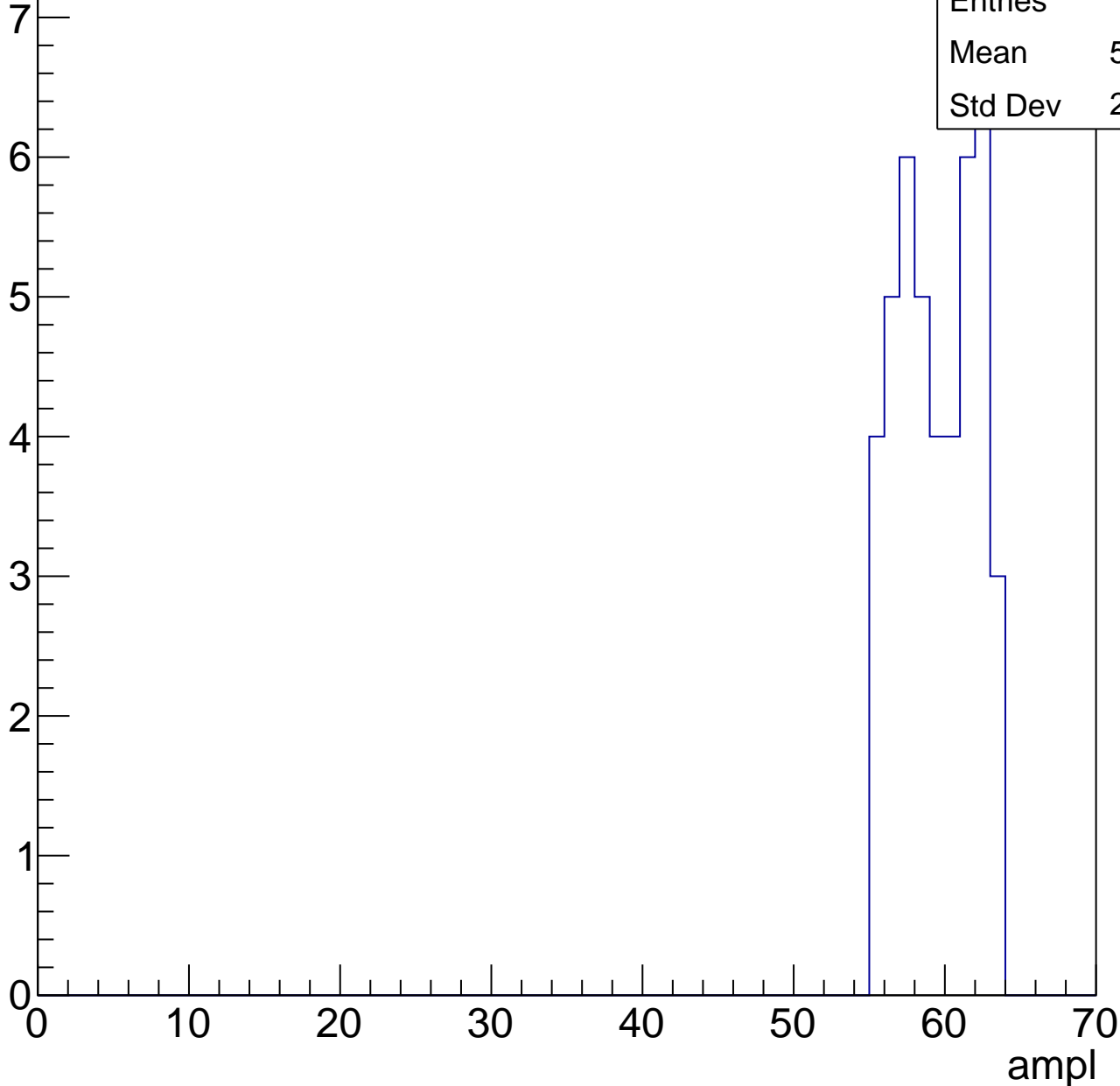


# B0L000S, U18-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	59.02
Std Dev	2.509

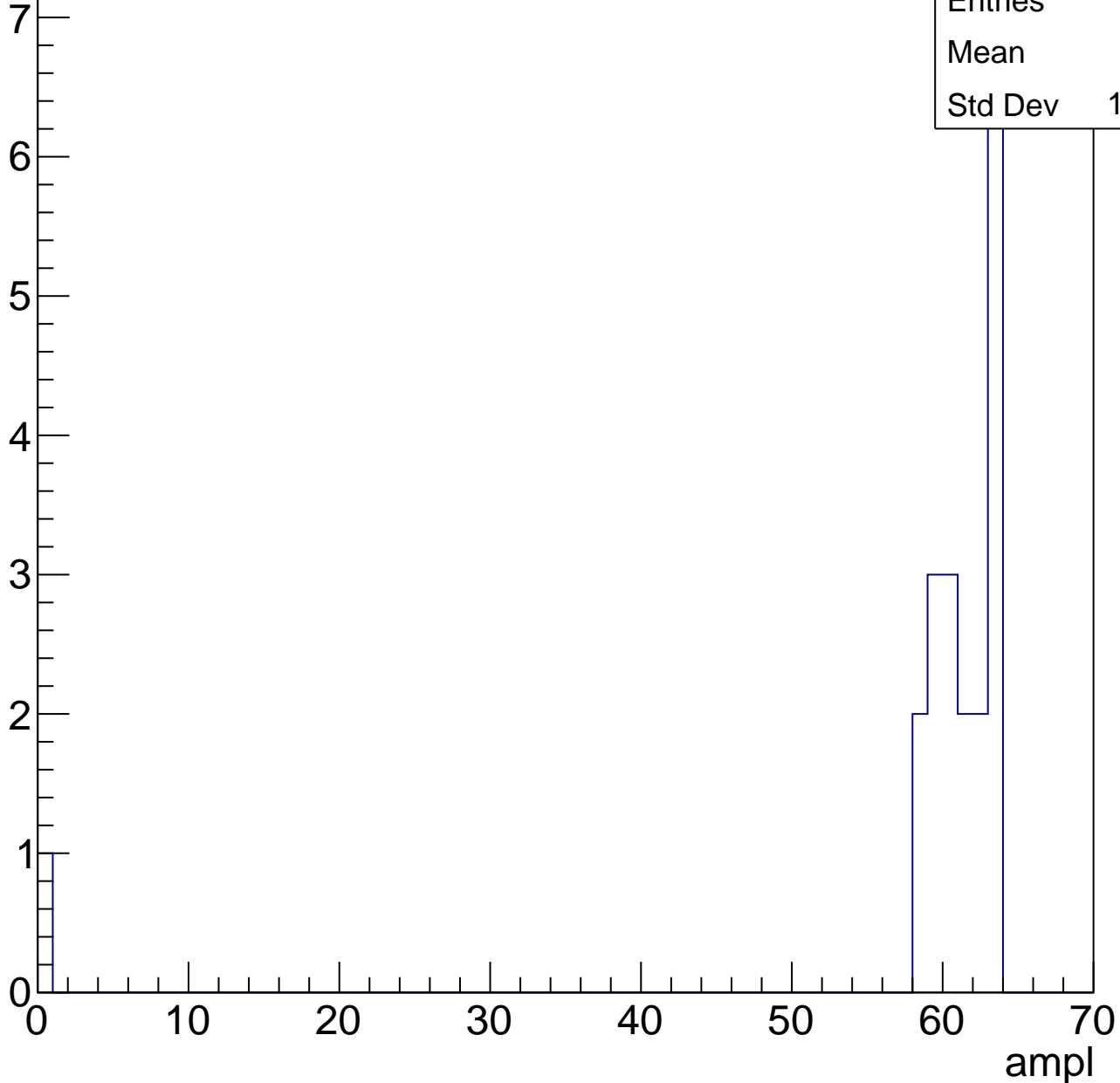


# B0L000S, U18-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	20
Mean	58
Std Dev	13.42





# B0L000S, U18-ch34, adc7

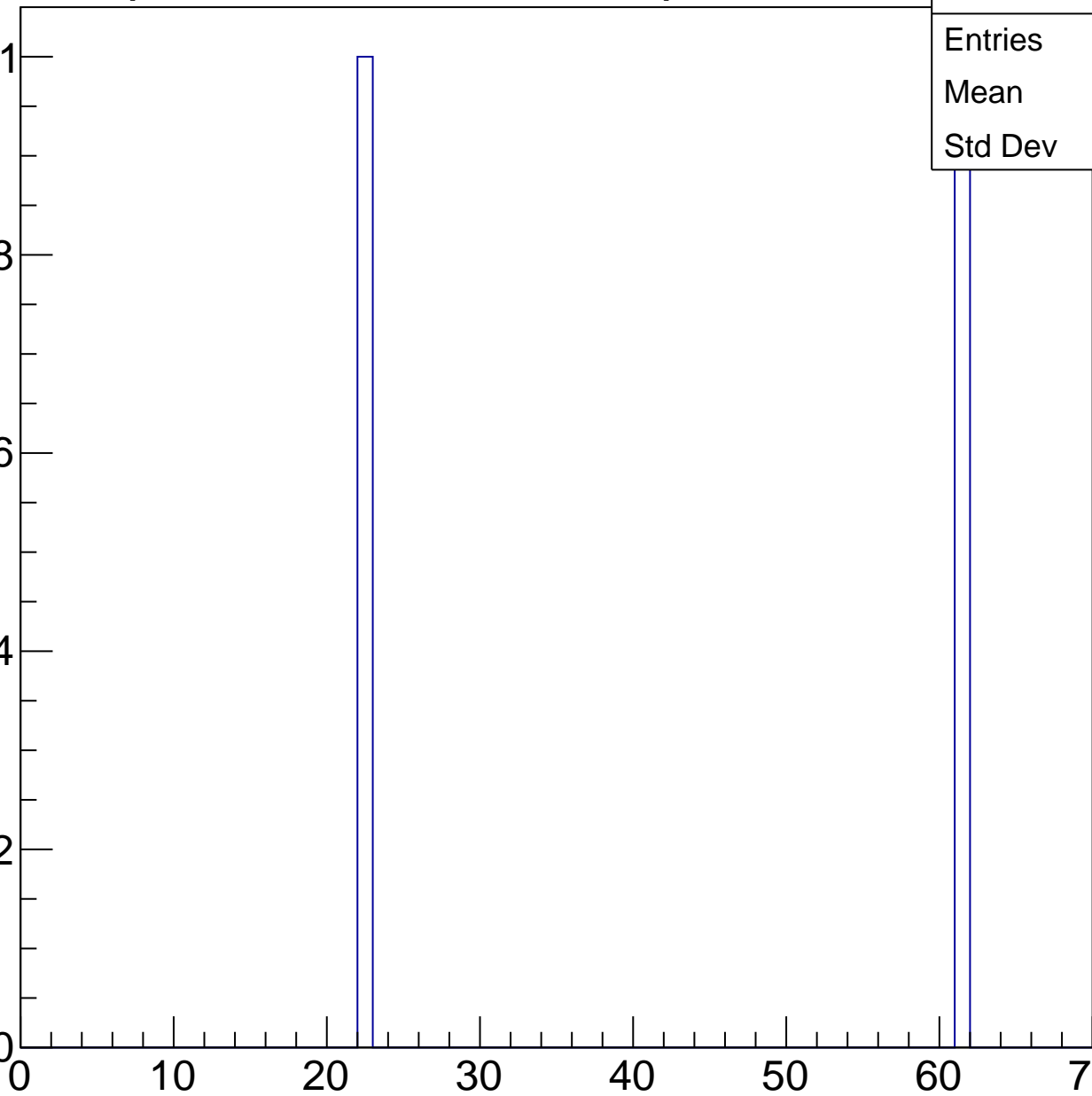
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	41.5
Std Dev	19.5

ampl



# B0L000S, U18-ch35, adc0

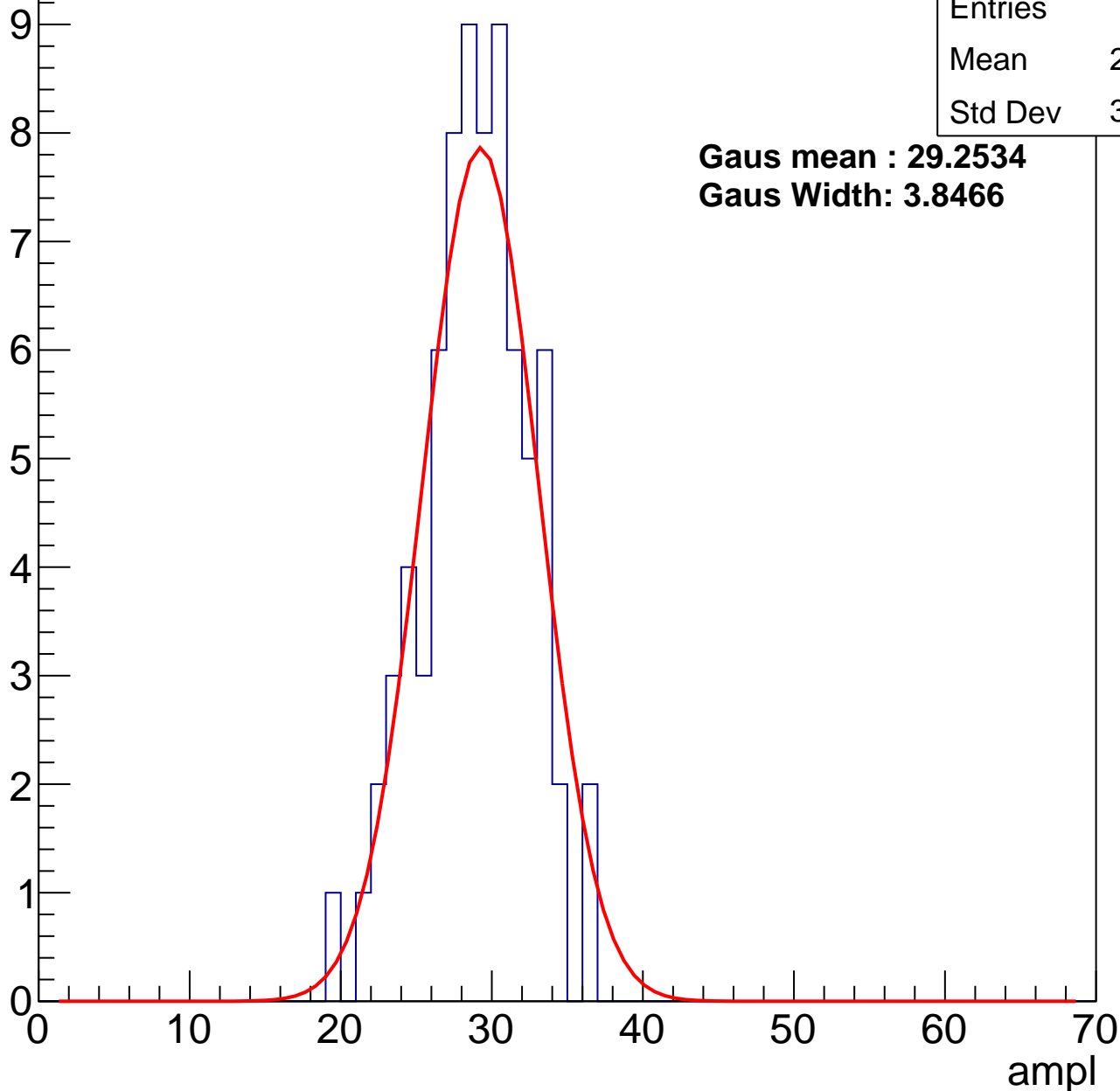
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	28.45
Std Dev	3.488

**Gaus mean : 29.2534**

**Gaus Width: 3.8466**



# B0L000S, U18-ch35, adc1

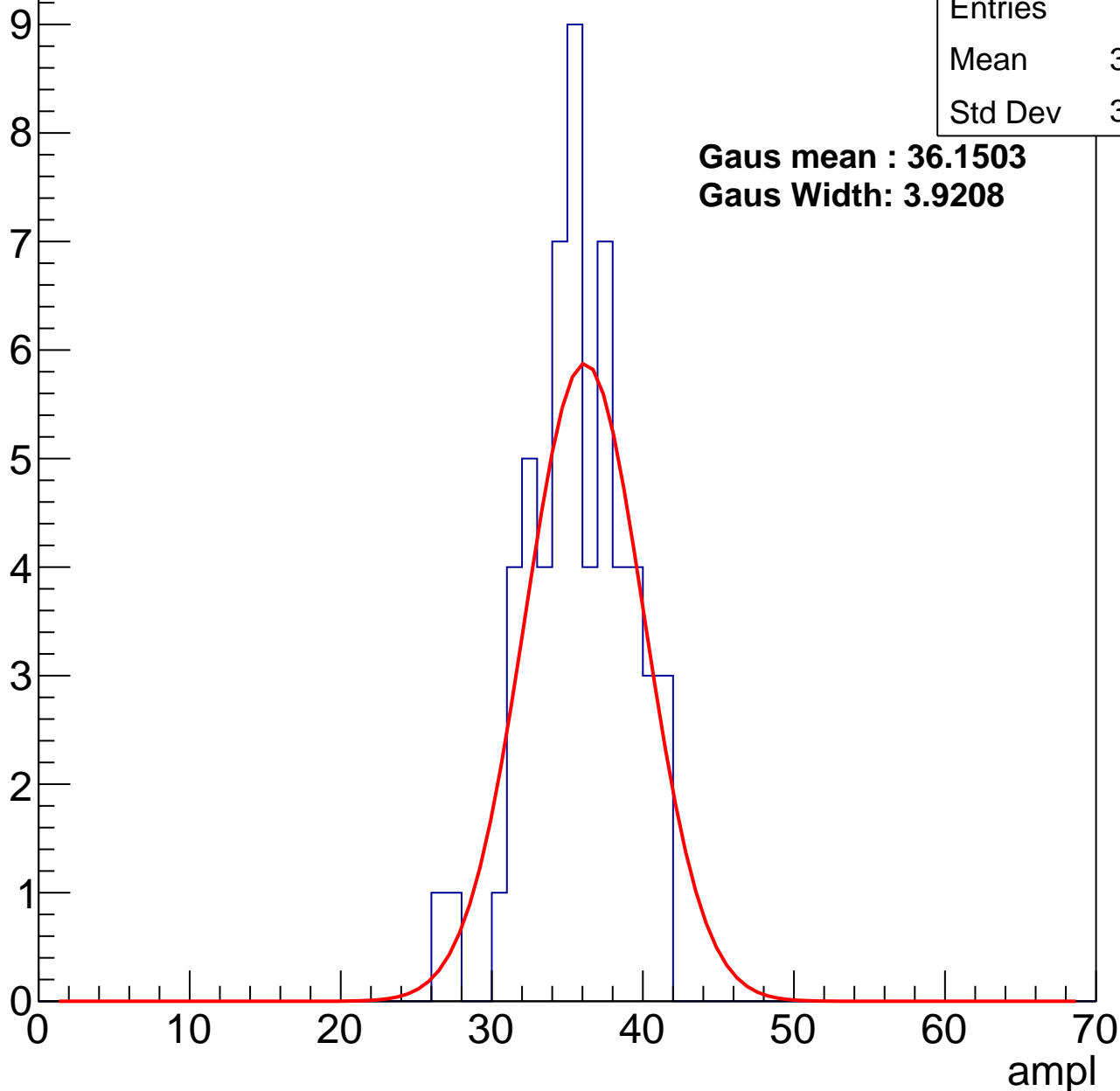
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.19
Std Dev	3.284

**Gaus mean : 36.1503**

**Gaus Width: 3.9208**



# B0L000S, U18-ch35, adc2

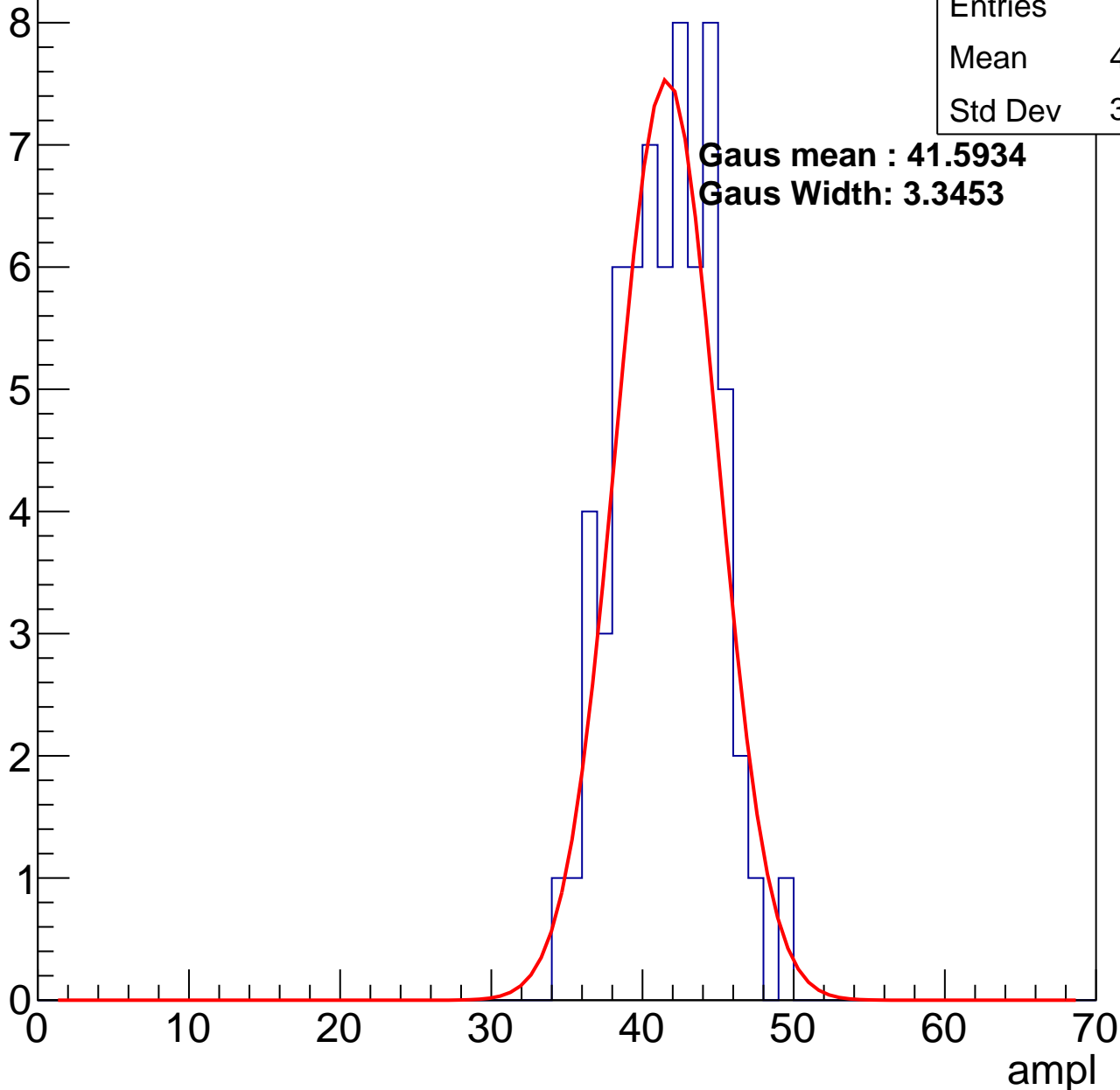
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.09
Std Dev	3.166

**Gaus mean : 41.5934**

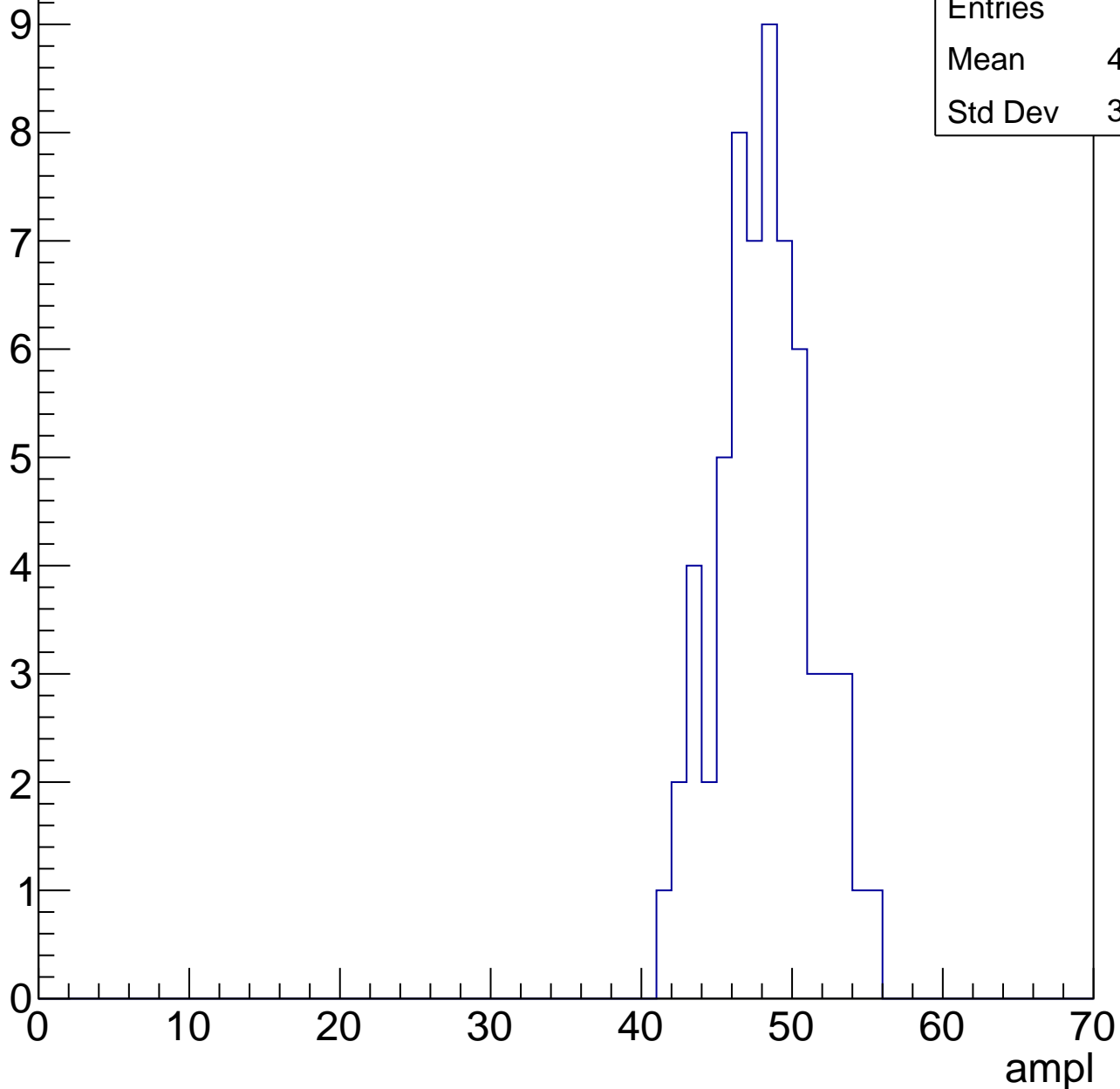
**Gaus Width: 3.3453**



# B0L000S, U18-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

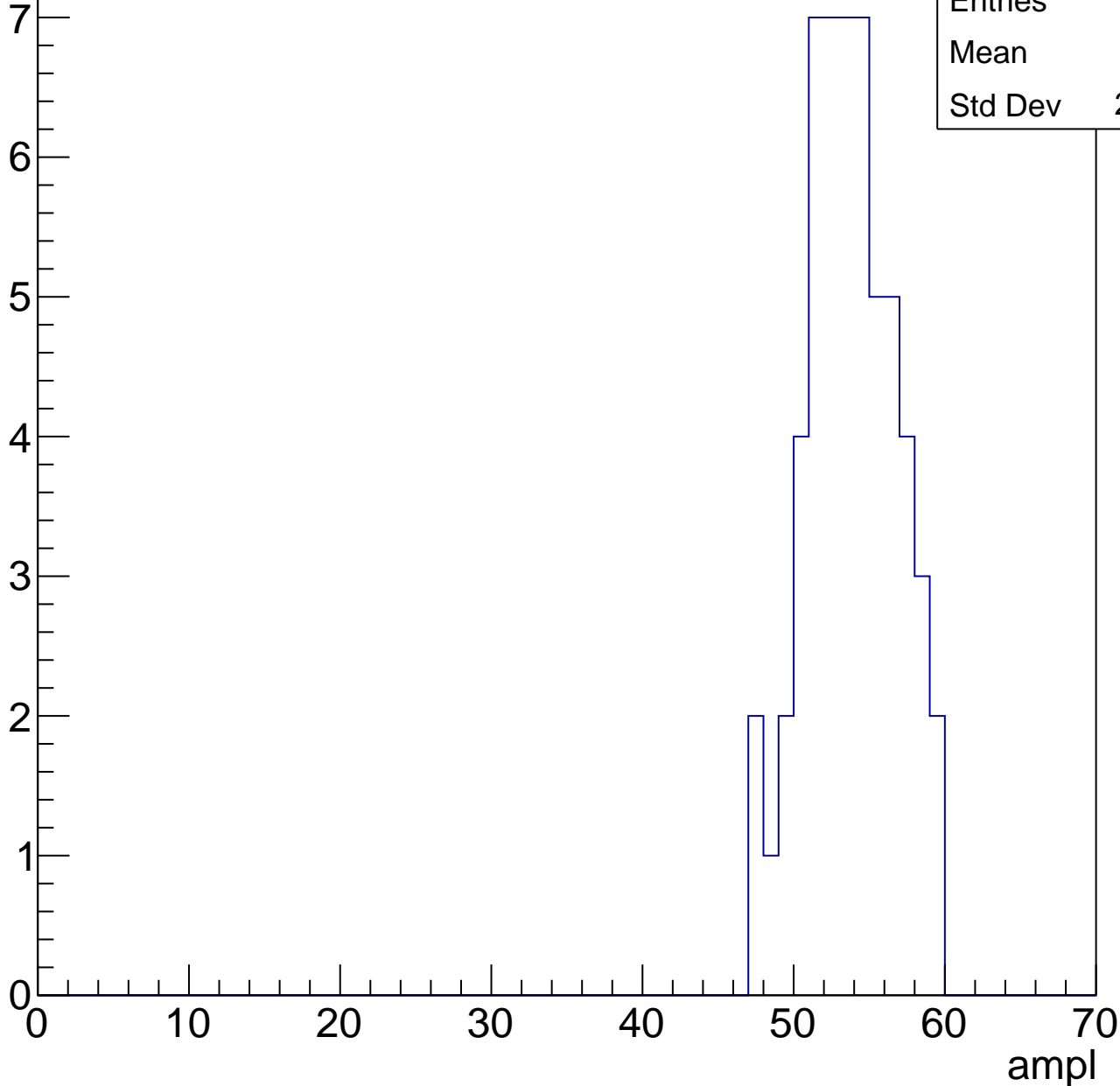


# B0L000S, U18-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	53.3
Std Dev	2.921



# B0L000S, U18-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

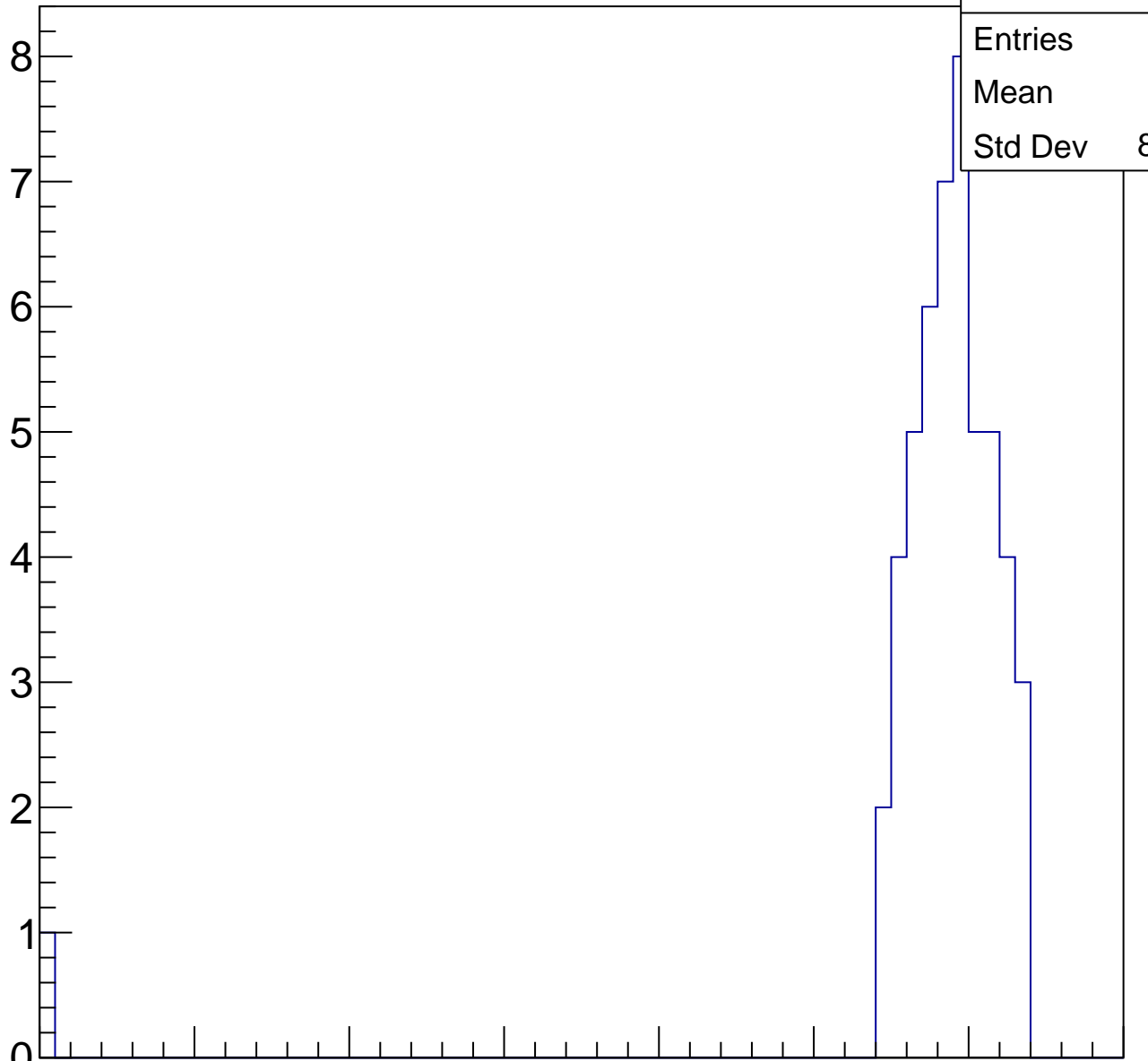
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	50
Mean	57.4
Std Dev	8.546

ampl

0 10 20 30 40 50 60 70

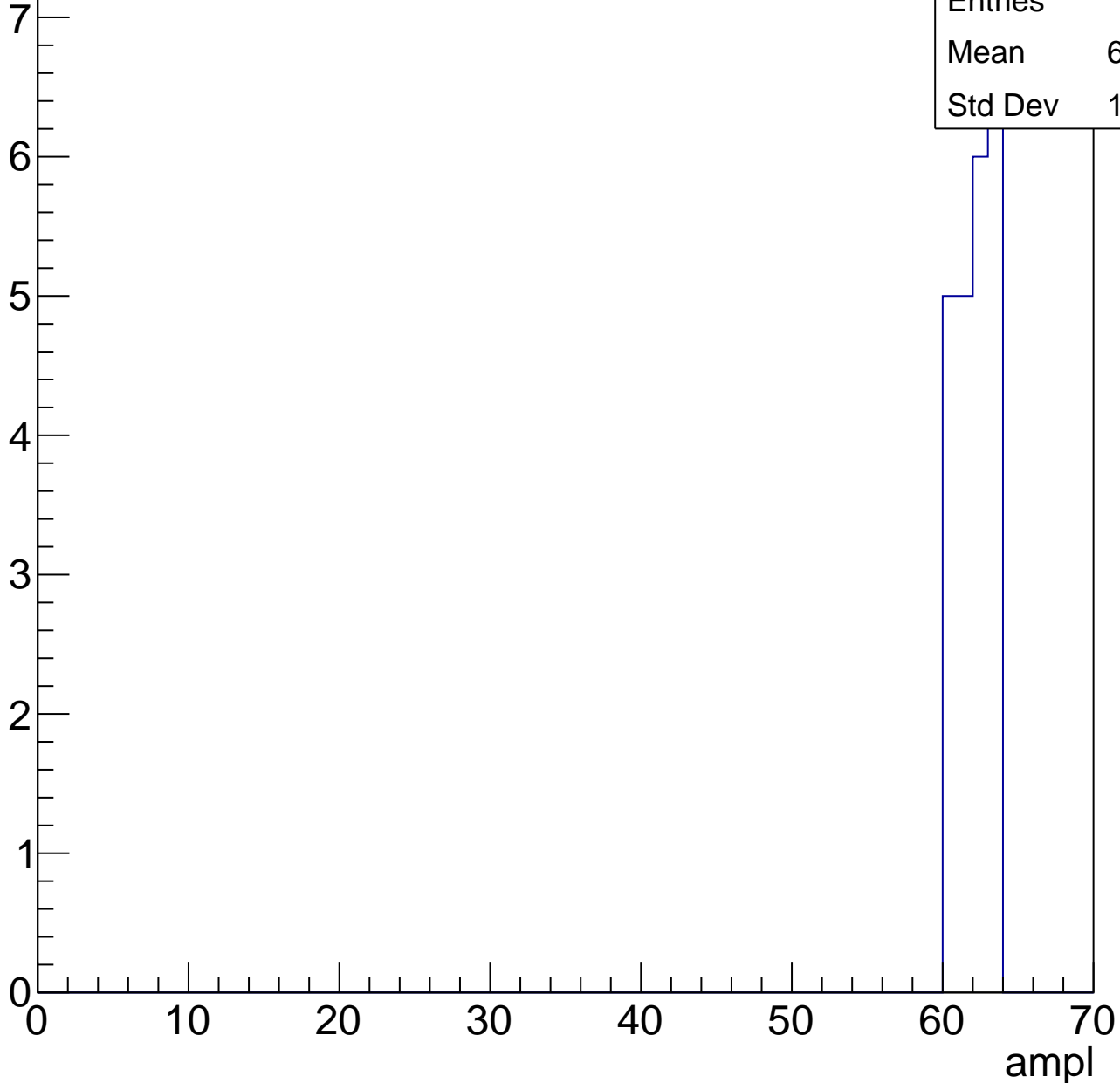


# B0L000S, U18-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	61.65
Std Dev	1.127





# B0L000S, U18-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch36, adc0

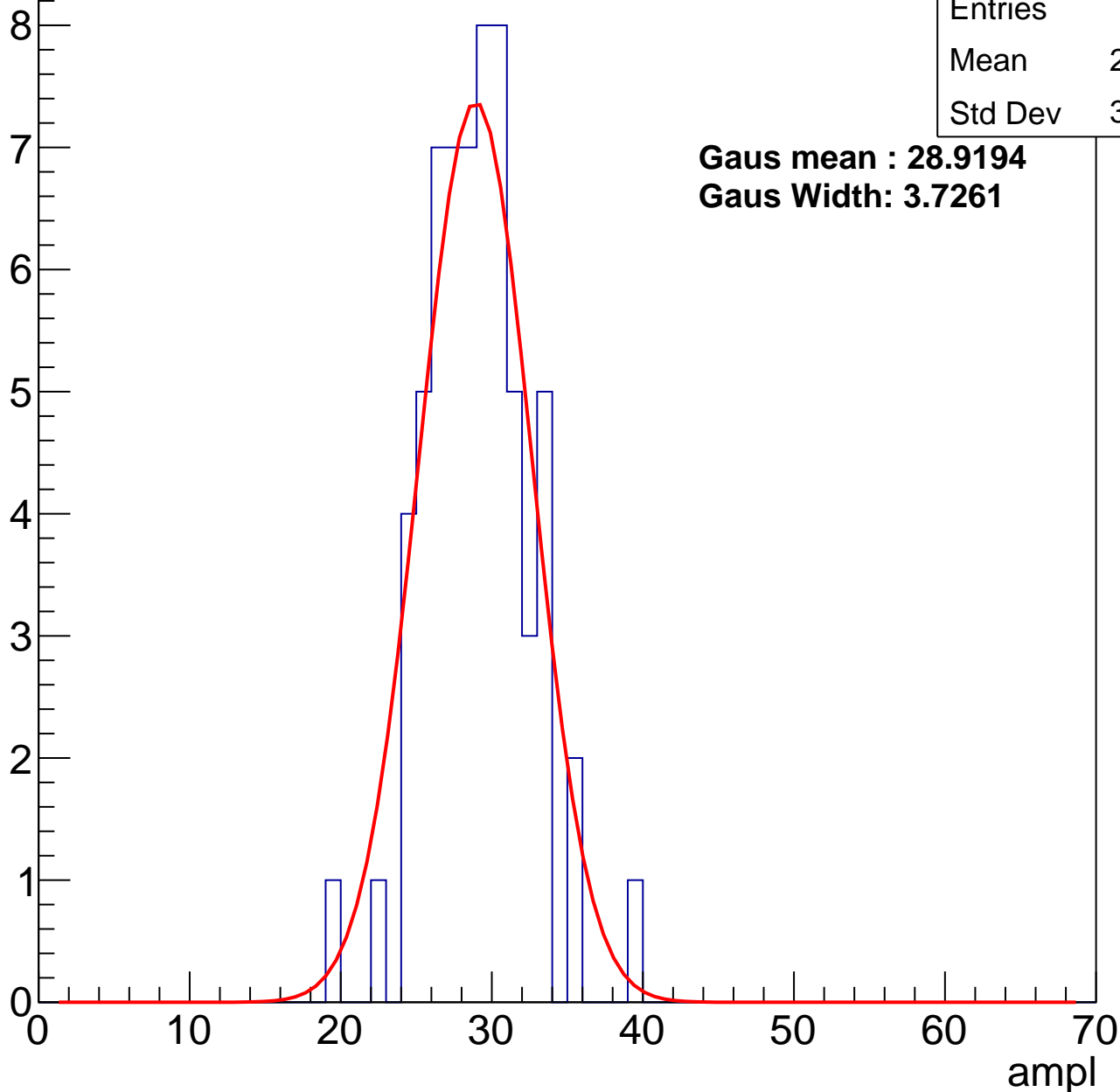
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	28.53
Std Dev	3.354

**Gaus mean : 28.9194**

**Gaus Width: 3.7261**



# B0L000S, U18-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	34.81
Std Dev	3.307

**Gaus mean : 34.6905**

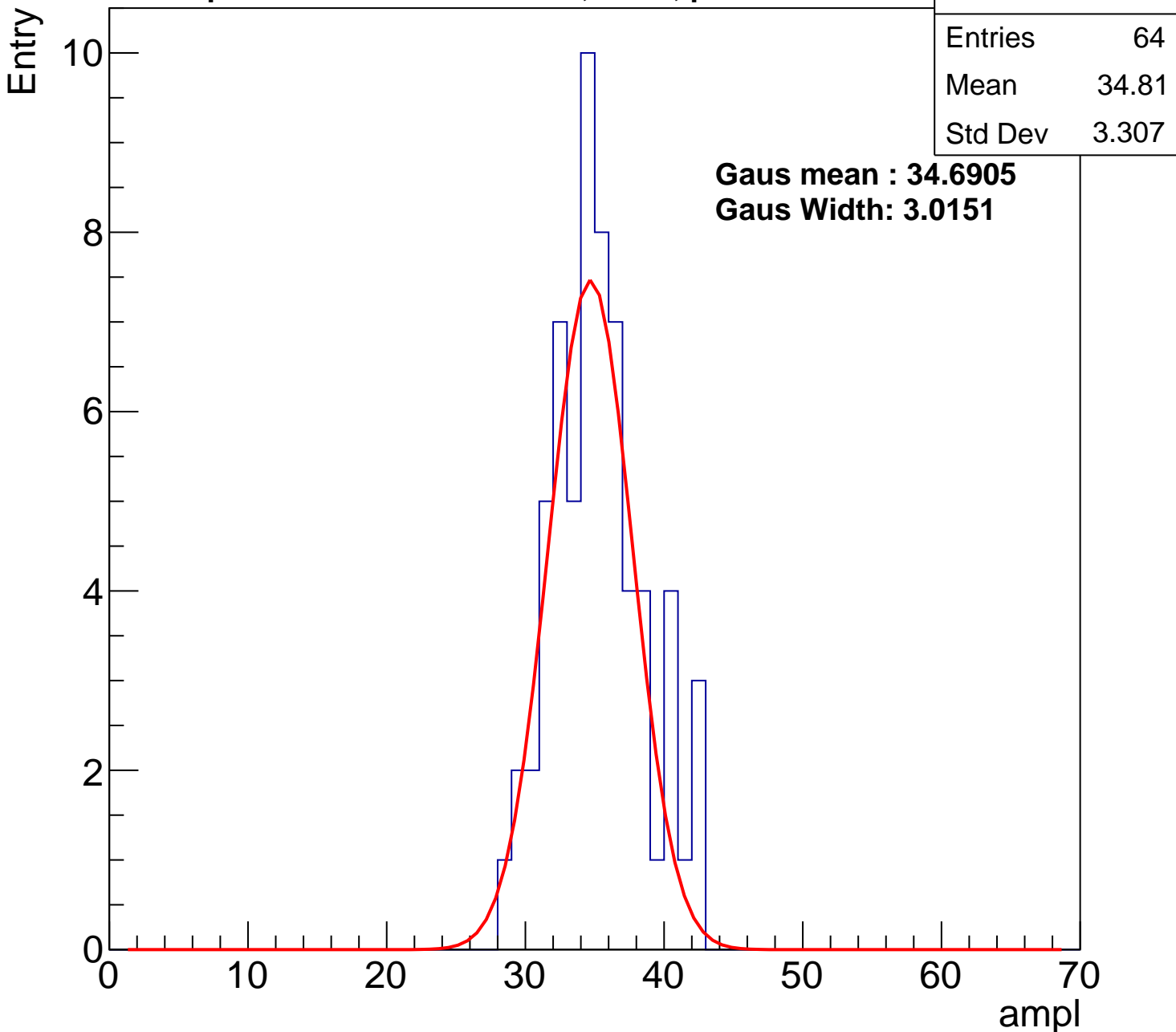
**Gaus Width: 3.0151**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch36, adc2

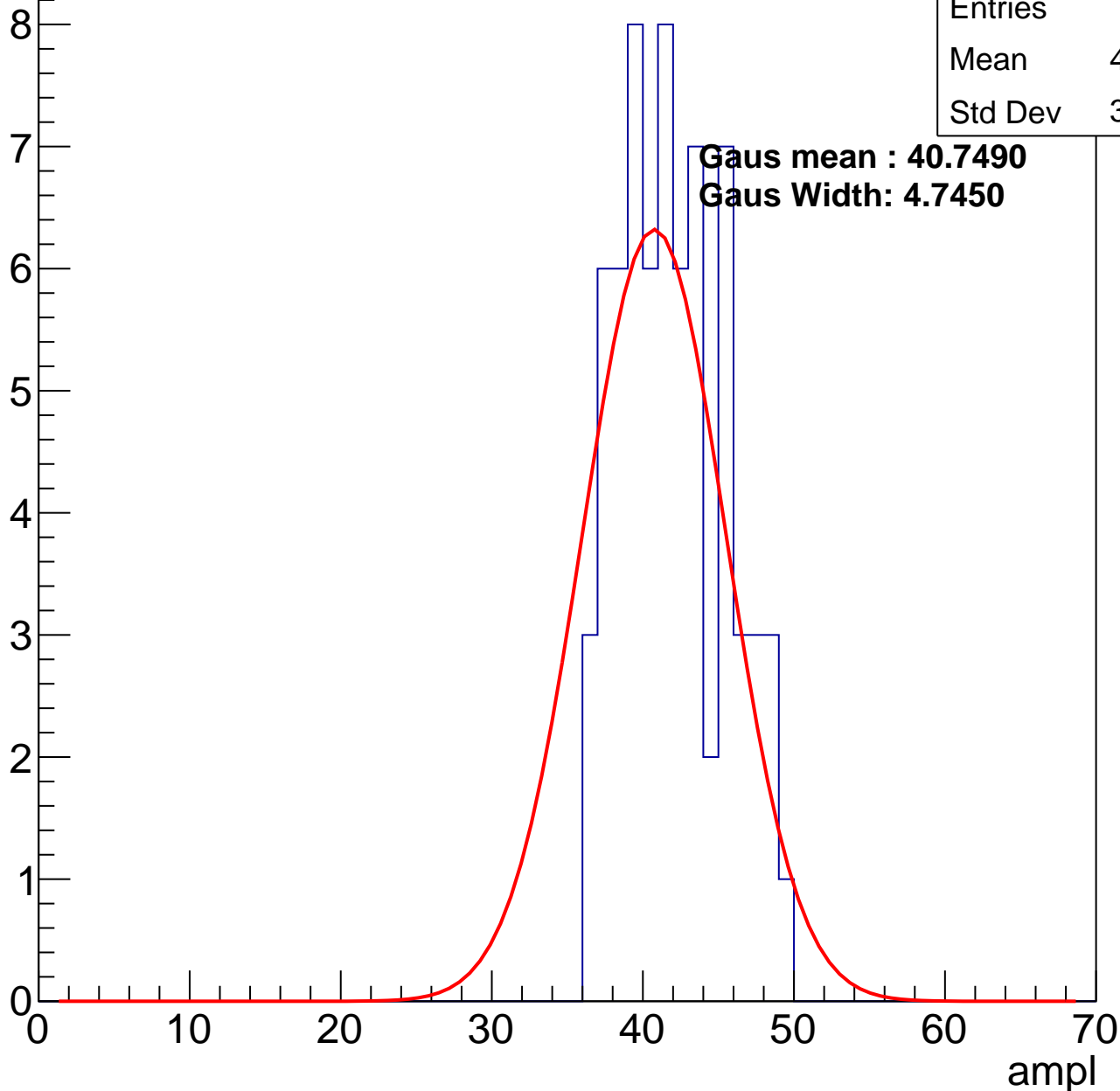
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	41.54
Std Dev	3.407

**Gaus mean : 40.7490**

**Gaus Width: 4.7450**

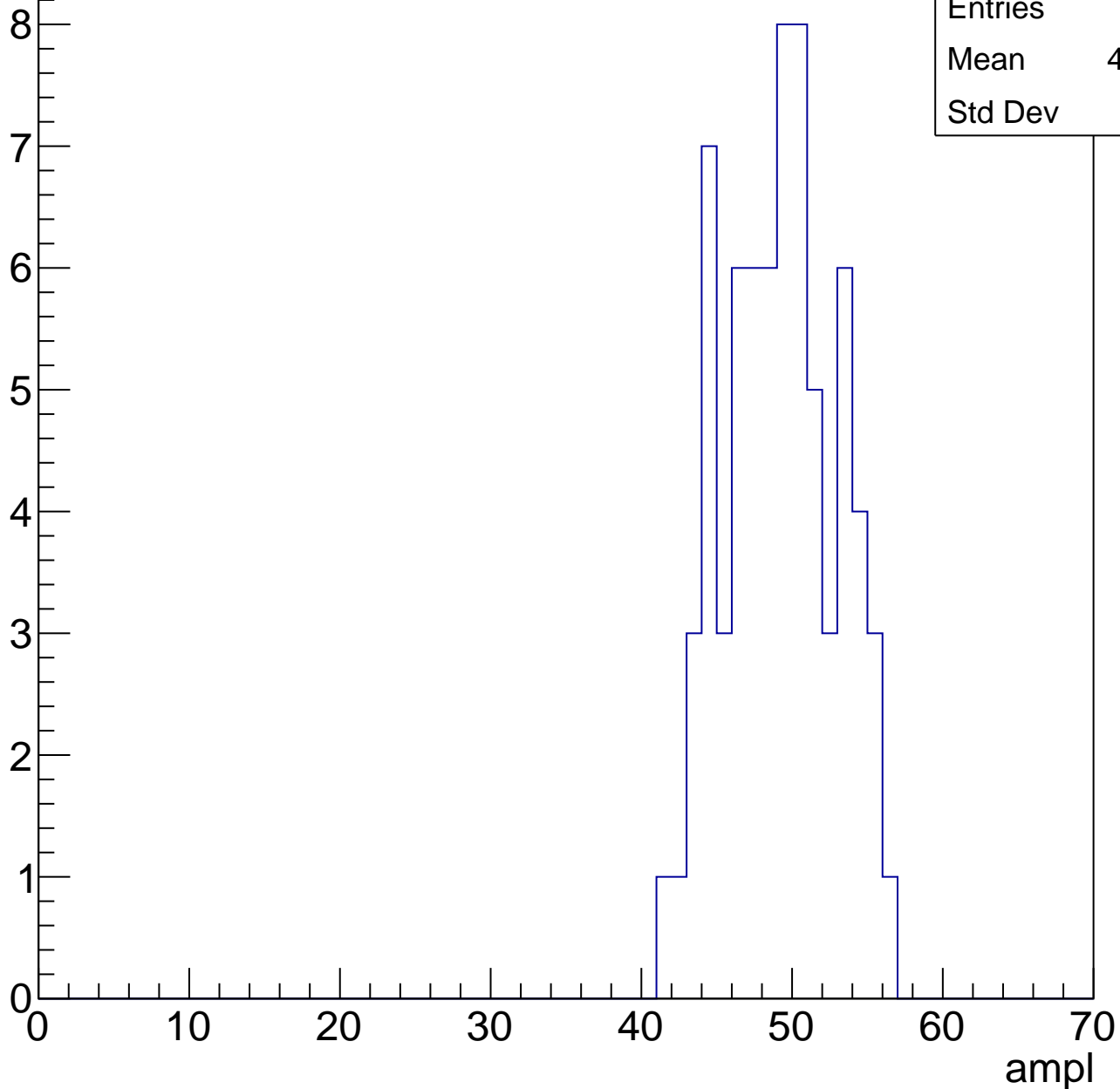


# B0L000S, U18-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.72
Std Dev	3.62

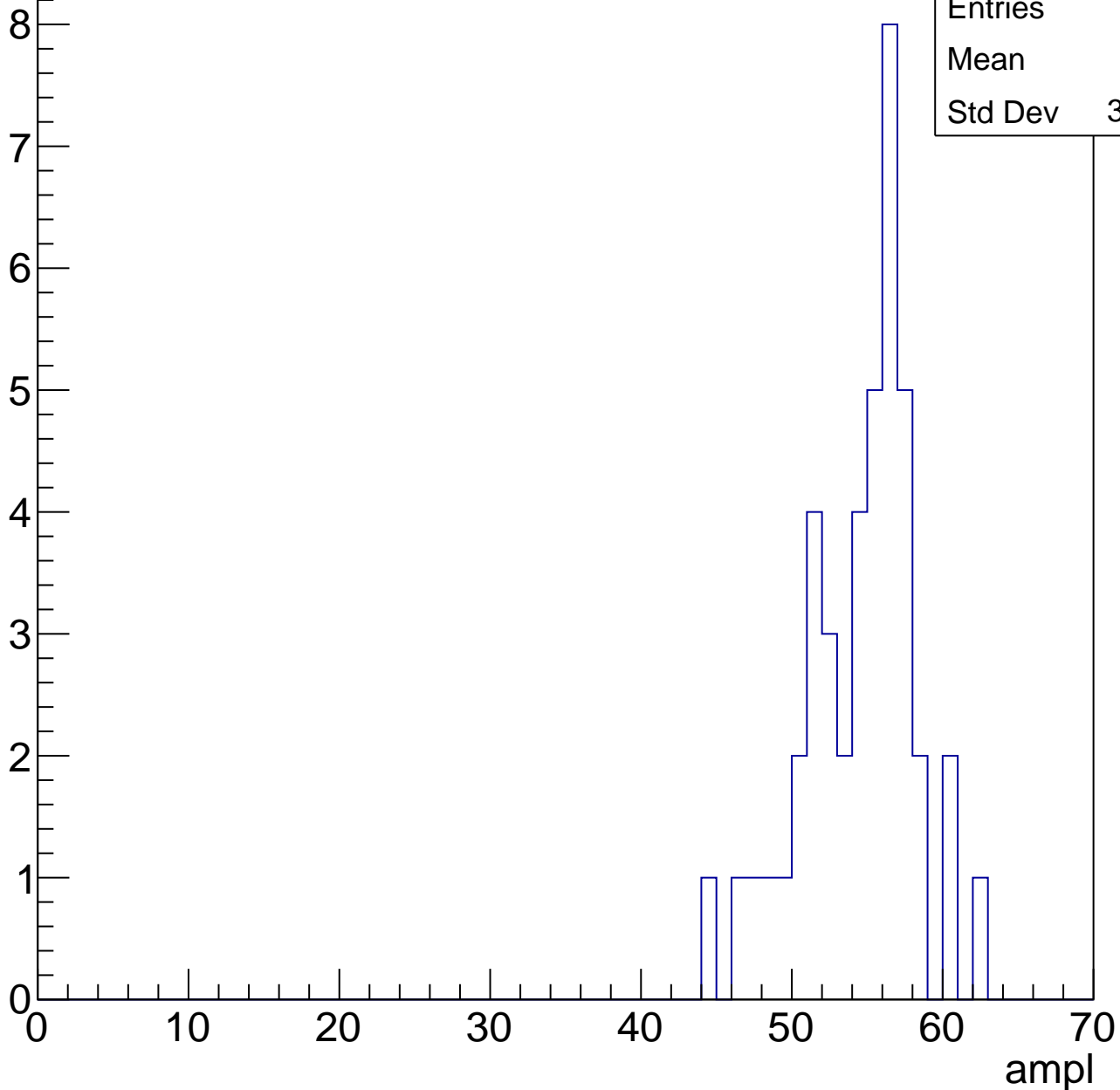


# B0L000S, U18-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

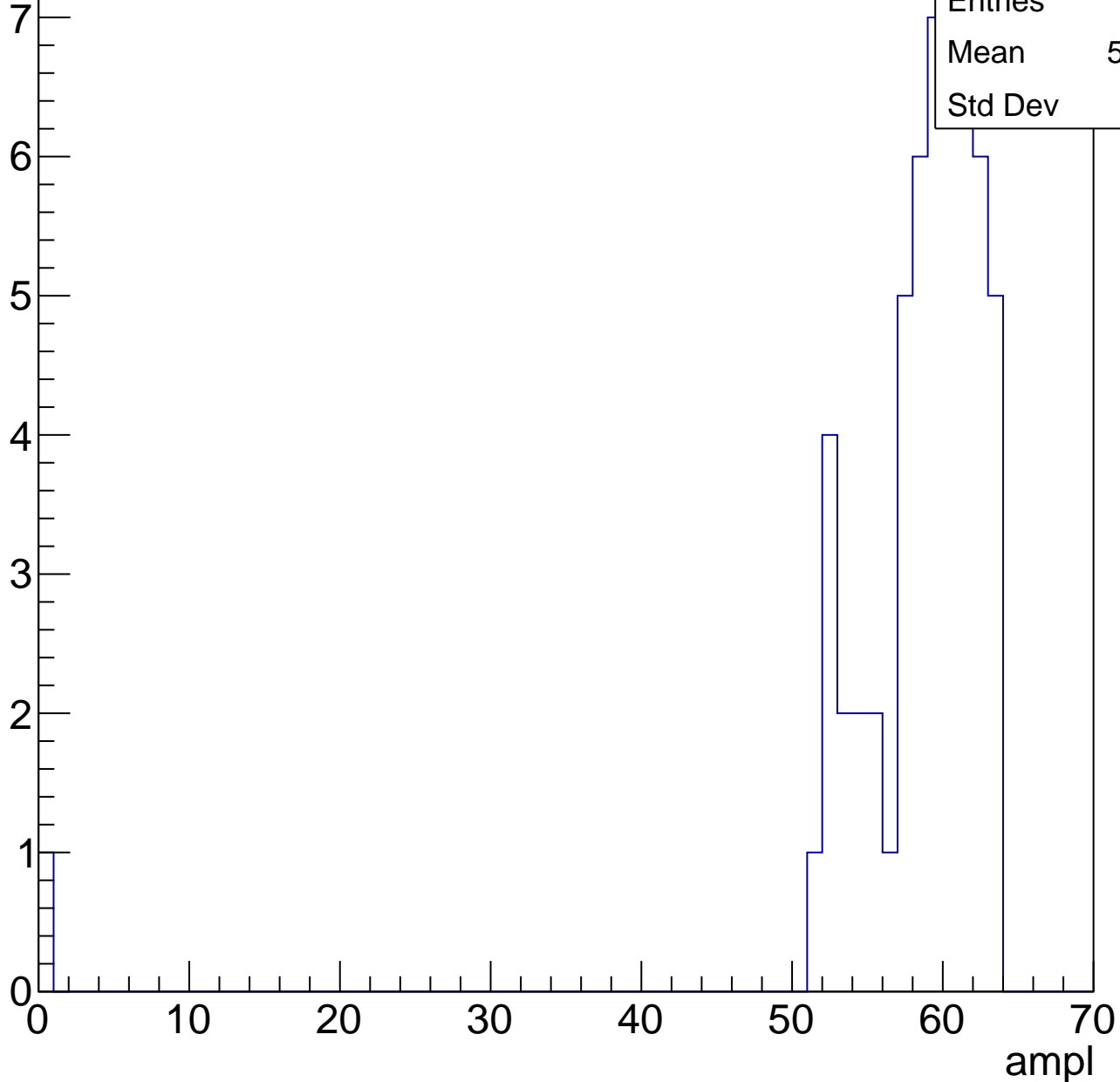
Entries	43
Mean	54
Std Dev	3.754



# B0L000S, U18-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

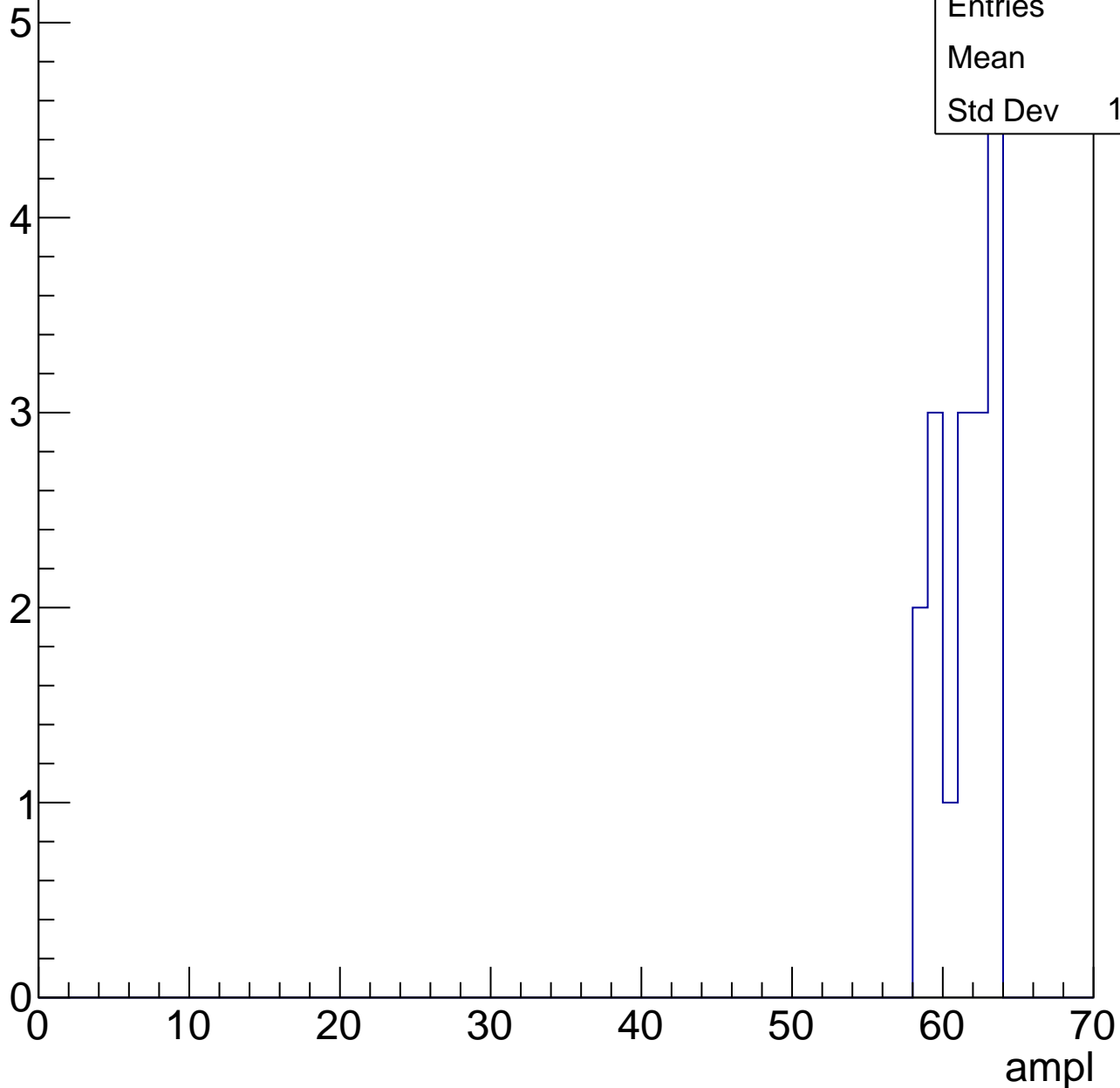
Entry



# B0L000S, U18-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch37, adc0

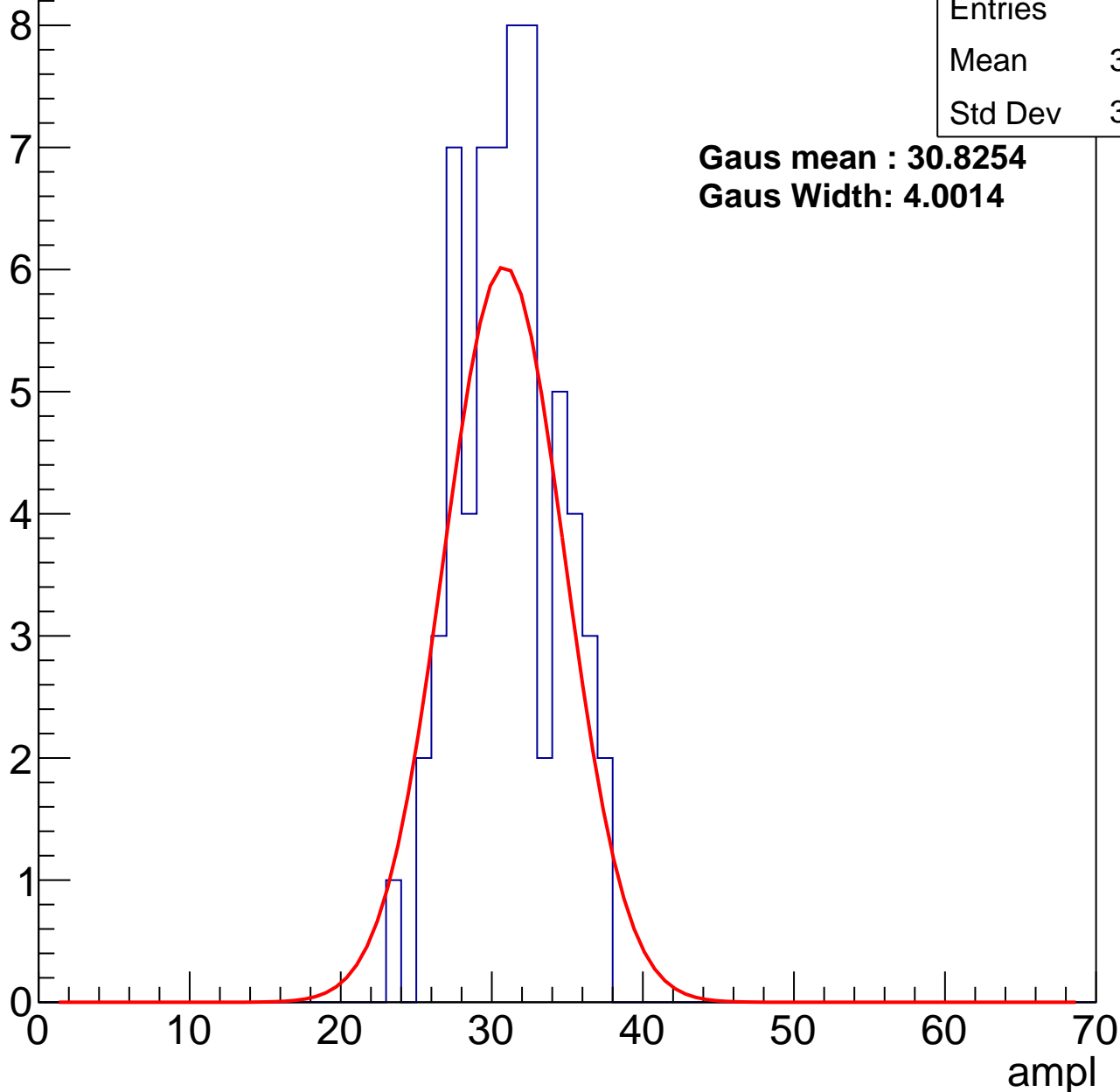
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	30.59
Std Dev	3.225

**Gaus mean : 30.8254**

**Gaus Width: 4.0014**



# B0L000S, U18-ch37, adc1

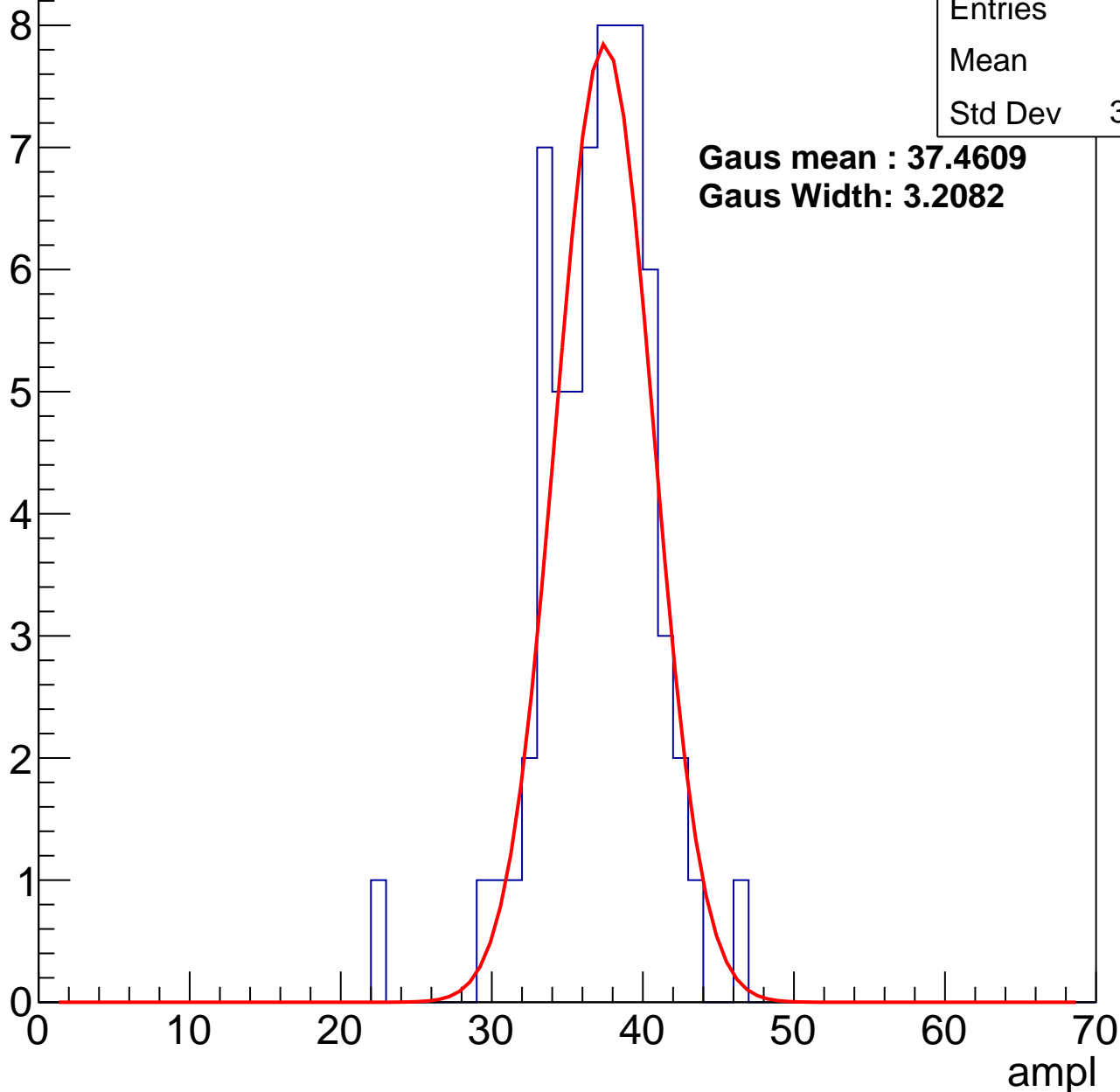
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	36.6
Std Dev	3.678

**Gaus mean : 37.4609**

**Gaus Width: 3.2082**



# B0L000S, U18-ch37, adc2

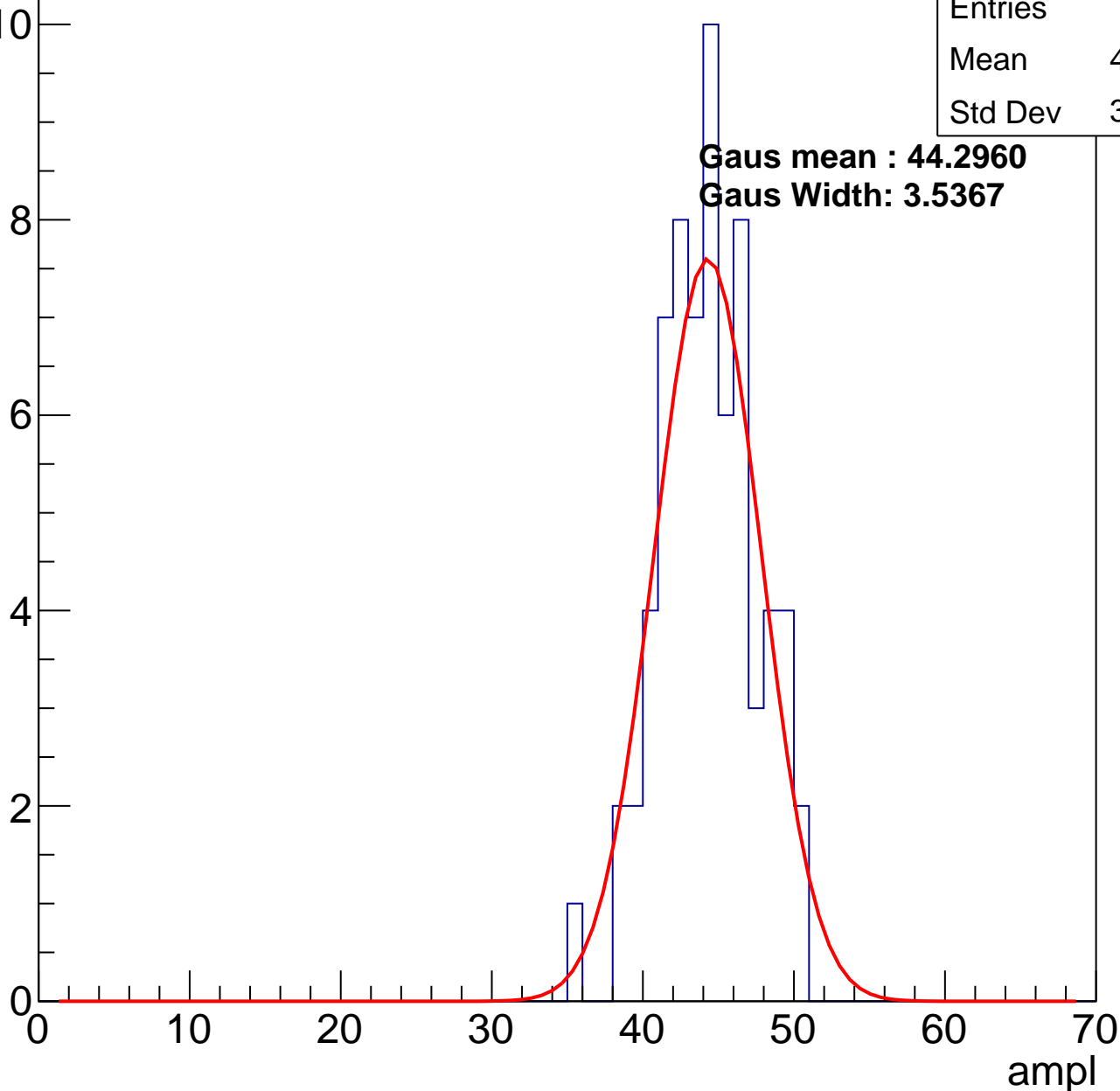
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.82
Std Dev	3.134

**Gaus mean : 44.2960**

**Gaus Width: 3.5367**



# B0L000S, U18-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	51.3
Std Dev	3.686

Entry

10

8

6

4

2

0

0

10

20

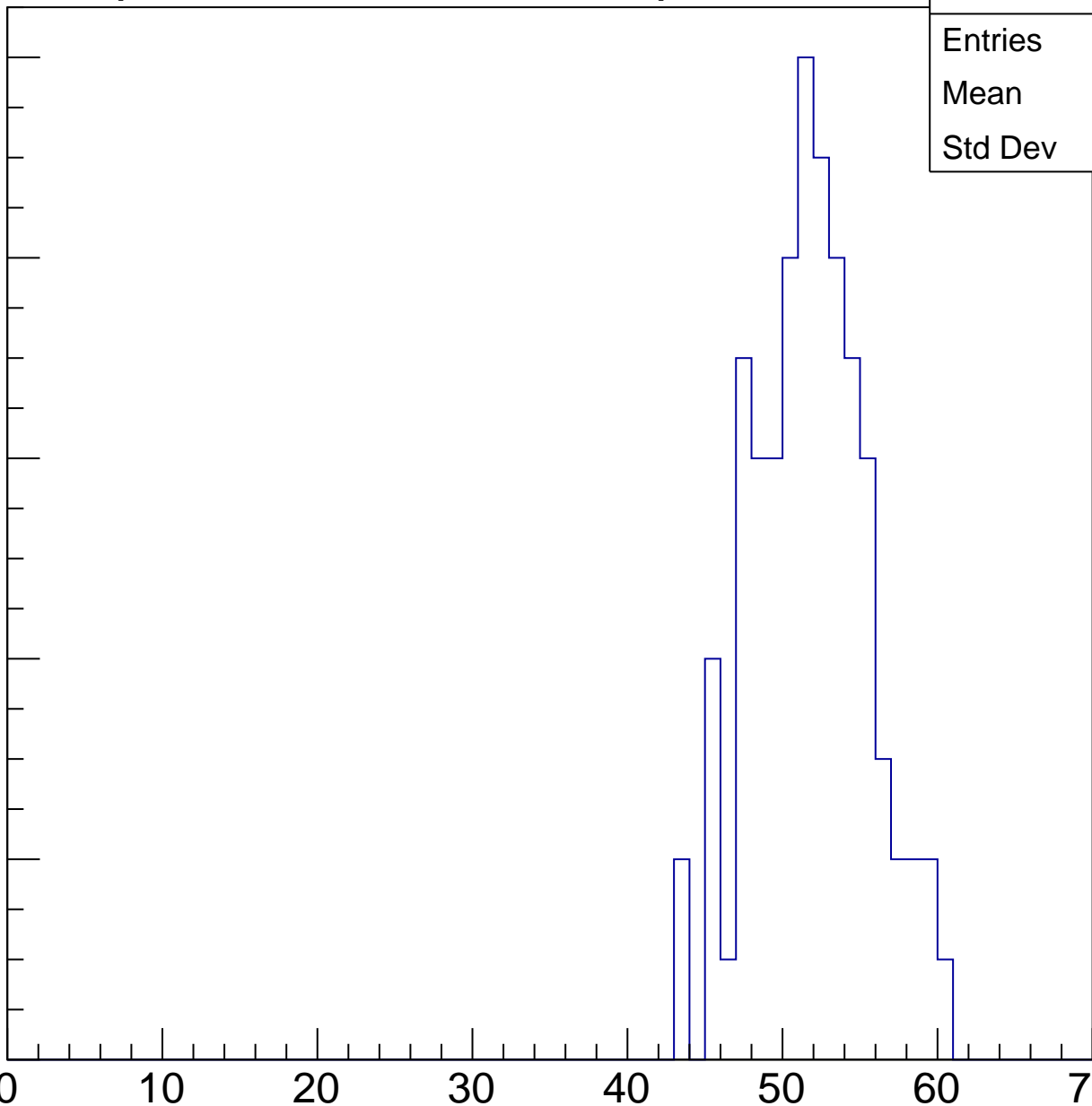
30

40

50

60

ampl

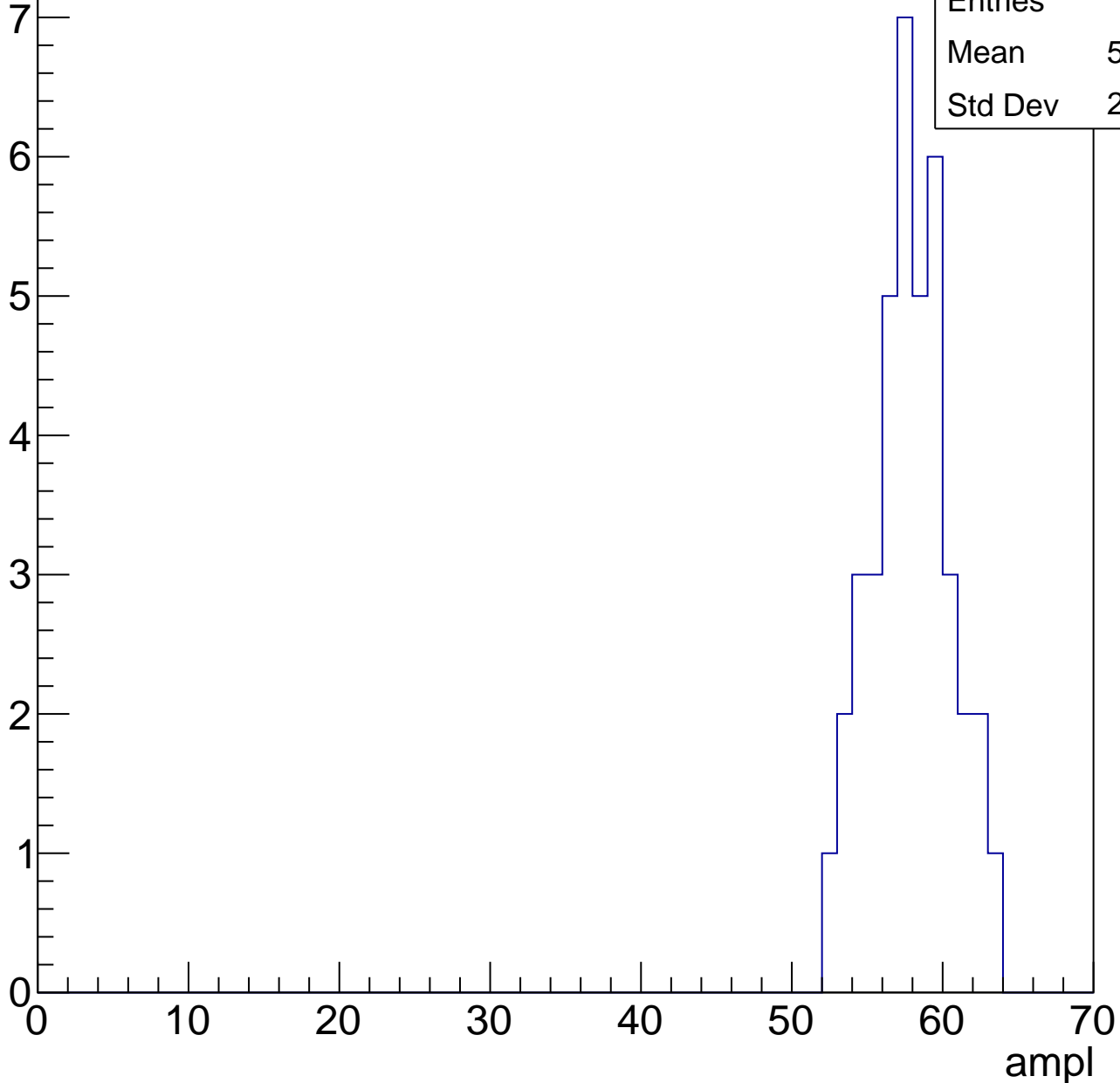


# B0L000S, U18-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	57.42
Std Dev	2.587

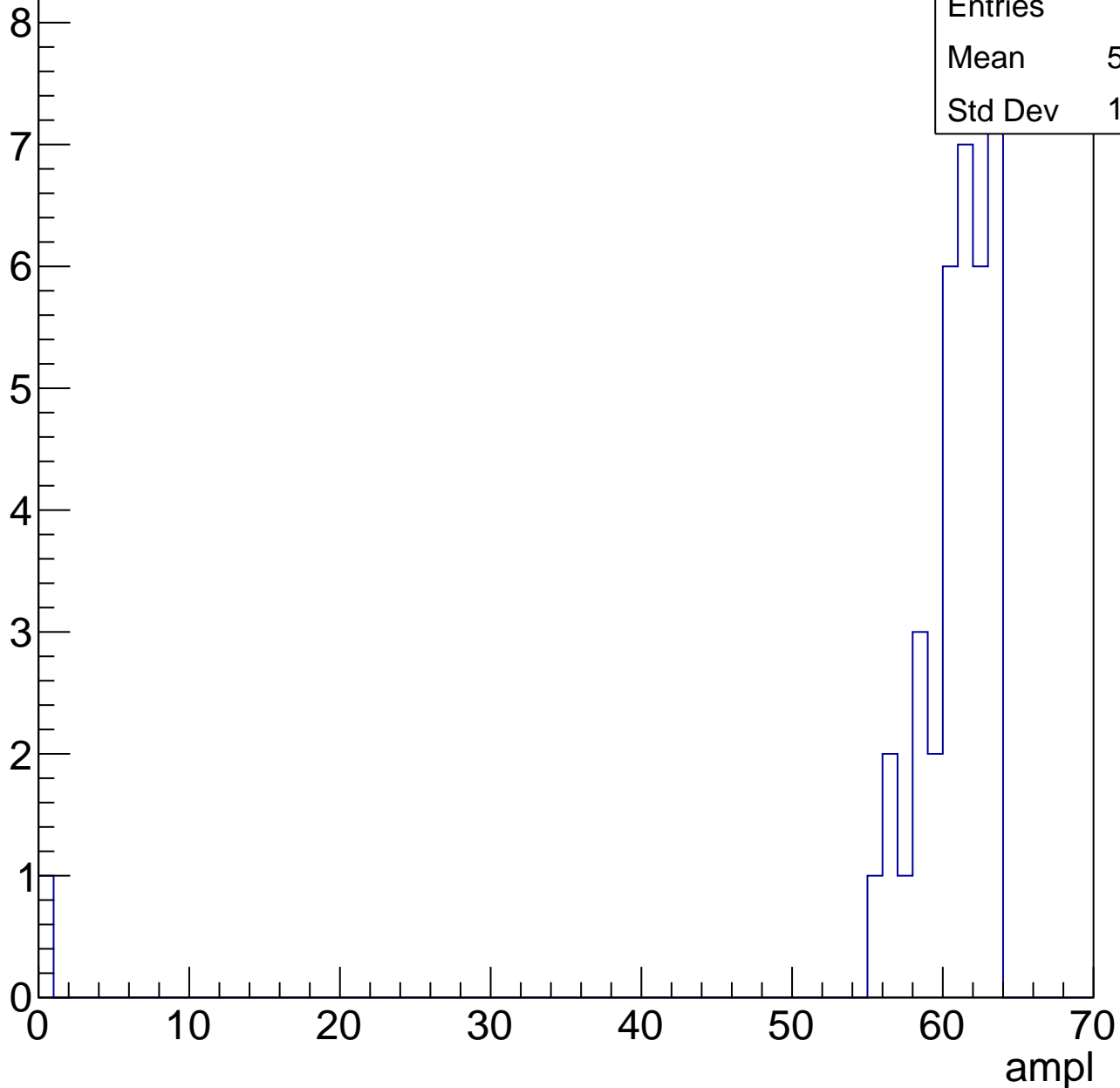


# B0L000S, U18-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.89
Std Dev	10.05



# B0L000S, U18-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch38, adc0

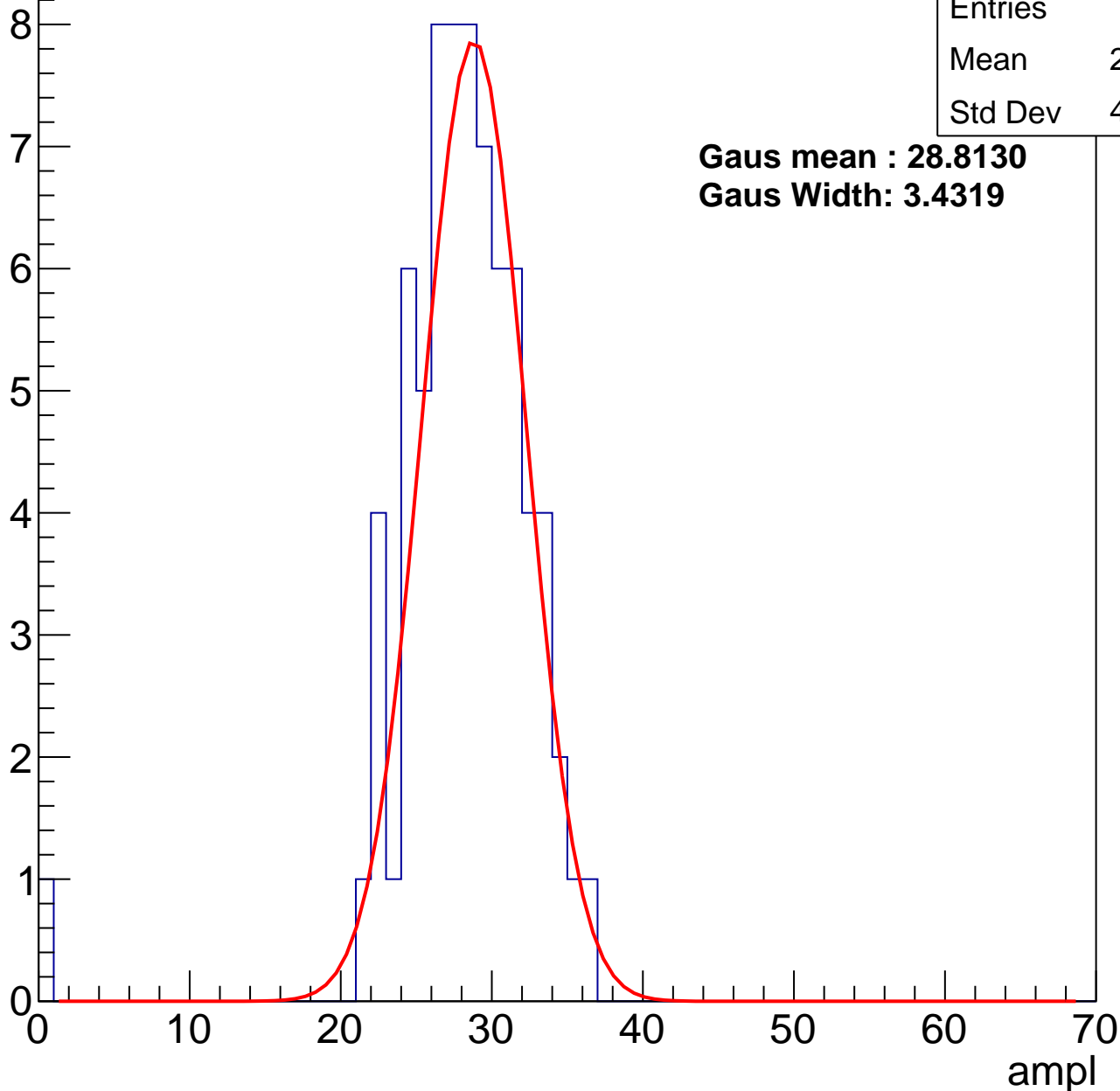
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	27.63
Std Dev	4.692

**Gaus mean : 28.8130**

**Gaus Width: 3.4319**



# B0L000S, U18-ch38, adc1

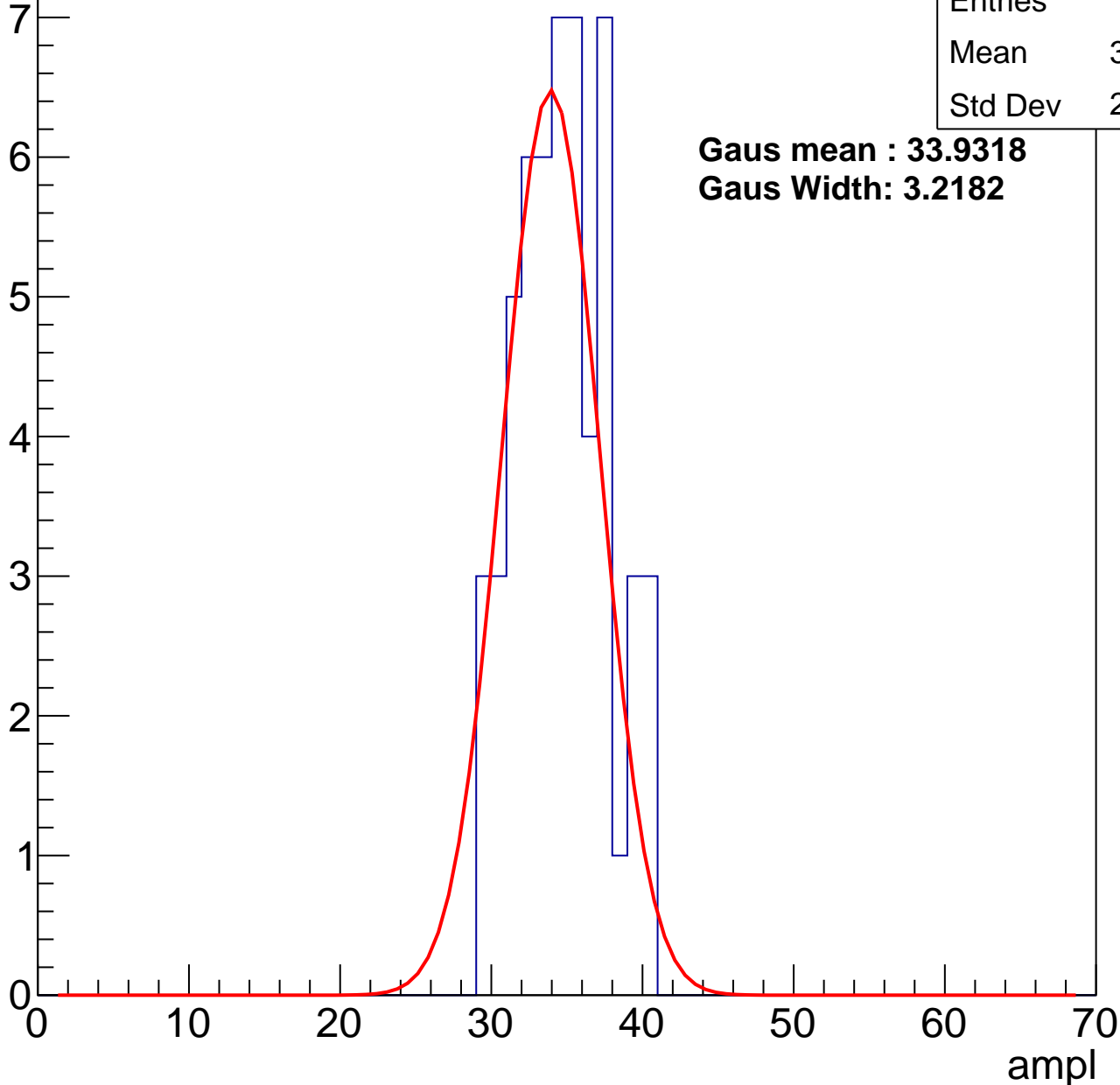
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	34.24
Std Dev	2.954

**Gaus mean : 33.9318**

**Gaus Width: 3.2182**



# B0L000S, U18-ch38, adc2

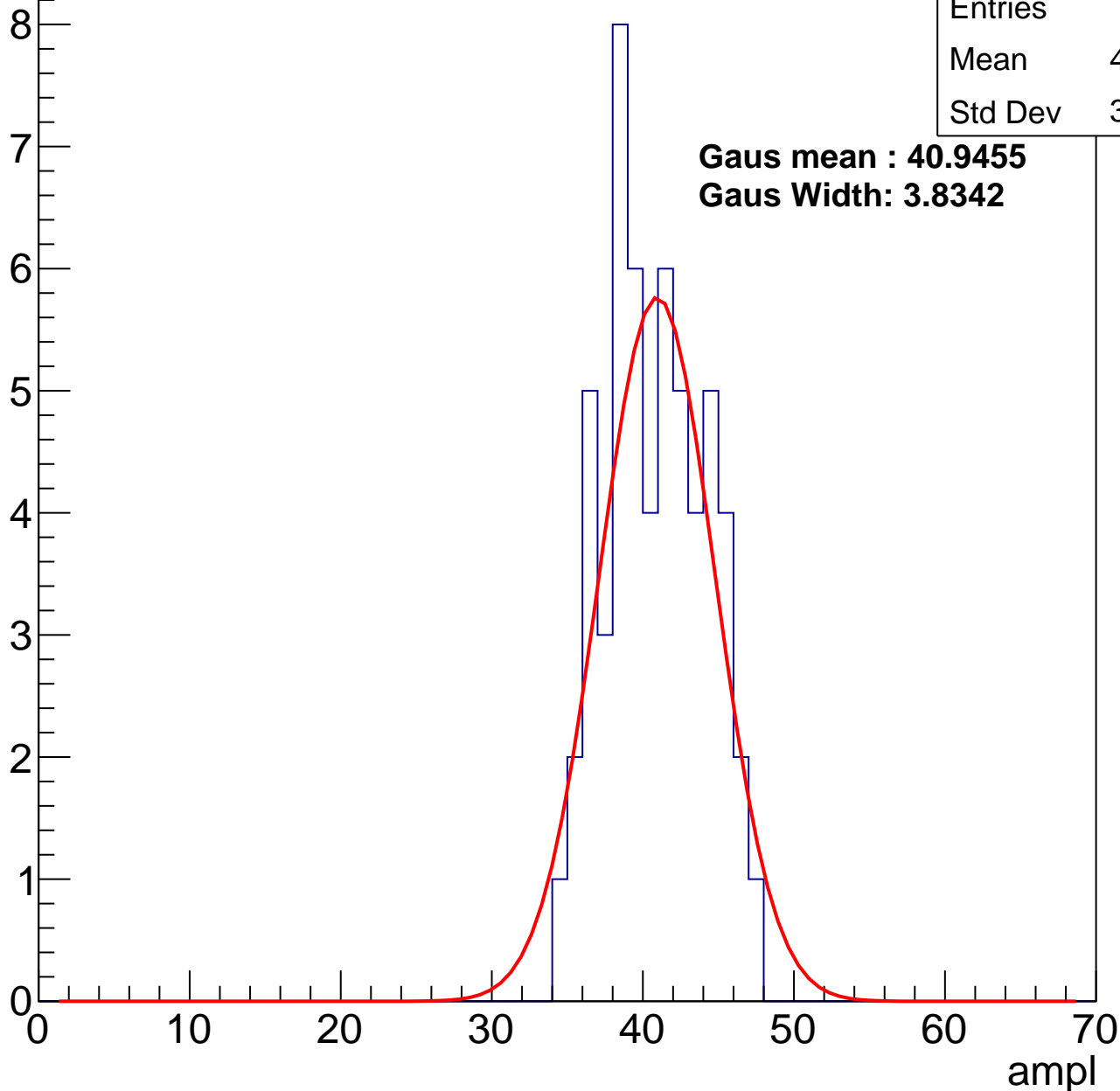
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	40.36
Std Dev	3.237

**Gaus mean : 40.9455**

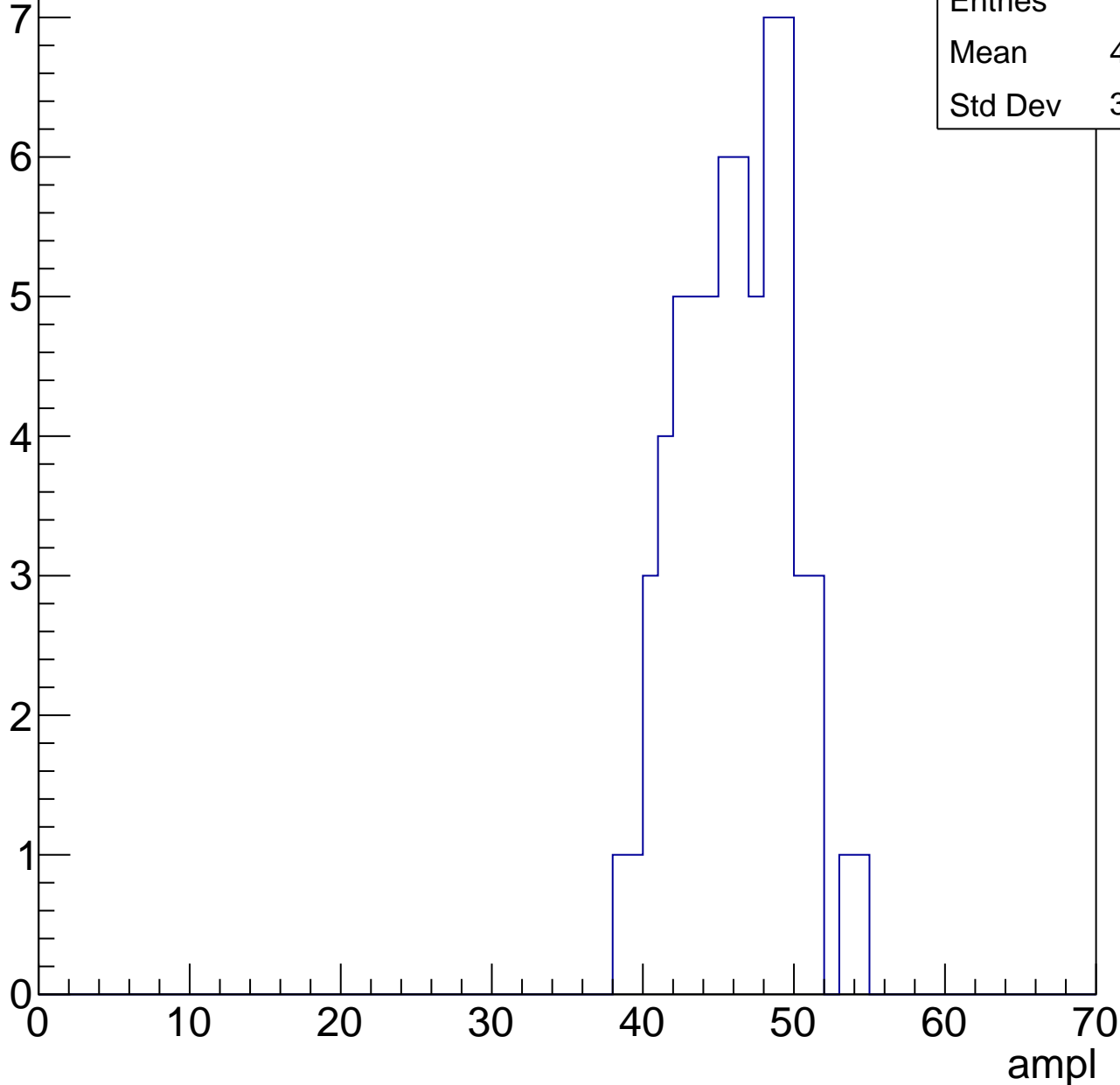
**Gaus Width: 3.8342**



# B0L000S, U18-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



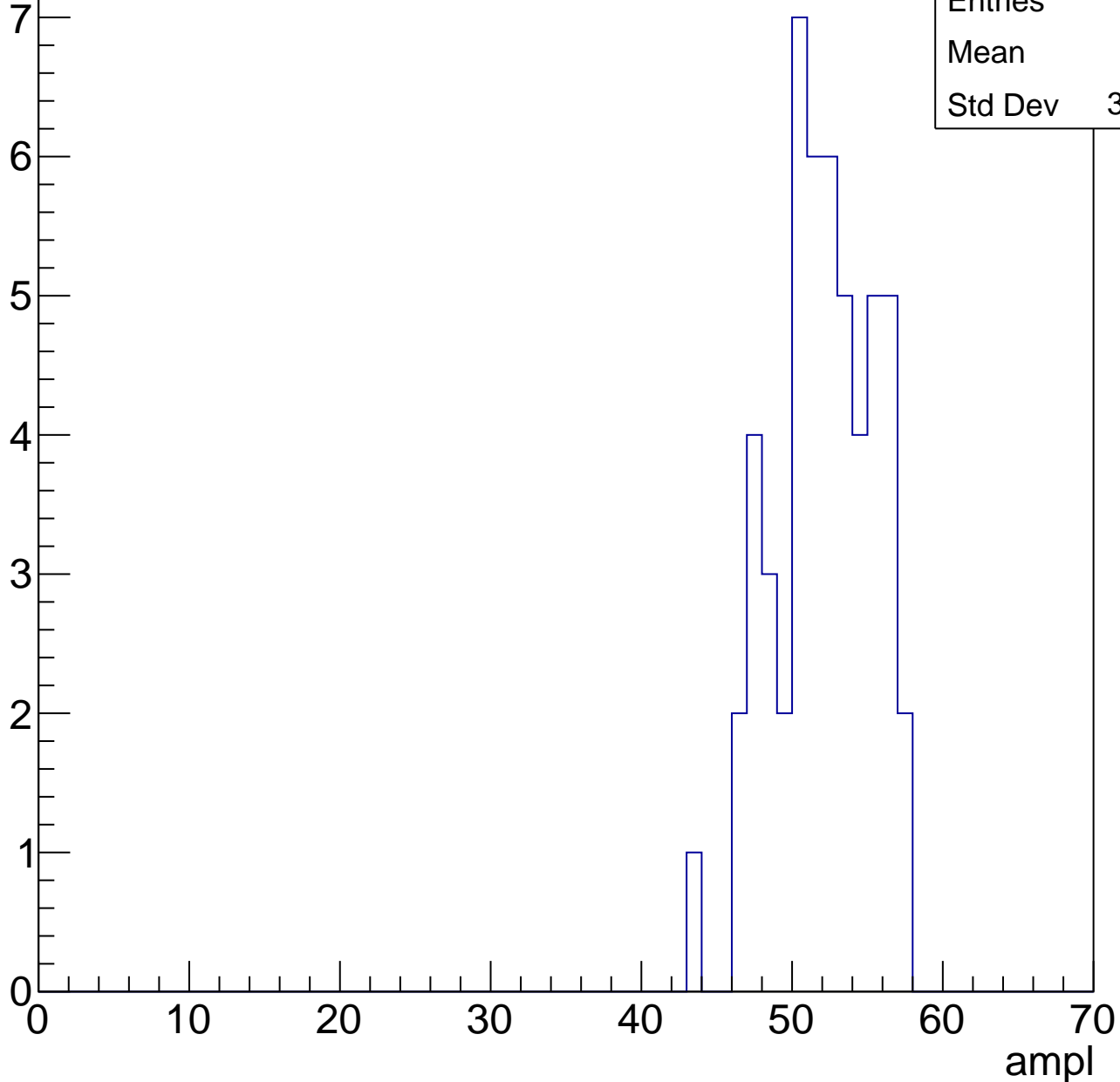
Entries	63
Mean	45.65
Std Dev	3.555

# B0L000S, U18-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	51.6
Std Dev	3.224



# B0L000S, U18-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

Entries 76

Mean 57.72

Std Dev 3.323

8

6

4

2

0

0

10

20

30

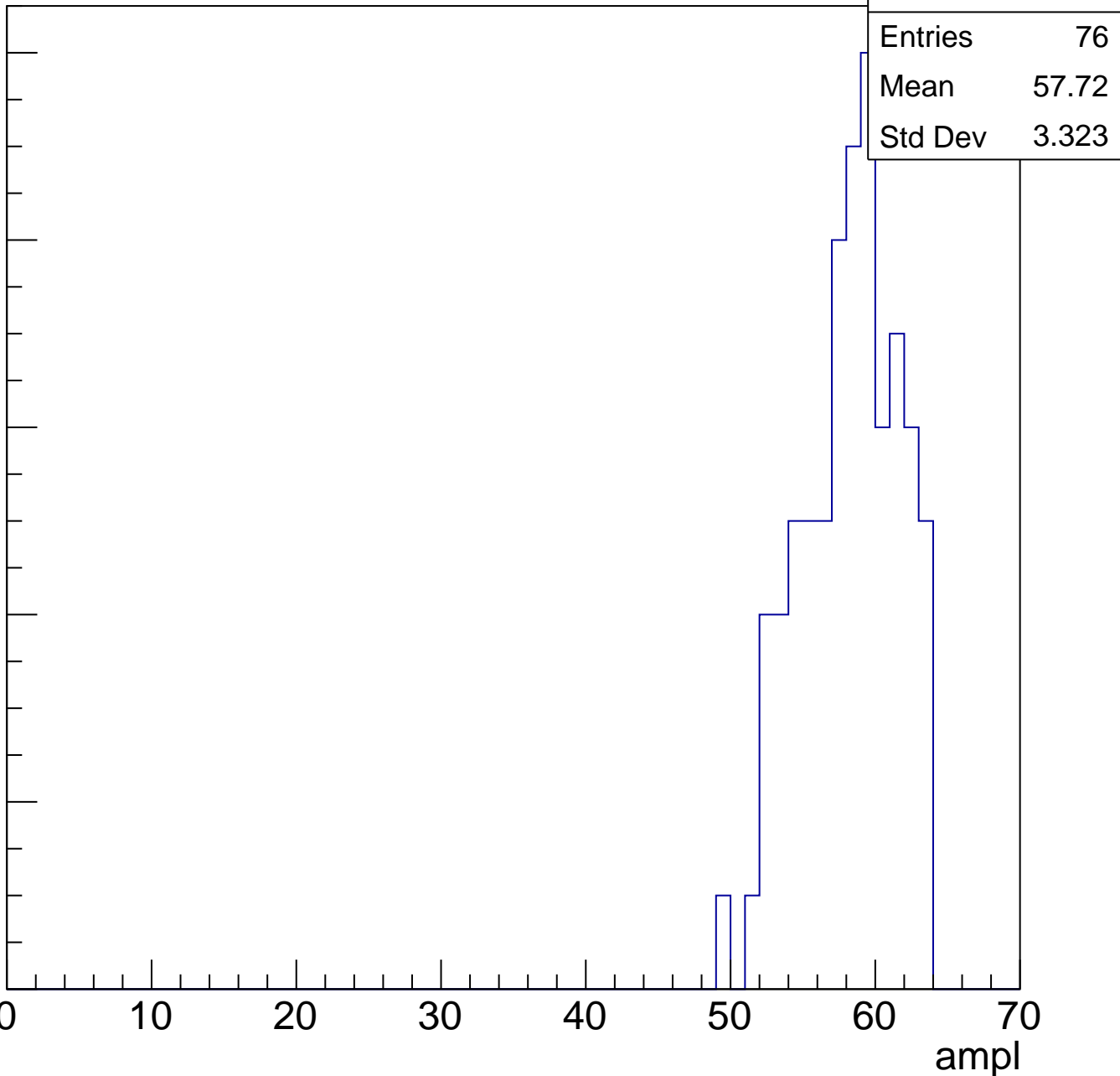
40

50

60

70

ampl

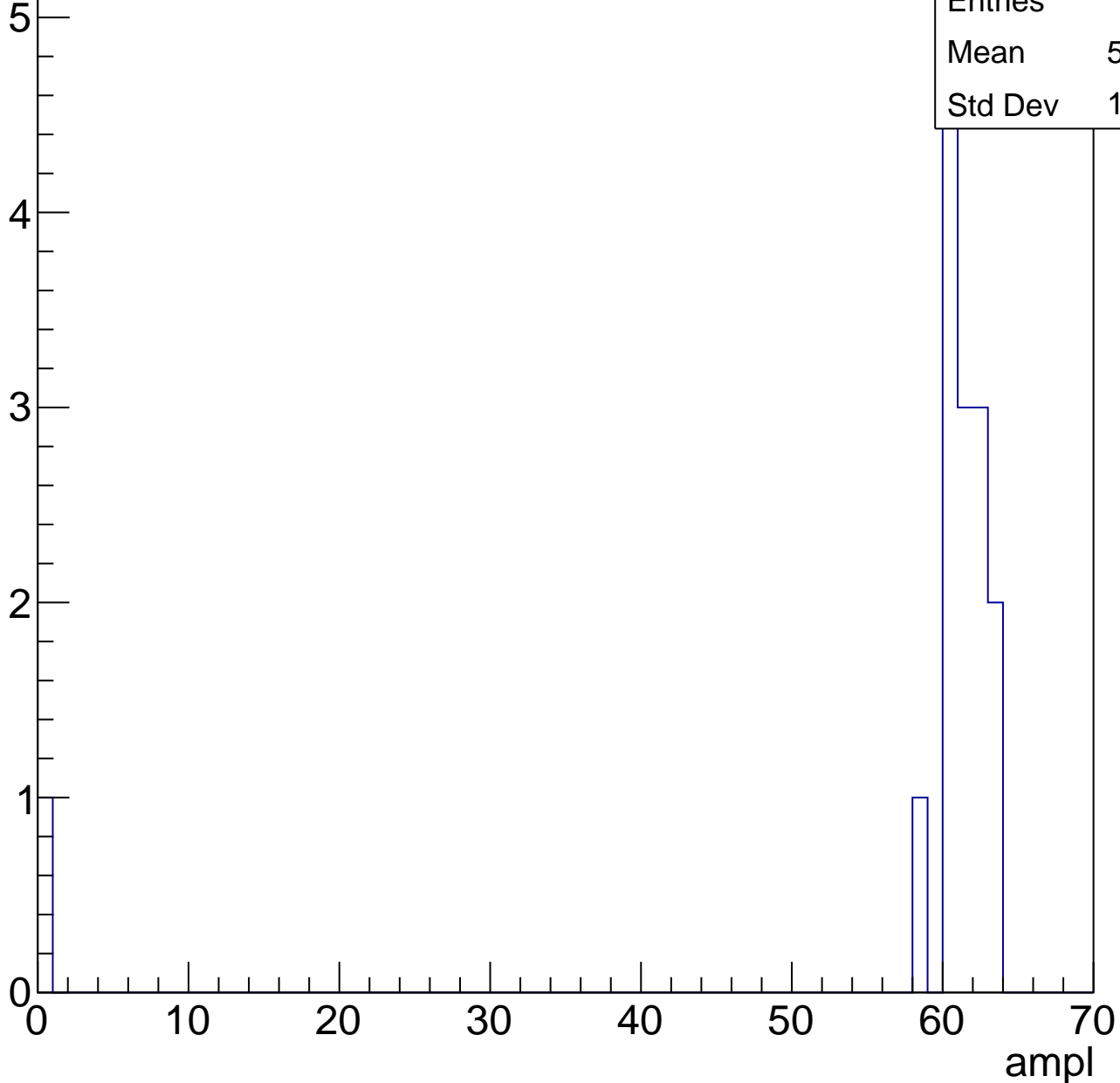


# B0L000S, U18-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	56.87
Std Dev	15.25

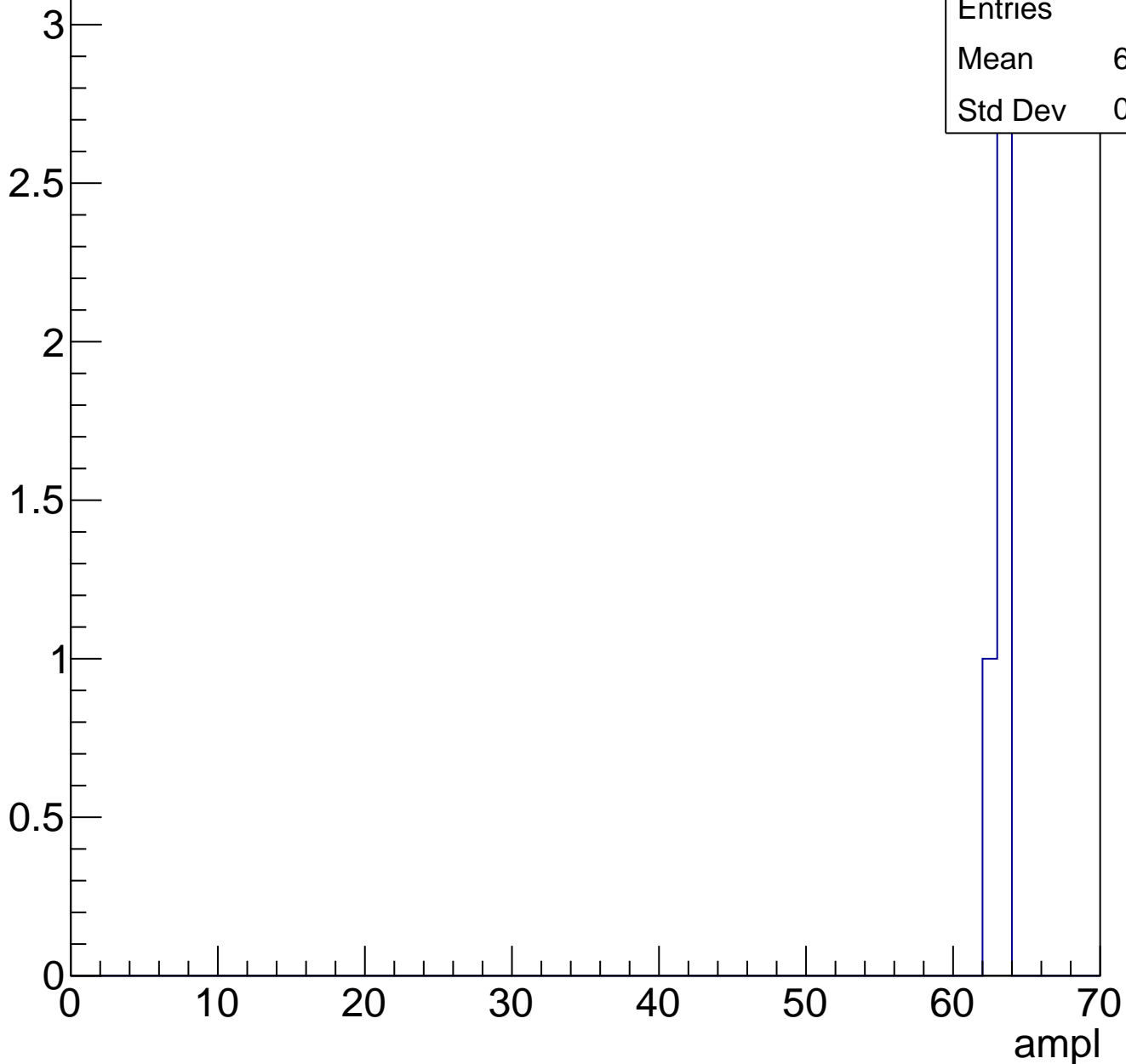




# B0L000S, U18-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch39, adc0

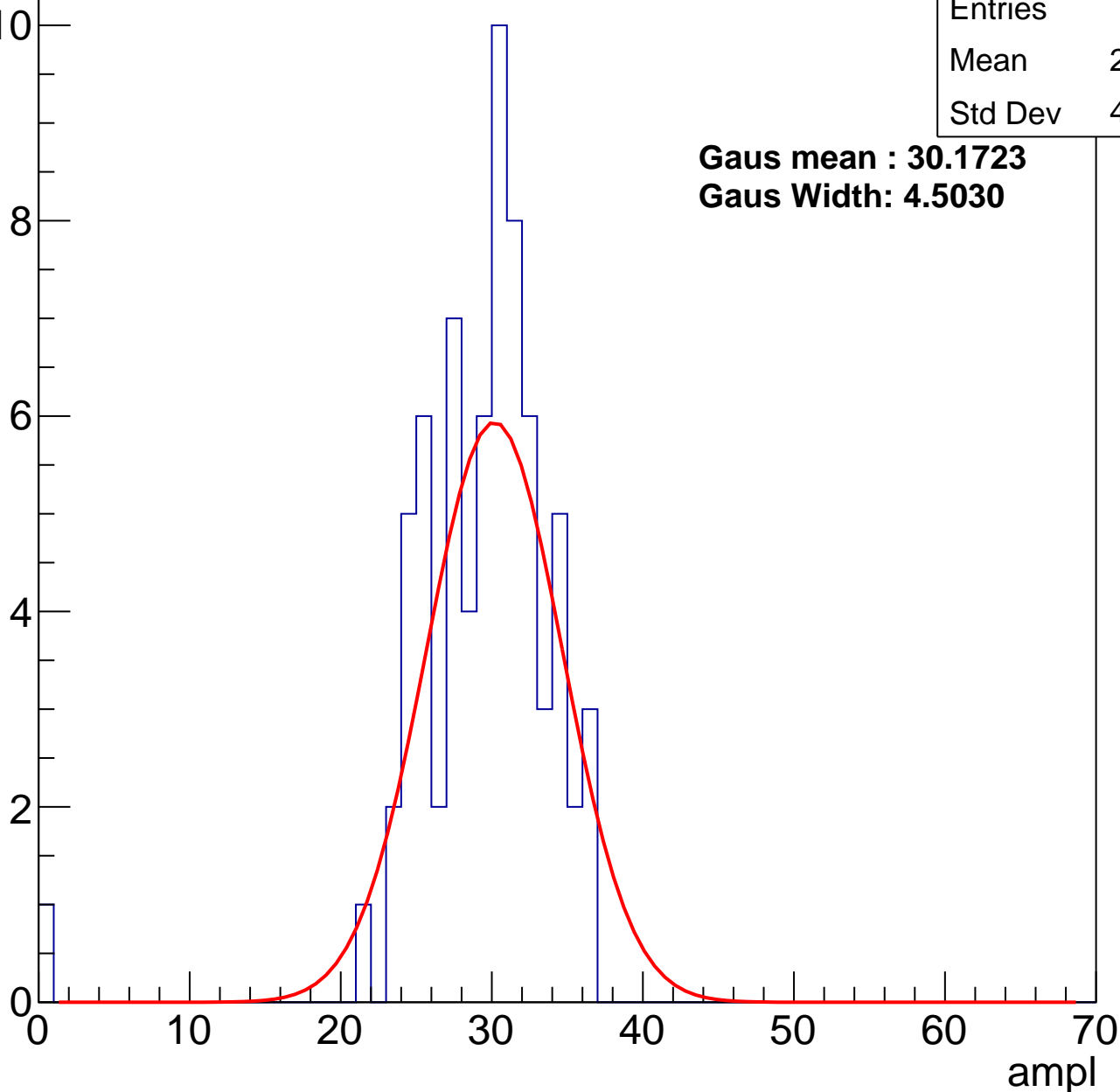
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.89
Std Dev	4.943

**Gaus mean : 30.1723**

**Gaus Width: 4.5030**



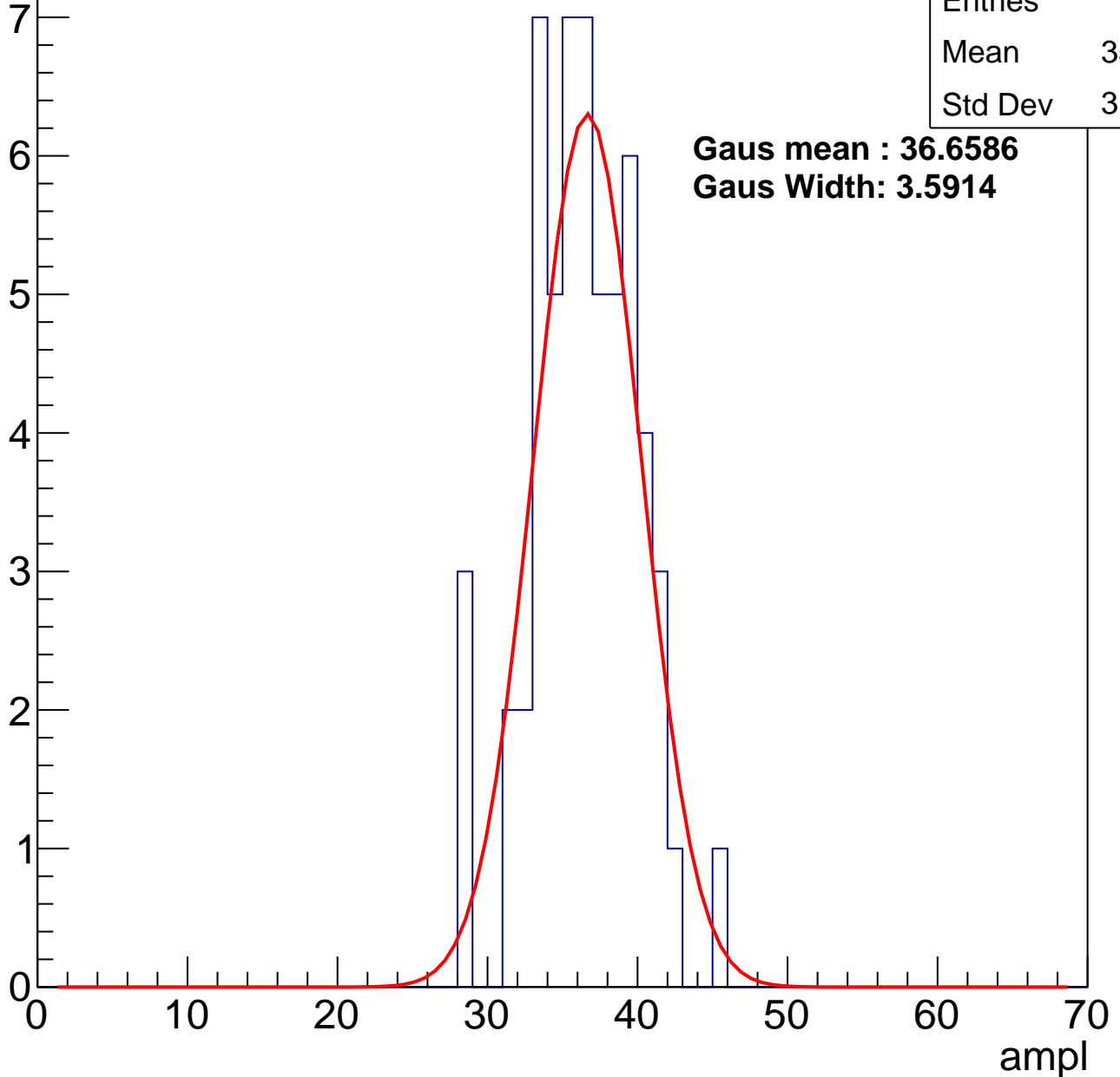
# B0L000S, U18-ch39, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.98
Std Dev	3.472

**Gaus mean : 36.6586**  
**Gaus Width: 3.5914**



# B0L000S, U18-ch39, adc2

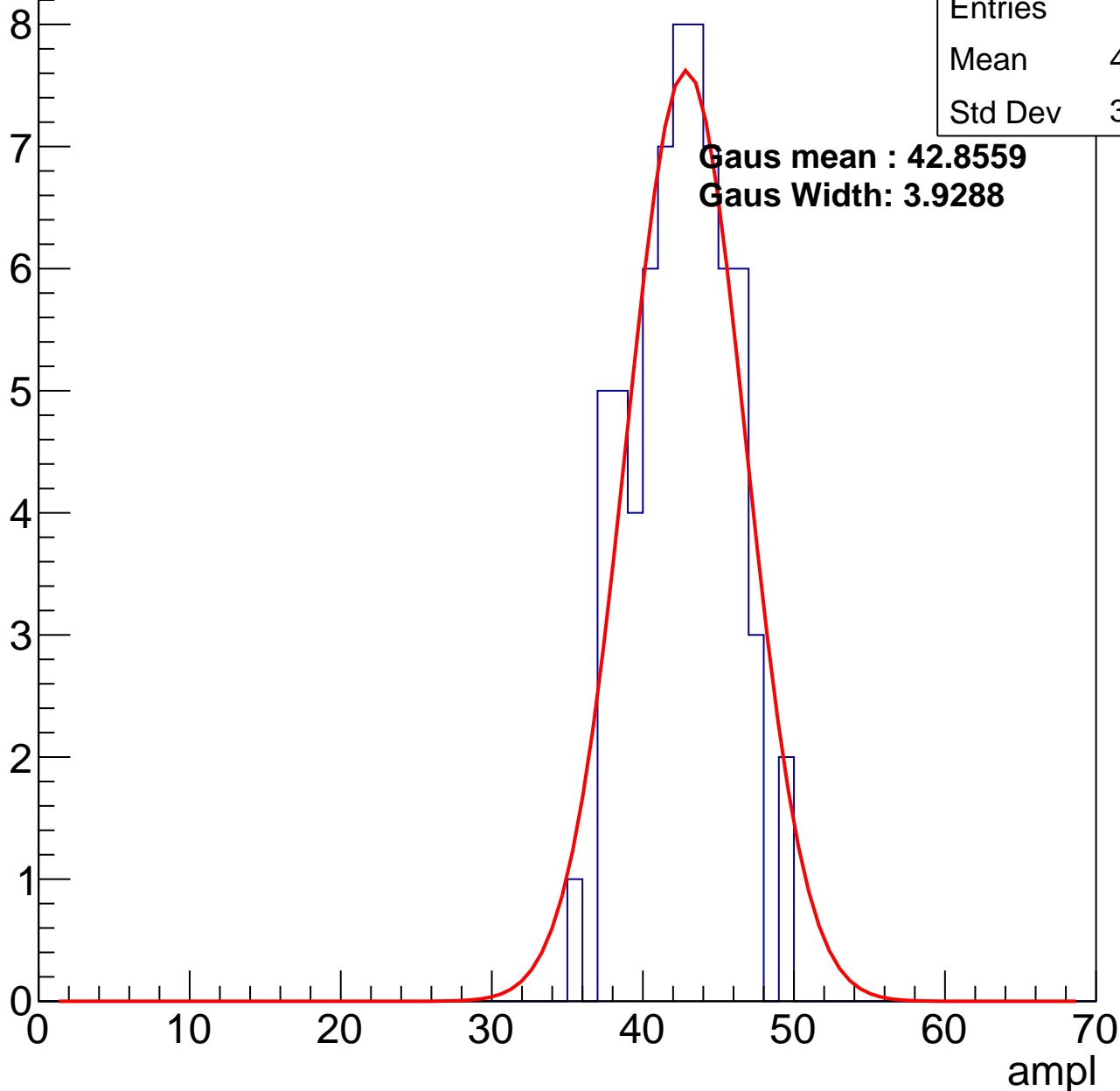
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.15
Std Dev	3.159

**Gaus mean : 42.8559**

**Gaus Width: 3.9288**



# B0L000S, U18-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

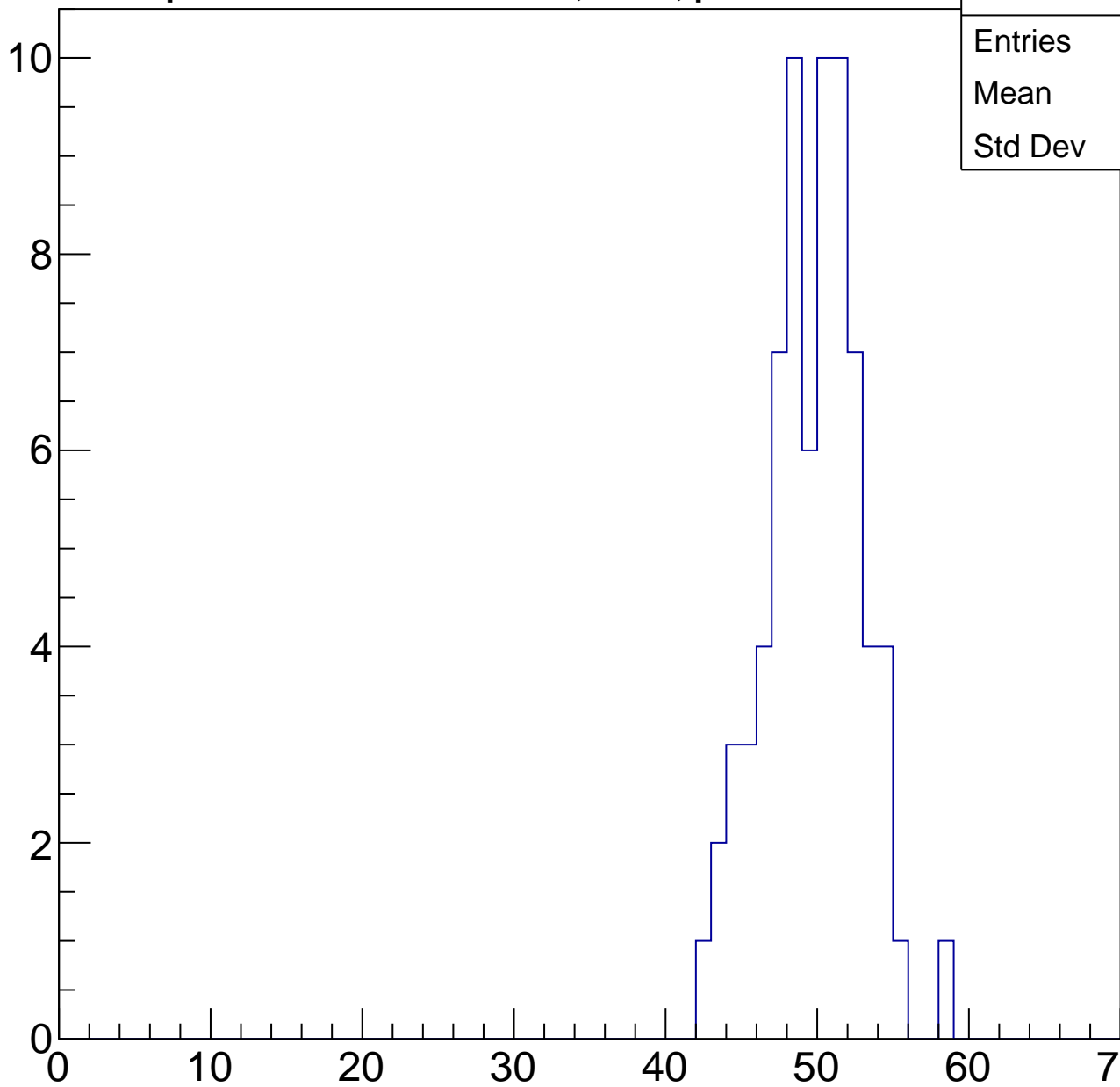
Entries	73
Mean	49.27
Std Dev	3.111

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

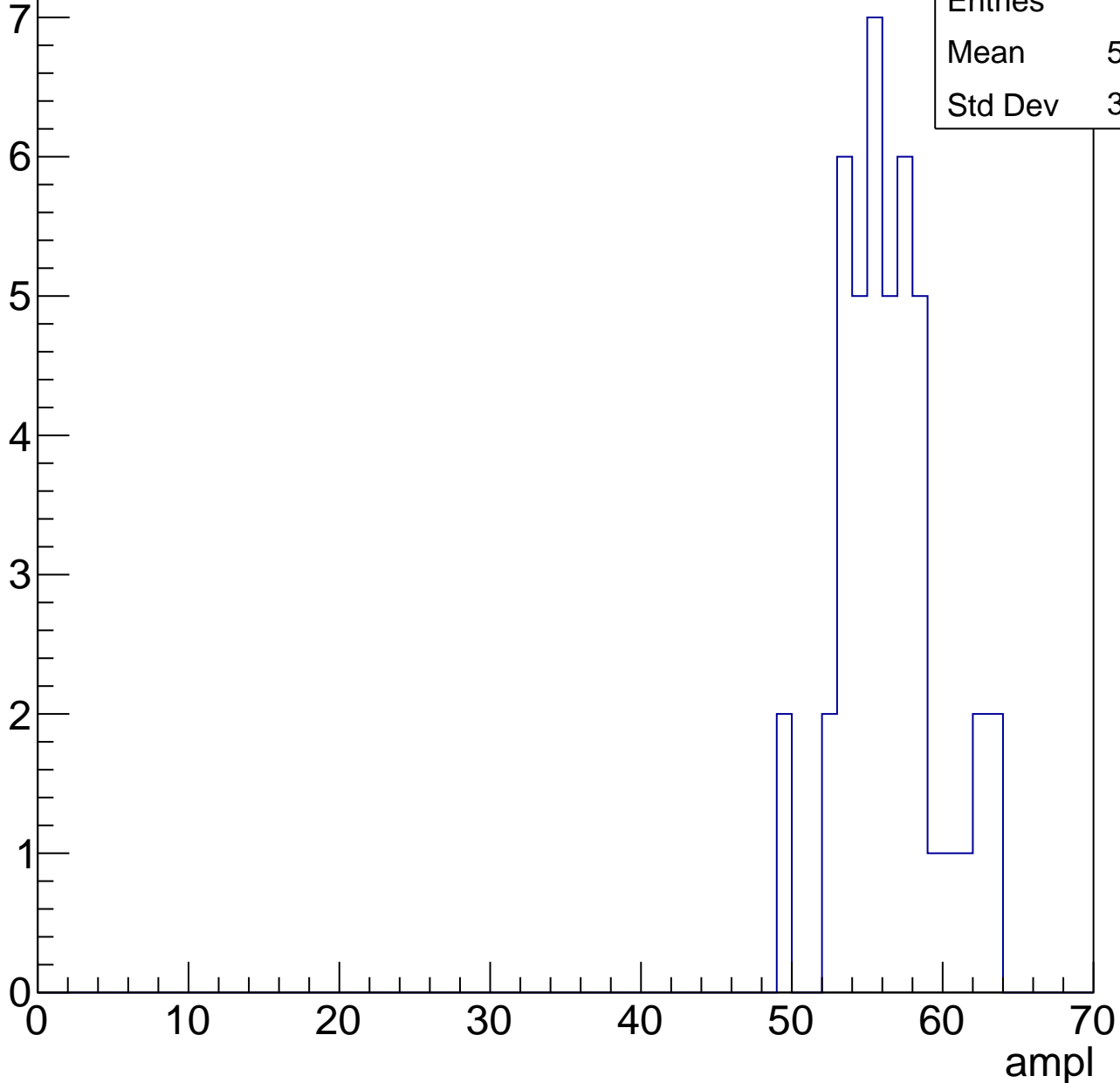


# B0L000S, U18-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

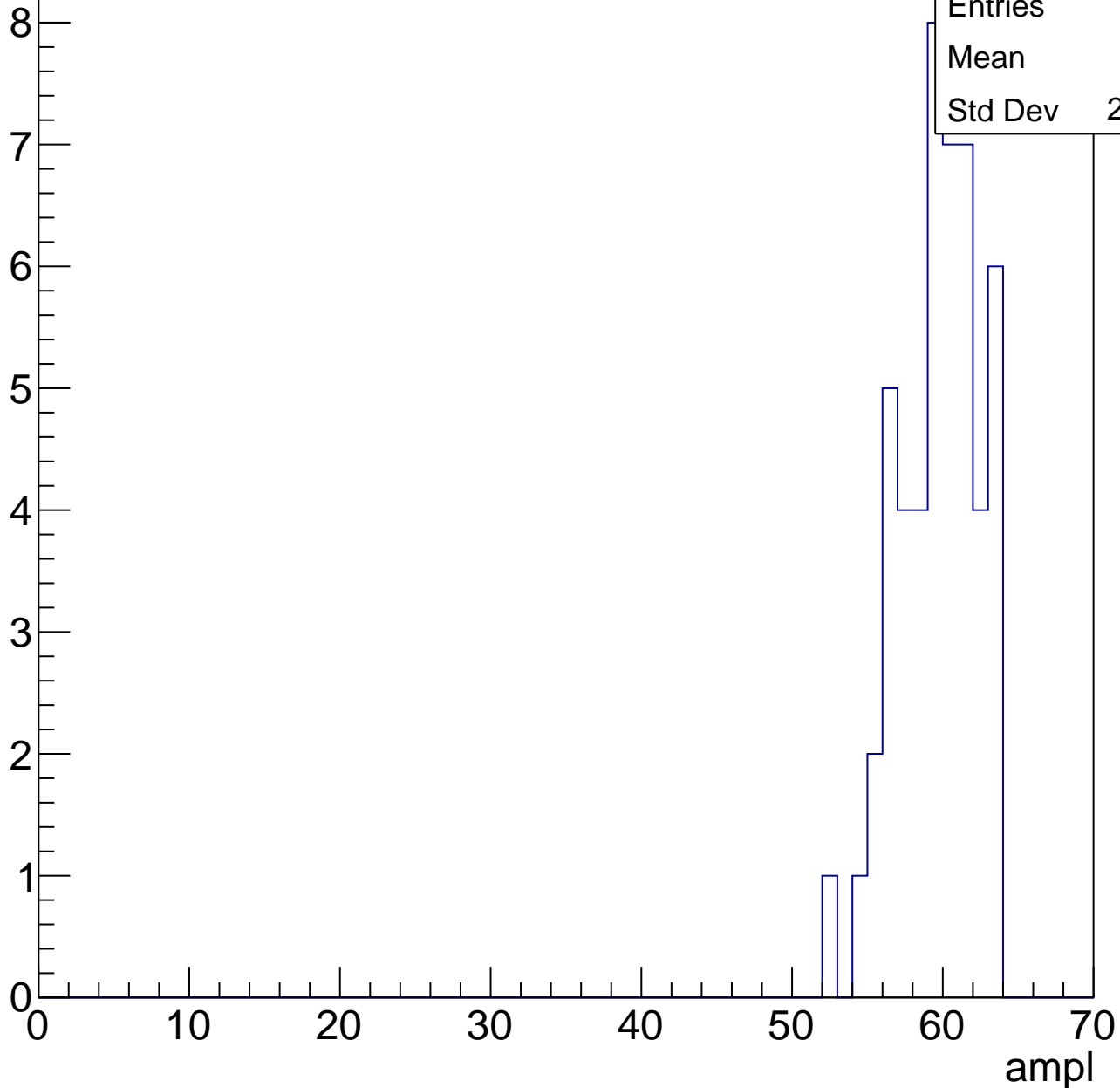
Entries	45
Mean	55.93
Std Dev	3.186



# B0L000S, U18-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

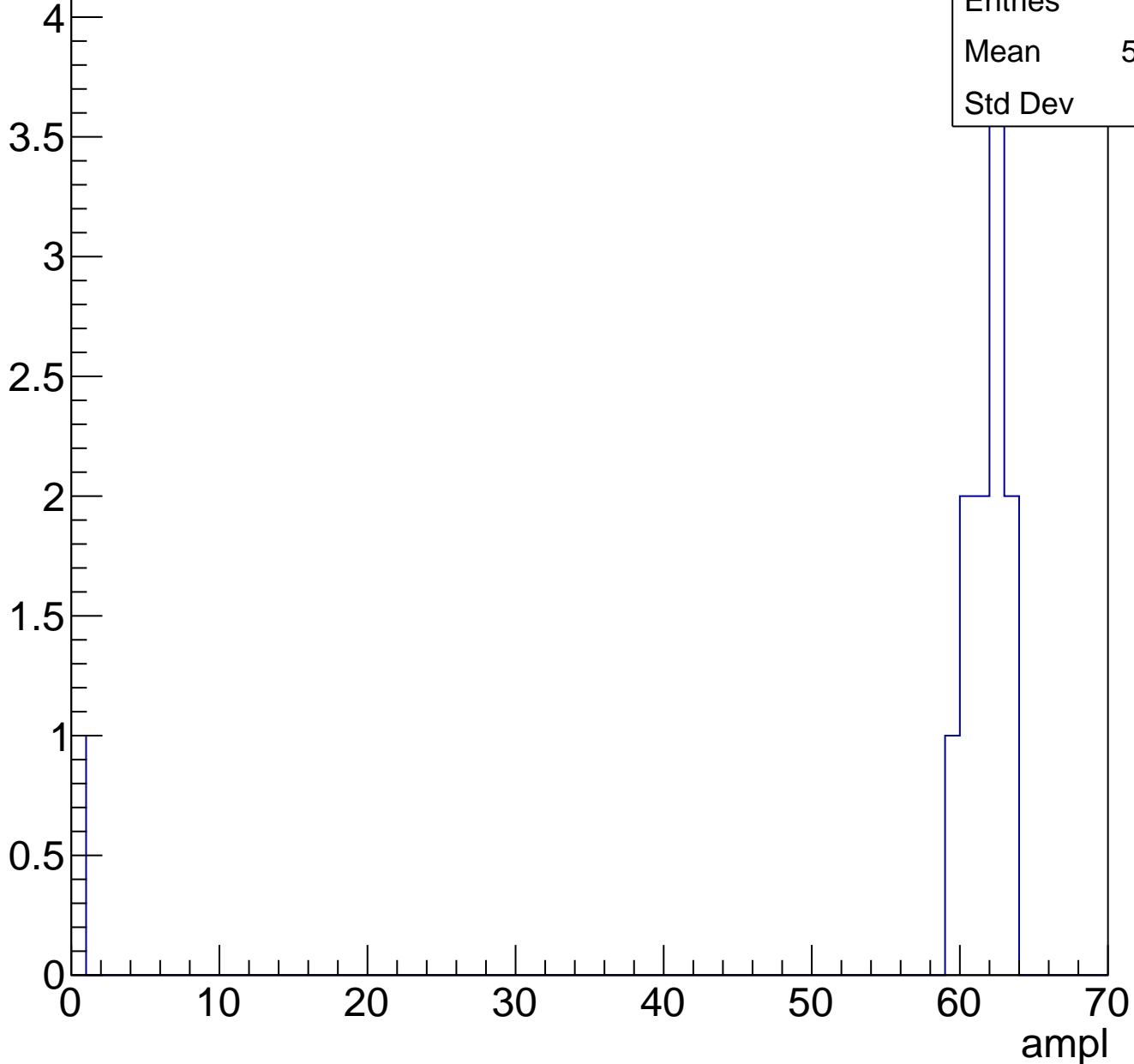
Entry



# B0L000S, U18-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch40, adc0

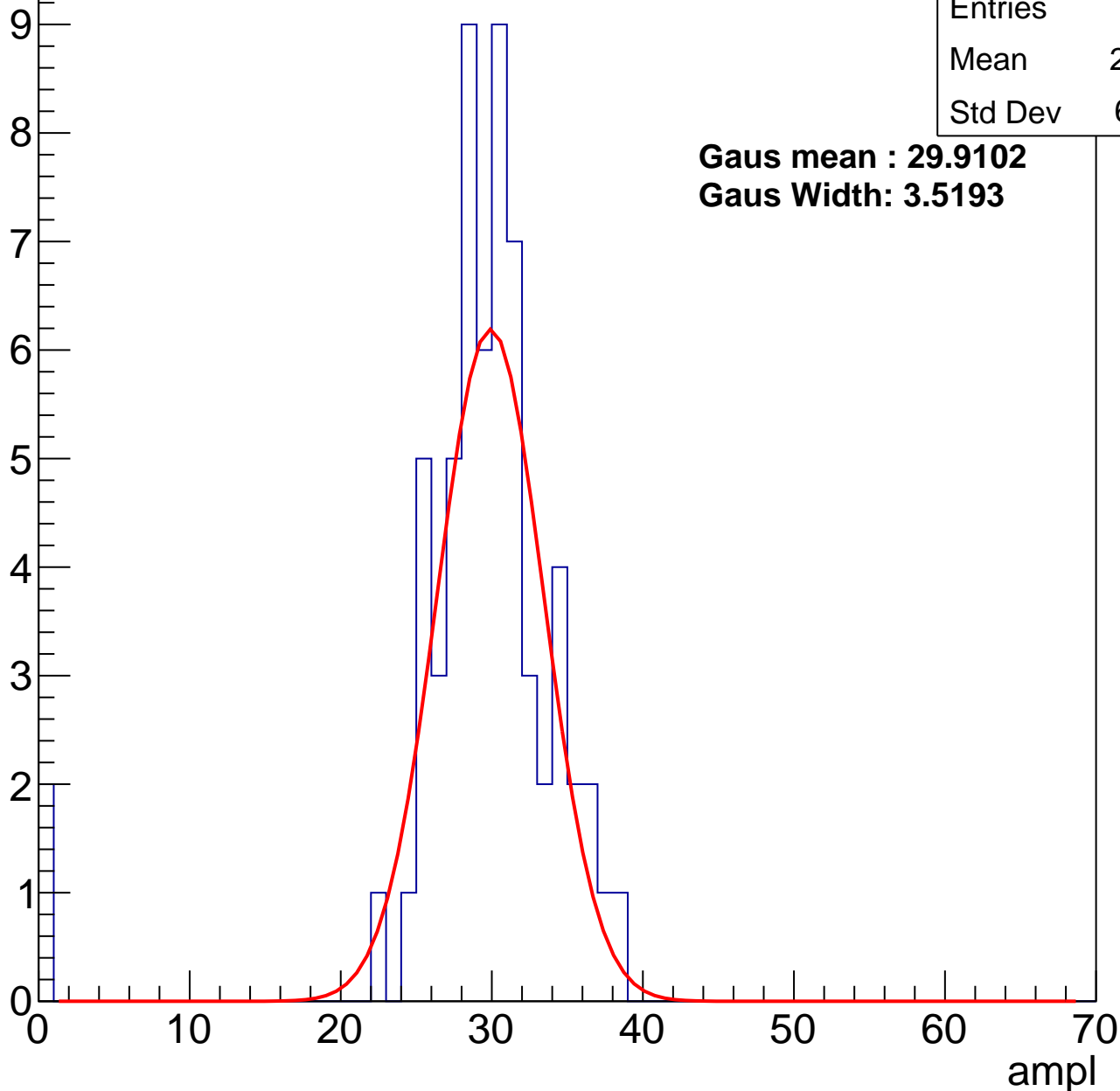
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	28.76
Std Dev	6.171

**Gaus mean : 29.9102**

**Gaus Width: 3.5193**



# B0L000S, U18-ch40, adc1

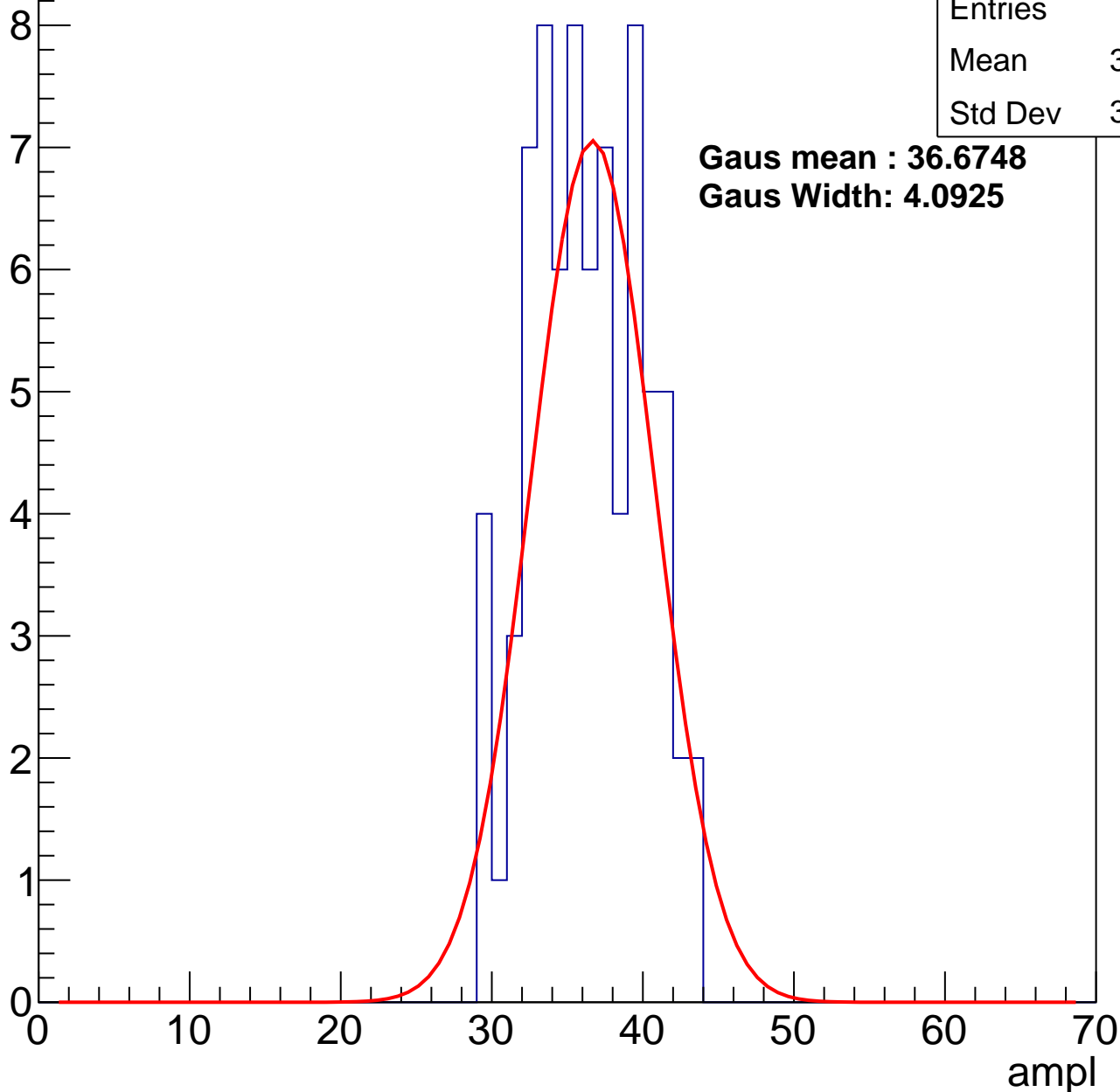
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	35.86
Std Dev	3.612

**Gaus mean : 36.6748**

**Gaus Width: 4.0925**



# B0L000S, U18-ch40, adc2

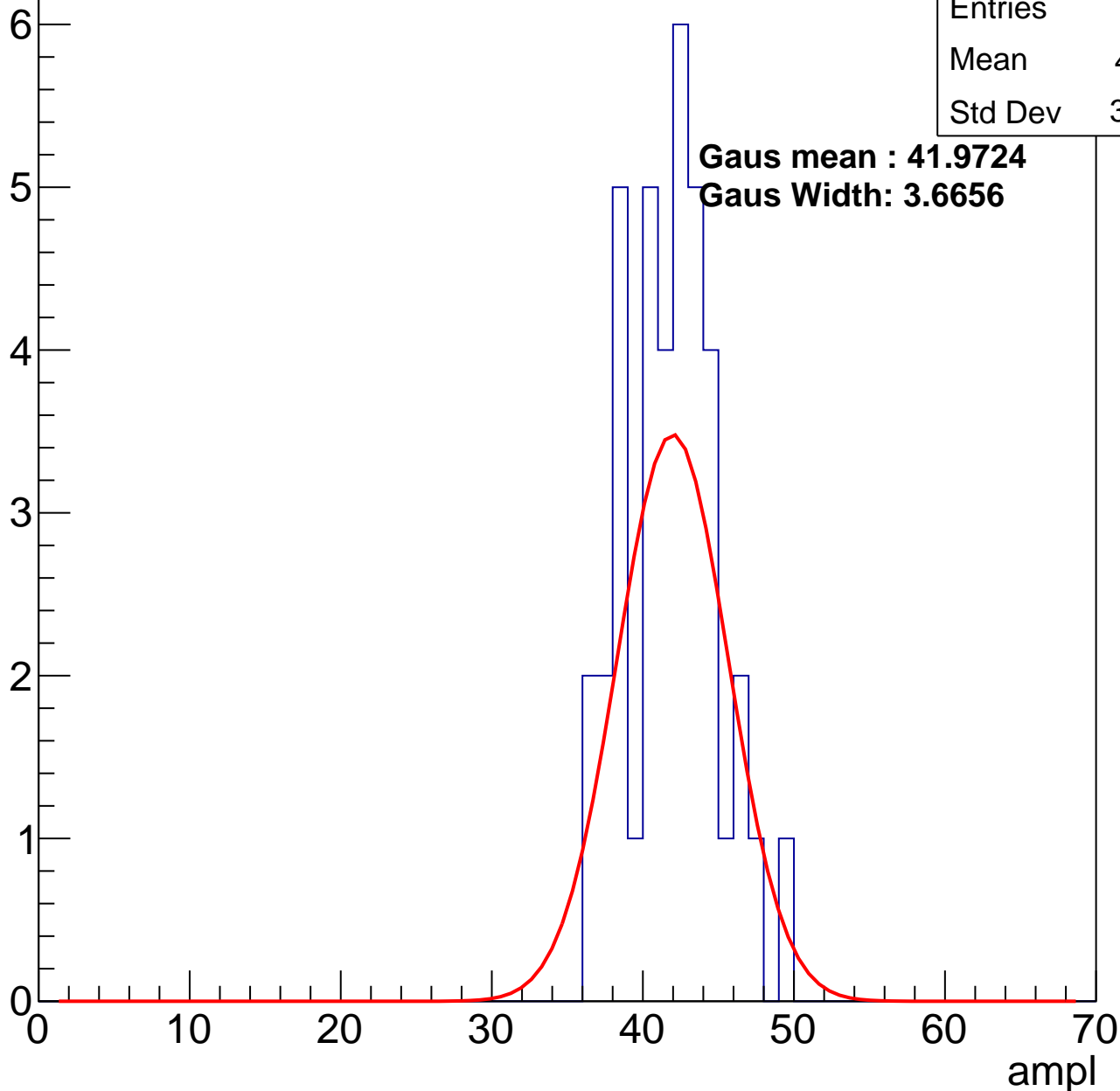
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	39
Mean	41.41
Std Dev	3.027

**Gaus mean : 41.9724**

**Gaus Width: 3.6656**

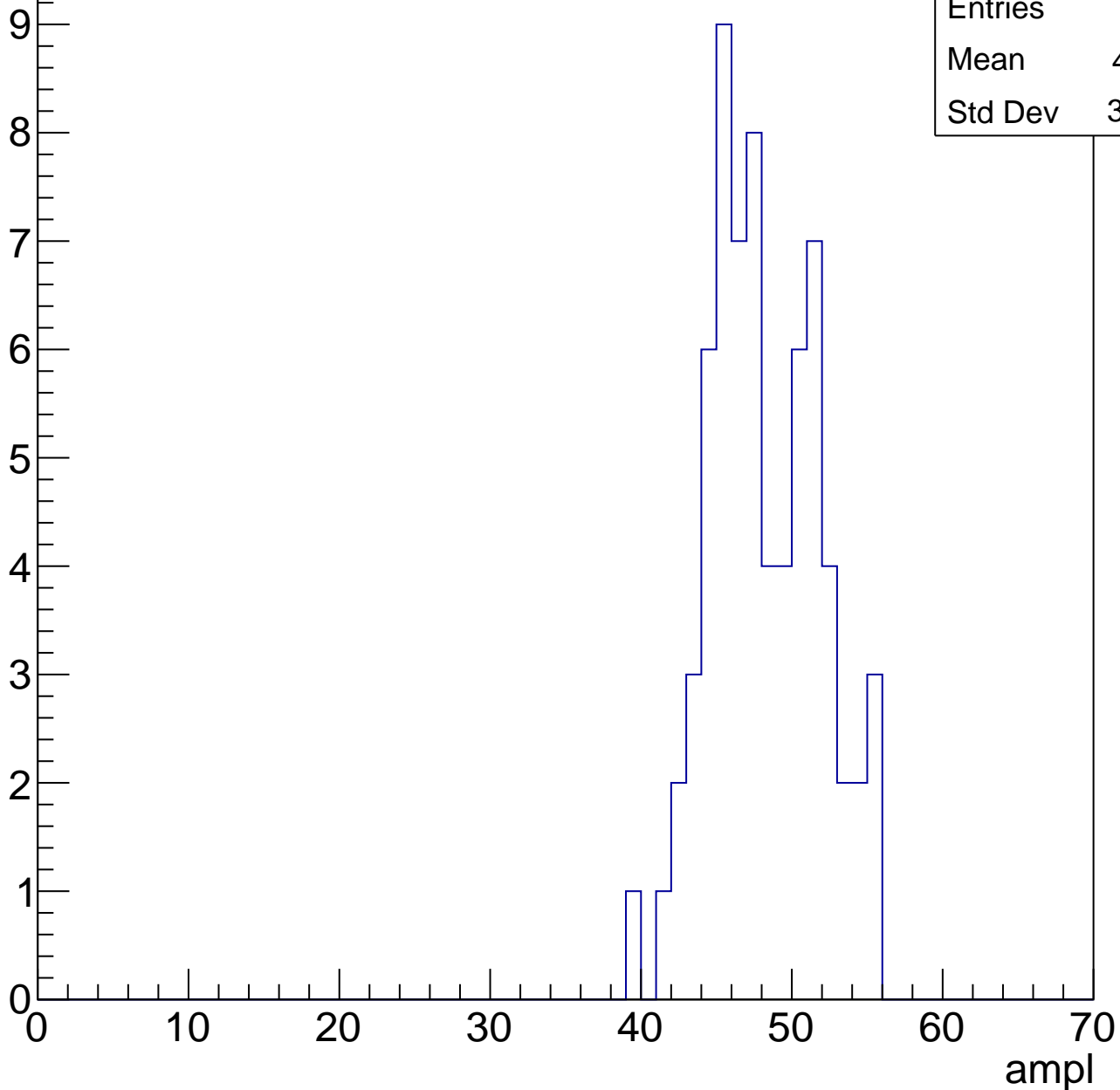


# B0L000S, U18-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	47.71
Std Dev	3.628

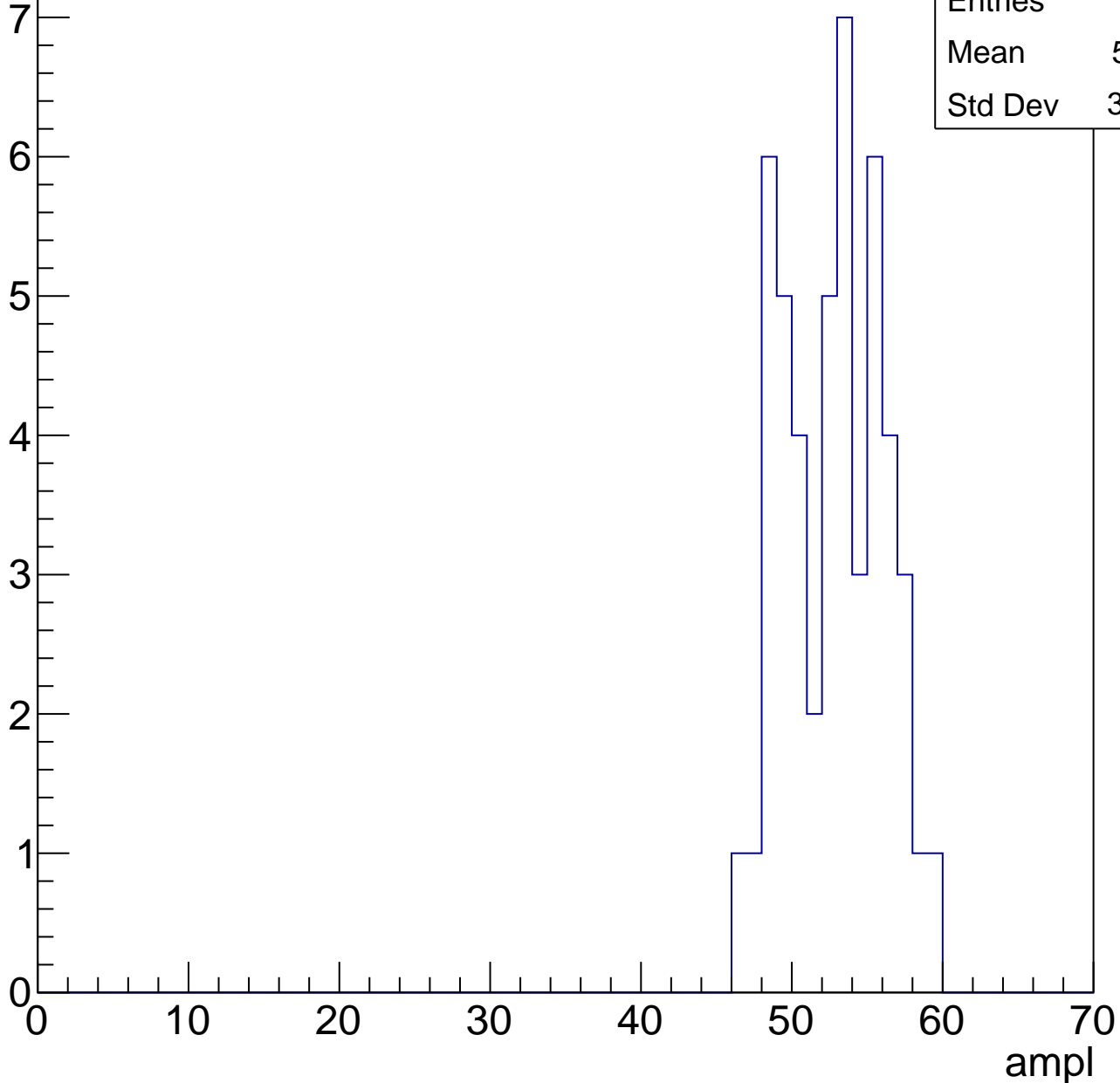


# B0L000S, U18-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	52.31
Std Dev	3.234

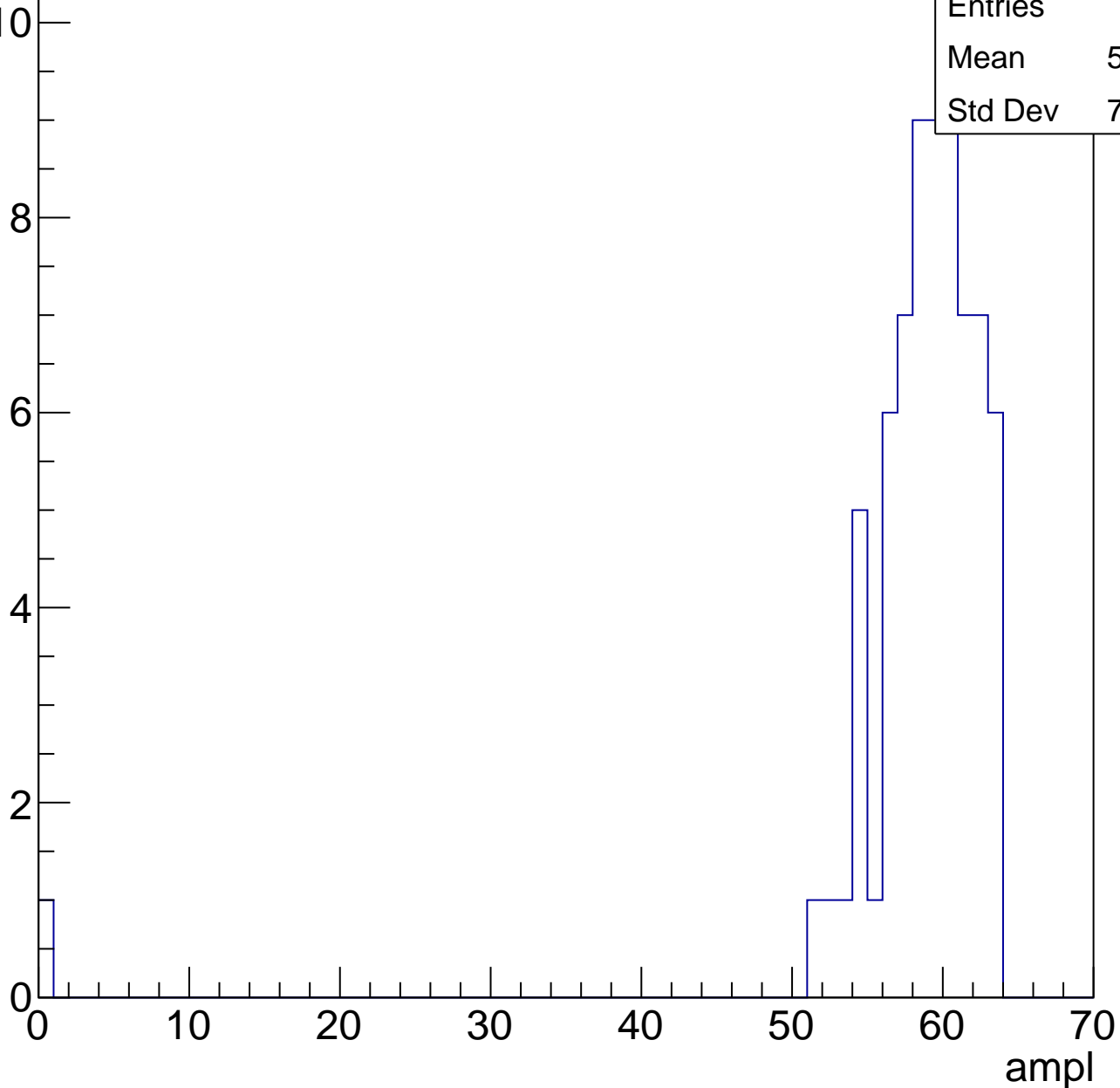


# B0L000S, U18-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

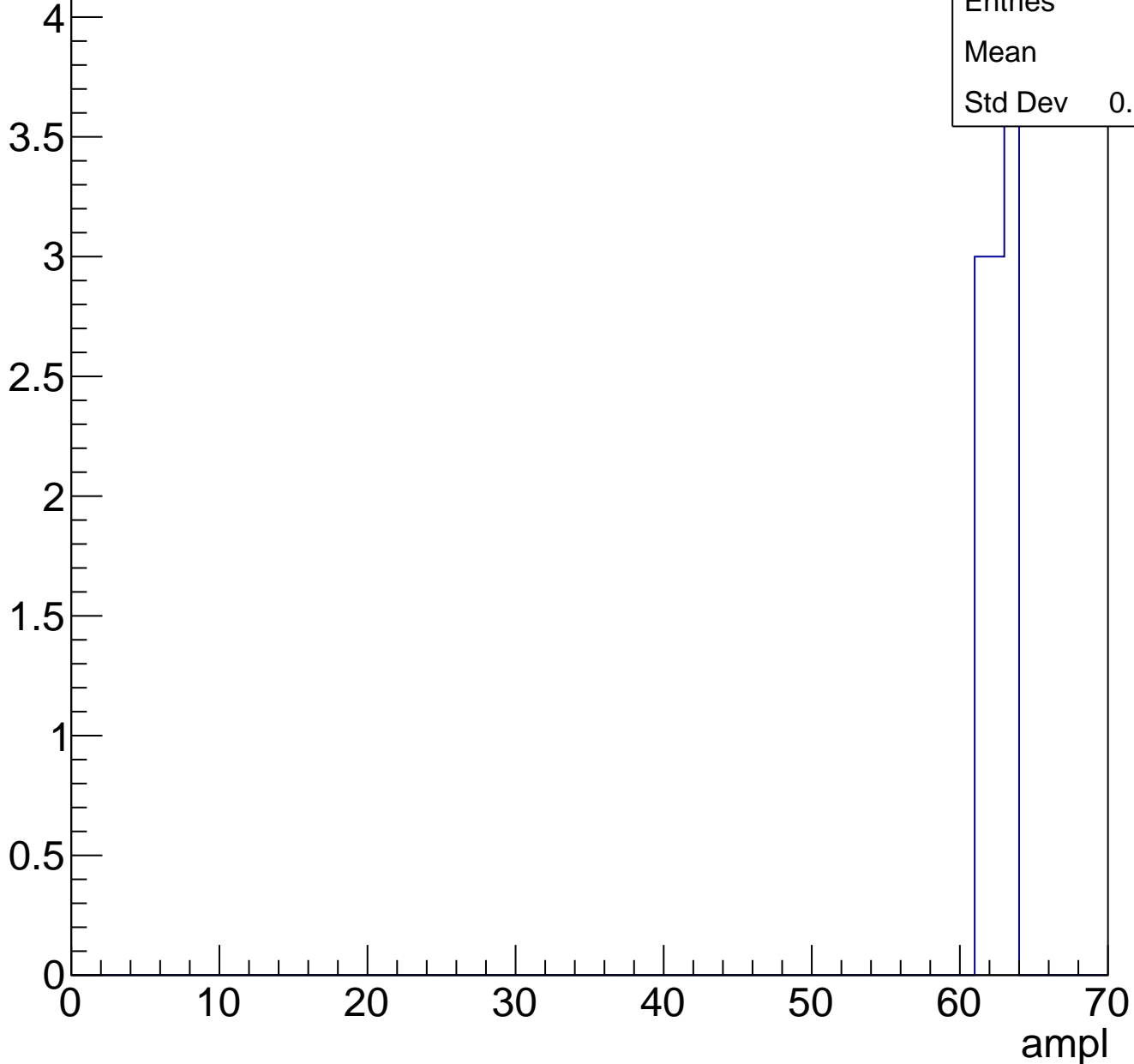
Entries	71
Mean	57.86
Std Dev	7.474



# B0L000S, U18-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch41, adc0

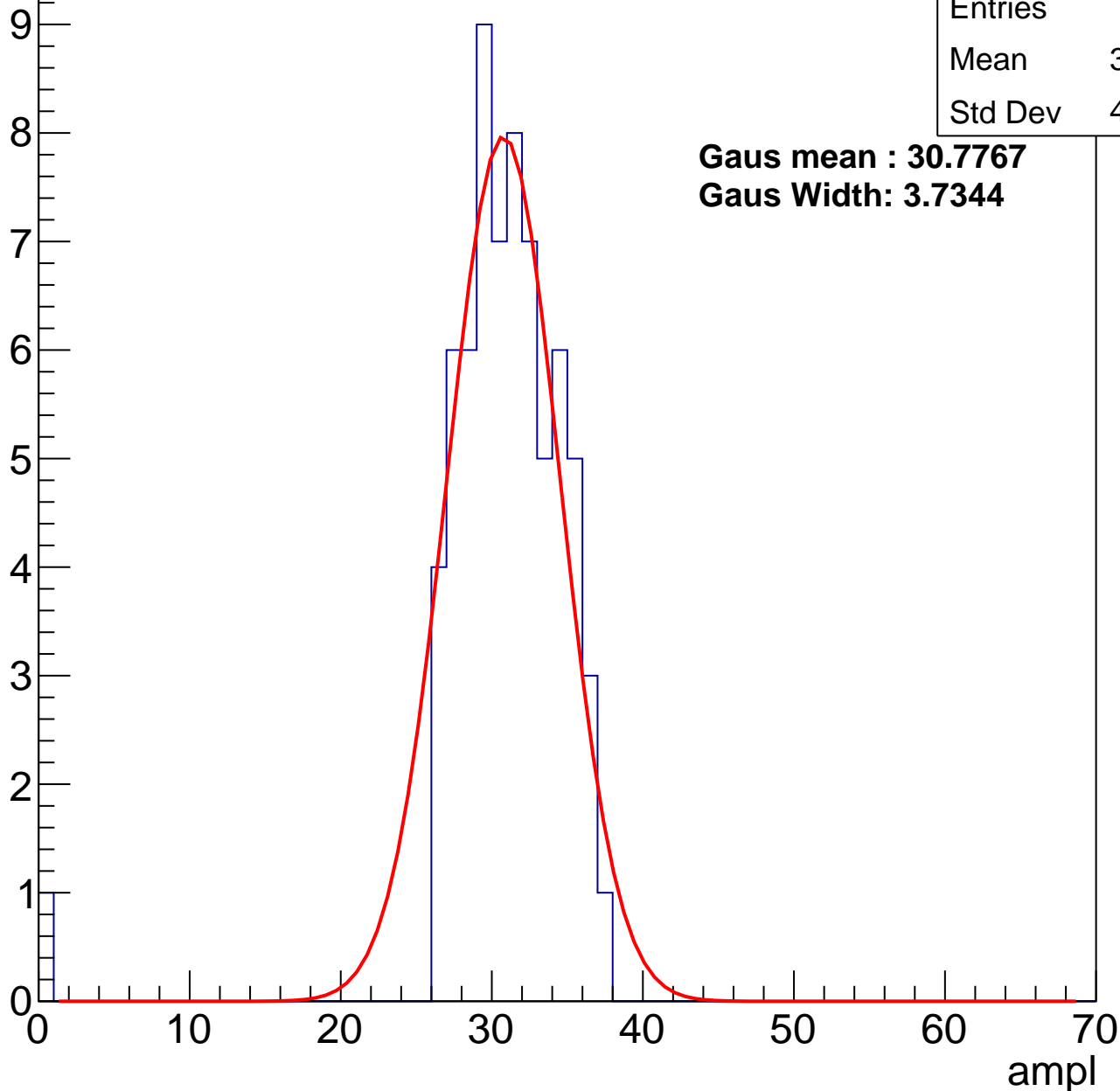
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.38
Std Dev	4.697

**Gaus mean : 30.7767**

**Gaus Width: 3.7344**



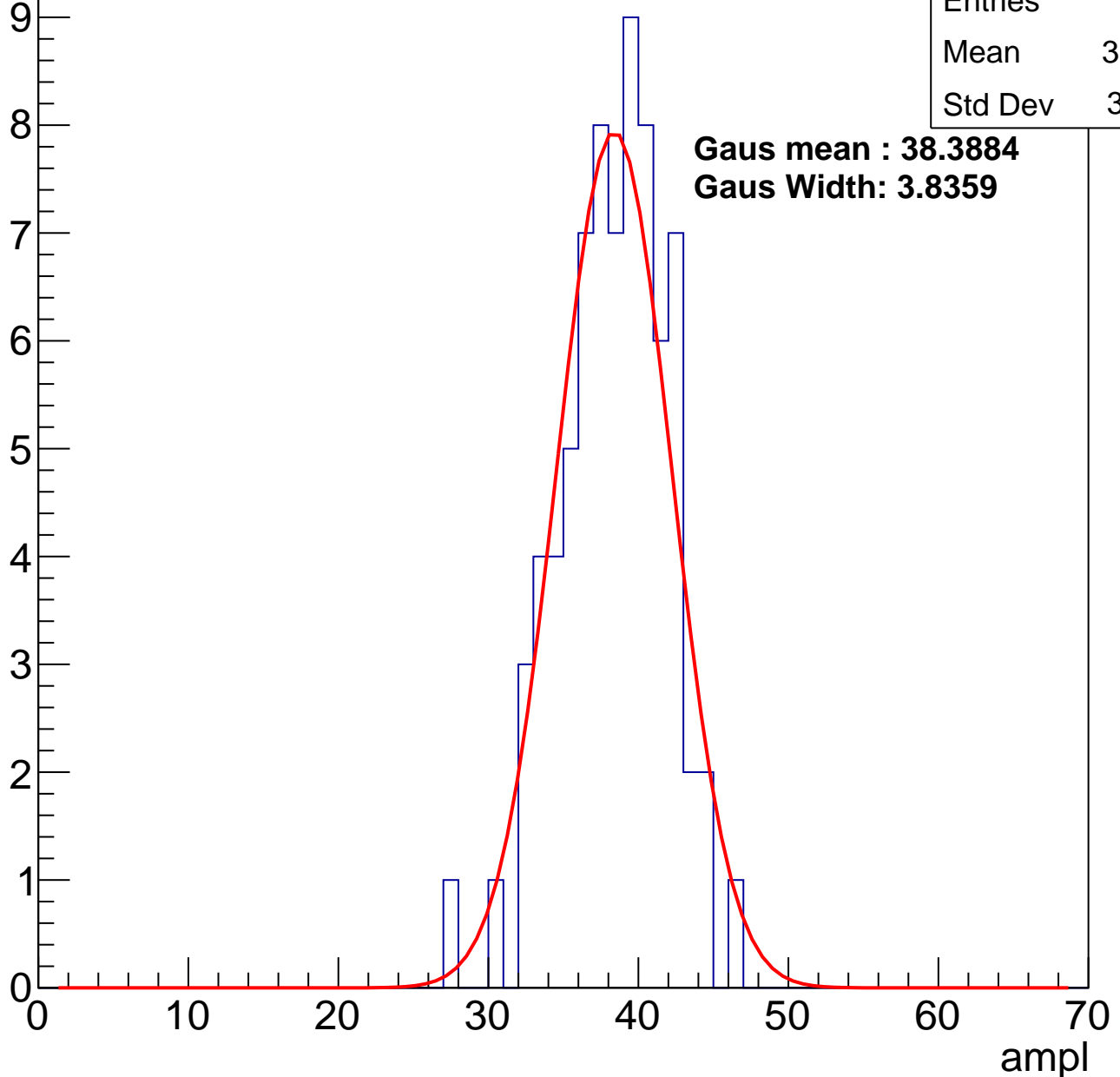
# B0L000S, U18-ch41, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.88
Std Dev	3.521

**Gaus mean : 38.3884**  
**Gaus Width: 3.8359**



# B0L000S, U18-ch41, adc2

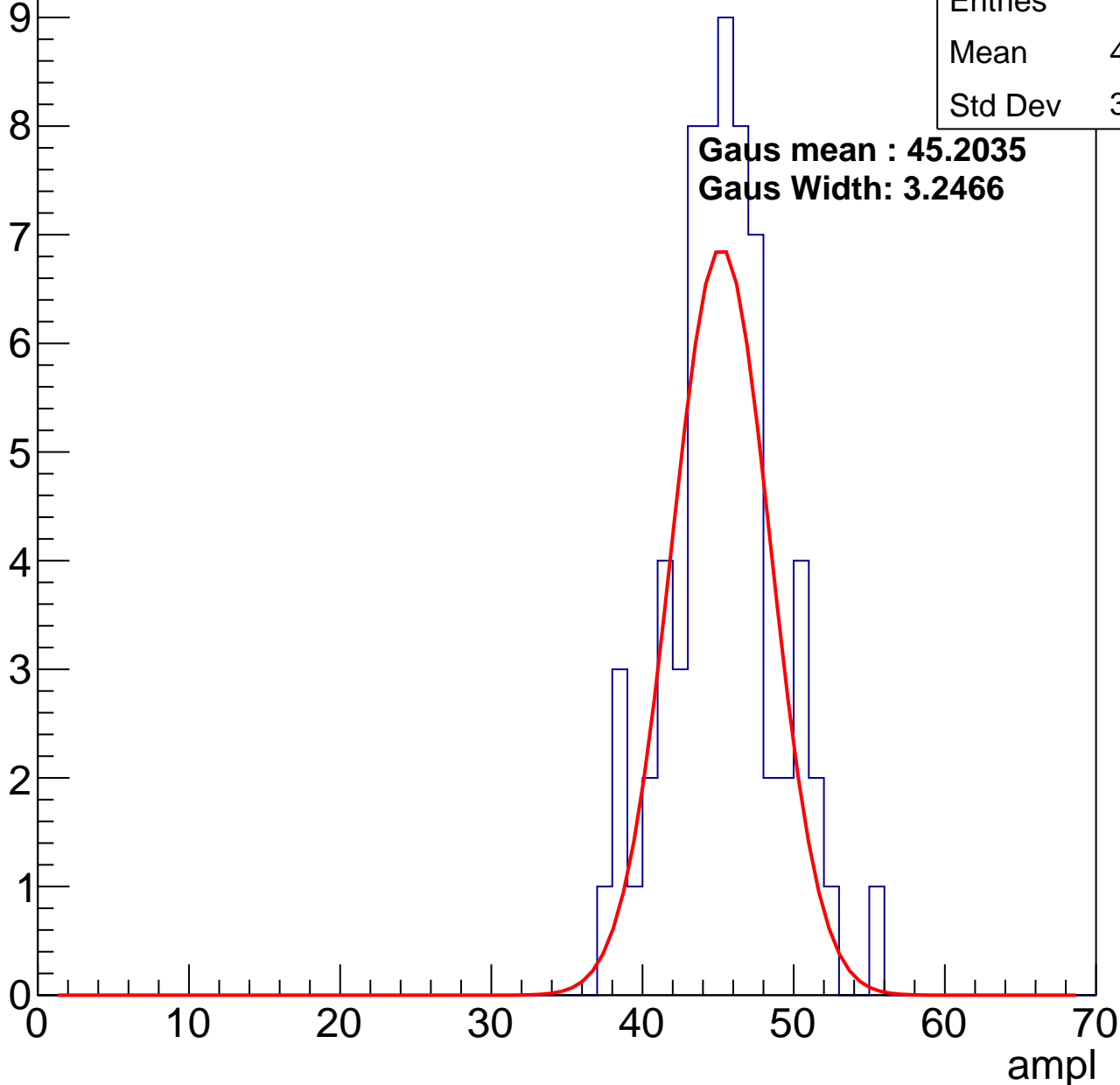
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.86
Std Dev	3.554

**Gaus mean : 45.2035**

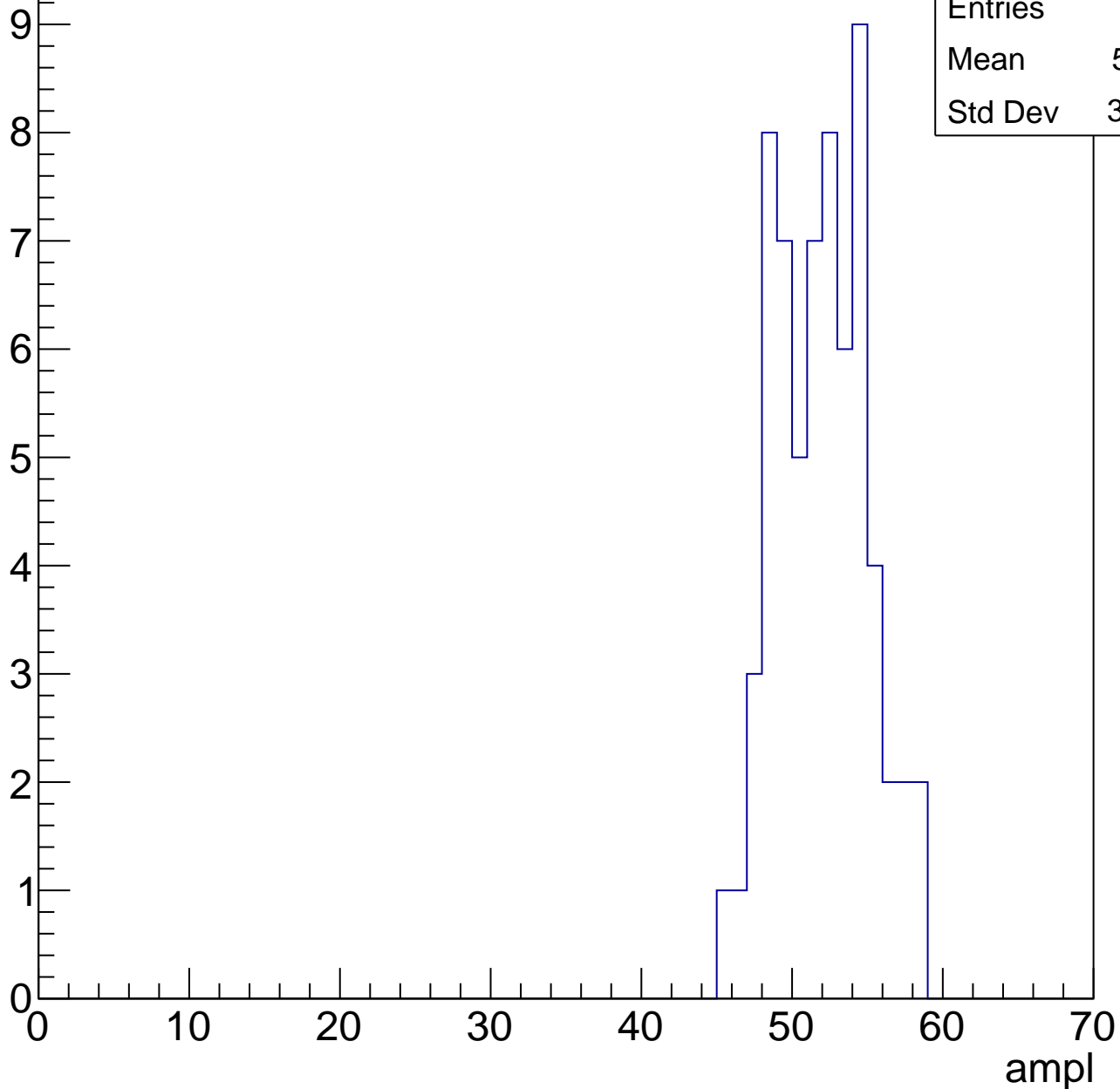
**Gaus Width: 3.2466**



# B0L000S, U18-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

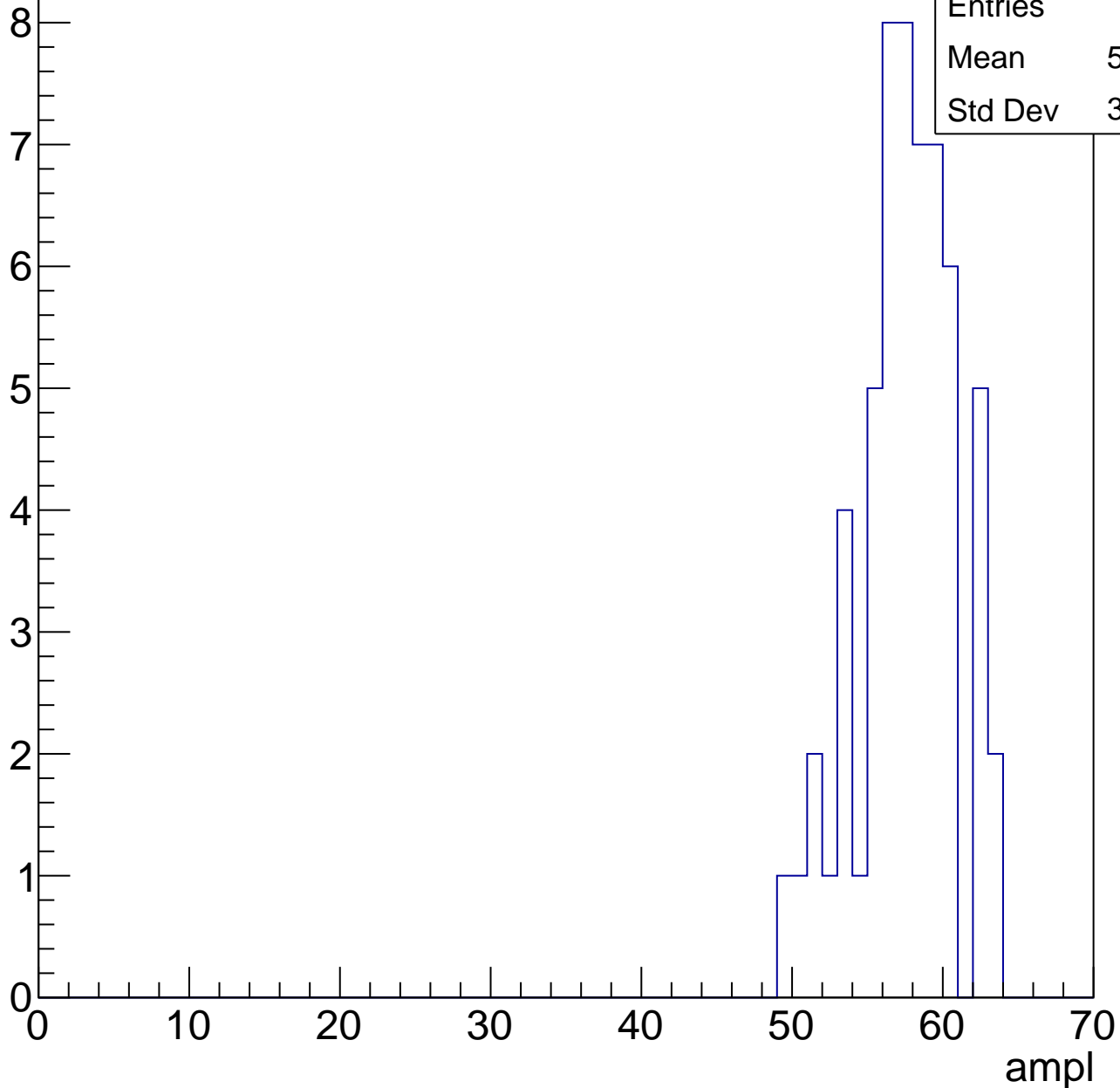


Entries	65
Mean	51.51
Std Dev	3.024

# B0L000S, U18-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

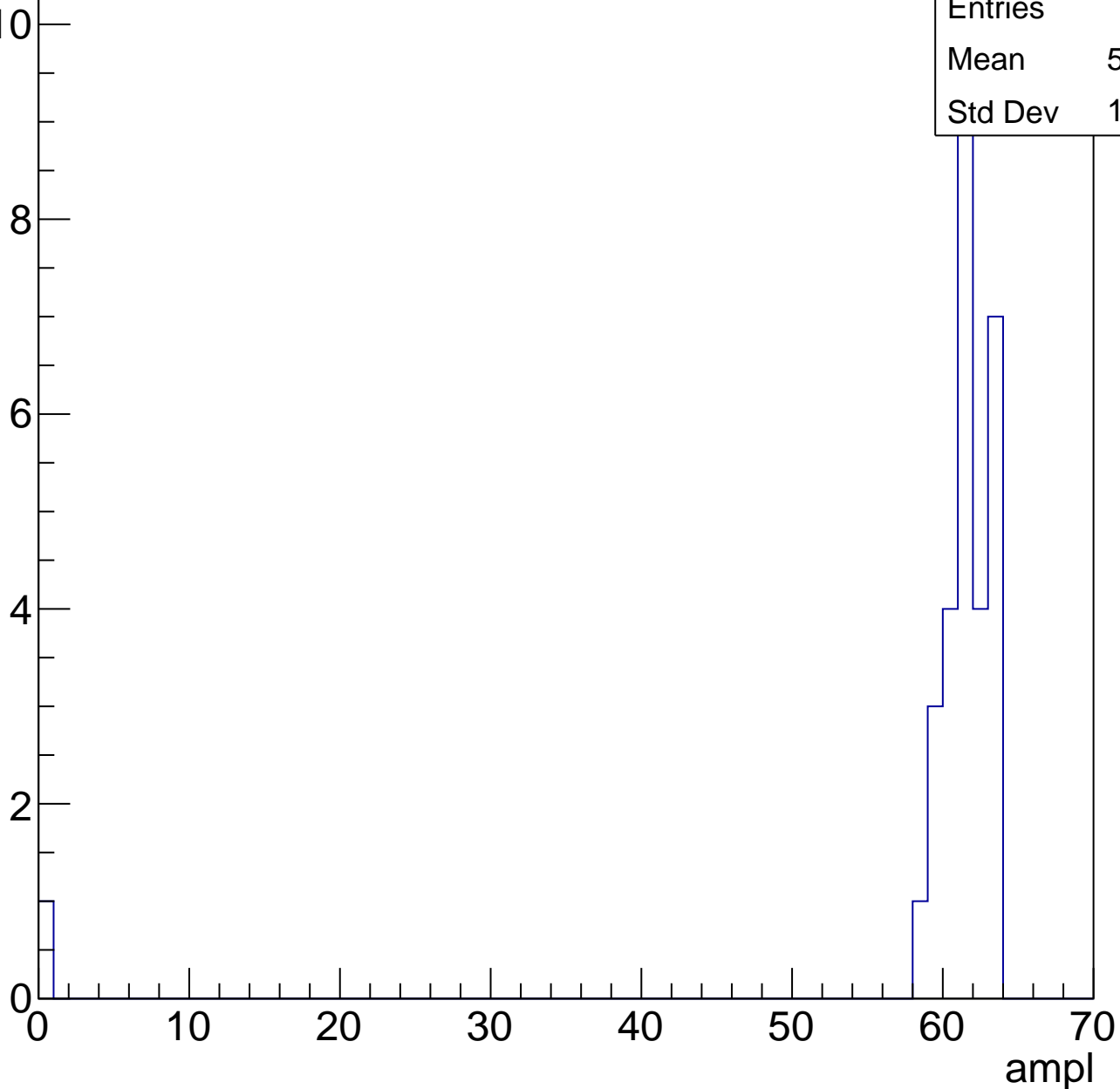


# B0L000S, U18-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	59.13
Std Dev	11.07



# B0L000S, U18-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	25
Std Dev	0

ampl

0 10 20 30 40 50 60 70

# B0L000S, U18-ch42, adc0

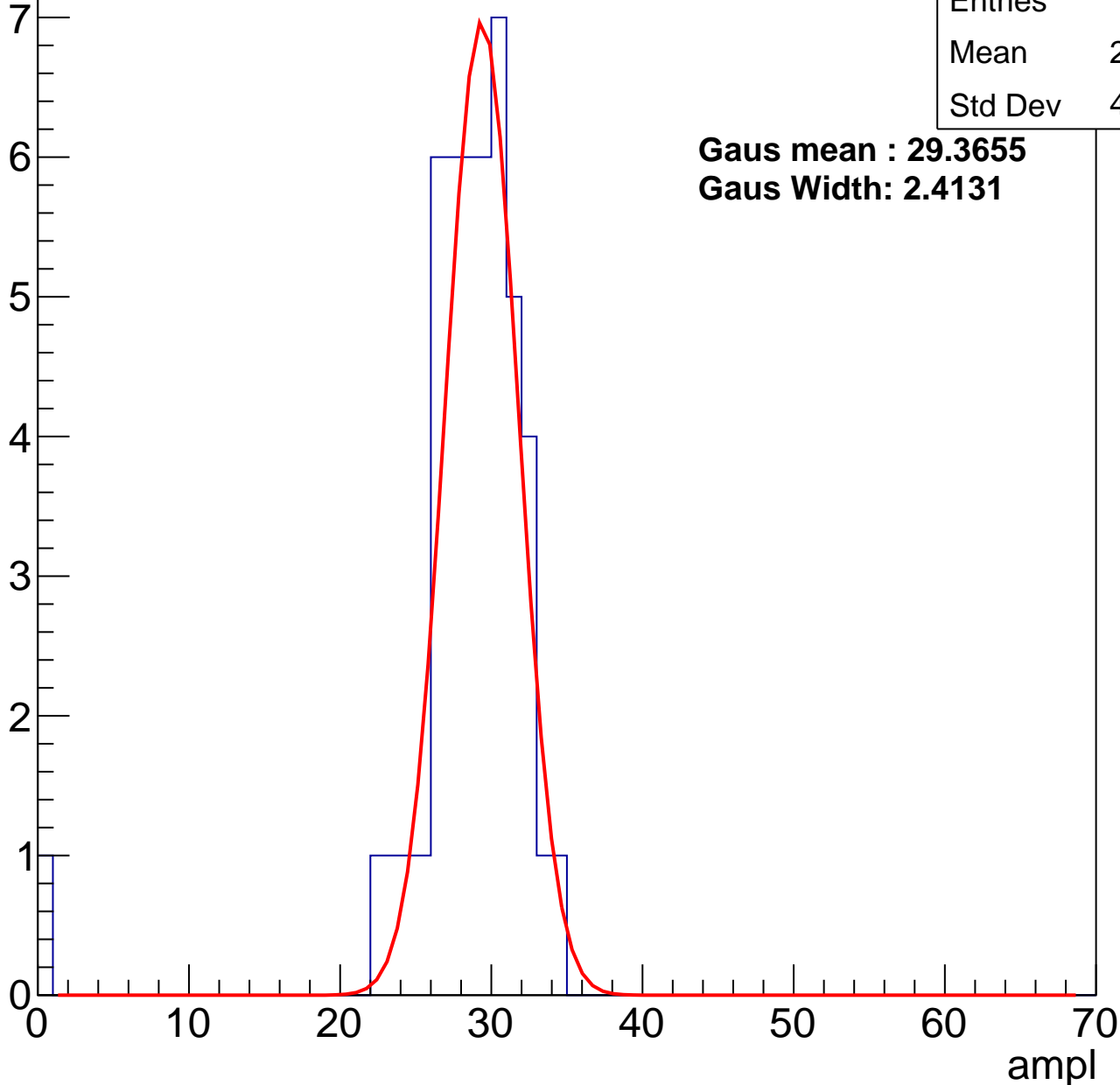
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	27.96
Std Dev	4.846

**Gaus mean : 29.3655**

**Gaus Width: 2.4131**



# B0L000S, U18-ch42, adc1

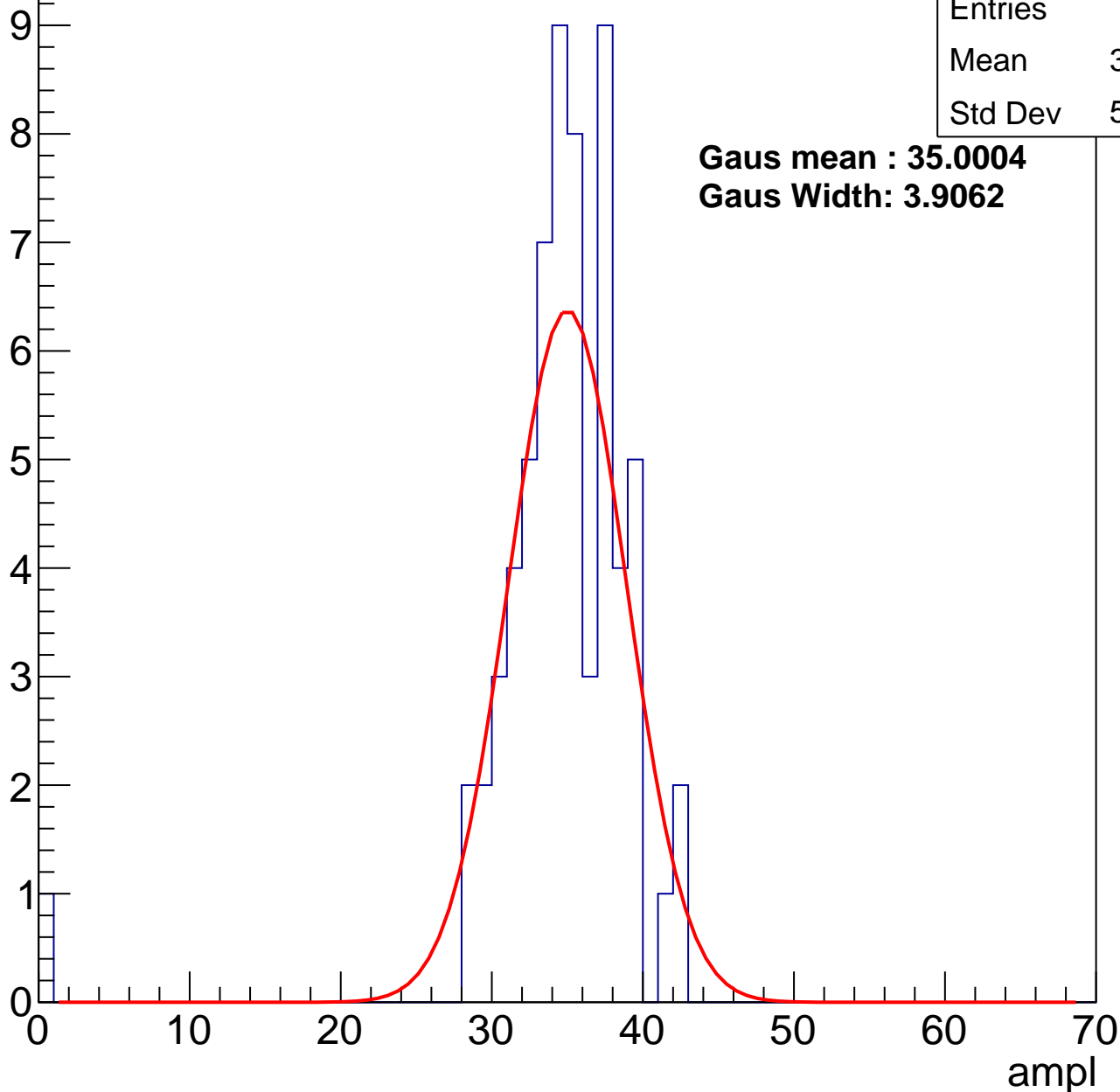
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.12
Std Dev	5.342

**Gaus mean : 35.0004**

**Gaus Width: 3.9062**



# B0L000S, U18-ch42, adc2

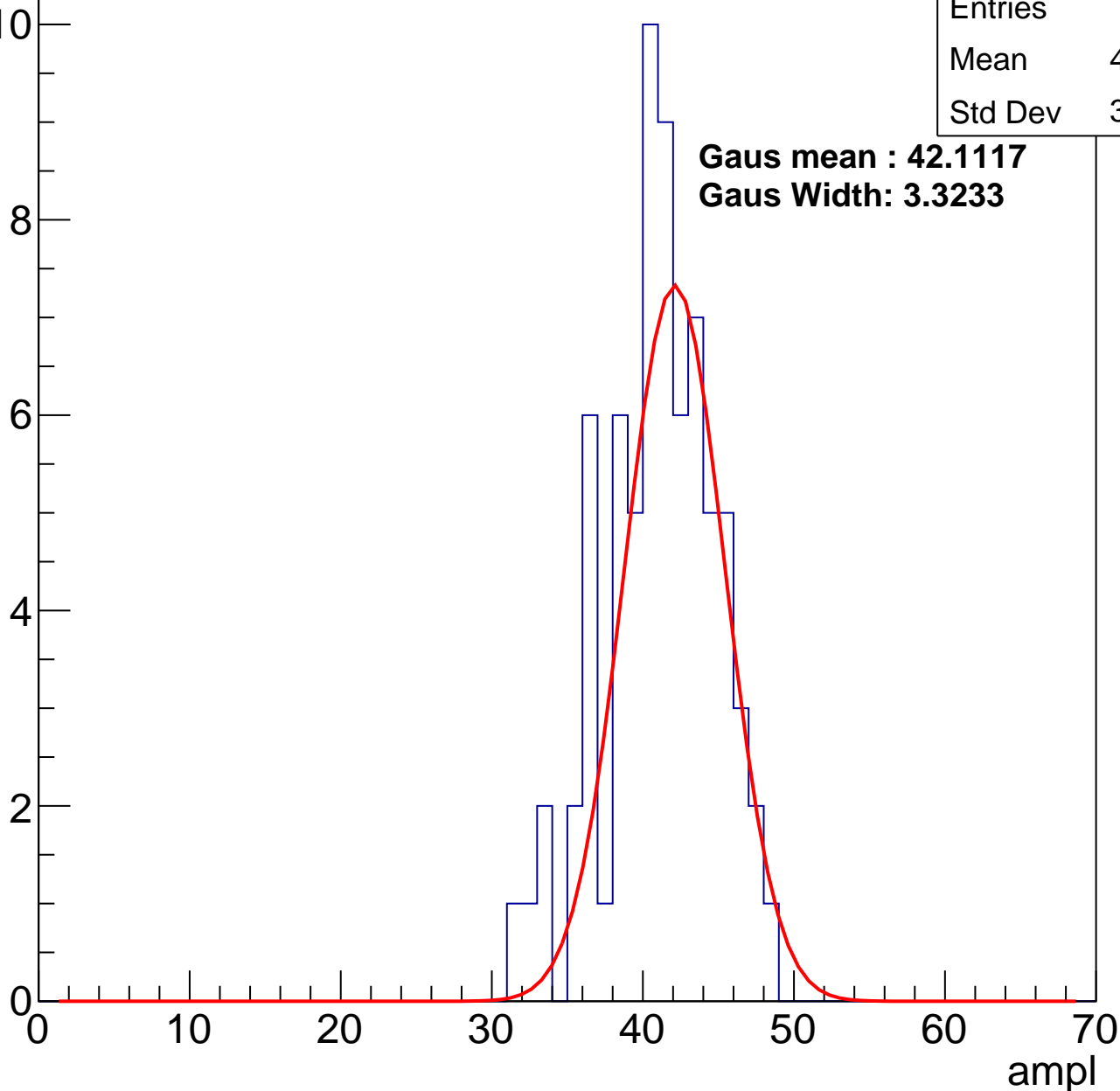
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	40.58
Std Dev	3.677

**Gaus mean : 42.1117**

**Gaus Width: 3.3233**

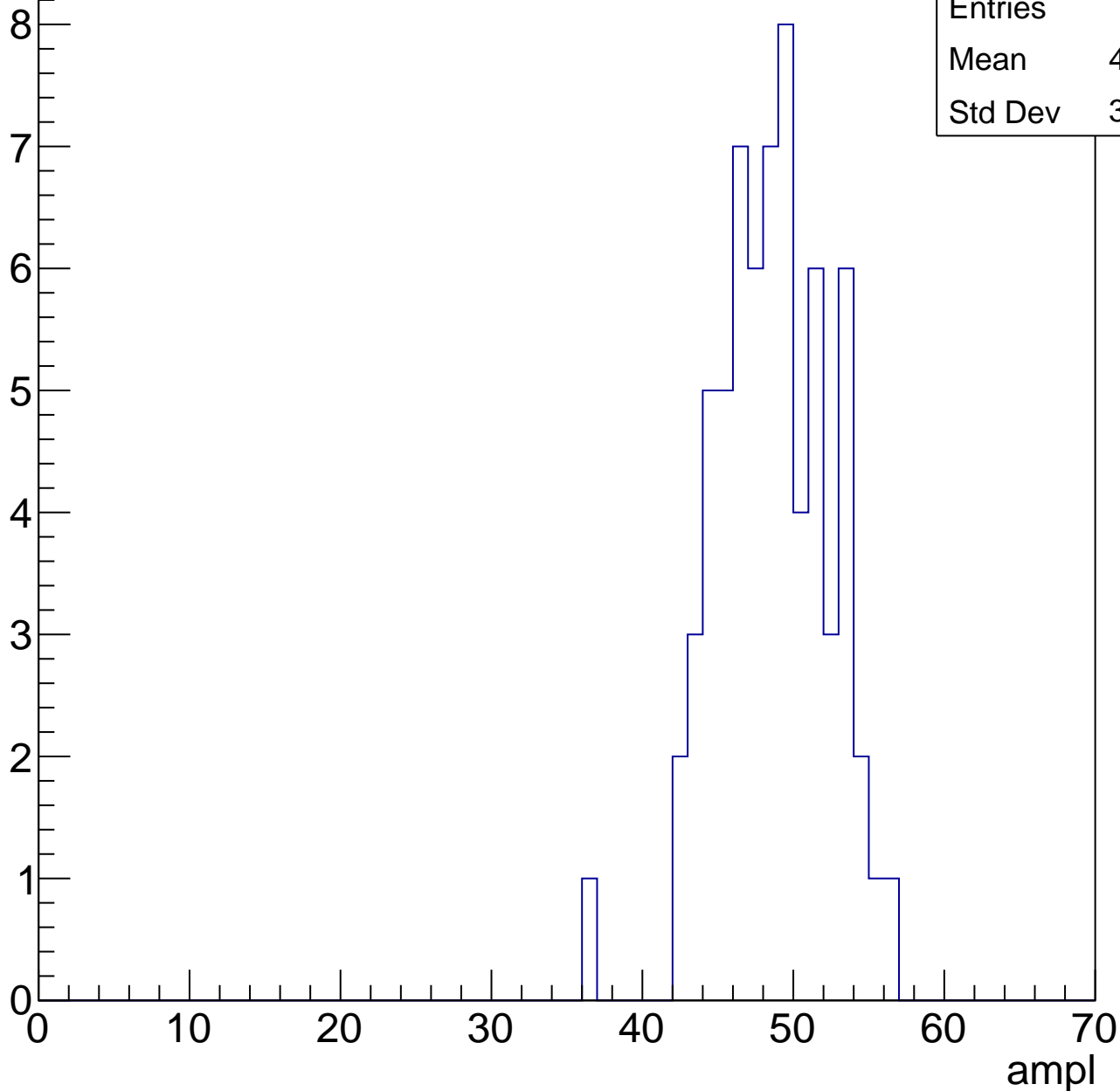


# B0L000S, U18-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

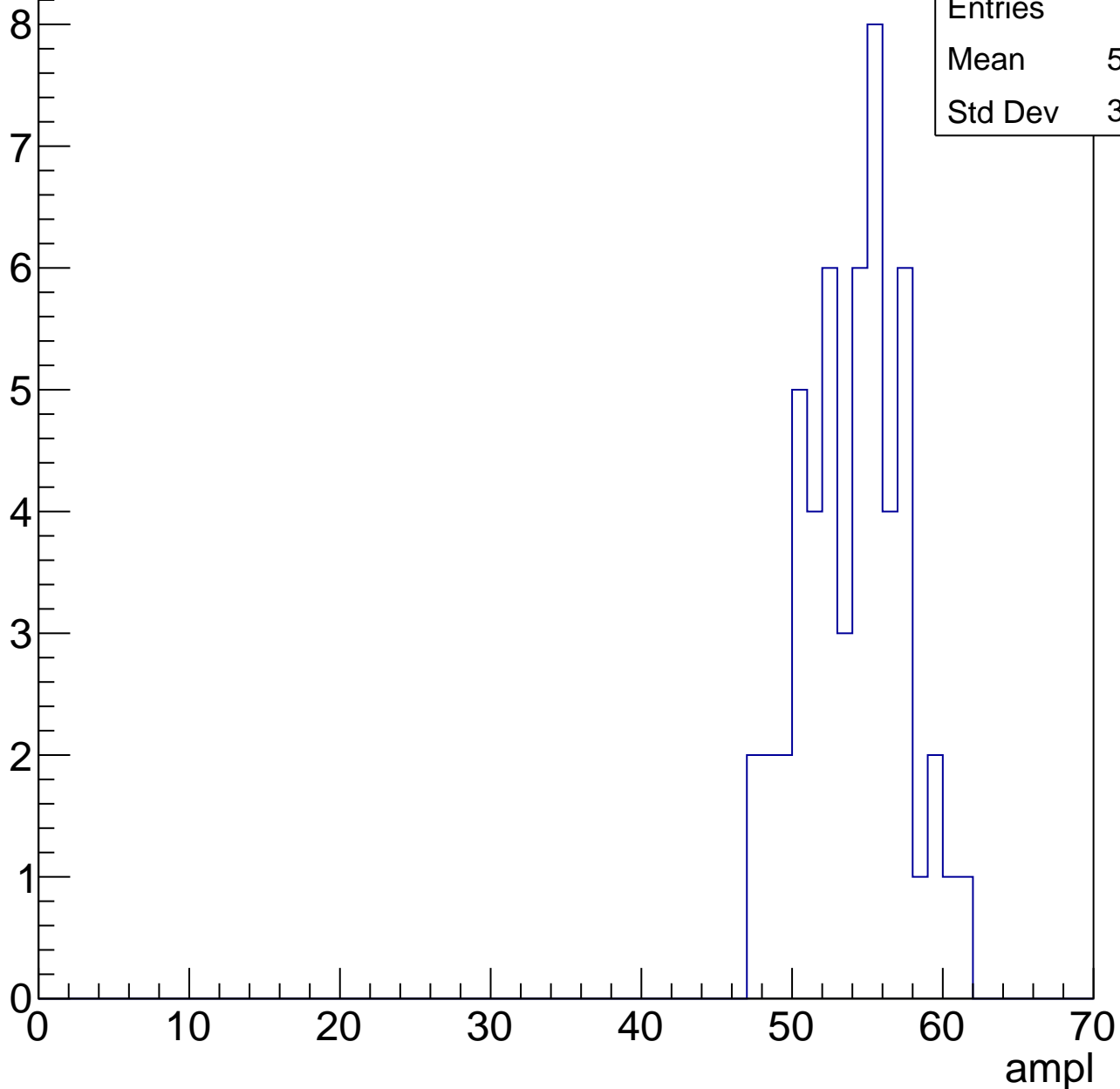
Entries	67
Mean	48.13
Std Dev	3.685



# B0L000S, U18-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

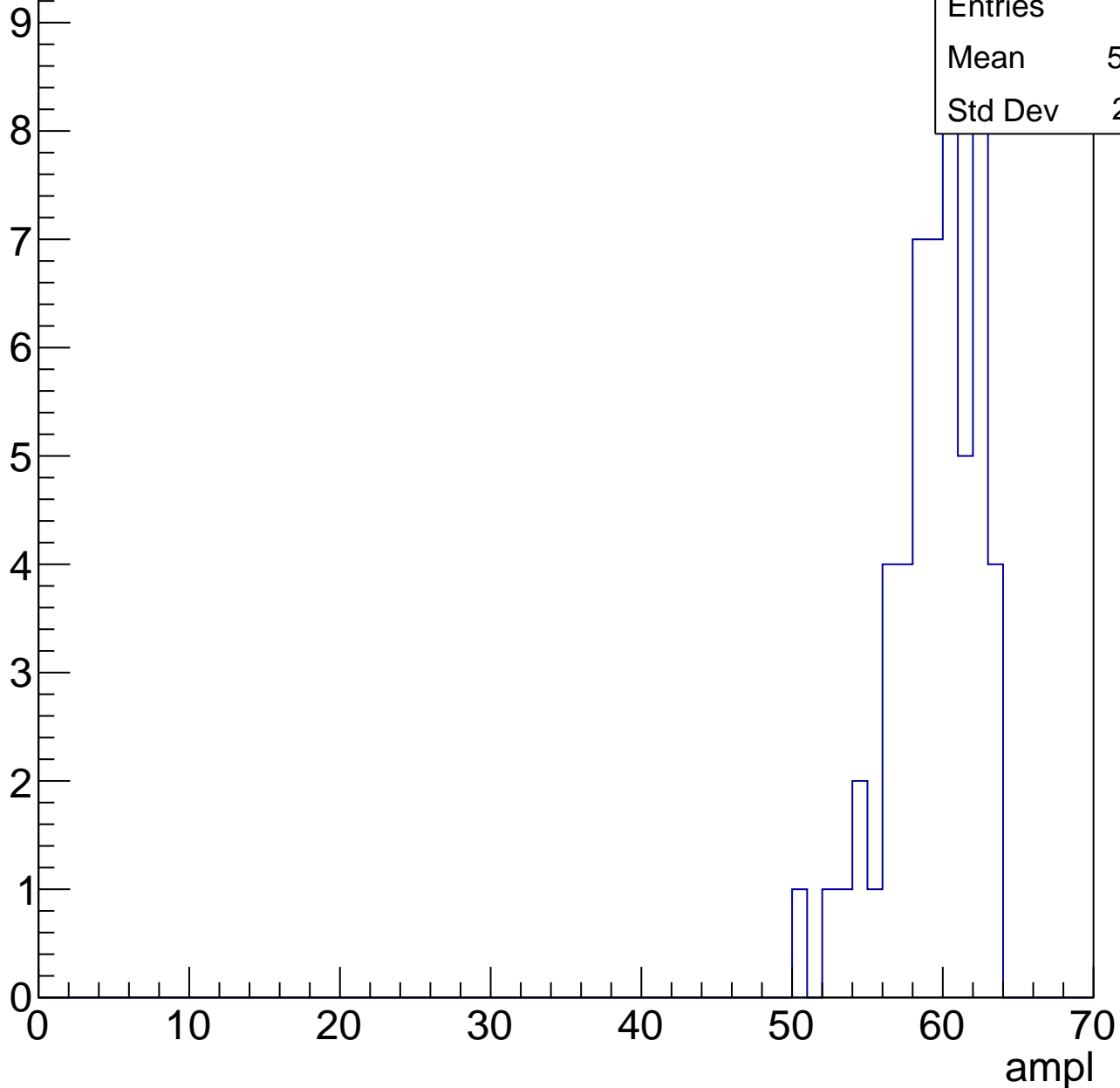
Entry



# B0L000S, U18-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

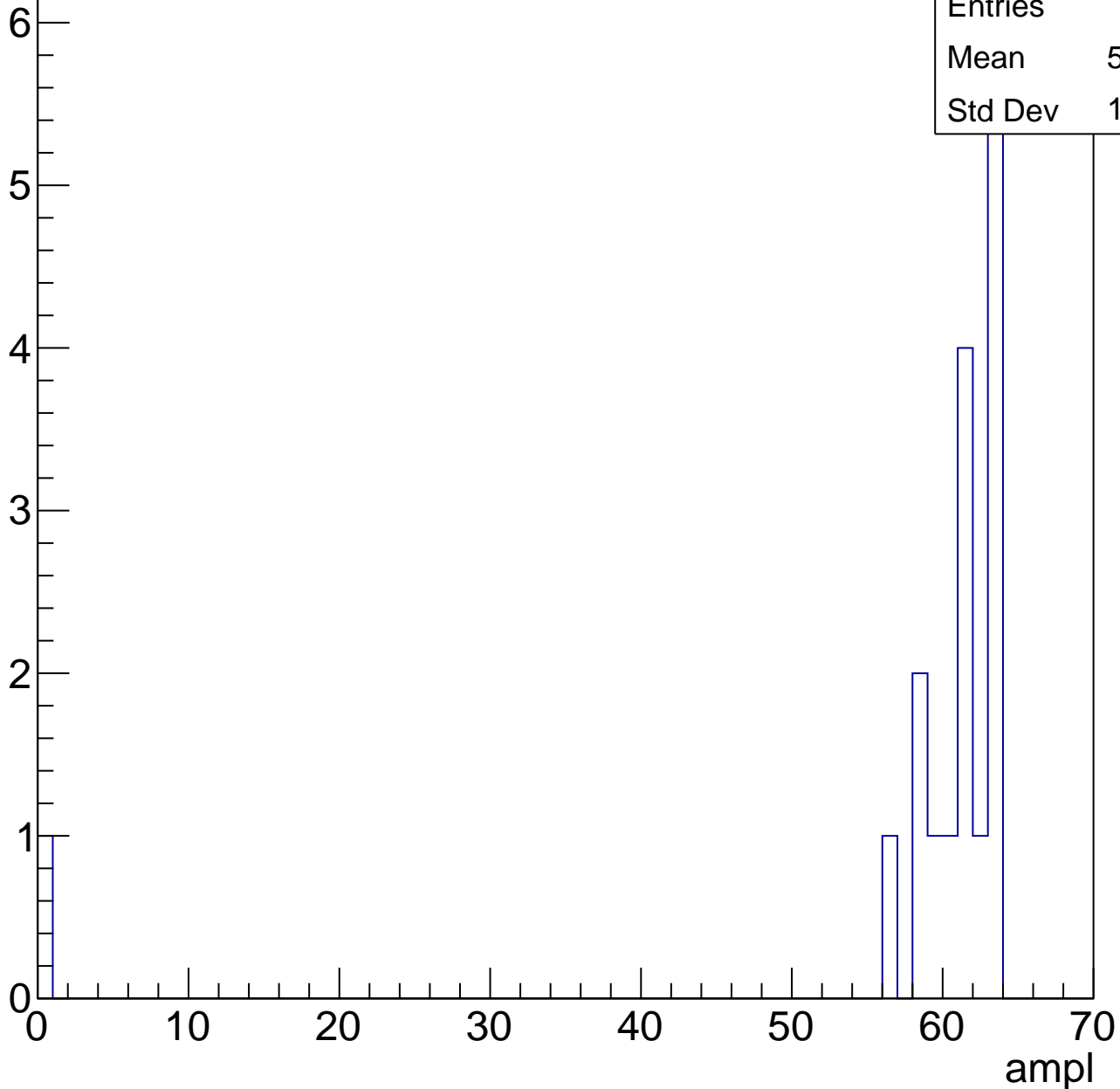


# B0L000S, U18-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.35
Std Dev	14.49

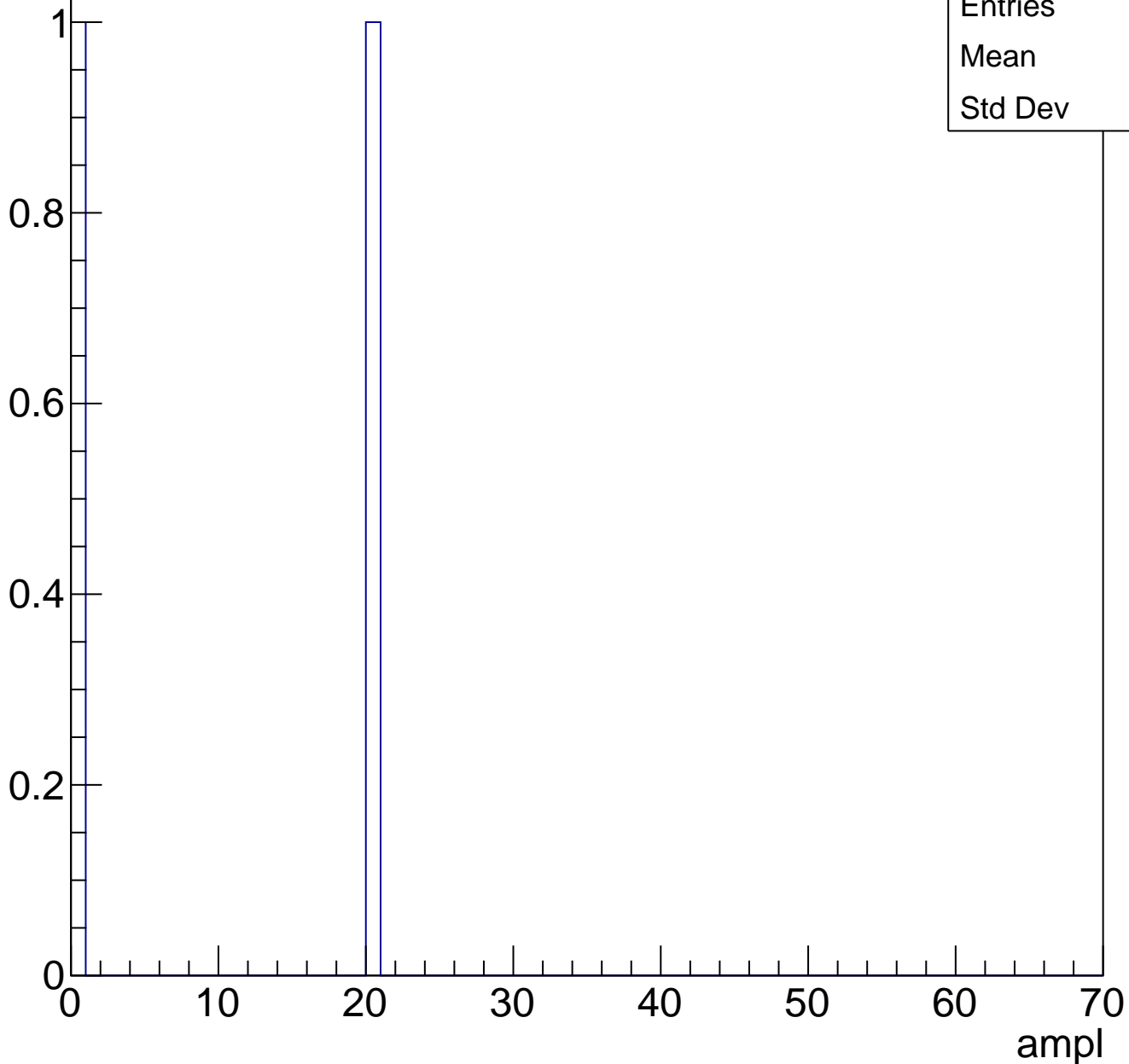




# B0L000S, U18-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U18-ch43, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	27.3
Std Dev	4.777

**Gaus mean : 28.0578**

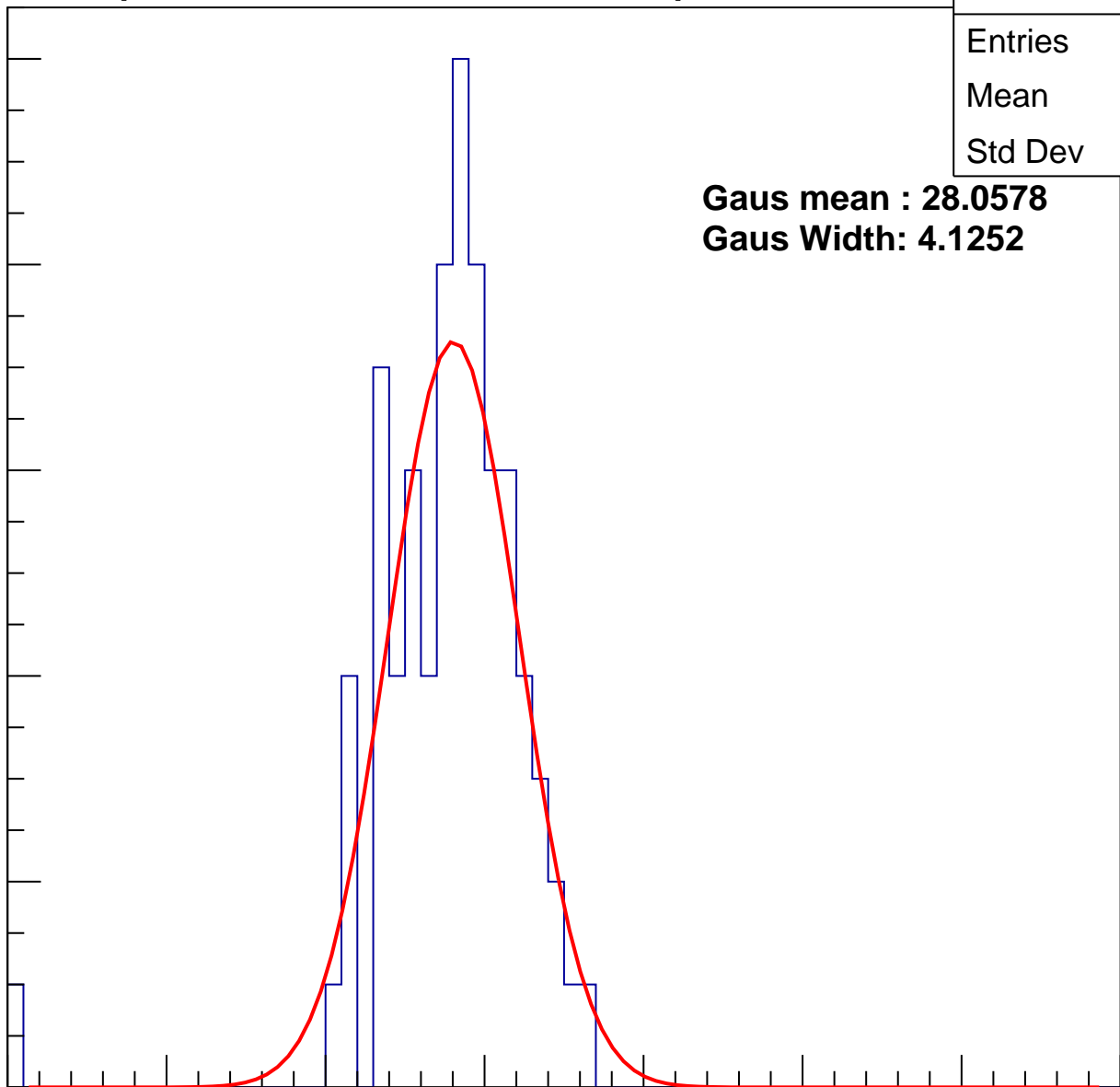
**Gaus Width: 4.1252**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch43, adc1

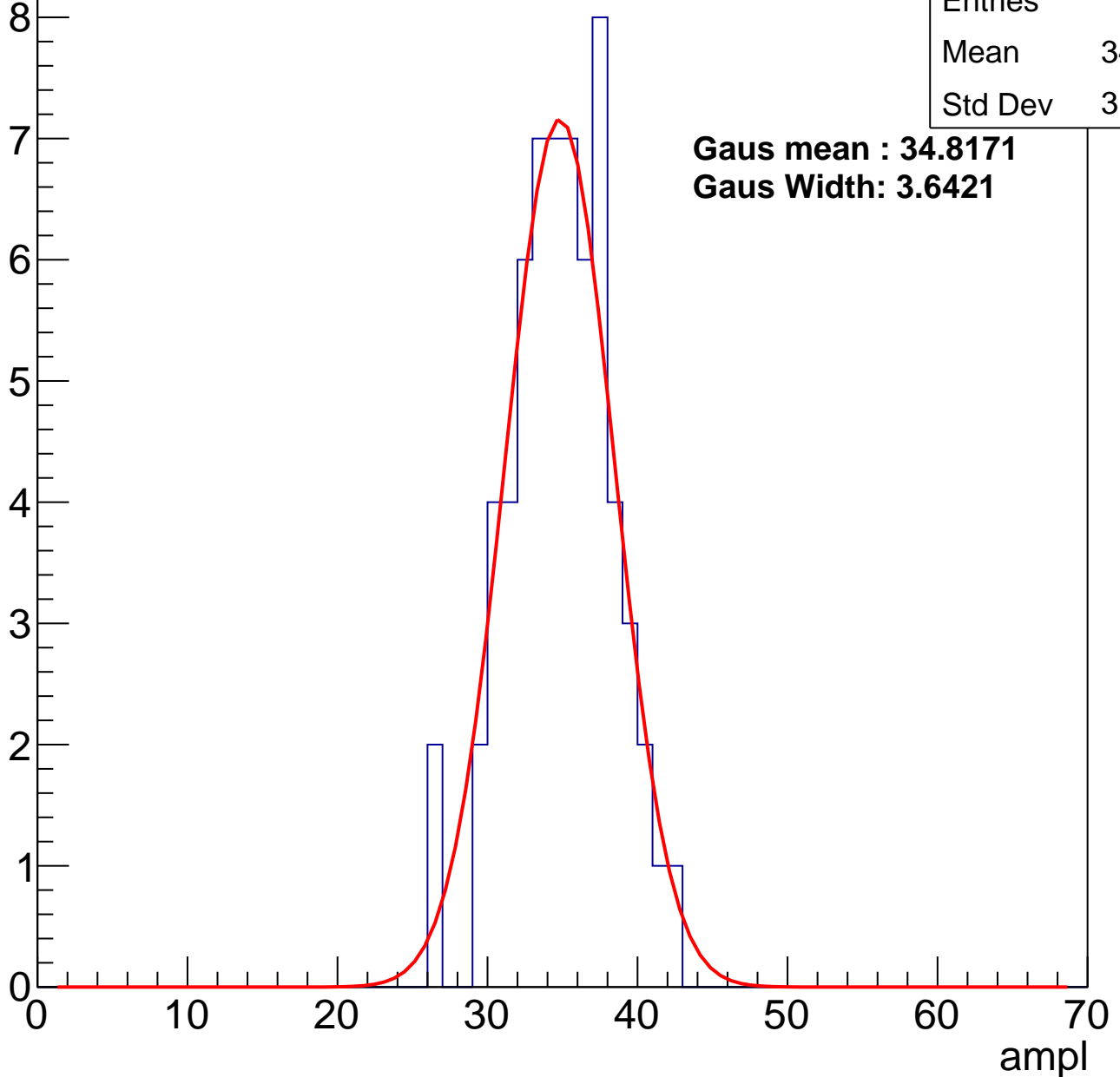
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	34.44
Std Dev	3.363

**Gaus mean : 34.8171**

**Gaus Width: 3.6421**



# B0L000S, U18-ch43, adc2

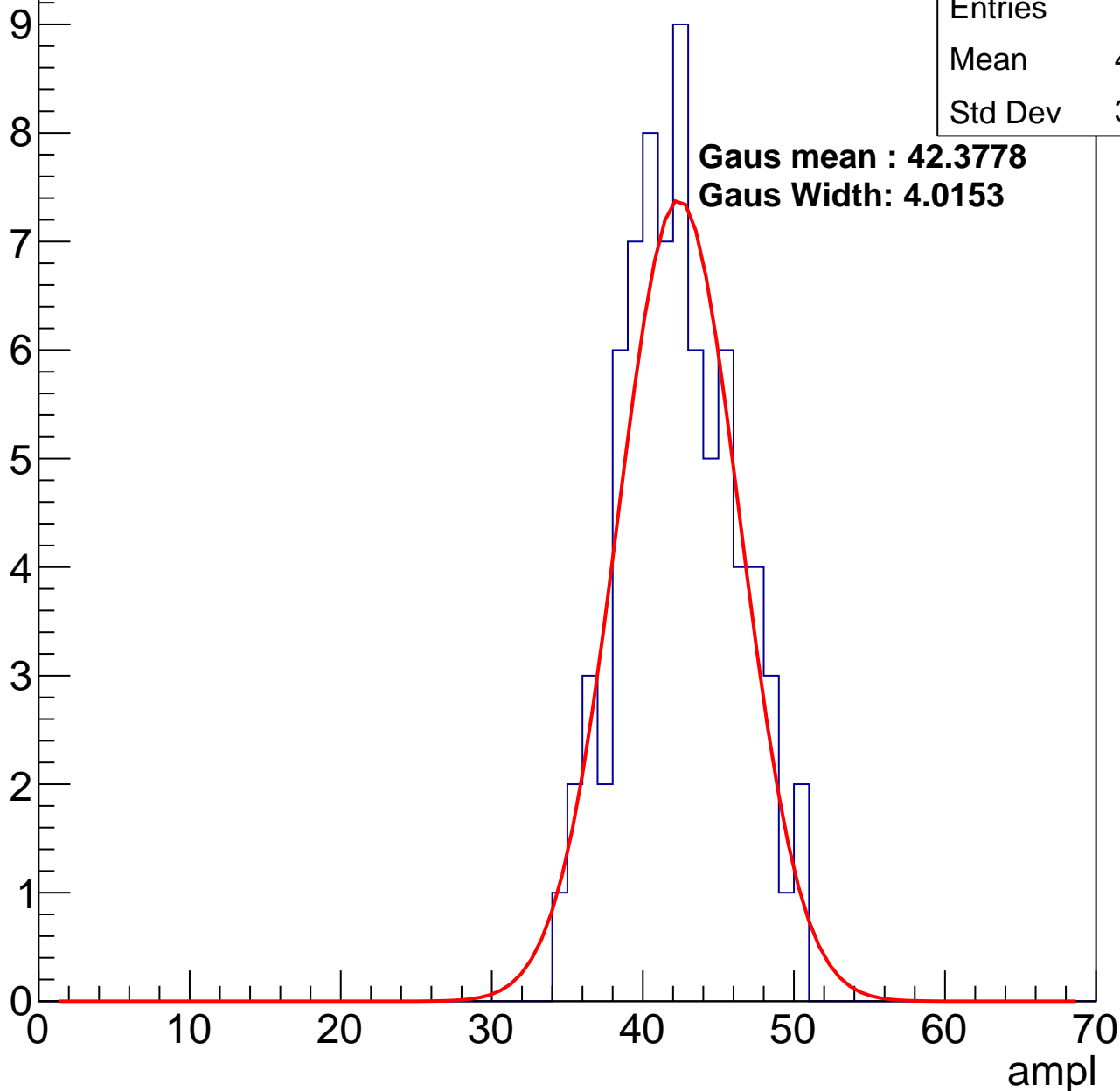
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	41.91
Std Dev	3.711

**Gaus mean : 42.3778**

**Gaus Width: 4.0153**

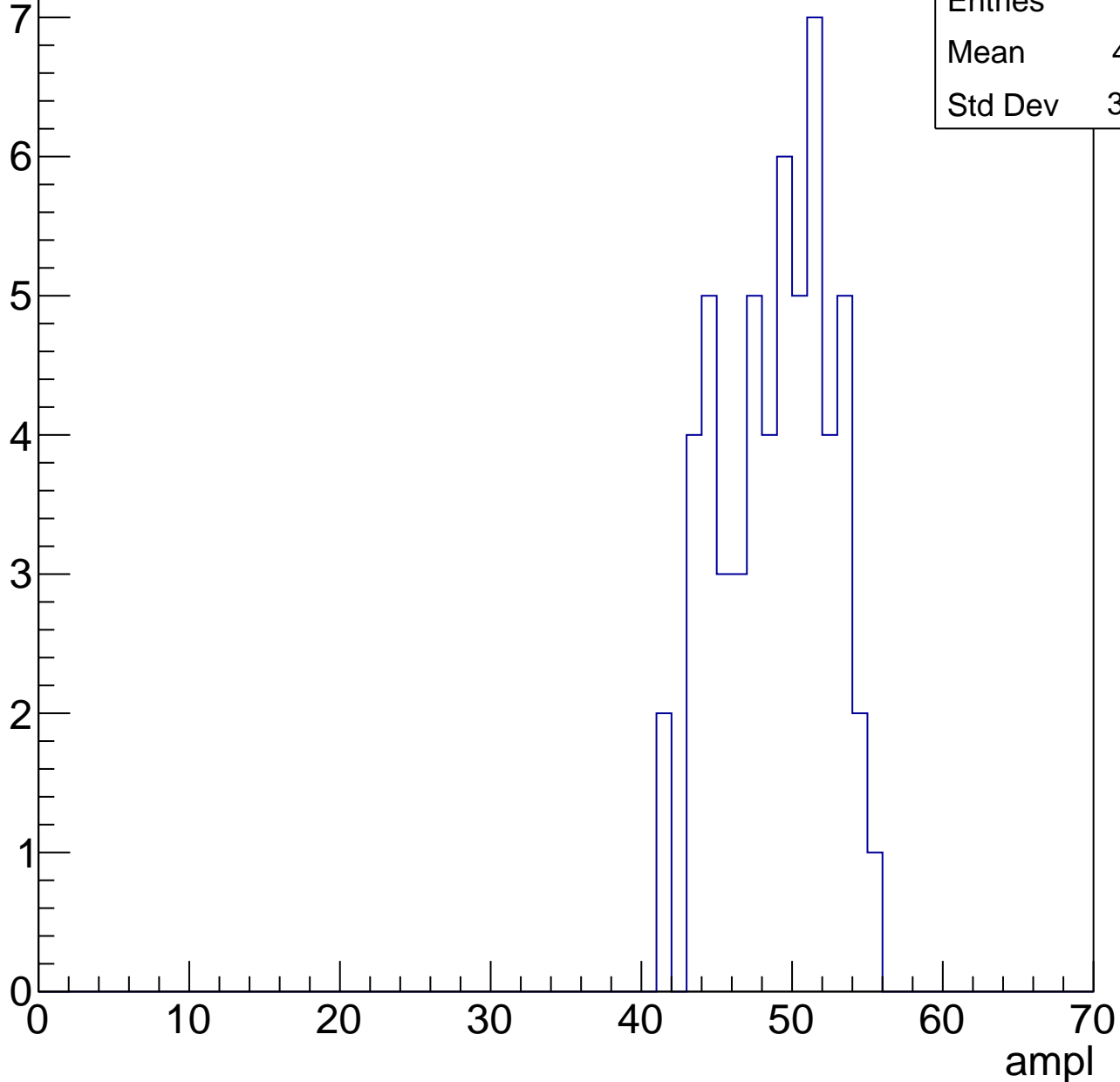


# B0L000S, U18-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	48.41
Std Dev	3.565

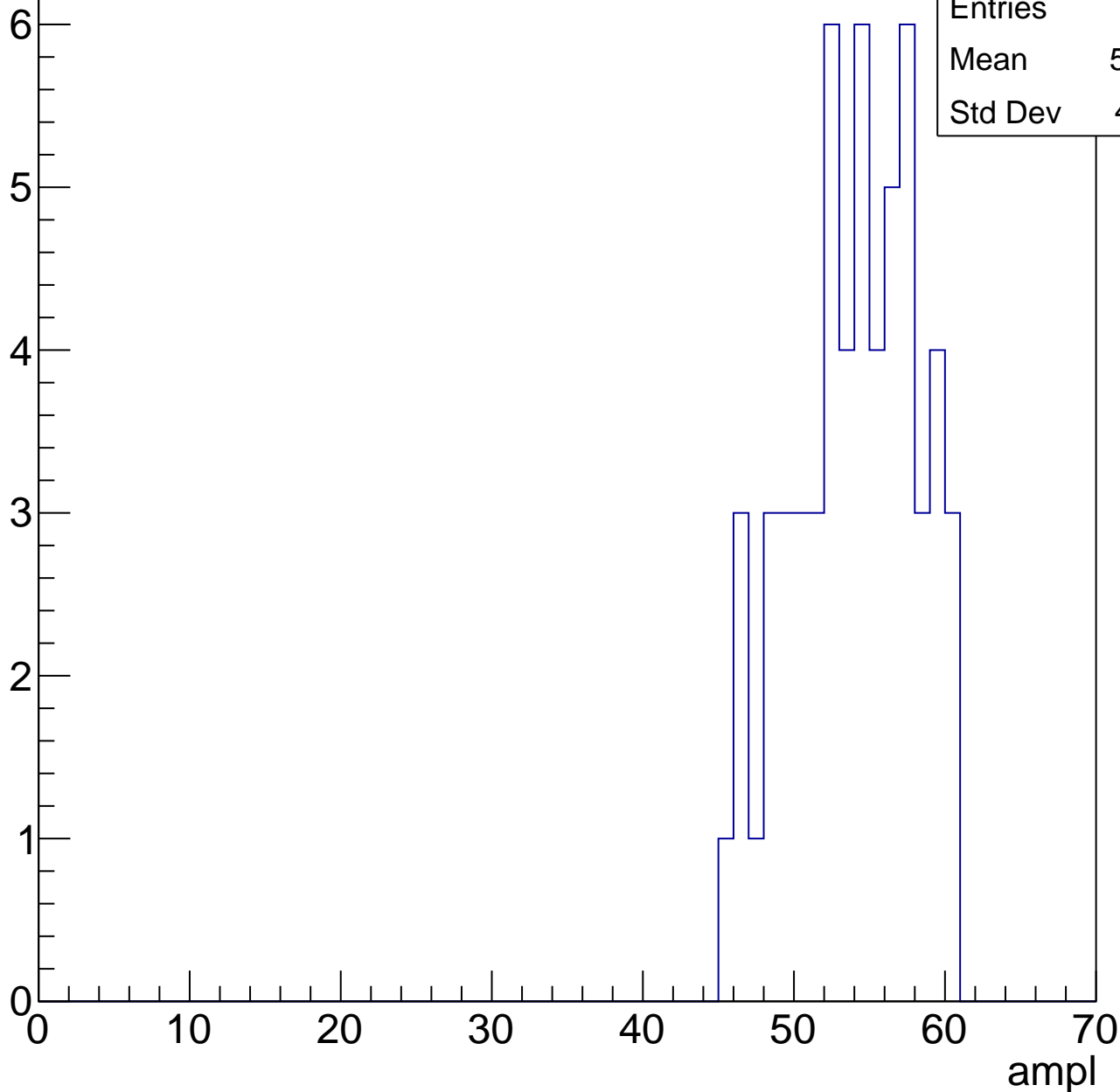


# B0L000S, U18-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	53.52
Std Dev	4.001

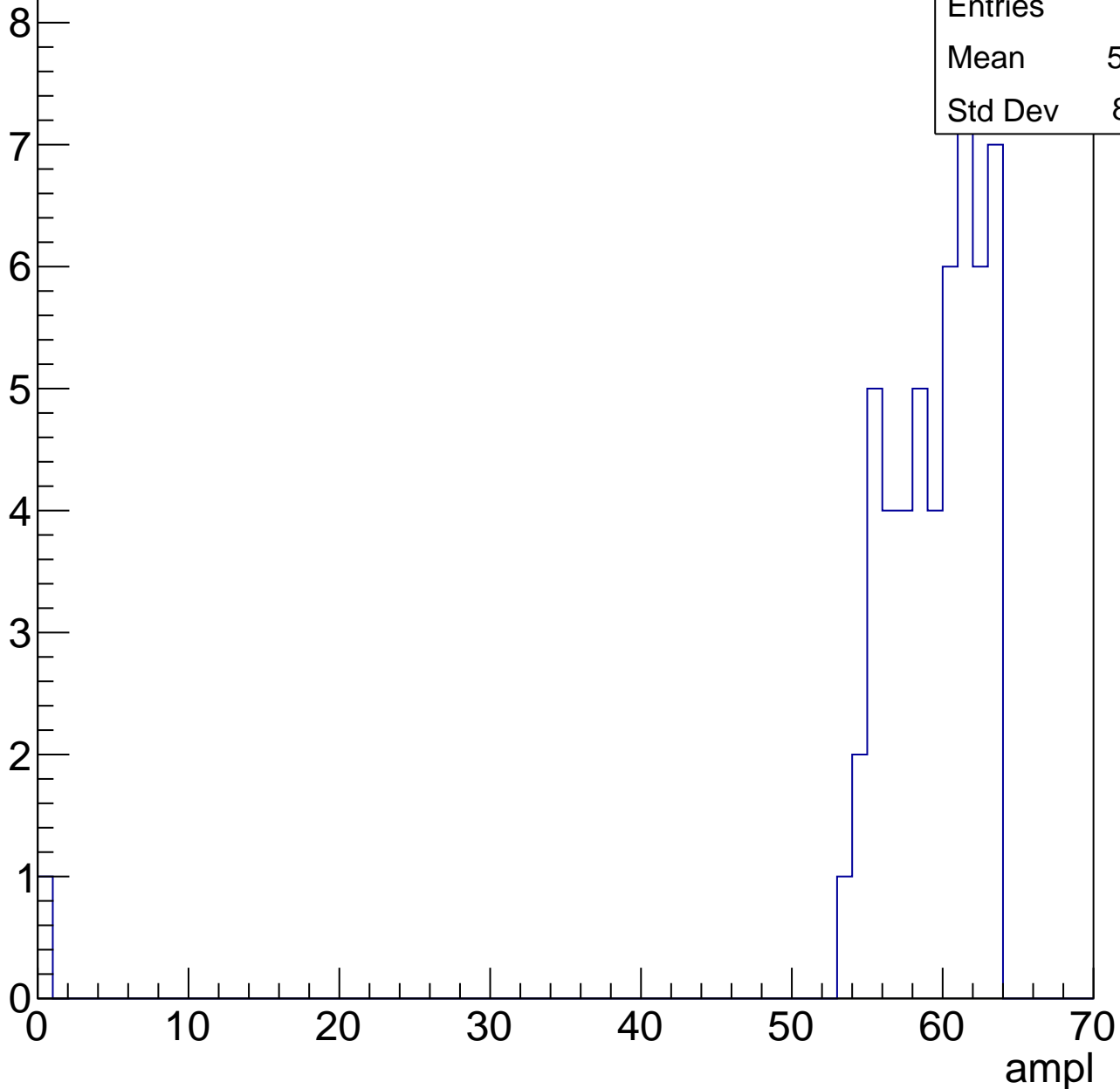


# B0L000S, U18-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

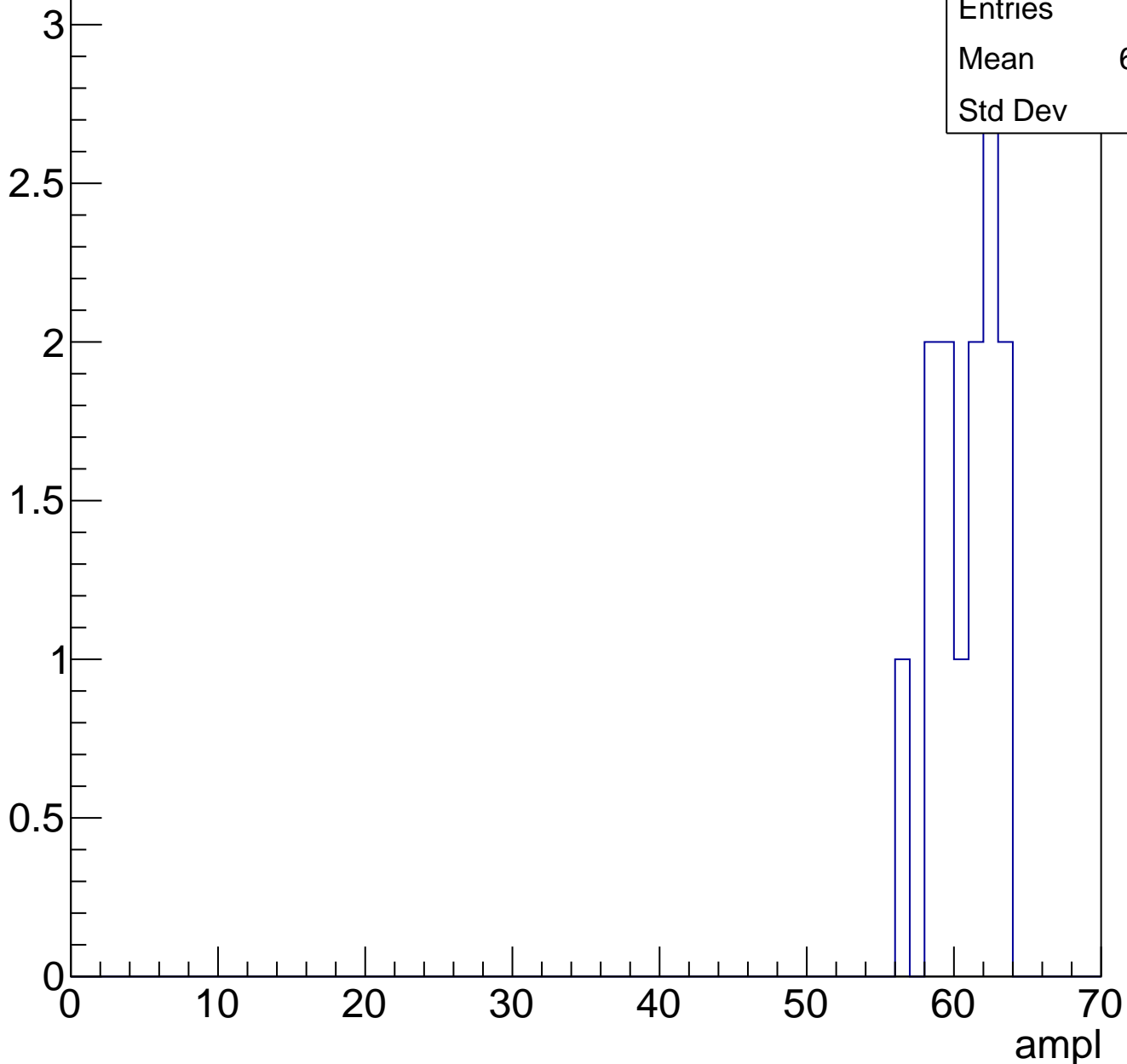
Entries	53
Mean	58.02
Std Dev	8.531



# B0L000S, U18-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch44, adc0

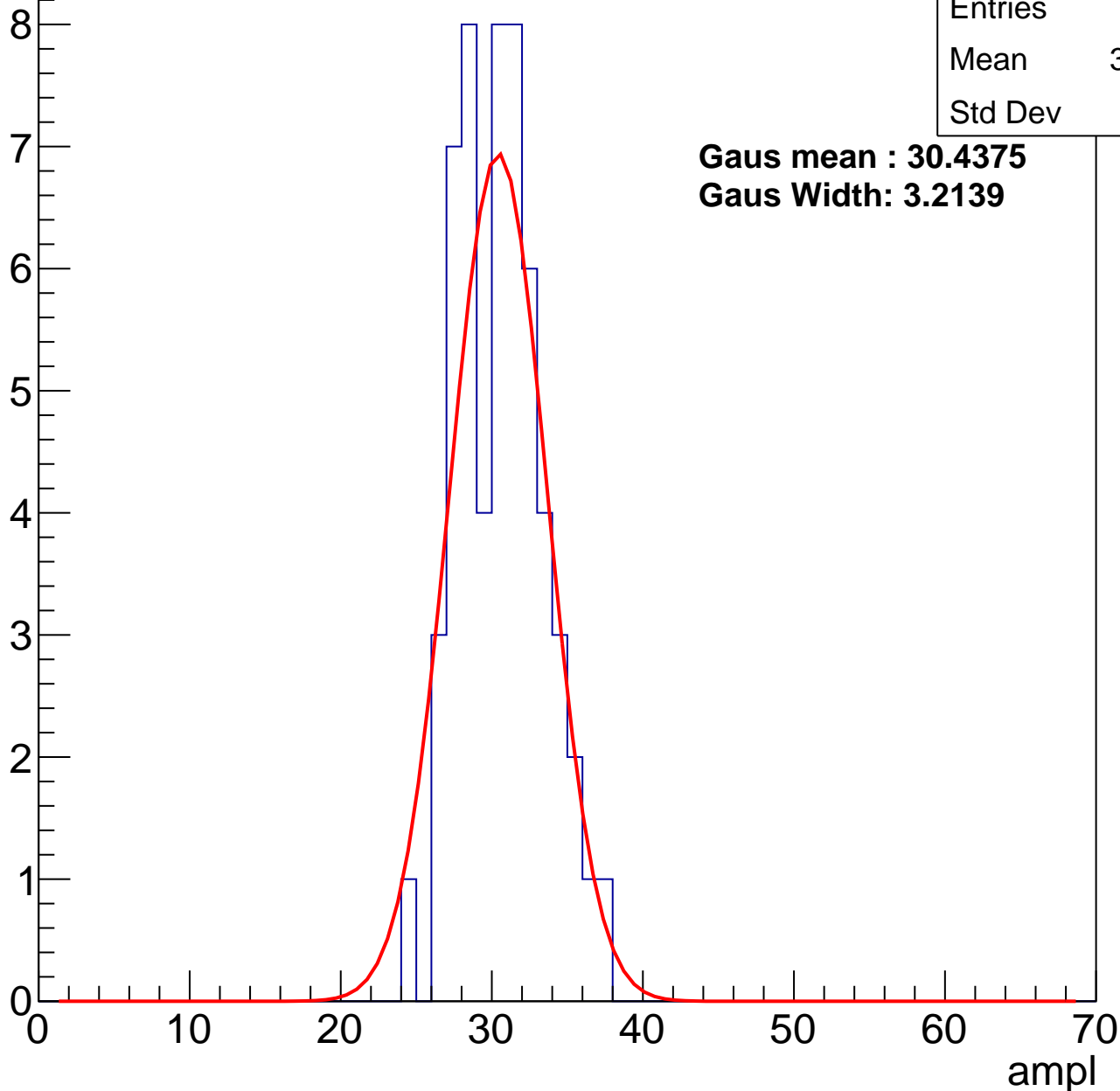
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	30.14
Std Dev	2.78

**Gaus mean : 30.4375**

**Gaus Width: 3.2139**



# B0L000S, U18-ch44, adc1

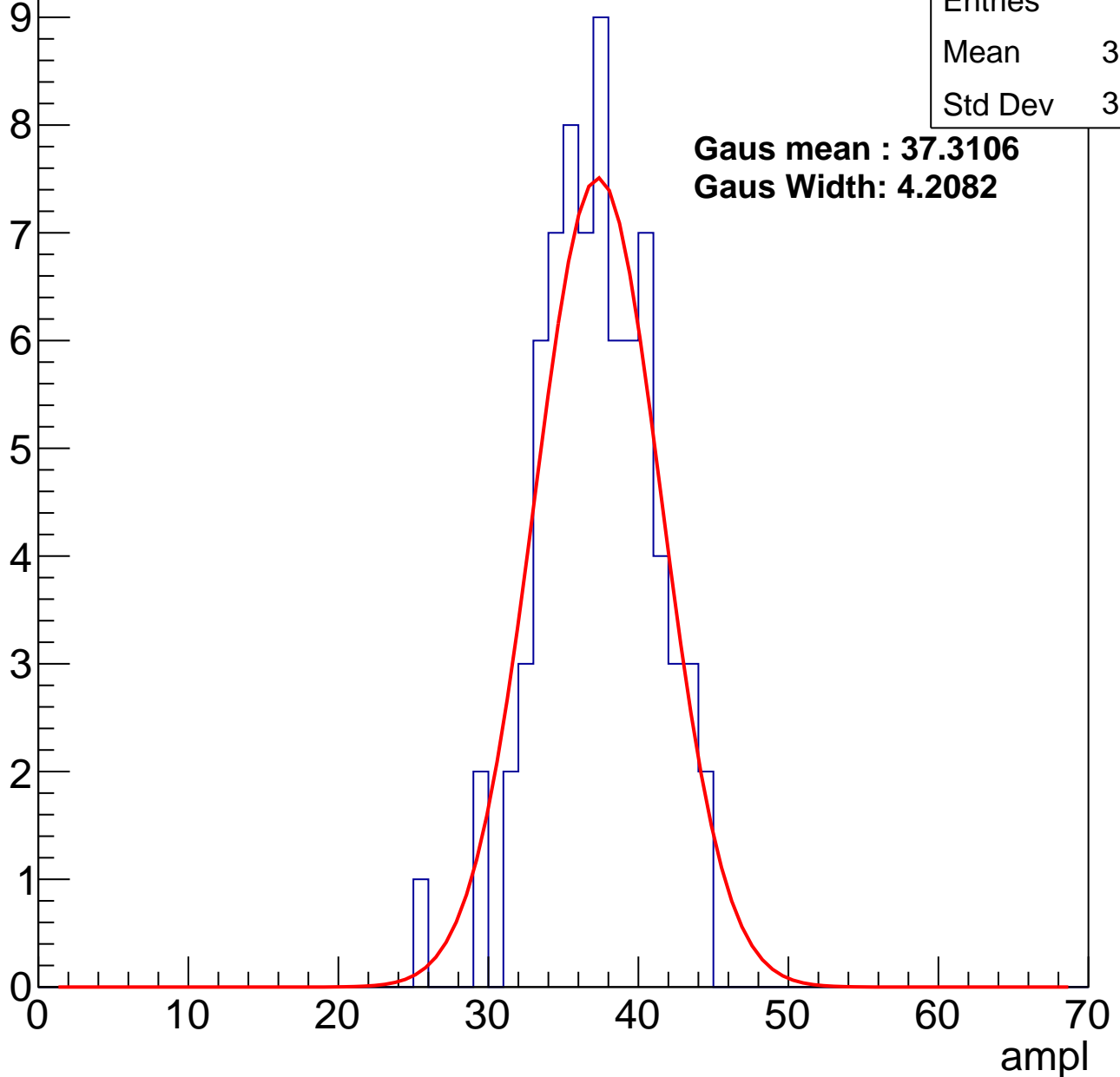
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.72
Std Dev	3.716

**Gaus mean : 37.3106**

**Gaus Width: 4.2082**



# B0L000S, U18-ch44, adc2

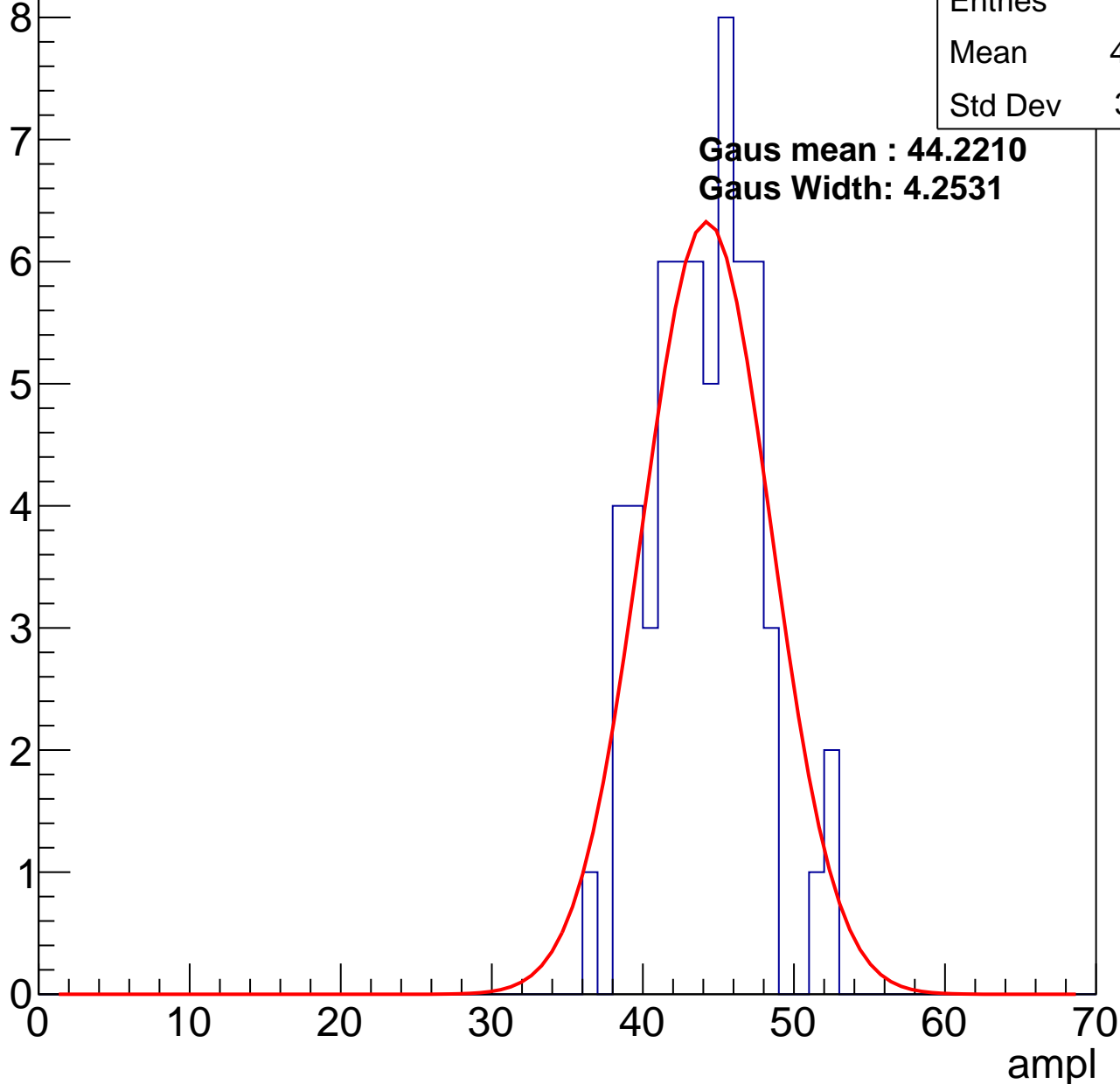
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.56
Std Dev	3.481

**Gaus mean : 44.2210**

**Gaus Width: 4.2531**



# B0L000S, U18-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

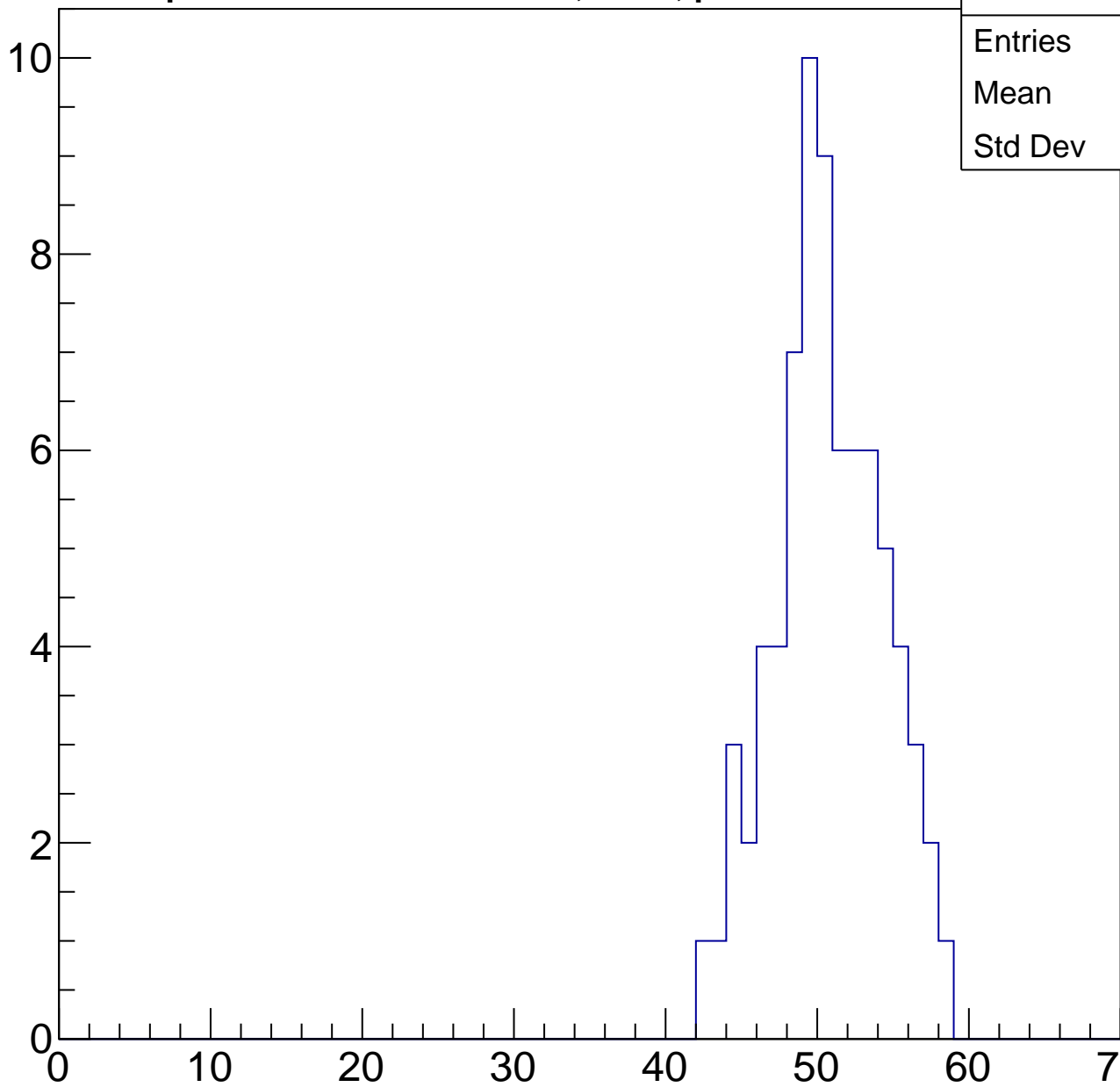
Entries	74
Mean	50.28
Std Dev	3.558

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

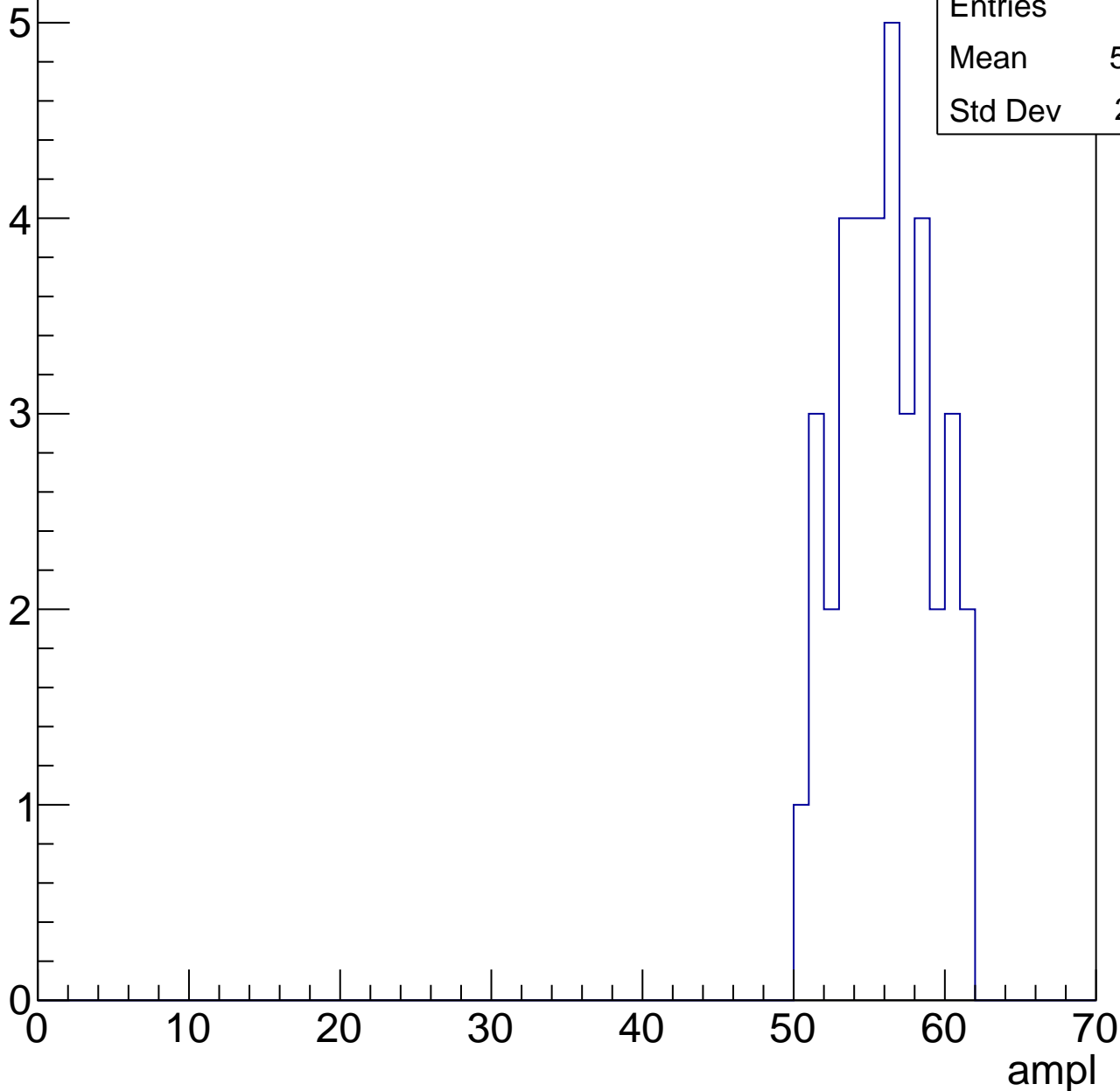


# B0L000S, U18-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

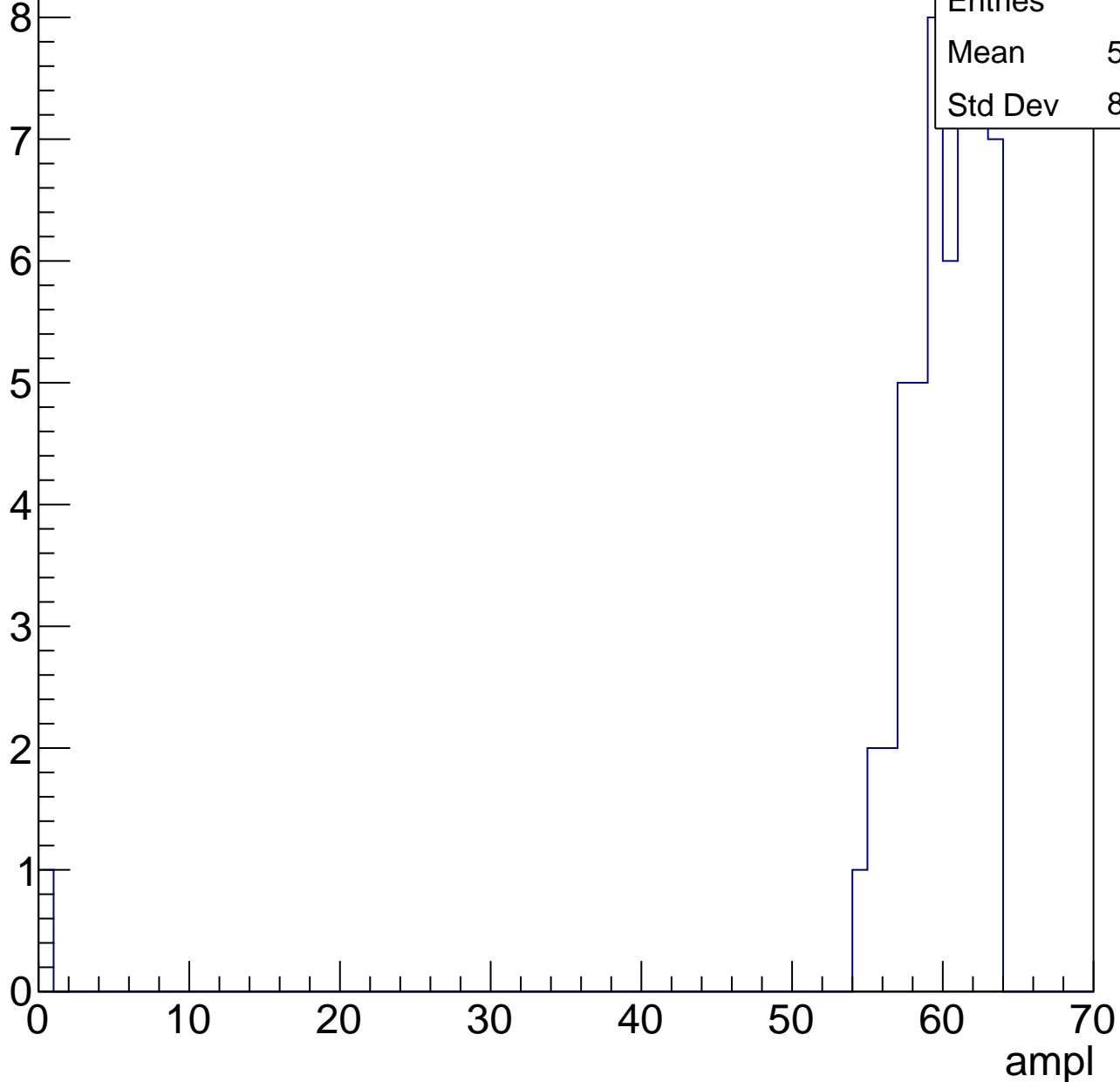
Entries	37
Mean	55.62
Std Dev	2.981



# B0L000S, U18-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

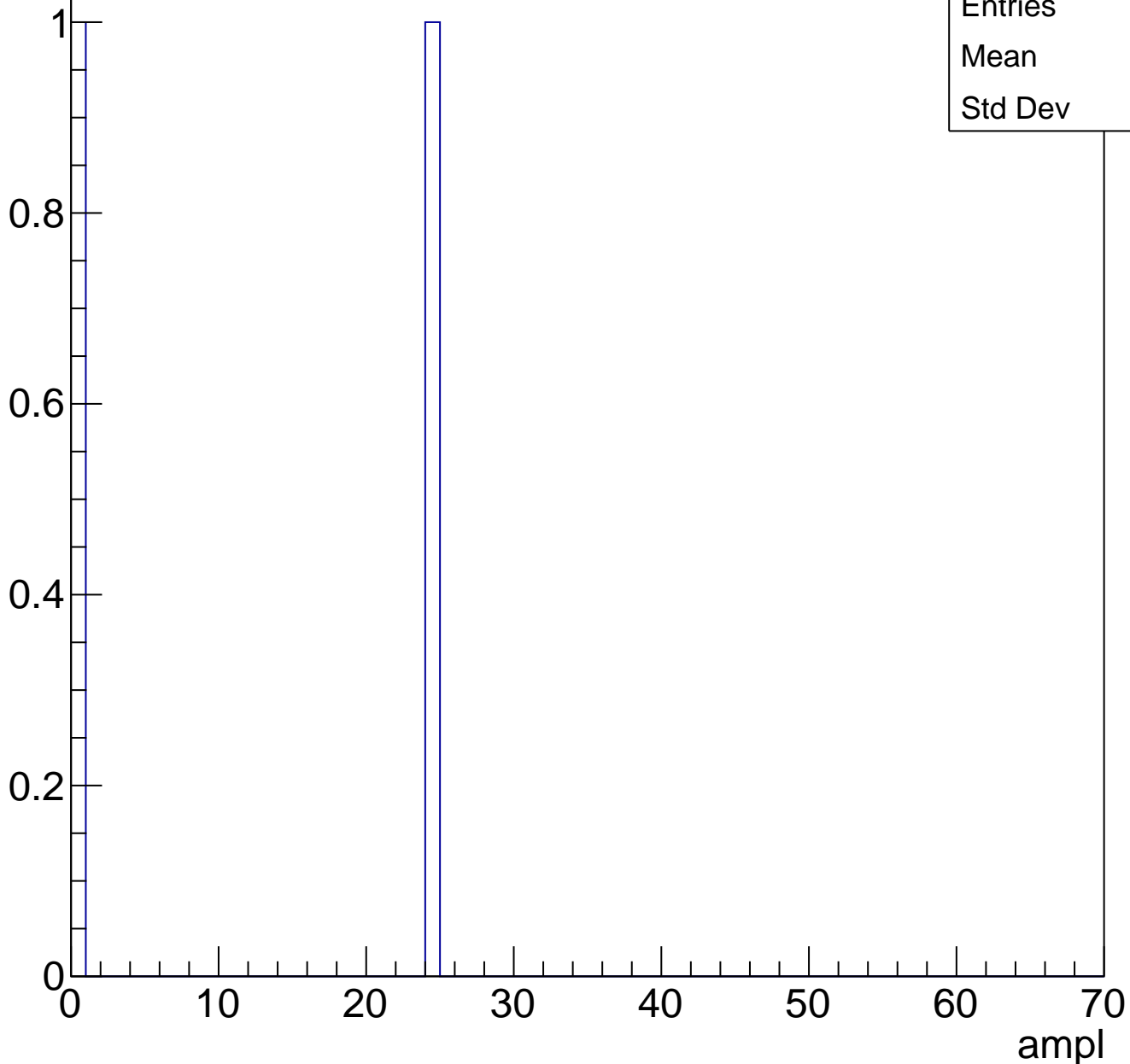




# B0L000S, U18-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch45, adc0

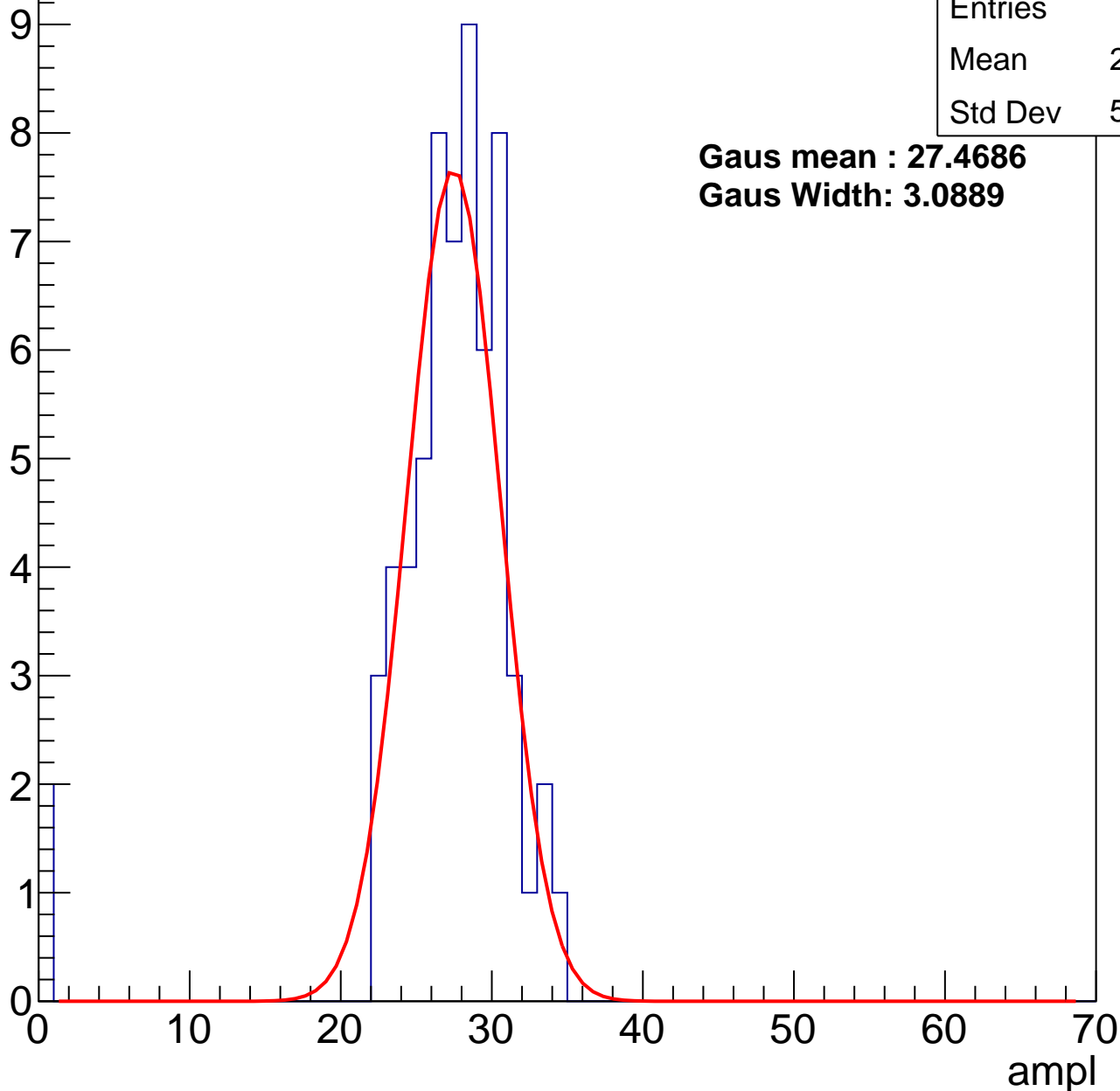
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	26.46
Std Dev	5.552

**Gaus mean : 27.4686**

**Gaus Width: 3.0889**



# B0L000S, U18-ch45, adc1

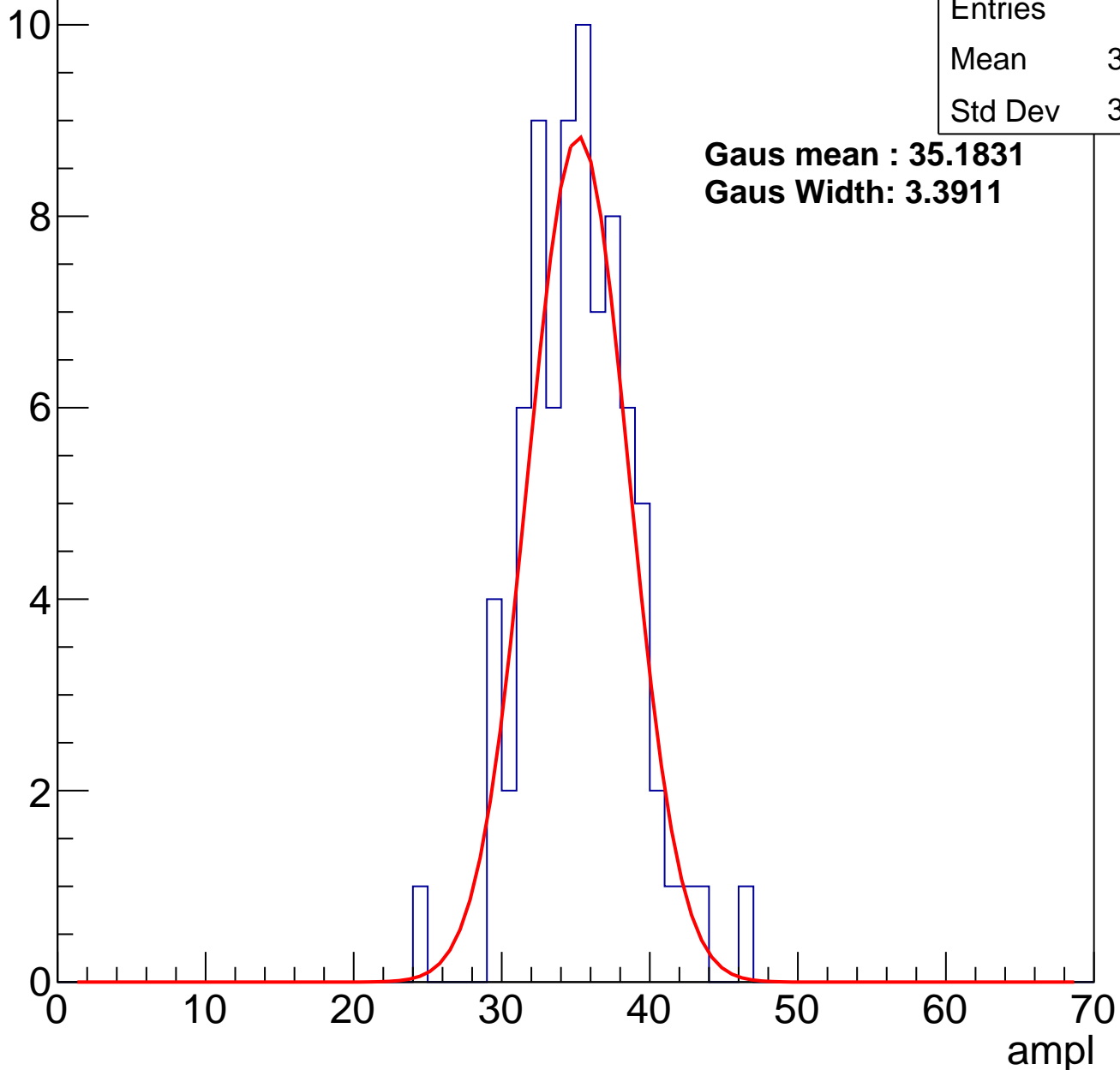
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	34.82
Std Dev	3.585

**Gaus mean : 35.1831**

**Gaus Width: 3.3911**

Entry



# B0L000S, U18-ch45, adc2

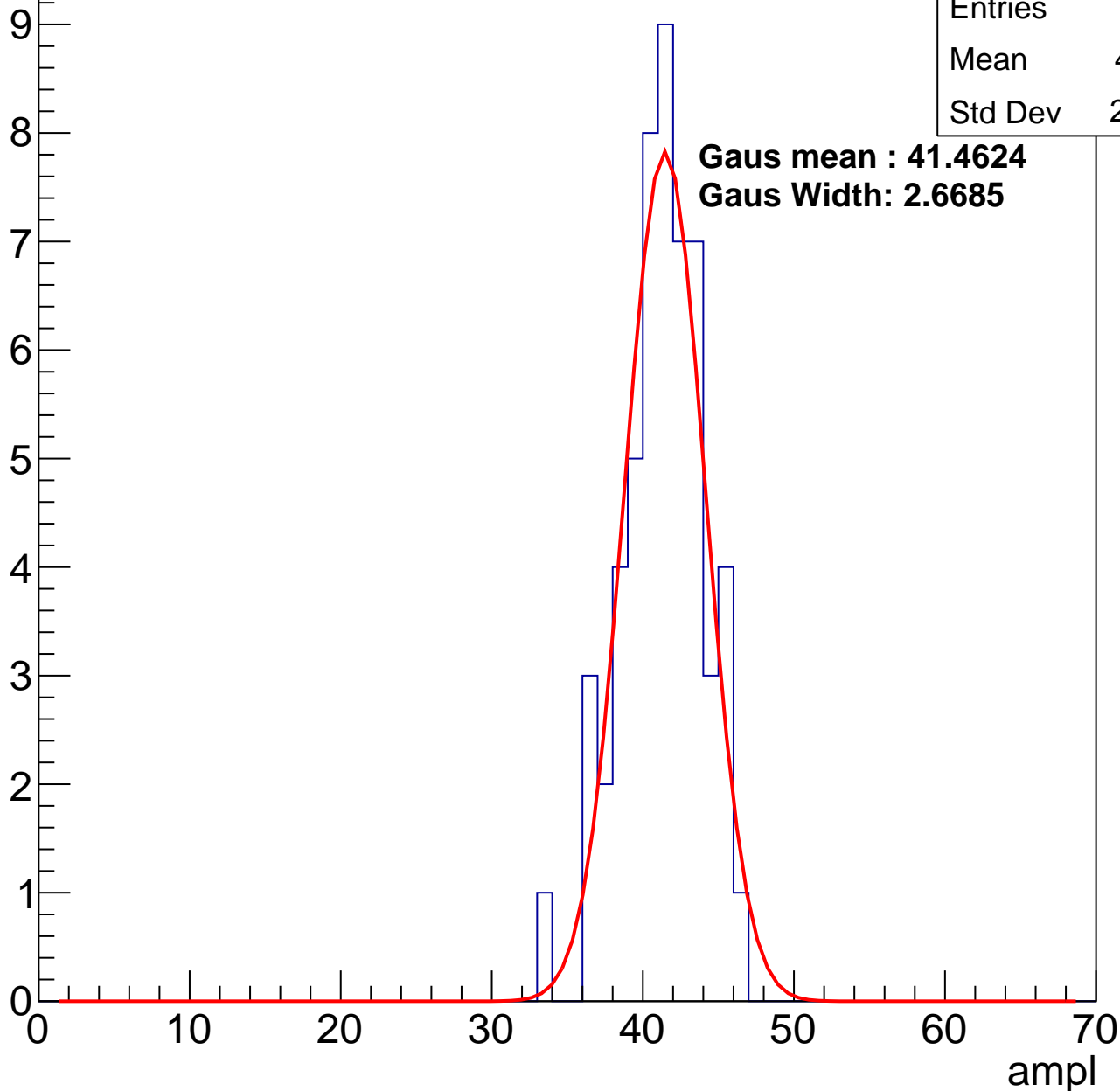
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	40.81
Std Dev	2.667

**Gaus mean : 41.4624**

**Gaus Width: 2.6685**

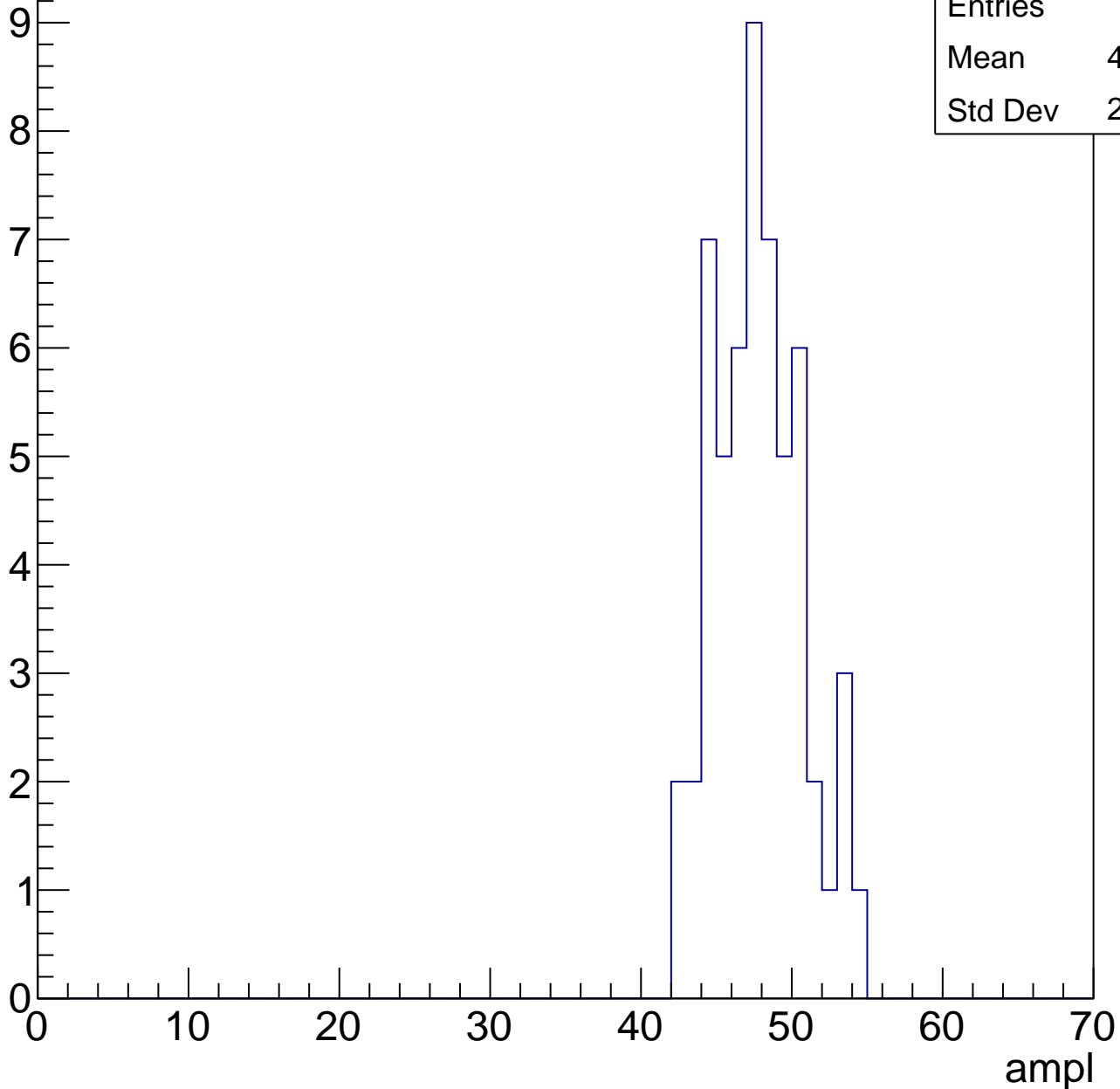


# B0L000S, U18-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	47.32
Std Dev	2.867

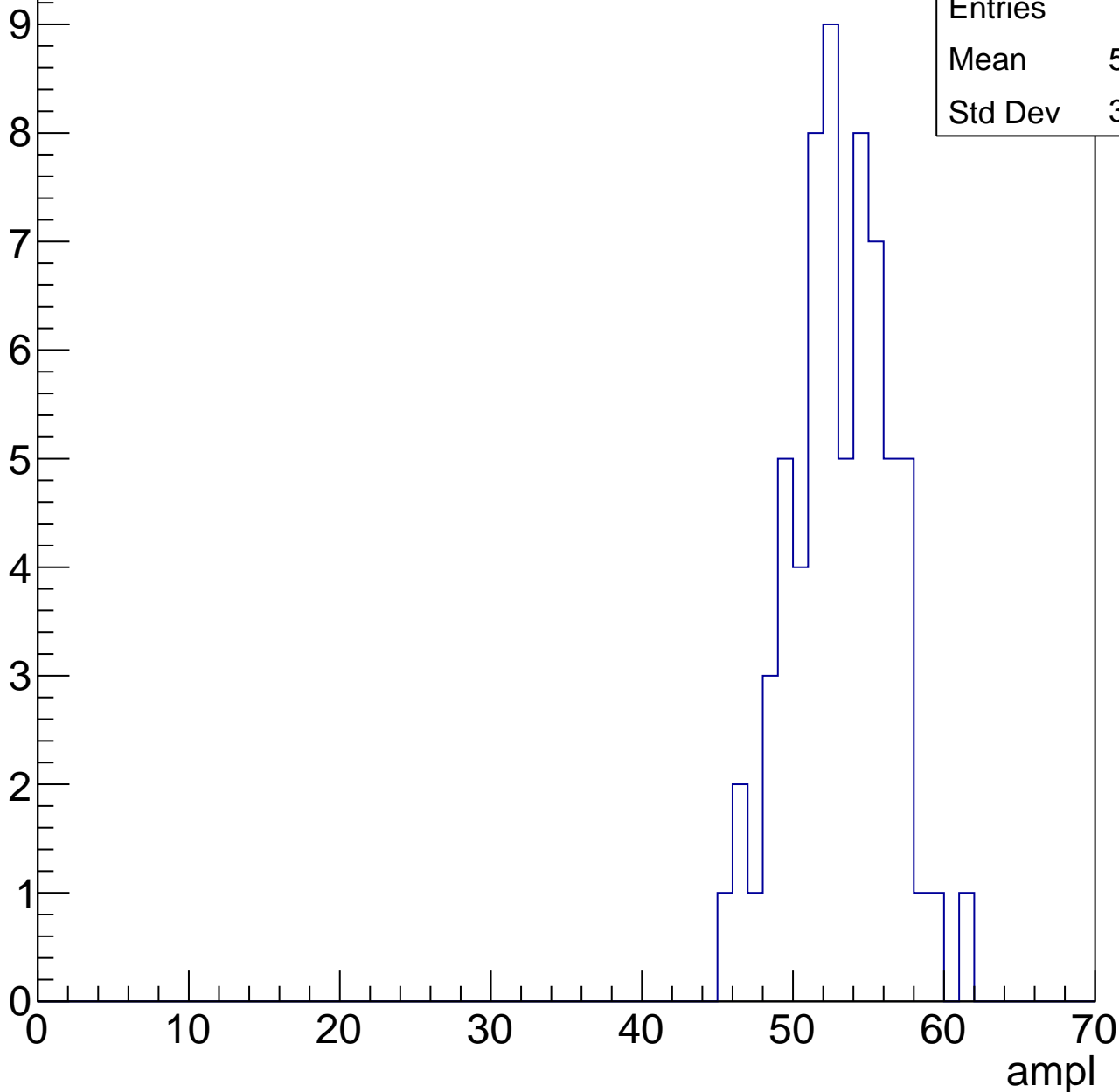


# B0L000S, U18-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	52.64
Std Dev	3.274

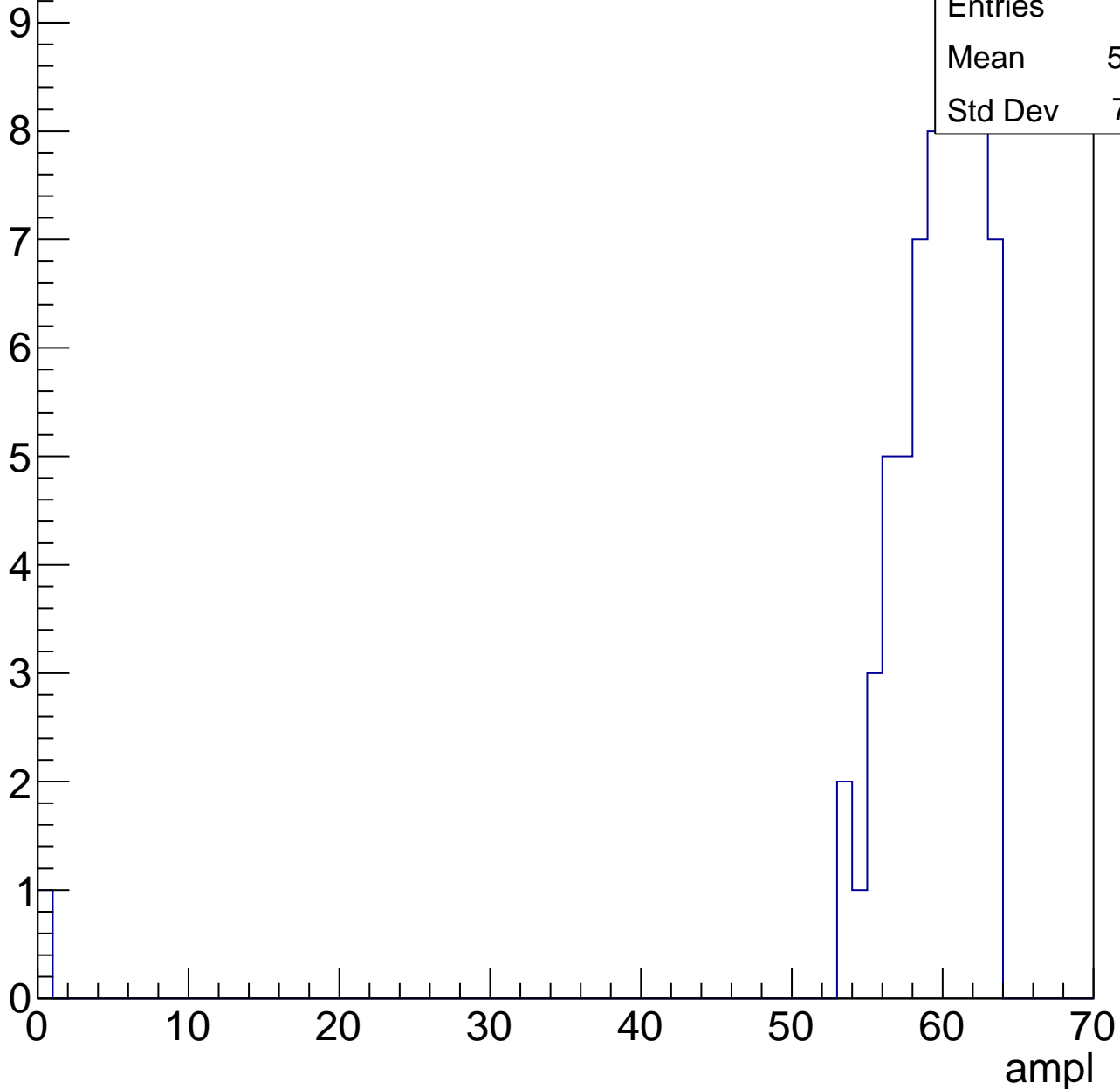


# B0L000S, U18-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	58.42
Std Dev	7.701



# B0L000S, U18-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

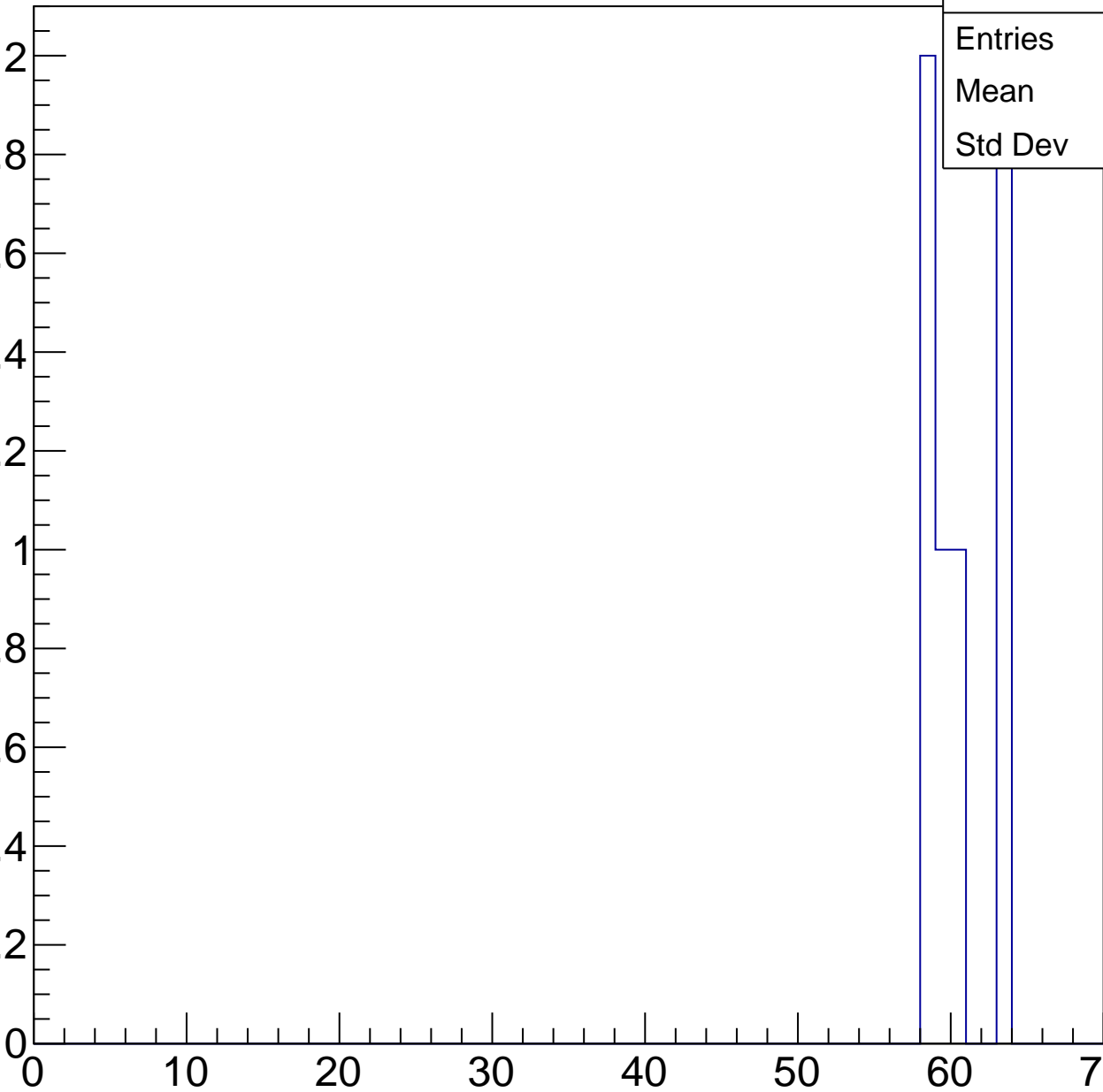
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	60.17
Std Dev	2.115

0 10 20 30 40 50 60 70

ampl





# B0L000S, U18-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch46, adc0

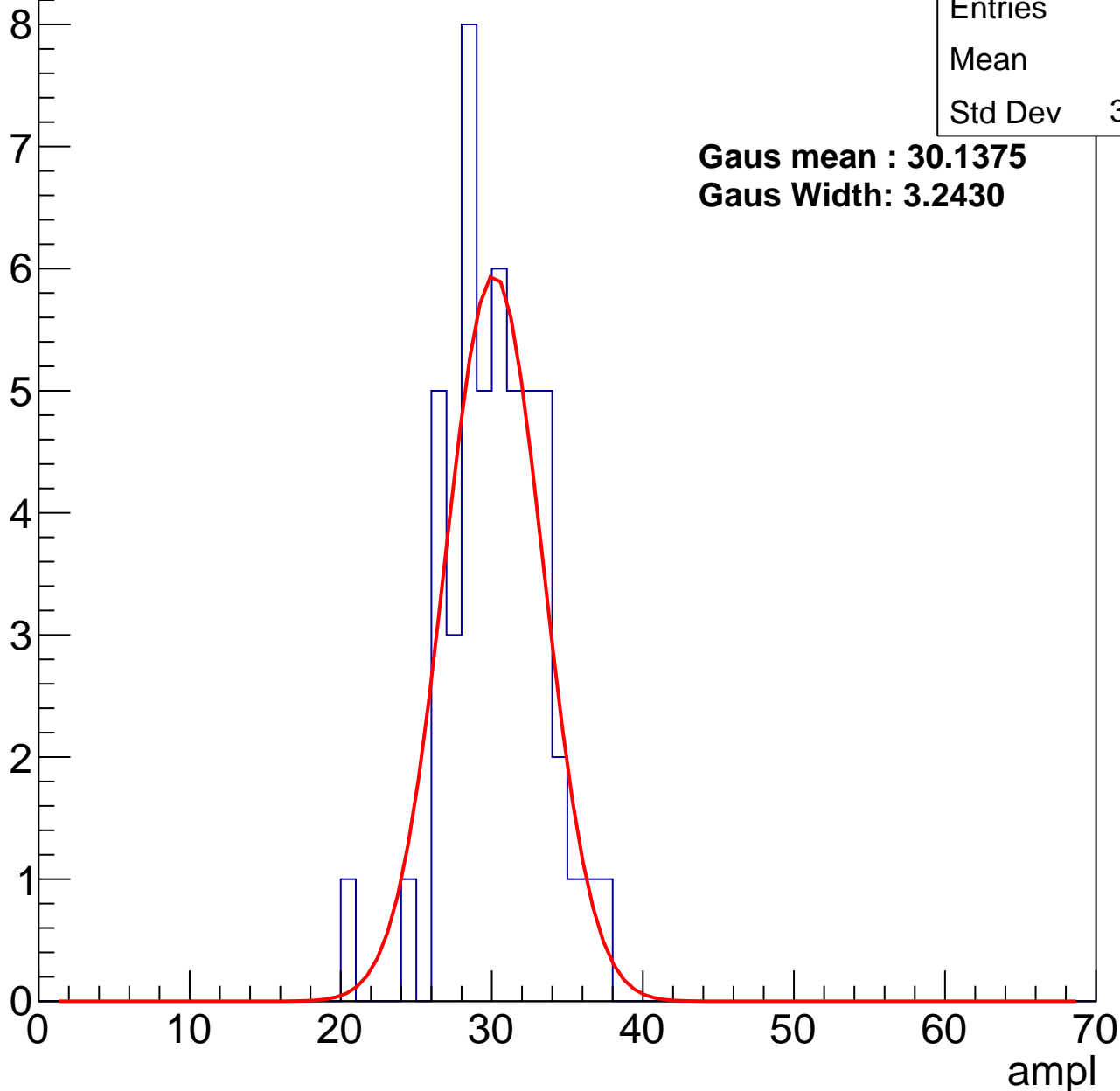
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	29.8
Std Dev	3.162

**Gaus mean : 30.1375**

**Gaus Width: 3.2430**



# B0L000S, U18-ch46, adc1

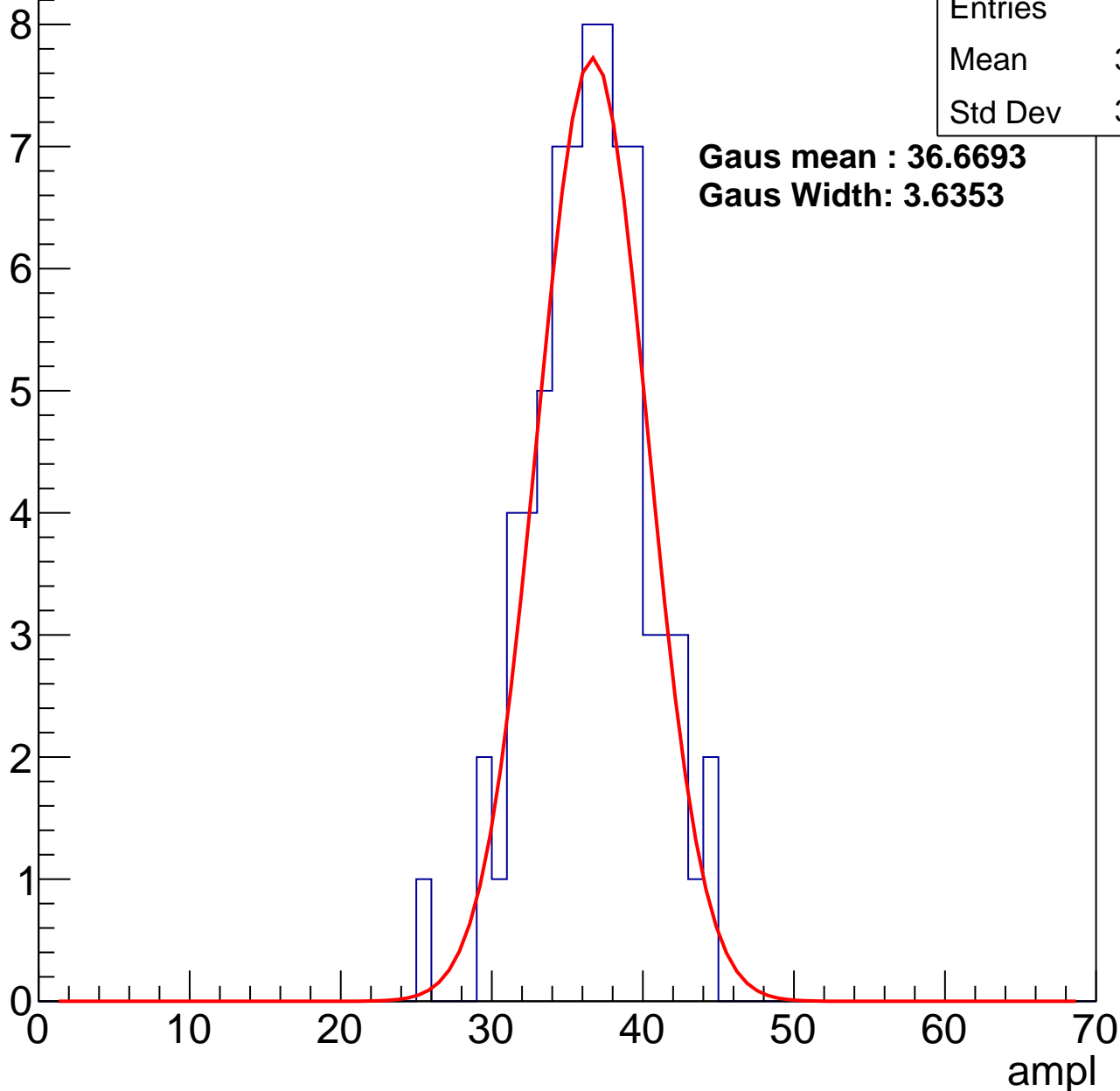
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.11
Std Dev	3.711

**Gaus mean : 36.6693**

**Gaus Width: 3.6353**



# B0L000S, U18-ch46, adc2

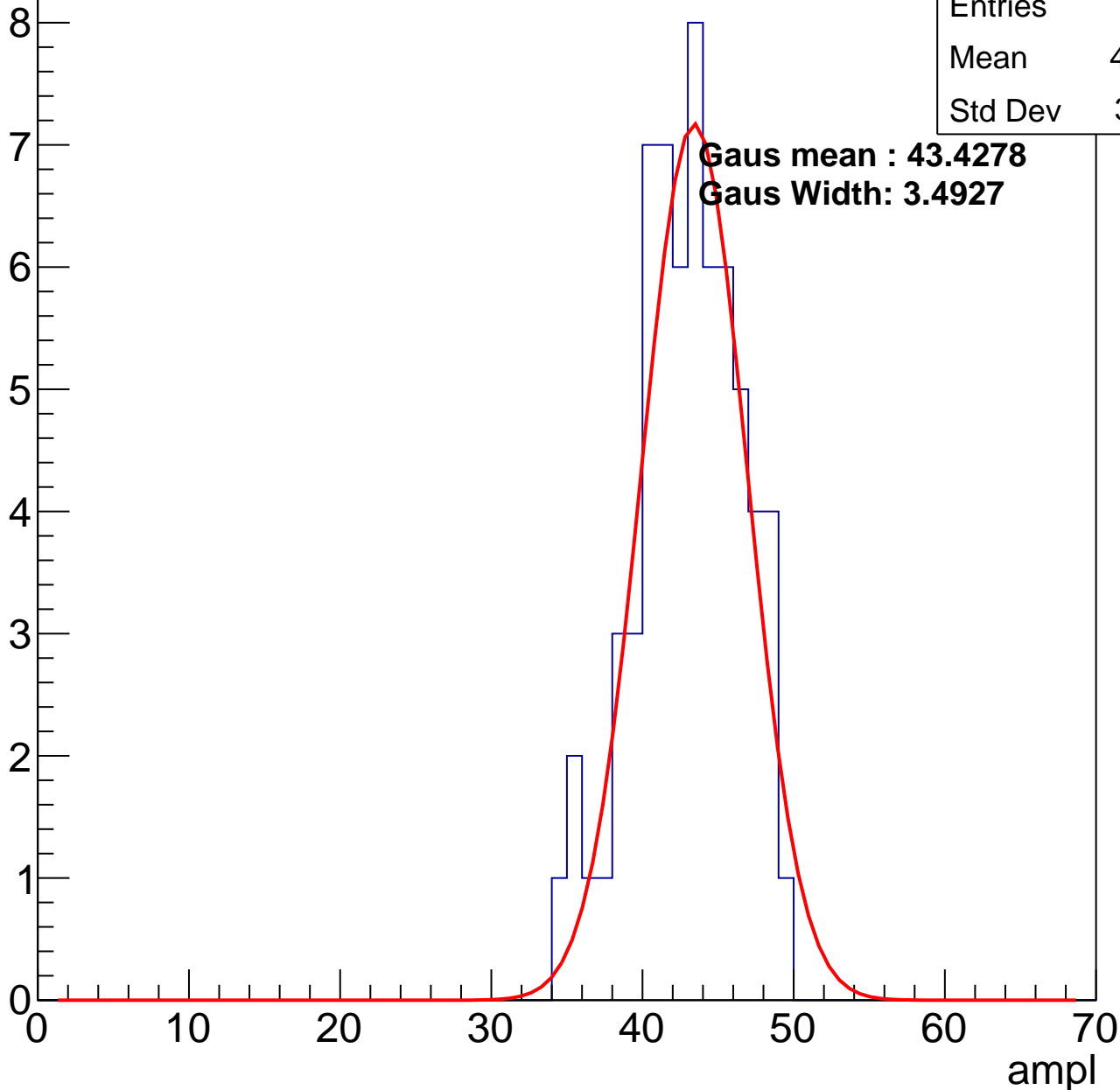
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.52
Std Dev	3.451

**Gaus mean : 43.4278**

**Gaus Width: 3.4927**

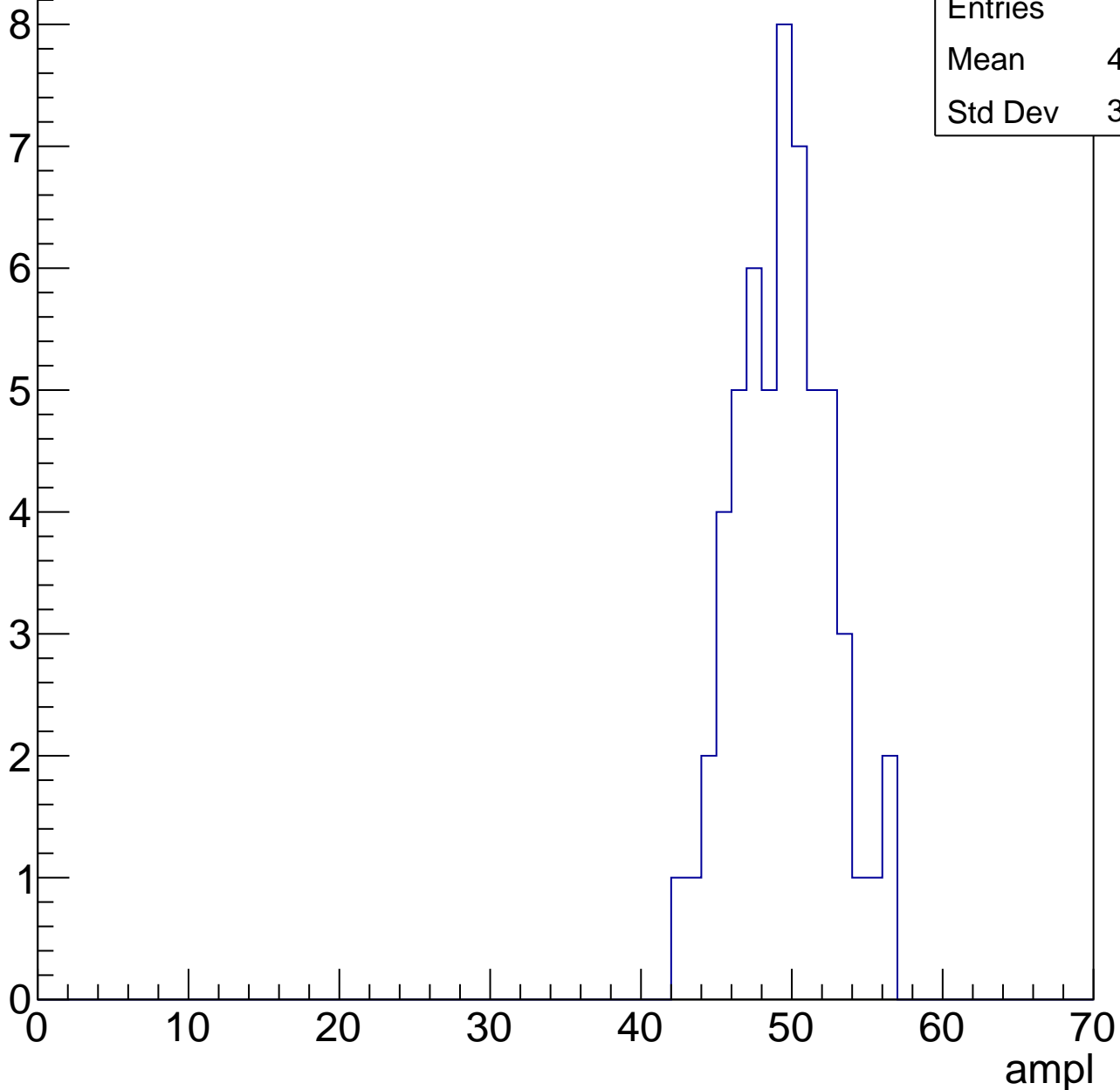


# B0L000S, U18-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	48.96
Std Dev	3.139

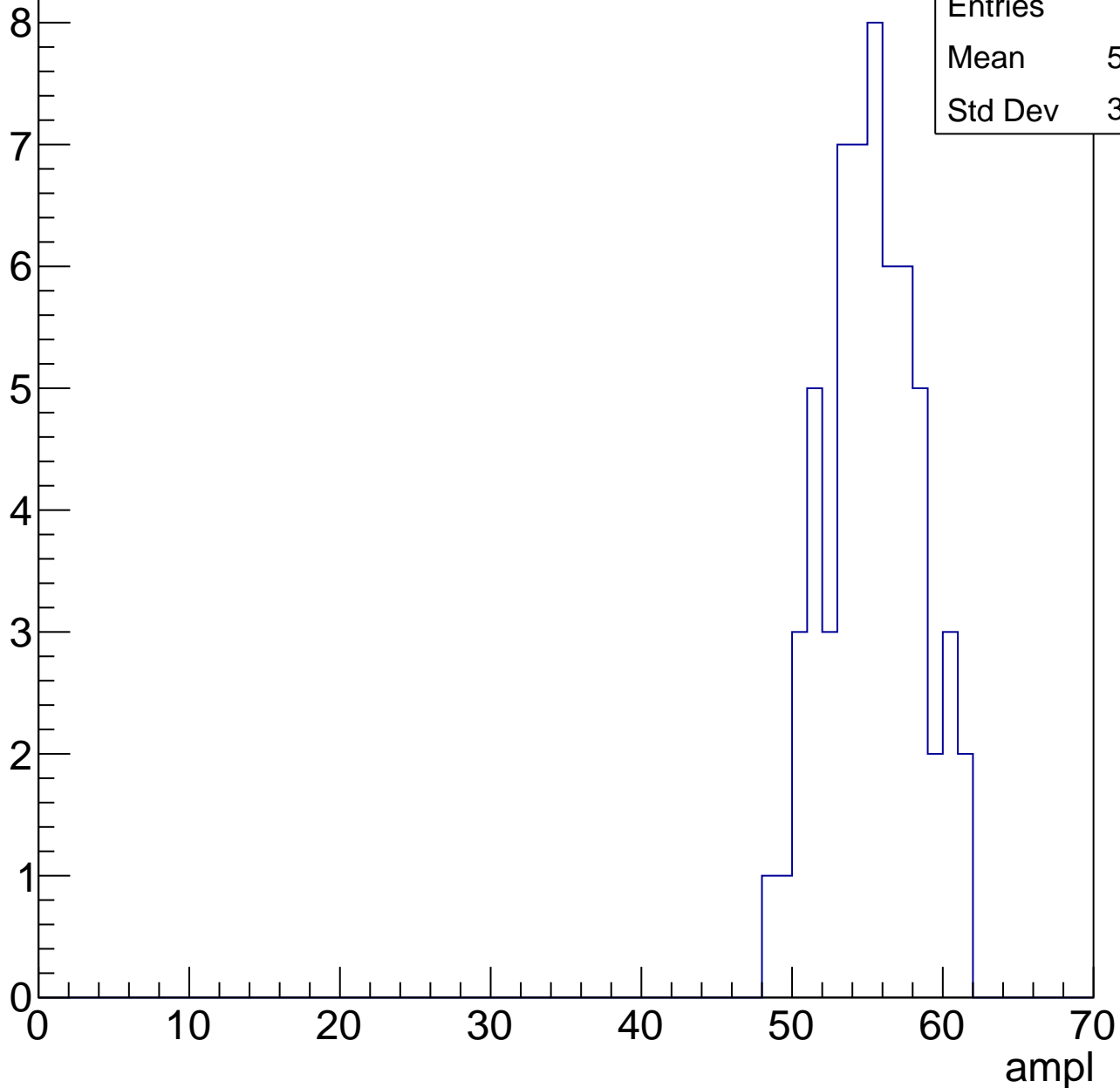


# B0L000S, U18-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

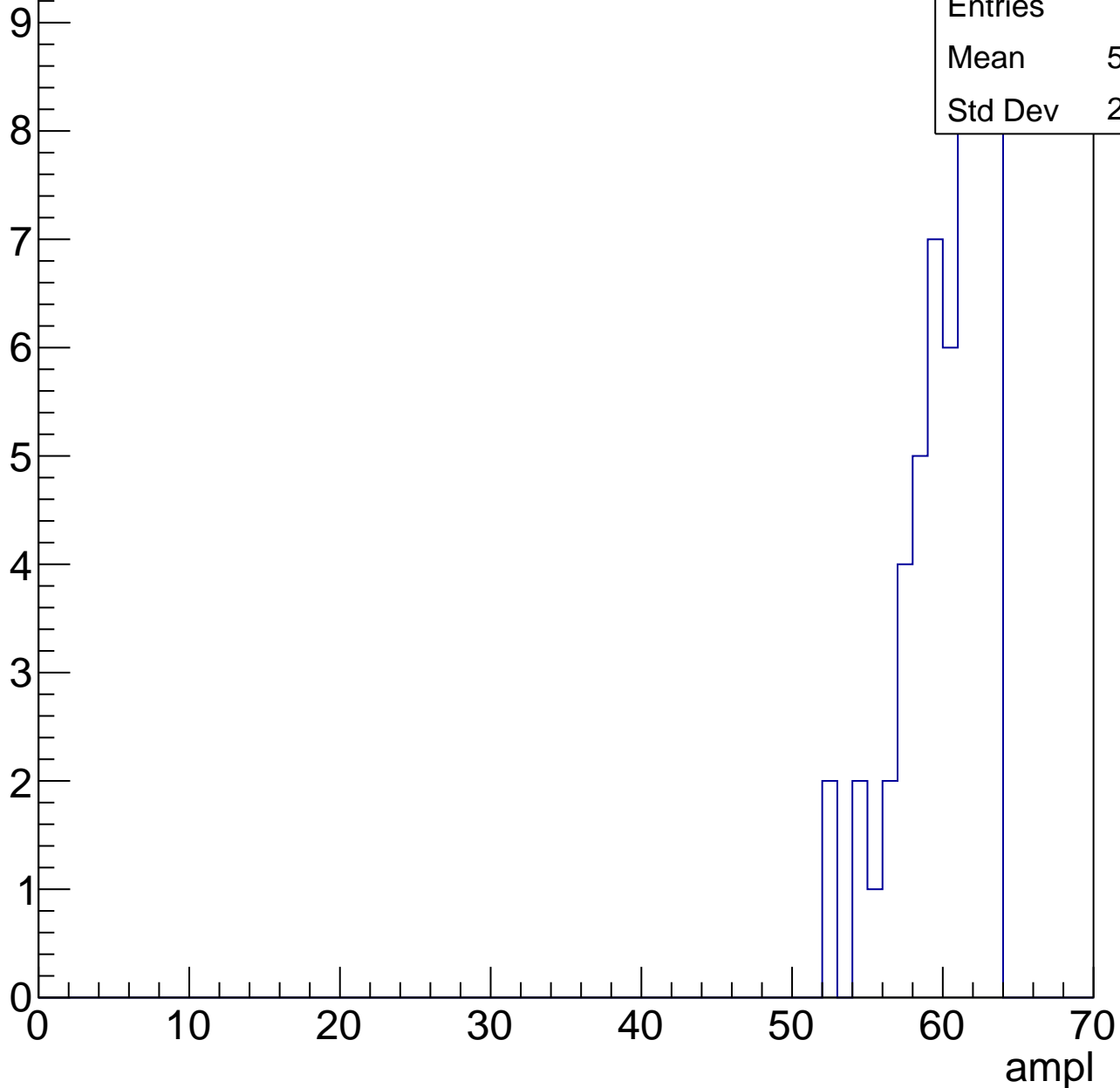
Entries	59
Mean	54.83
Std Dev	3.065



# B0L000S, U18-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

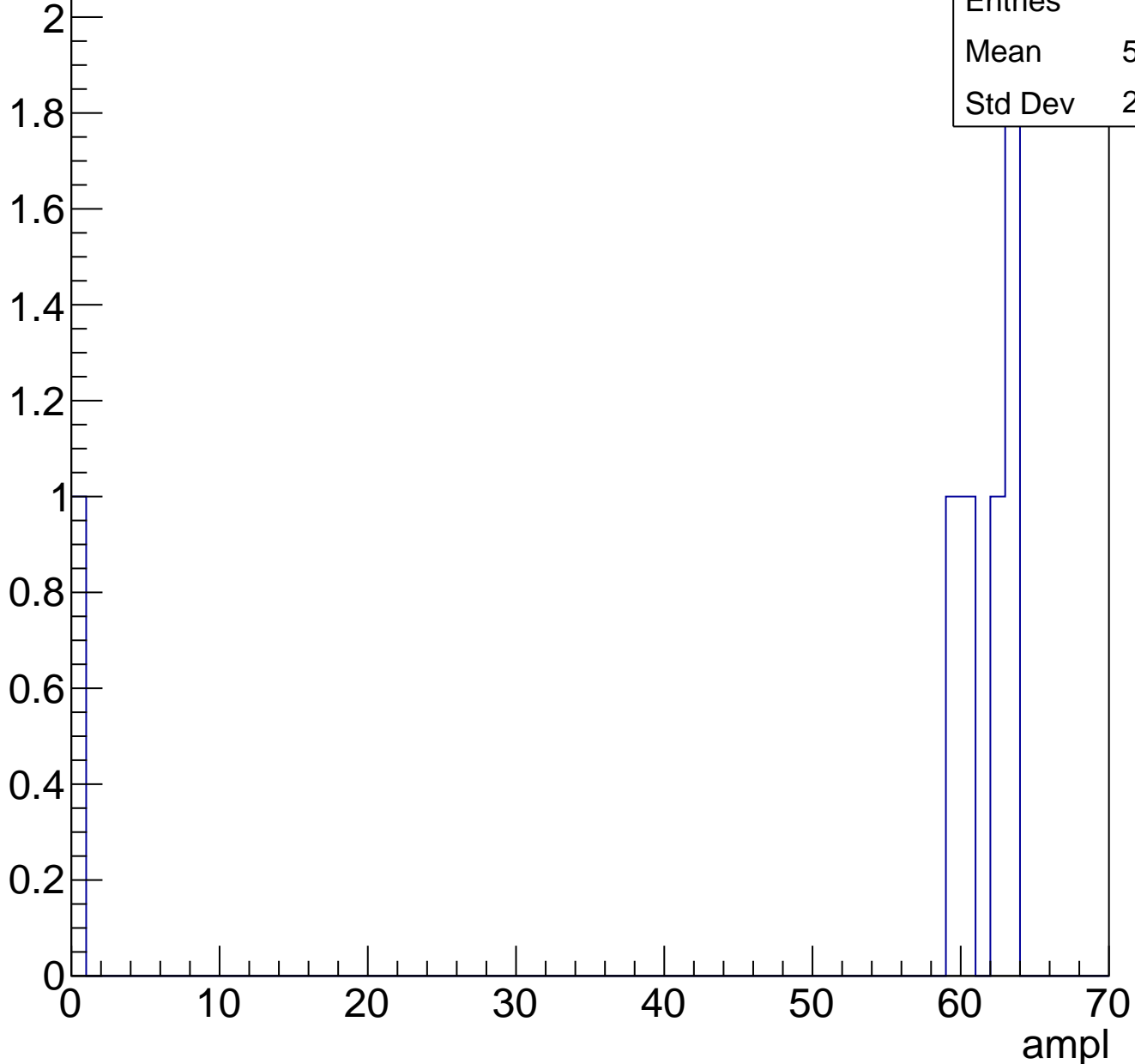
Entry



# B0L000S, U18-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	6
Mean	51.17
Std Dev	22.93



# B0L000S, U18-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	95
Mean	29.63
Std Dev	5.138

**Gaus mean : 30.0155**

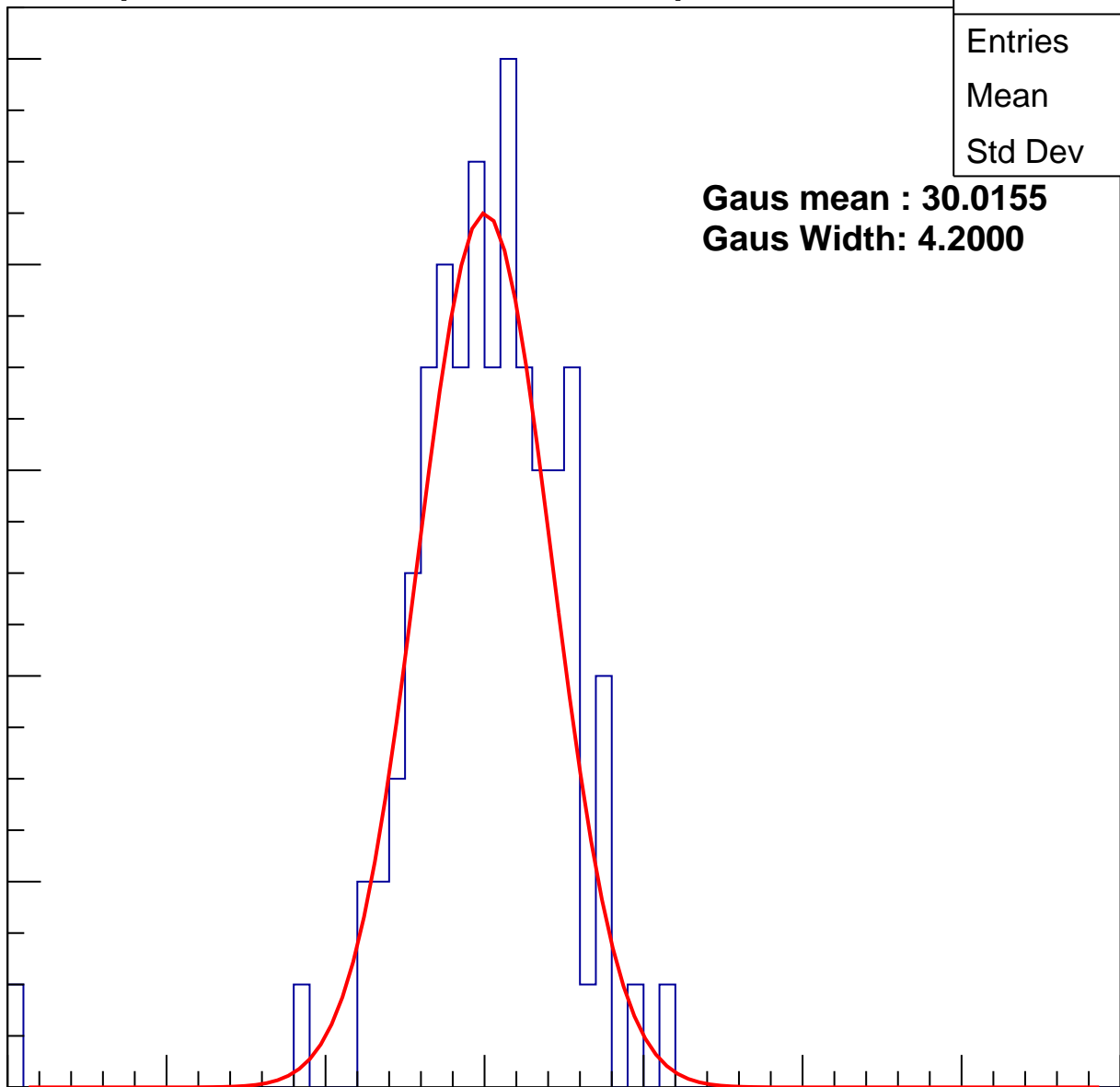
**Gaus Width: 4.2000**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch47, adc1

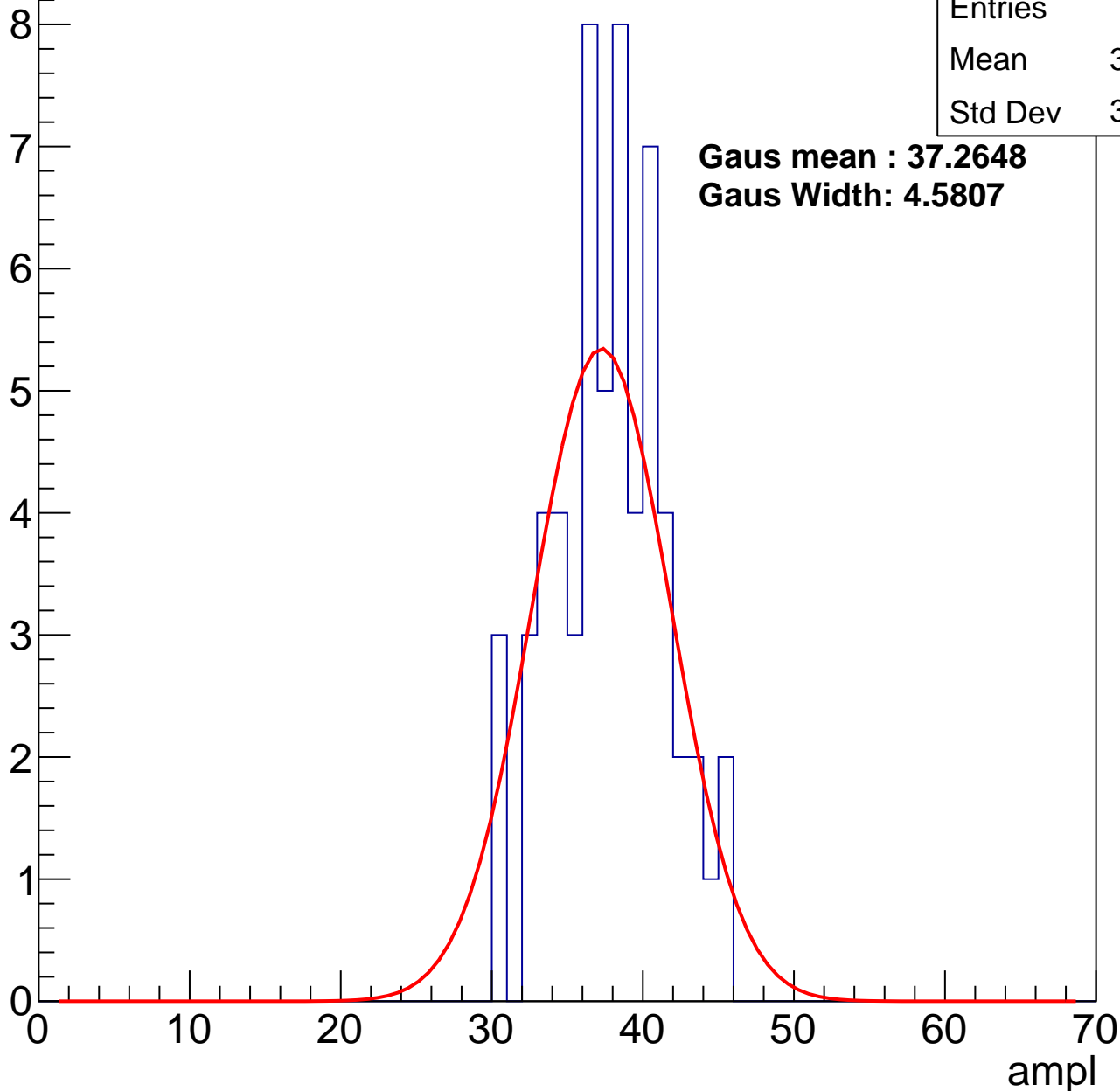
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	37.33
Std Dev	3.618

**Gaus mean : 37.2648**

**Gaus Width: 4.5807**



# B0L000S, U18-ch47, adc2

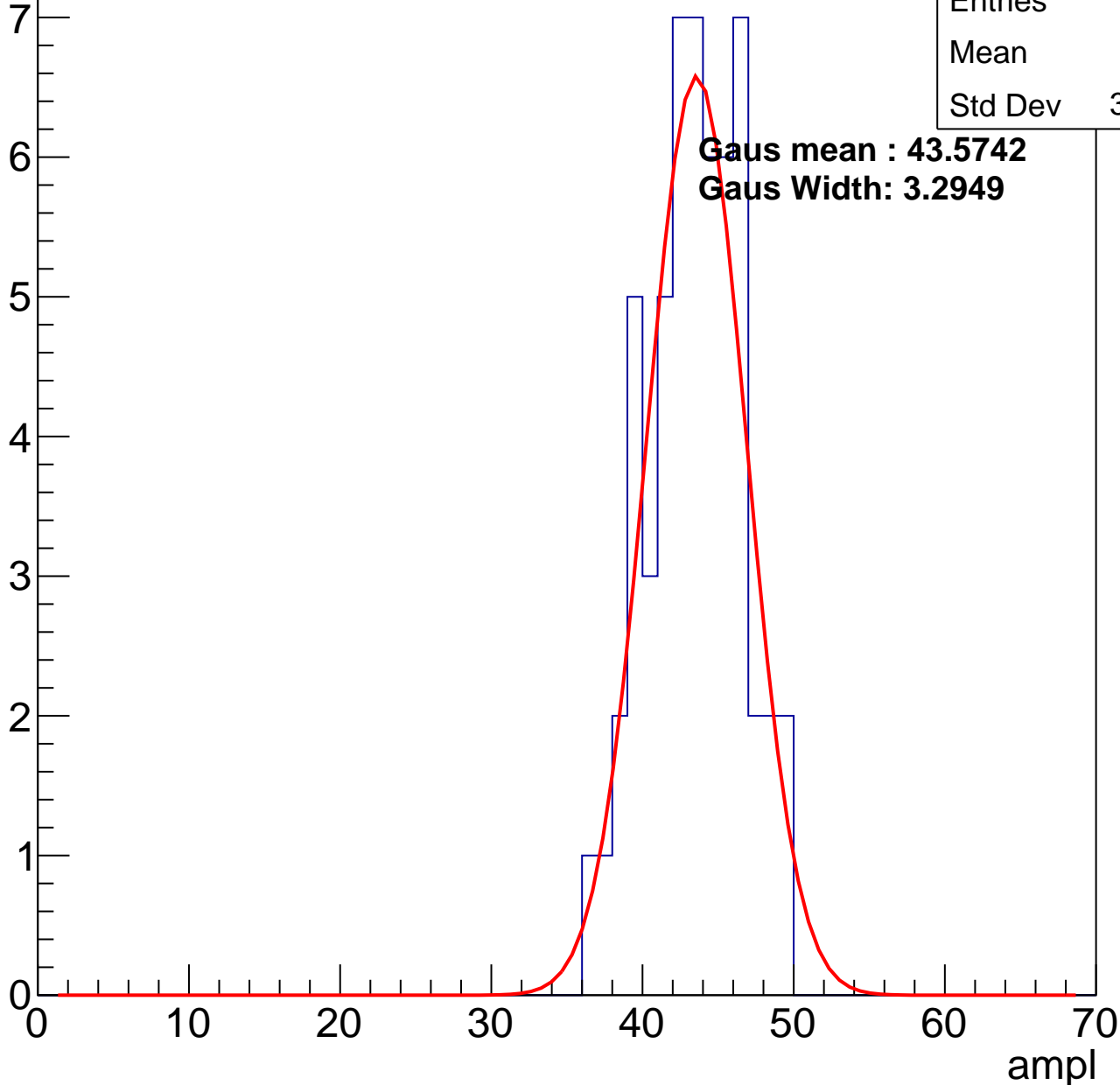
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43
Std Dev	3.036

**Gaus mean : 43.5742**

**Gaus Width: 3.2949**

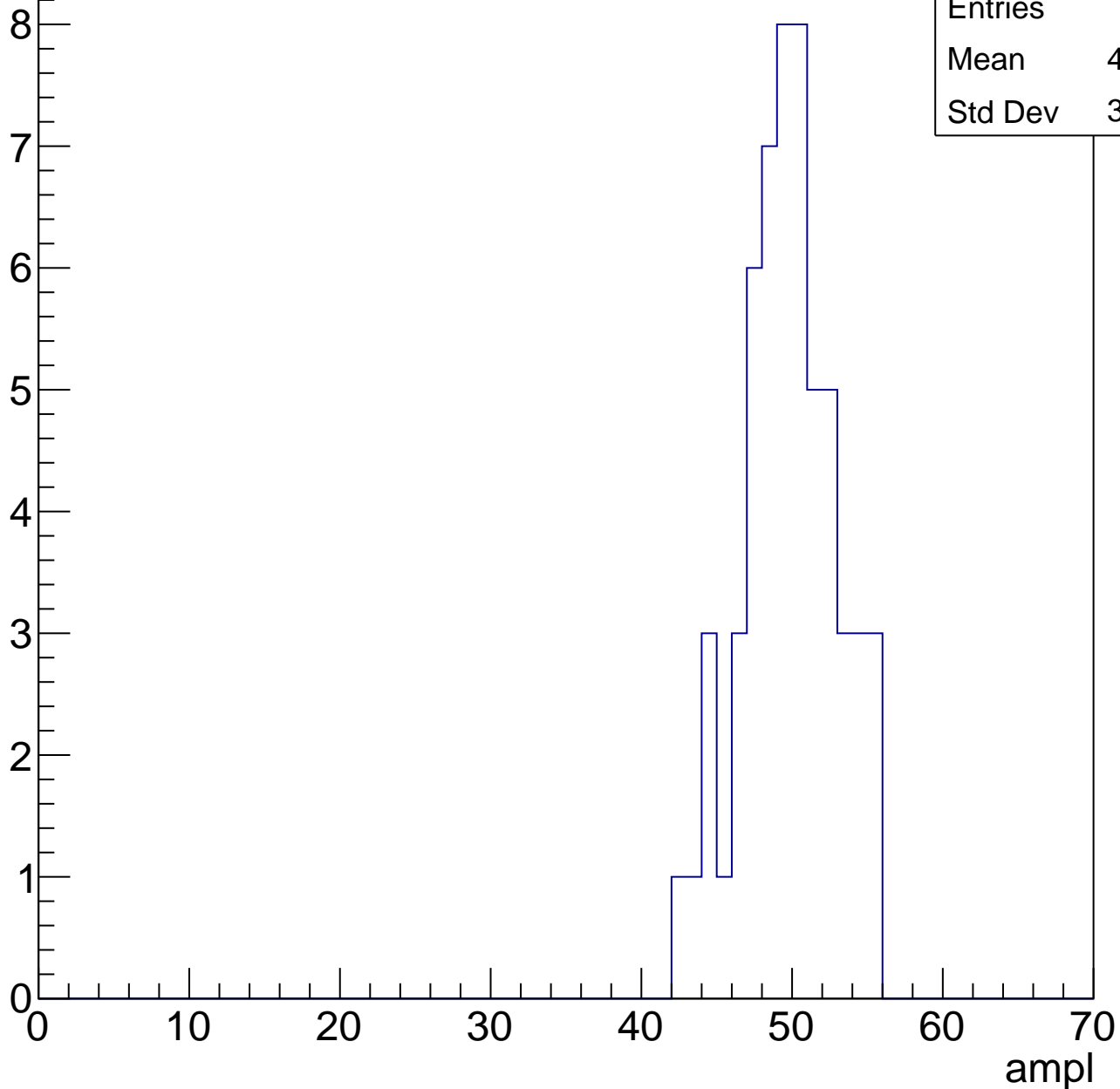


# B0L000S, U18-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.32
Std Dev	3.056

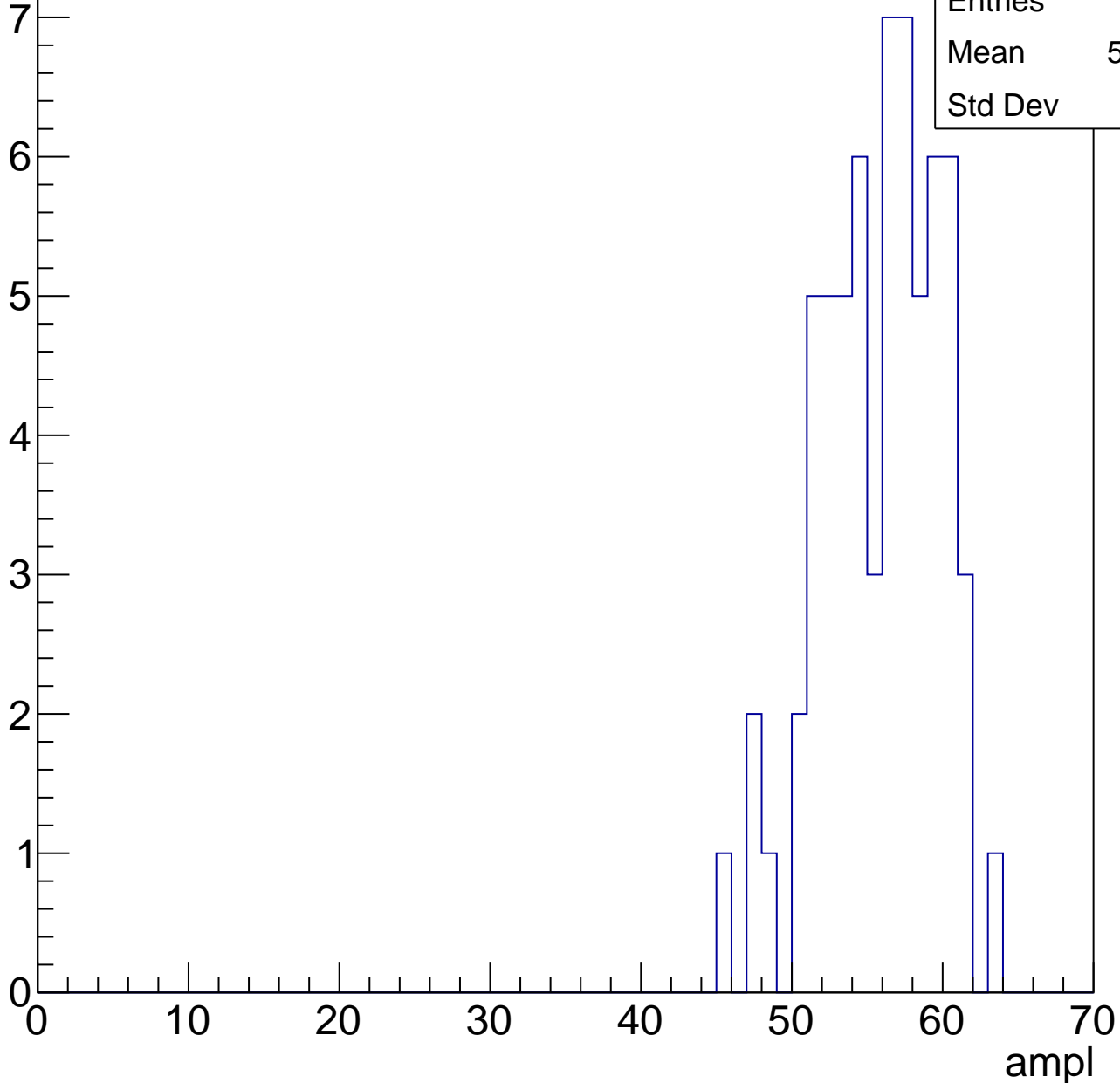


# B0L000S, U18-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	55.34
Std Dev	3.86

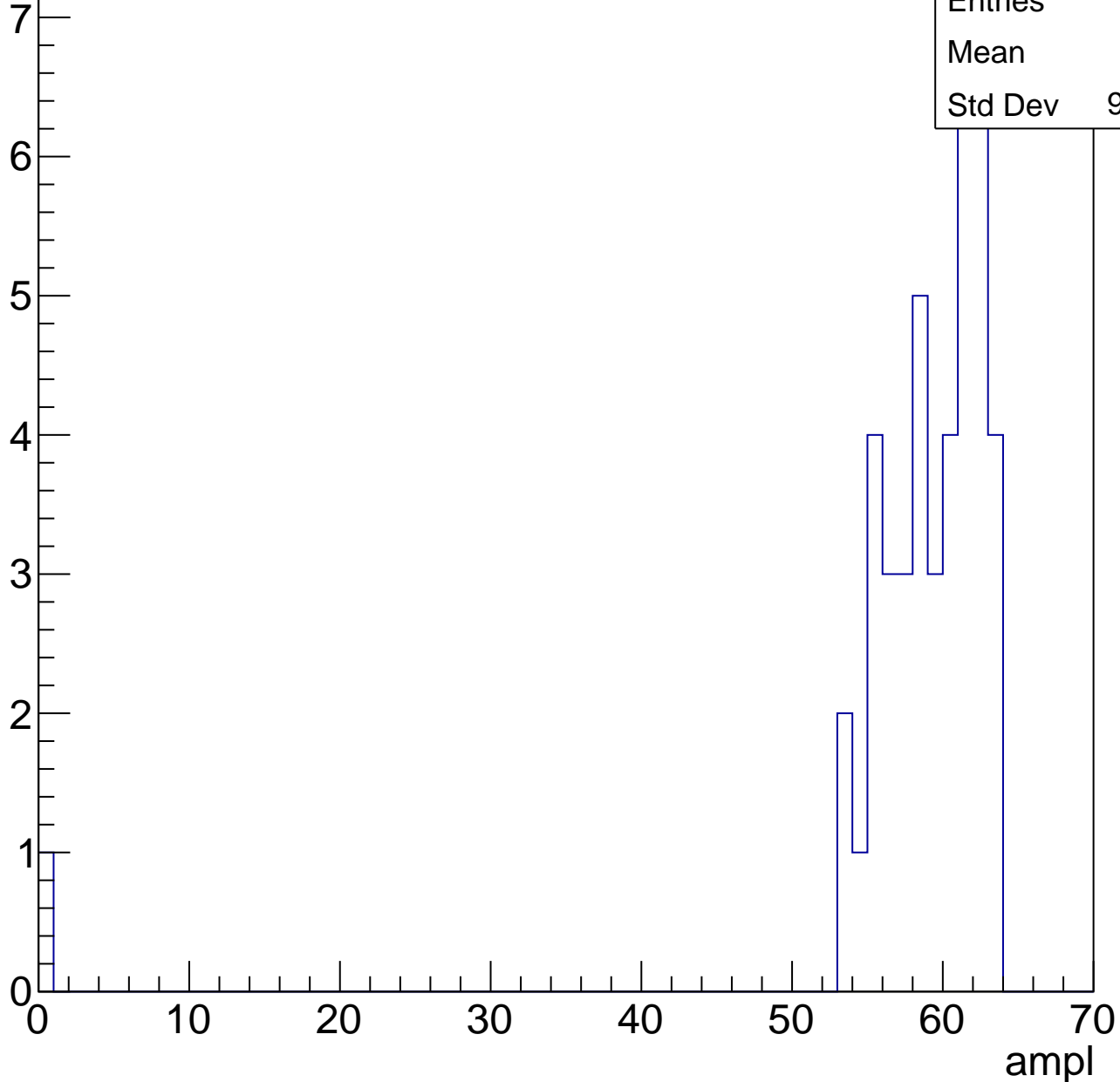


# B0L000S, U18-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	57.7
Std Dev	9.258

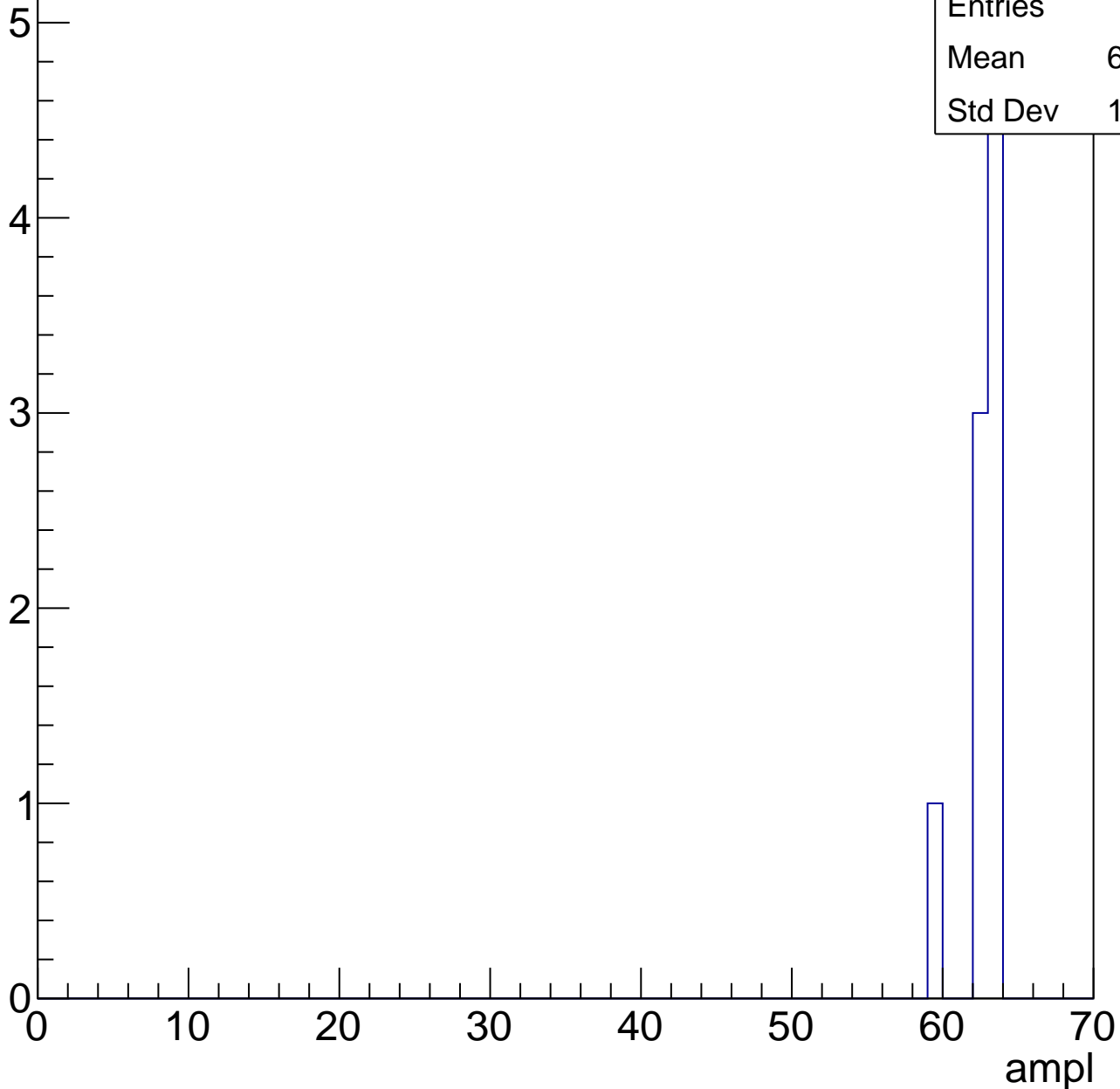


# B0L000S, U18-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.22
Std Dev	1.227





# B0L000S, U18-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch48, adc0

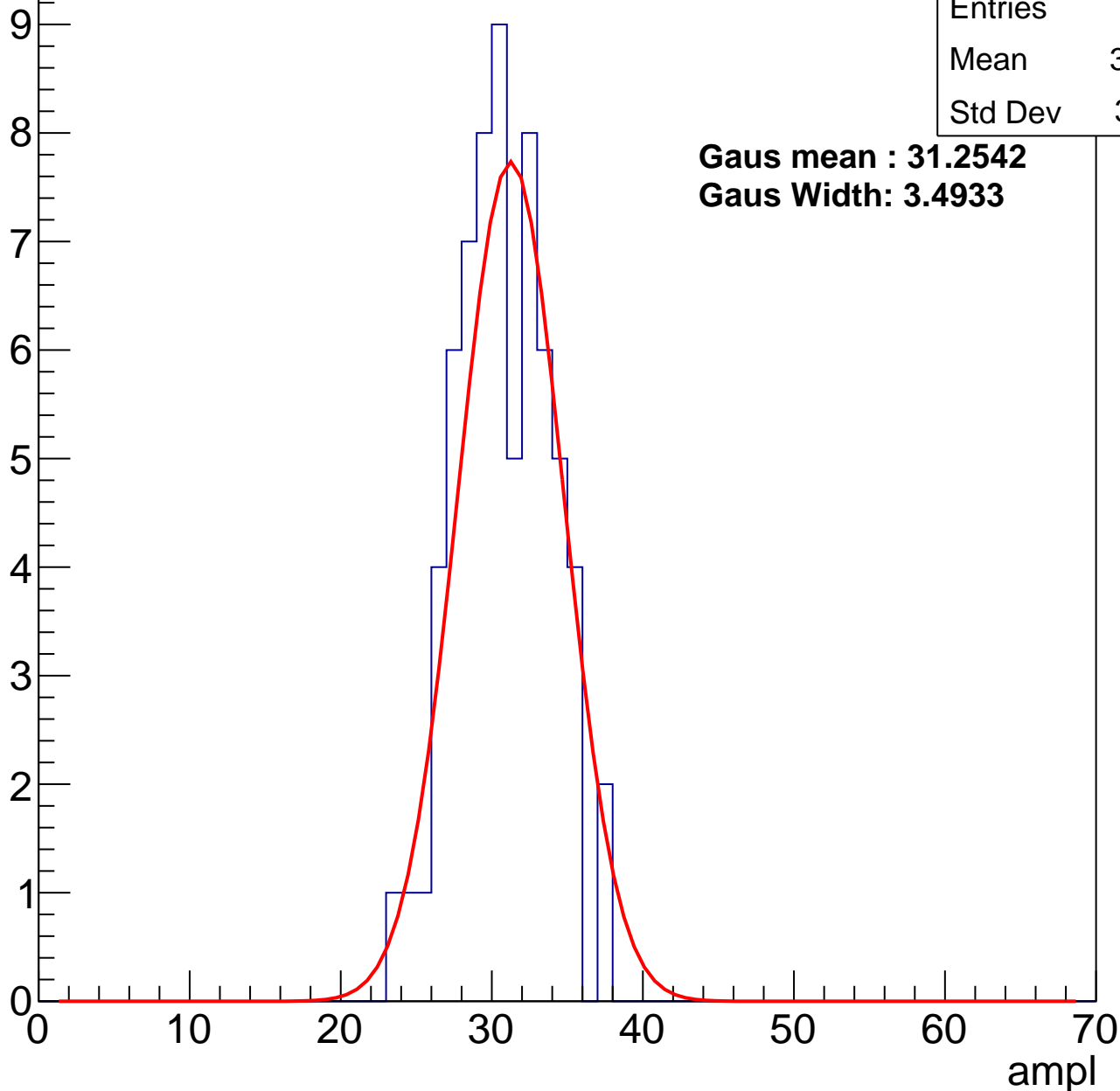
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.28
Std Dev	3.061

**Gaus mean : 31.2542**

**Gaus Width: 3.4933**



# B0L000S, U18-ch48, adc1

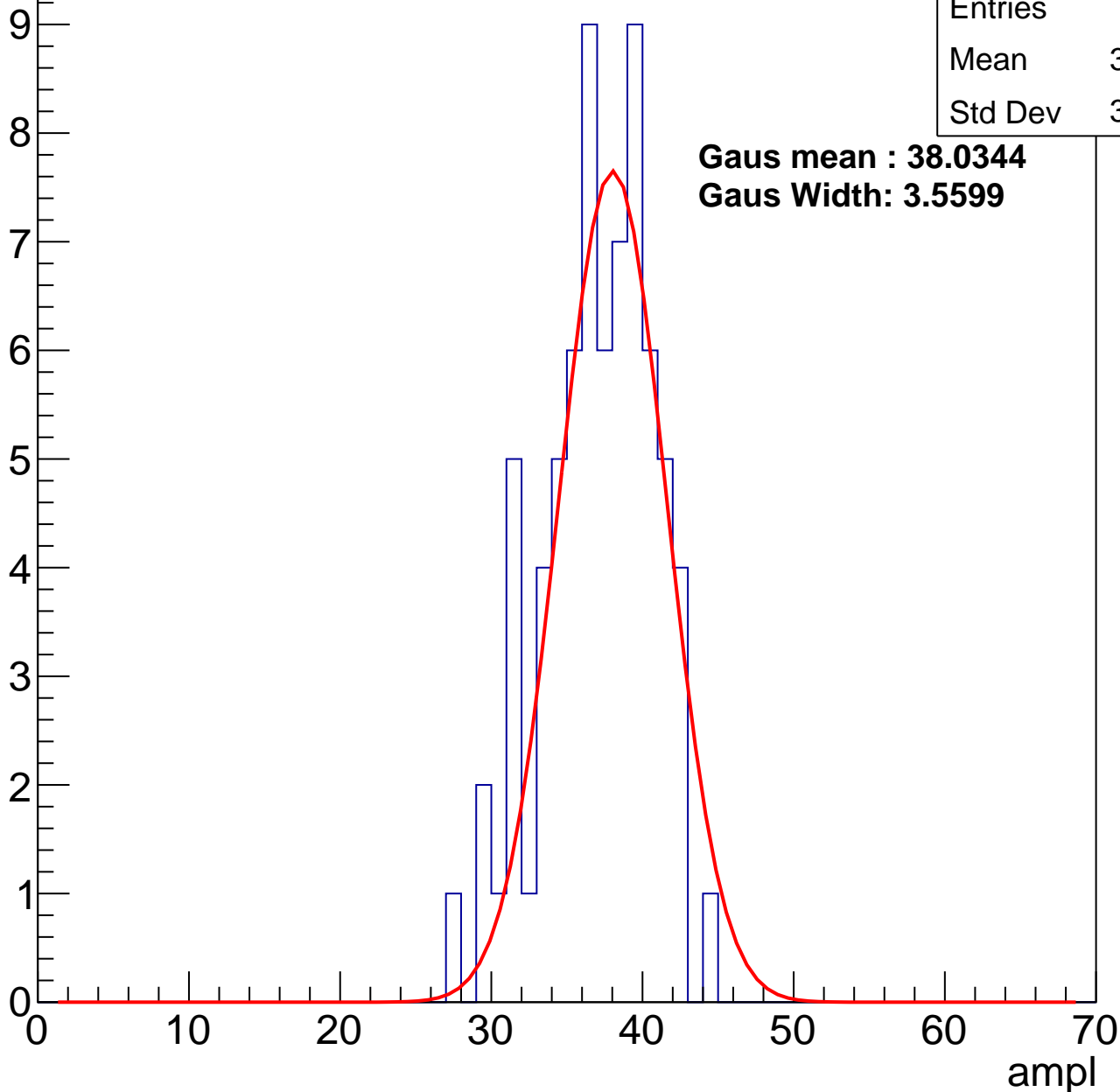
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.58
Std Dev	3.608

**Gaus mean : 38.0344**

**Gaus Width: 3.5599**



# B0L000S, U18-ch48, adc2

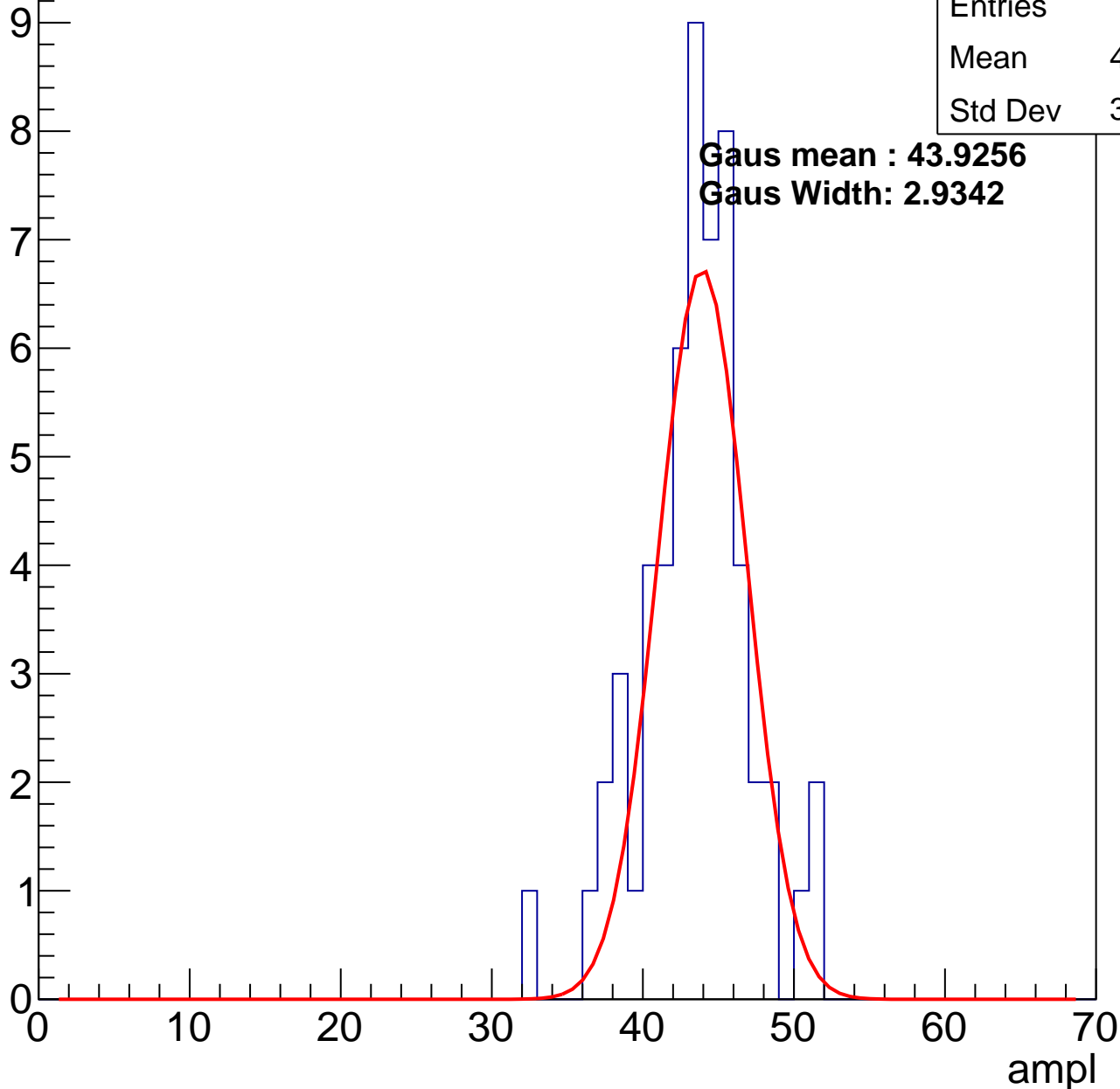
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.02
Std Dev	3.566

**Gaus mean : 43.9256**

**Gaus Width: 2.9342**



# B0L000S, U18-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

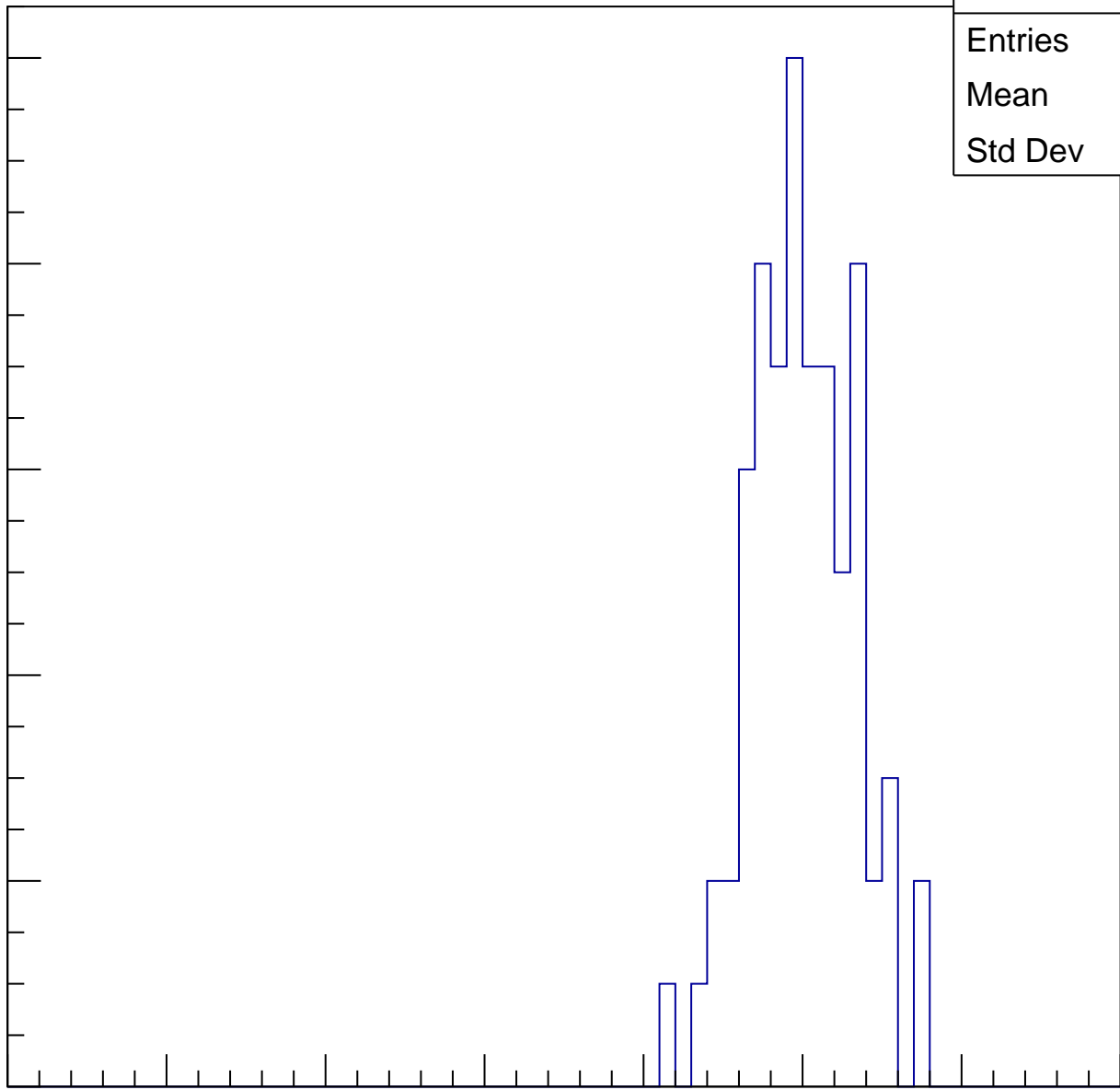
Entries	71
Mean	49.55
Std Dev	3.245

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

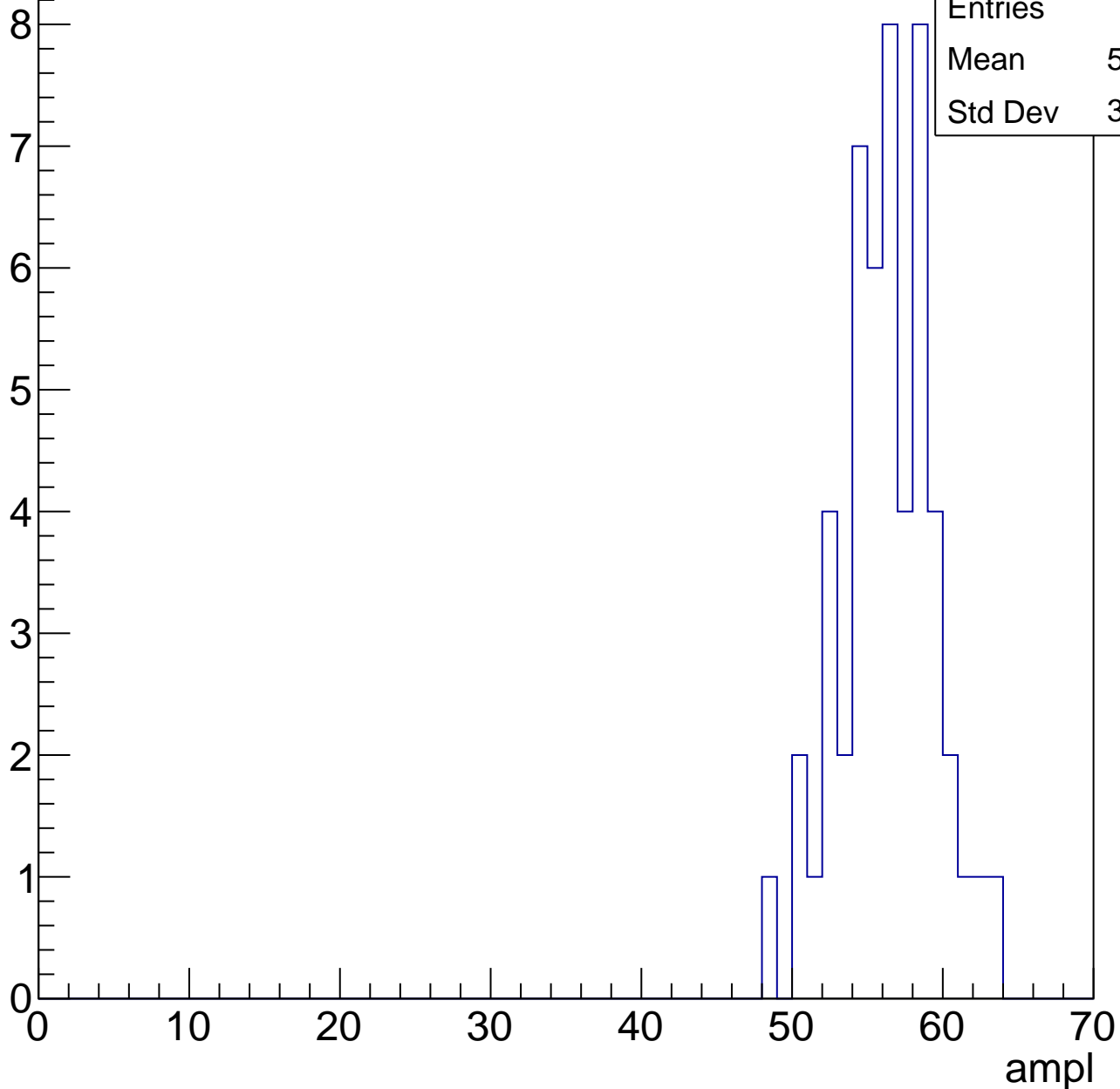


# B0L000S, U18-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	55.83
Std Dev	3.068

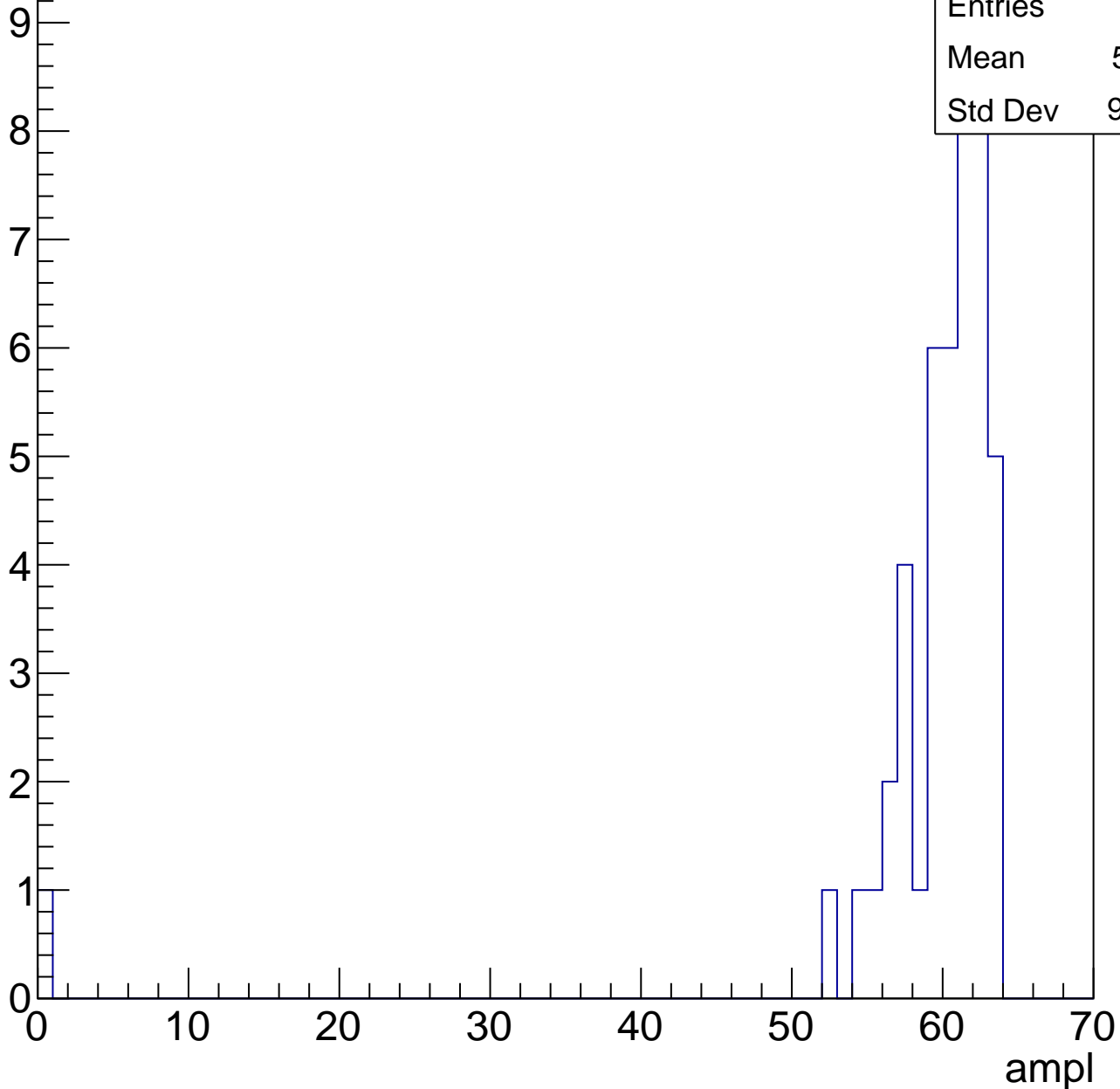


# B0L000S, U18-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

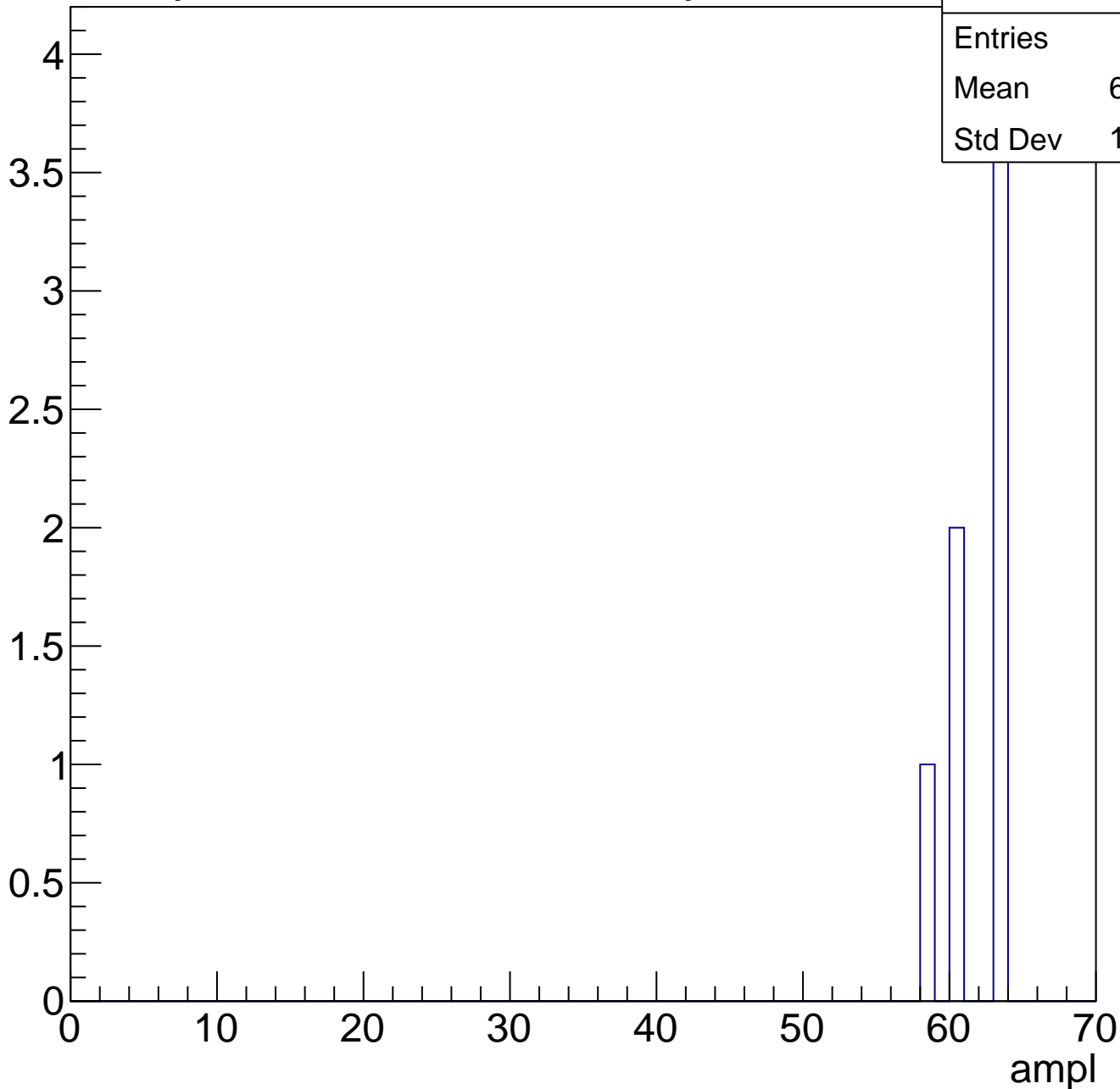
Entries	45
Mean	58.51
Std Dev	9.176



# B0L000S, U18-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch49, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	94
Mean	29.81
Std Dev	4.195

**Gaus mean : 30.2876**

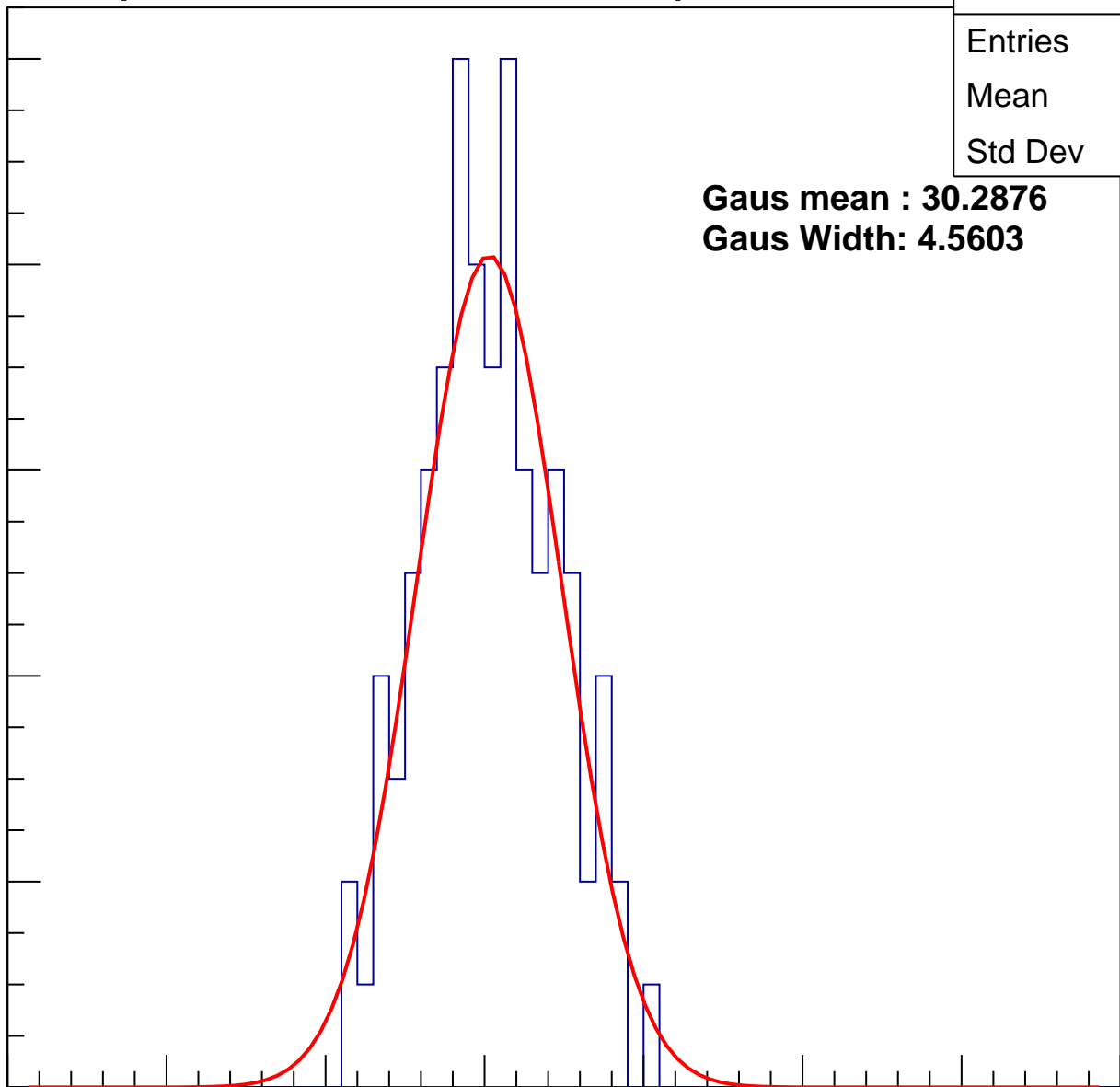
**Gaus Width: 4.5603**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch49, adc1

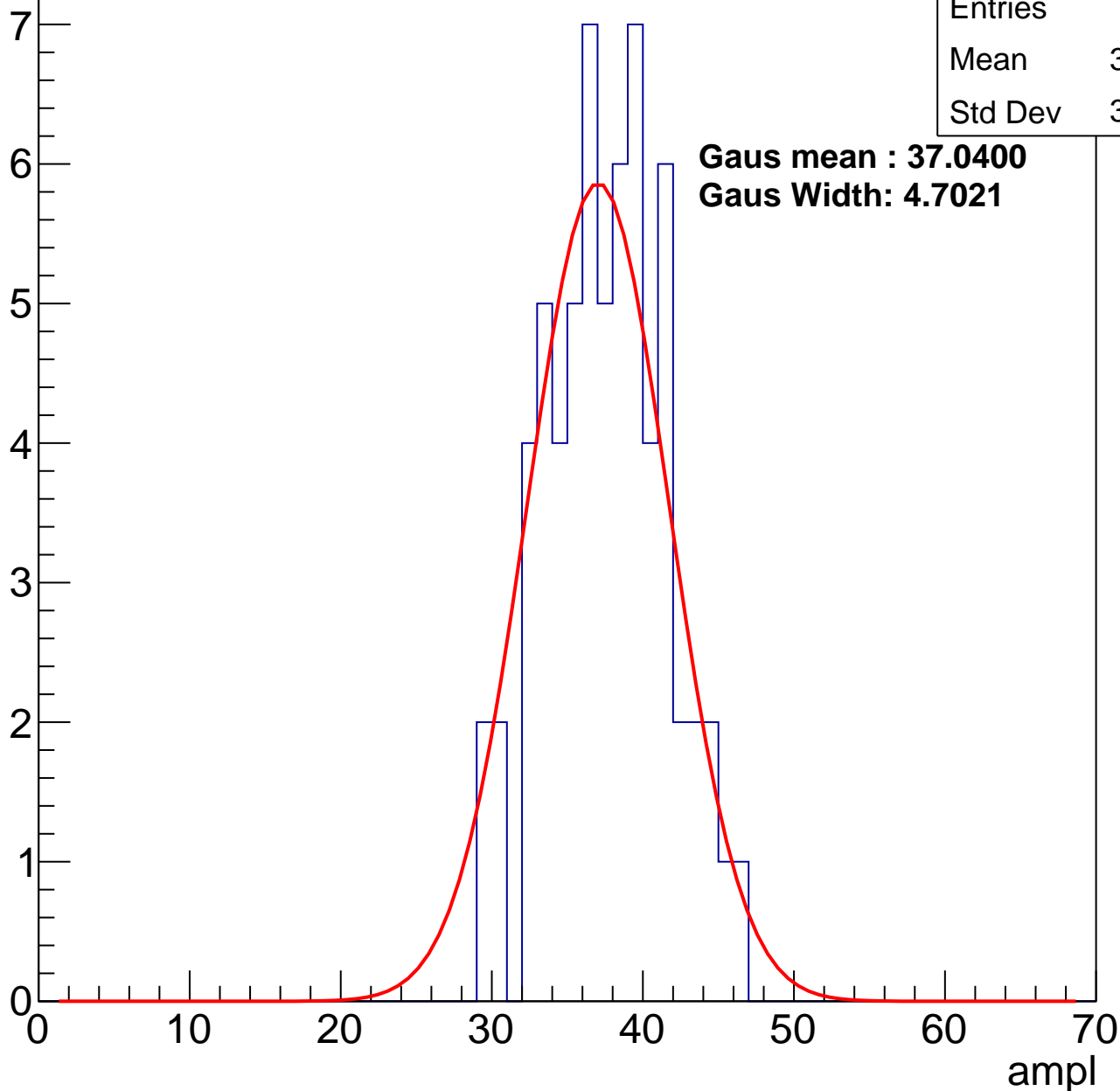
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.15
Std Dev	3.919

**Gaus mean : 37.0400**

**Gaus Width: 4.7021**



# B0L000S, U18-ch49, adc2

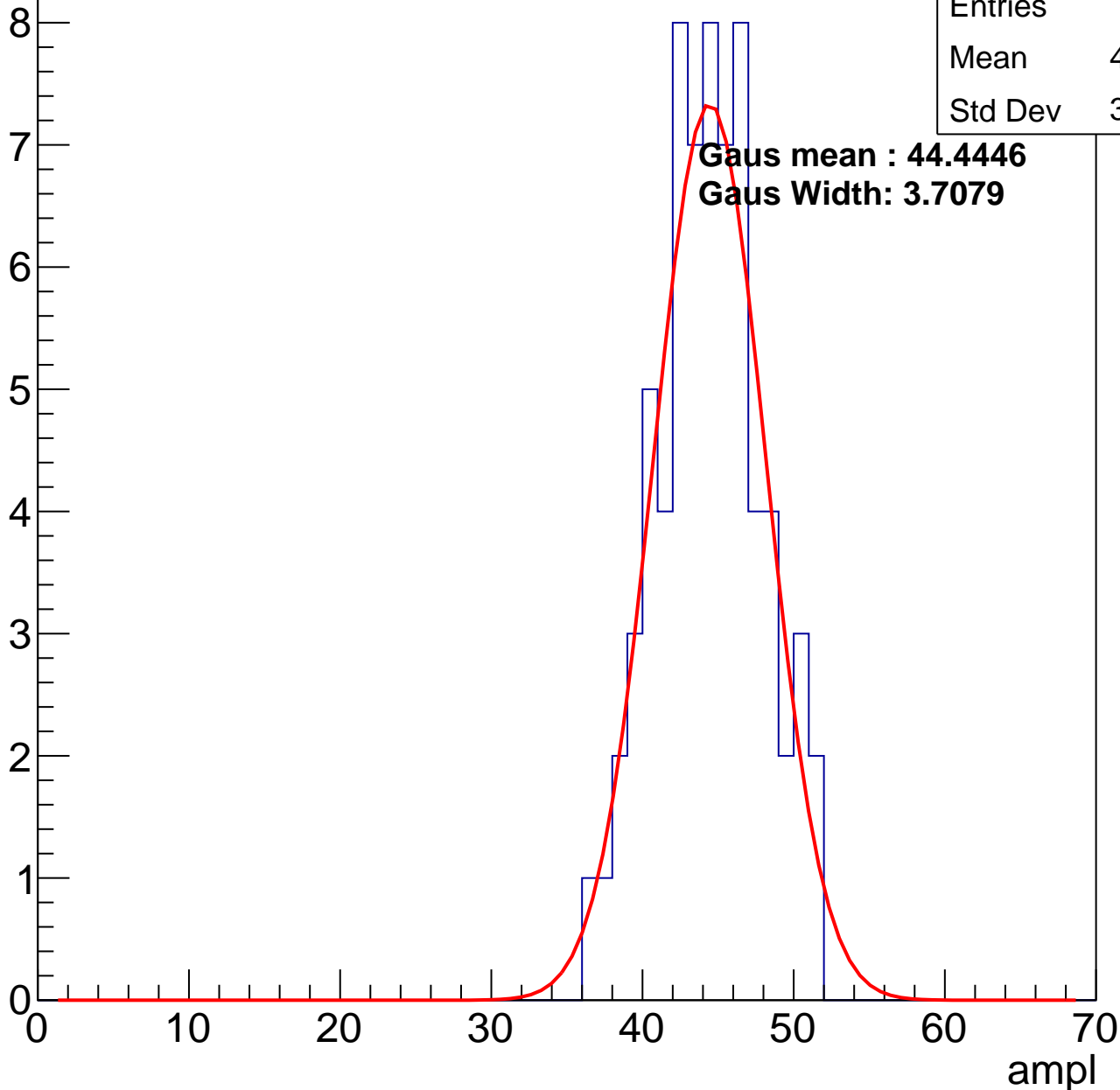
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.94
Std Dev	3.426

**Gaus mean : 44.4446**

**Gaus Width: 3.7079**

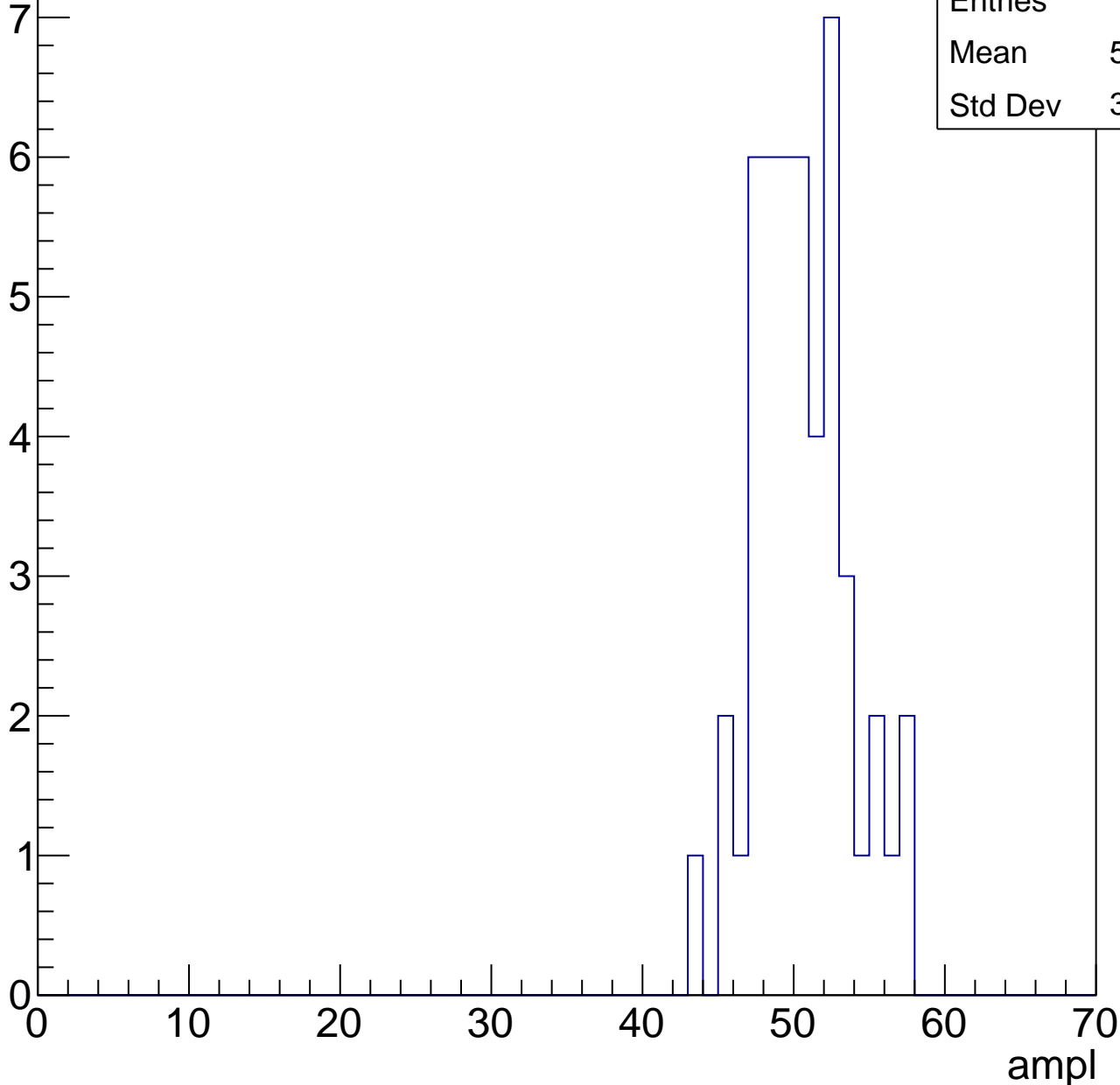


# B0L000S, U18-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

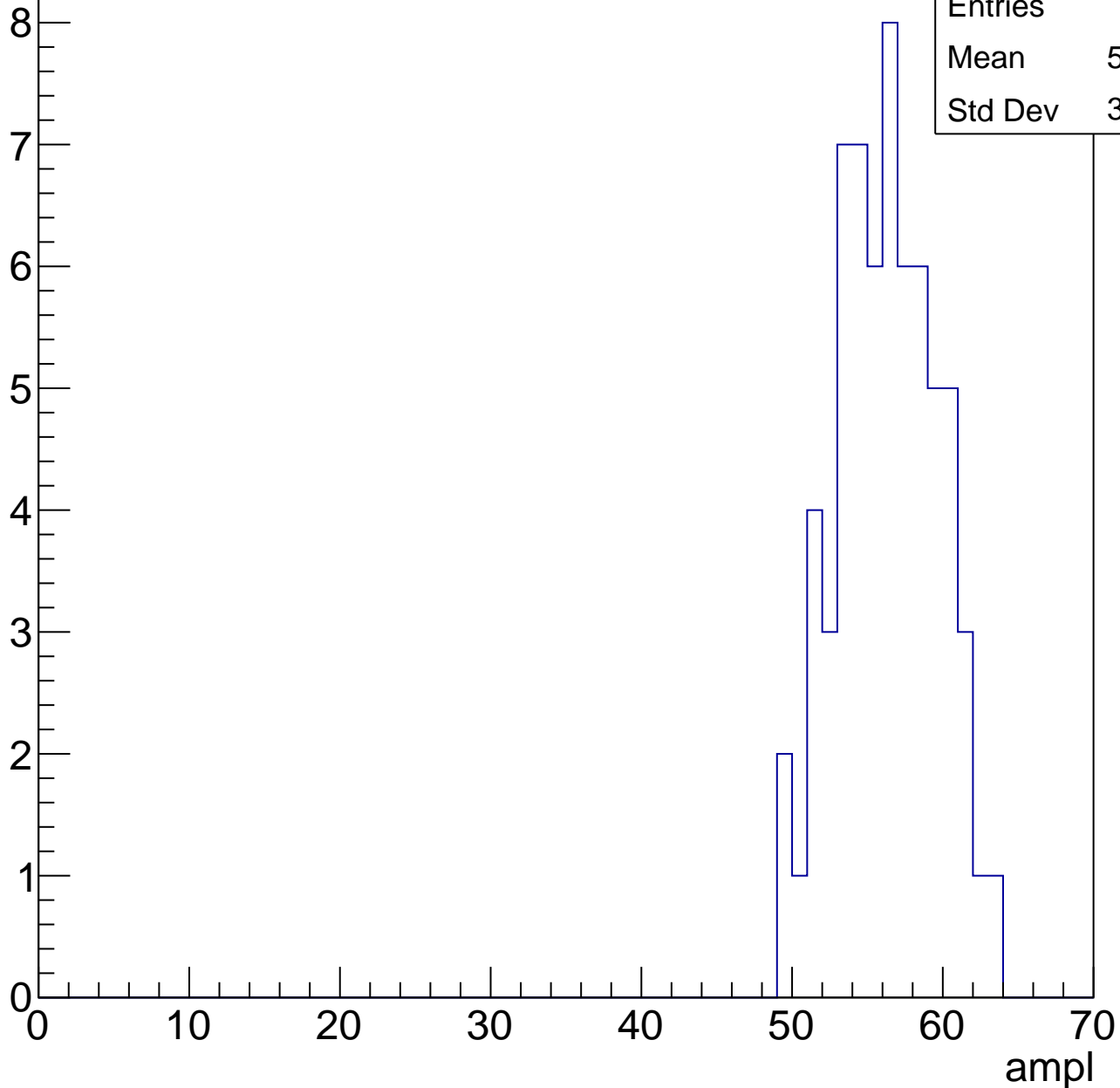
Entries	48
Mean	50.08
Std Dev	3.088



# B0L000S, U18-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

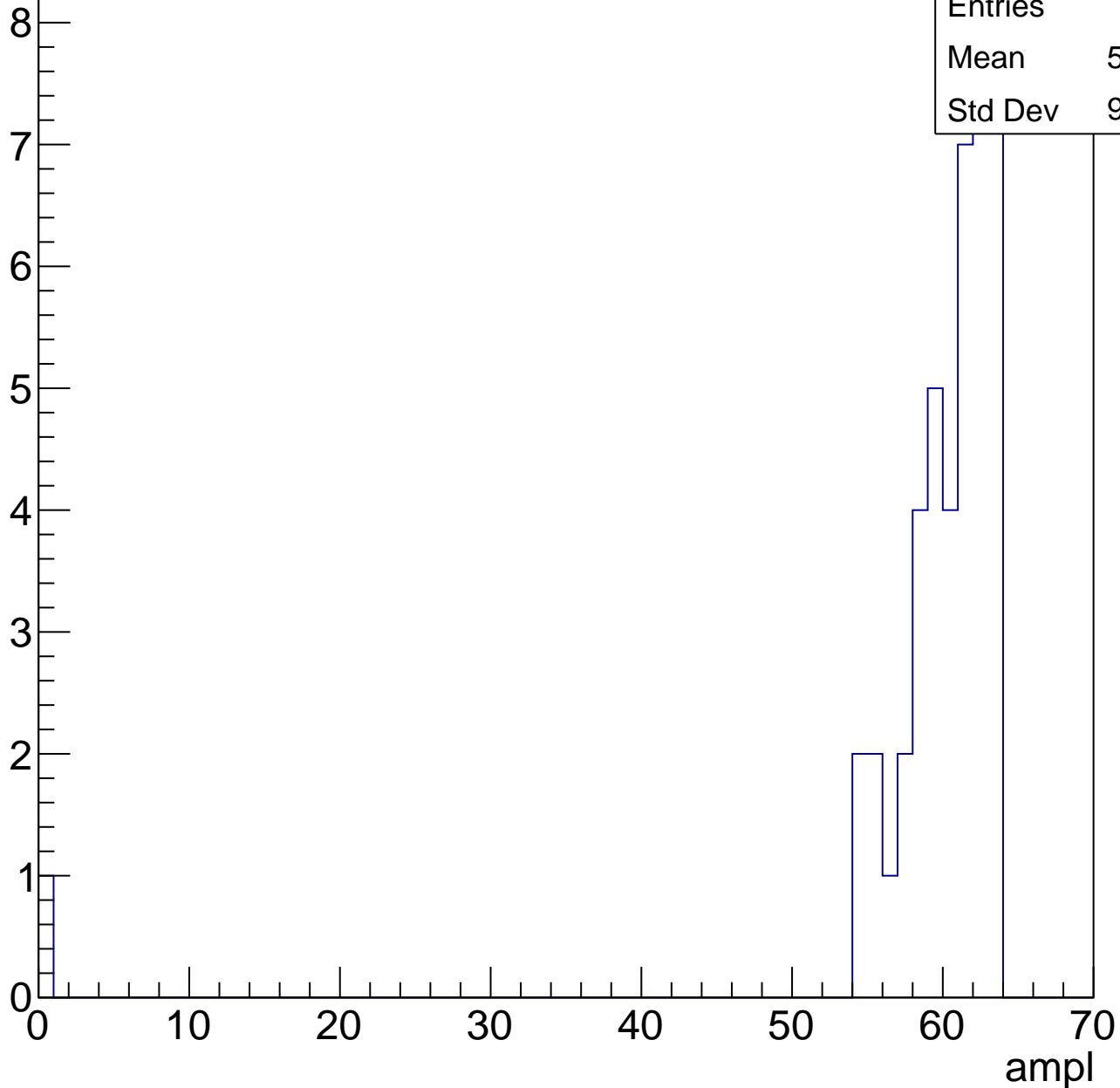


# B0L000S, U18-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.68
Std Dev	9.307



# B0L000S, U18-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch50, adc0

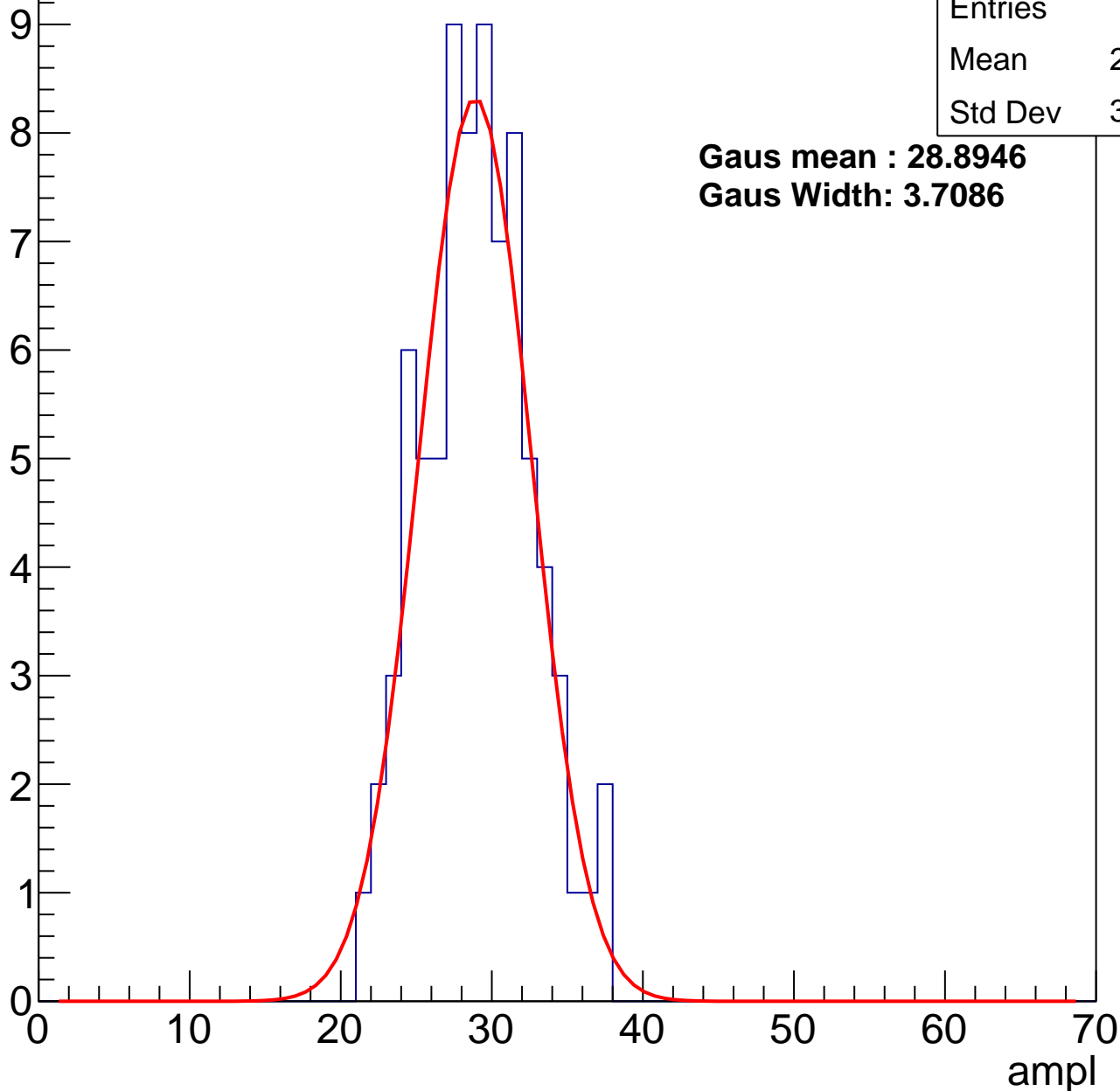
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	28.58
Std Dev	3.578

**Gaus mean : 28.8946**

**Gaus Width: 3.7086**



# B0L000S, U18-ch50, adc1

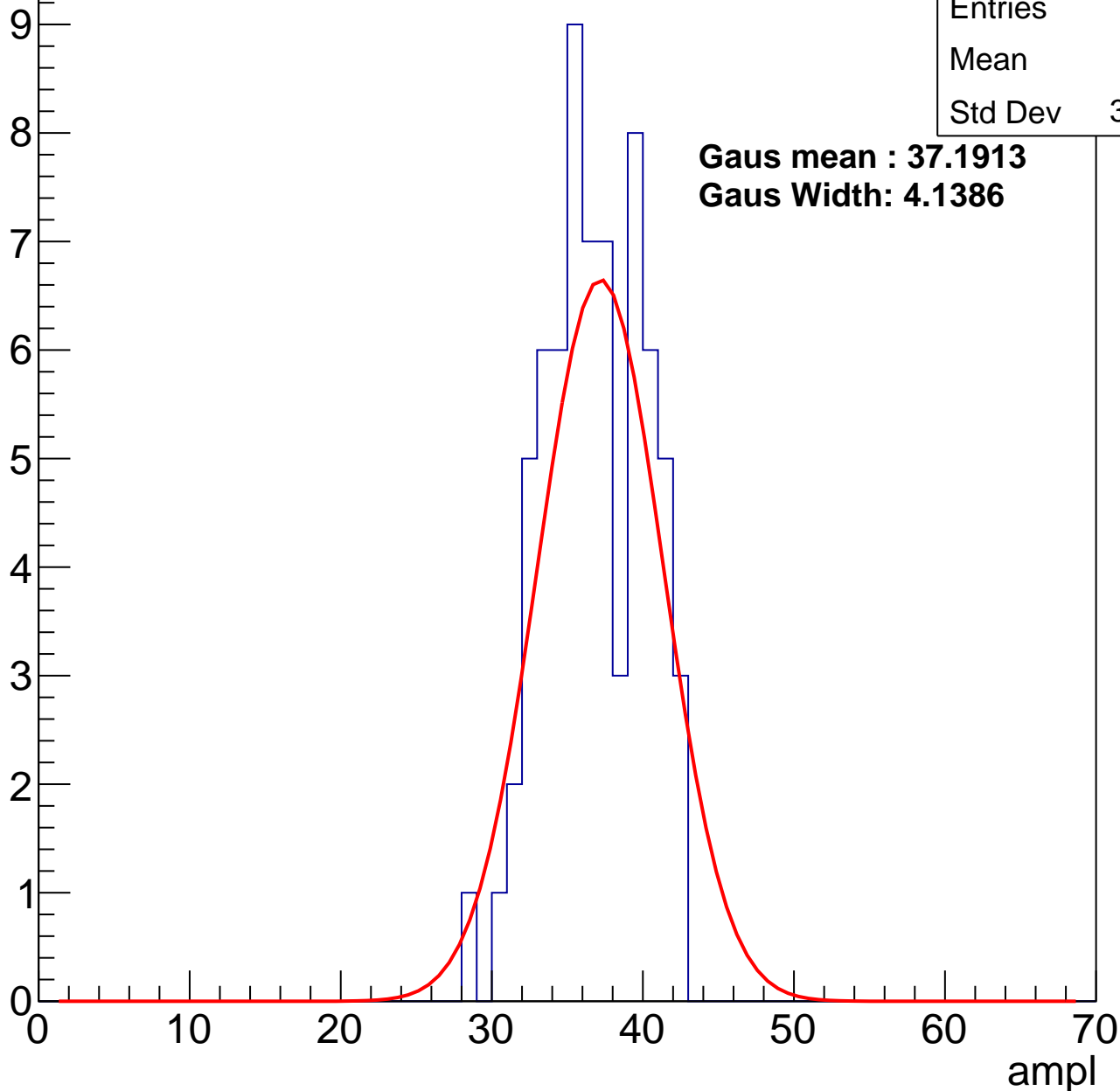
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.3
Std Dev	3.254

**Gaus mean : 37.1913**

**Gaus Width: 4.1386**



# B0L000S, U18-ch50, adc2

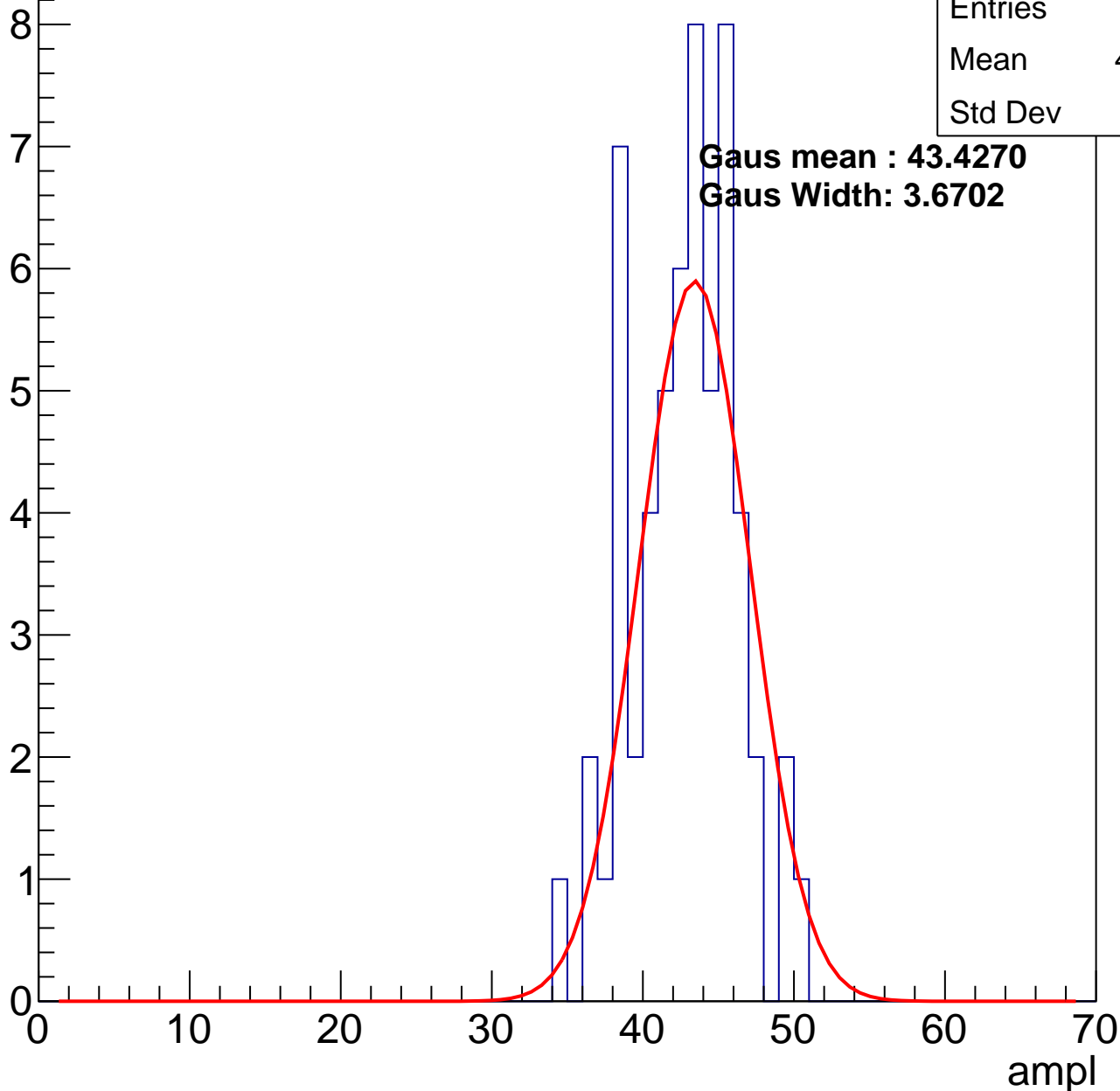
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.31
Std Dev	3.43

**Gaus mean : 43.4270**

**Gaus Width: 3.6702**

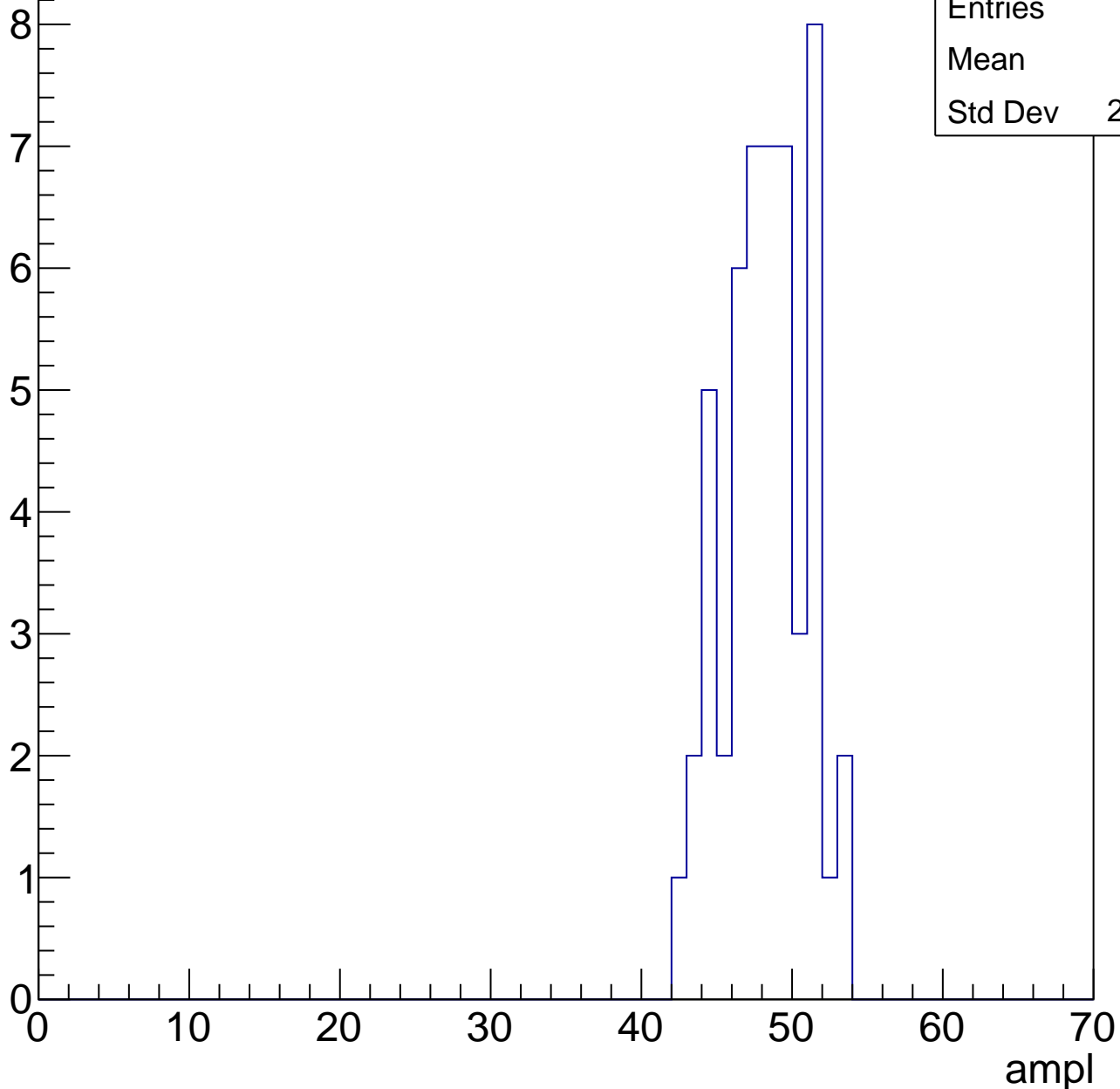


# B0L000S, U18-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	47.8
Std Dev	2.694

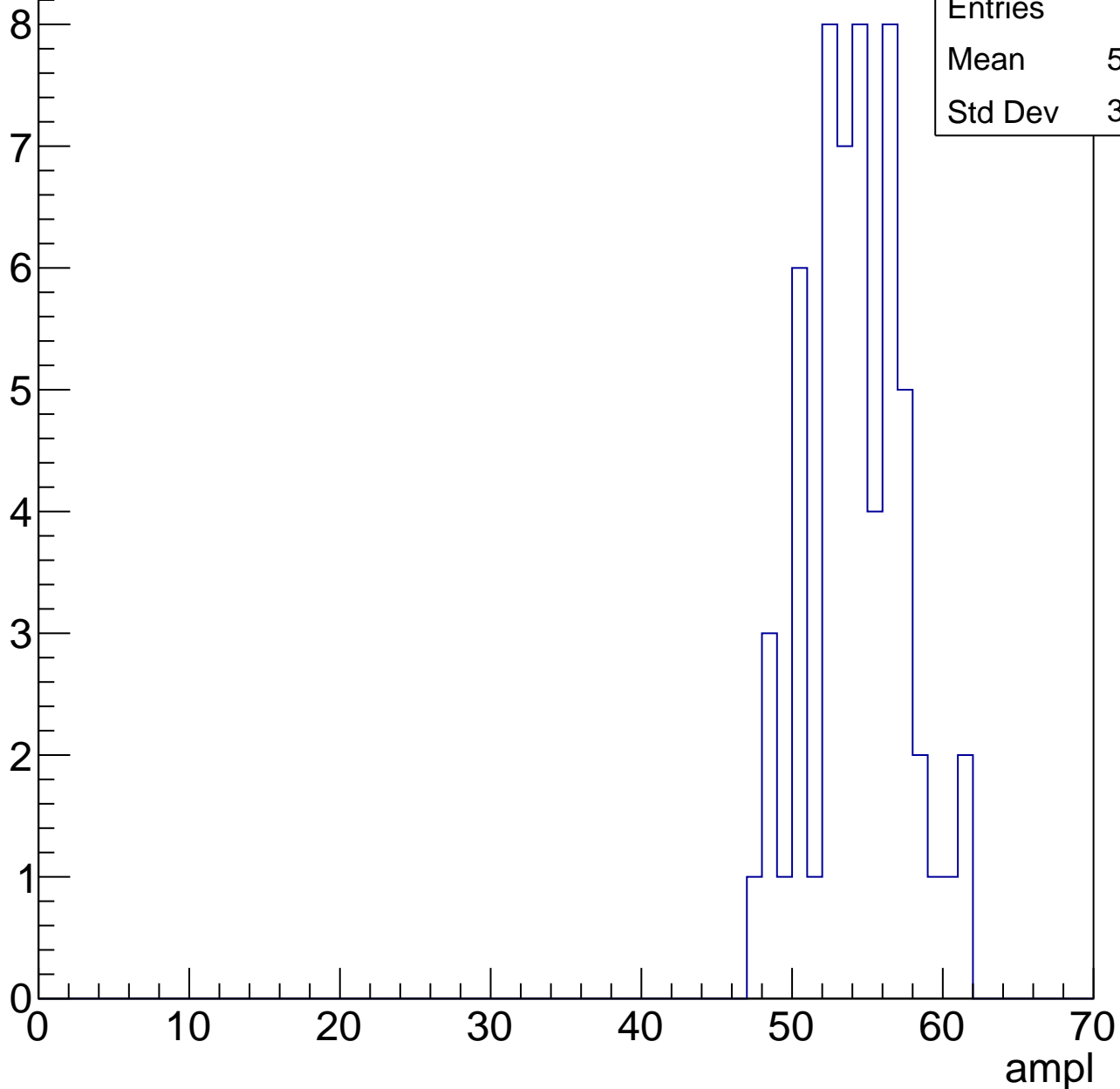


# B0L000S, U18-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

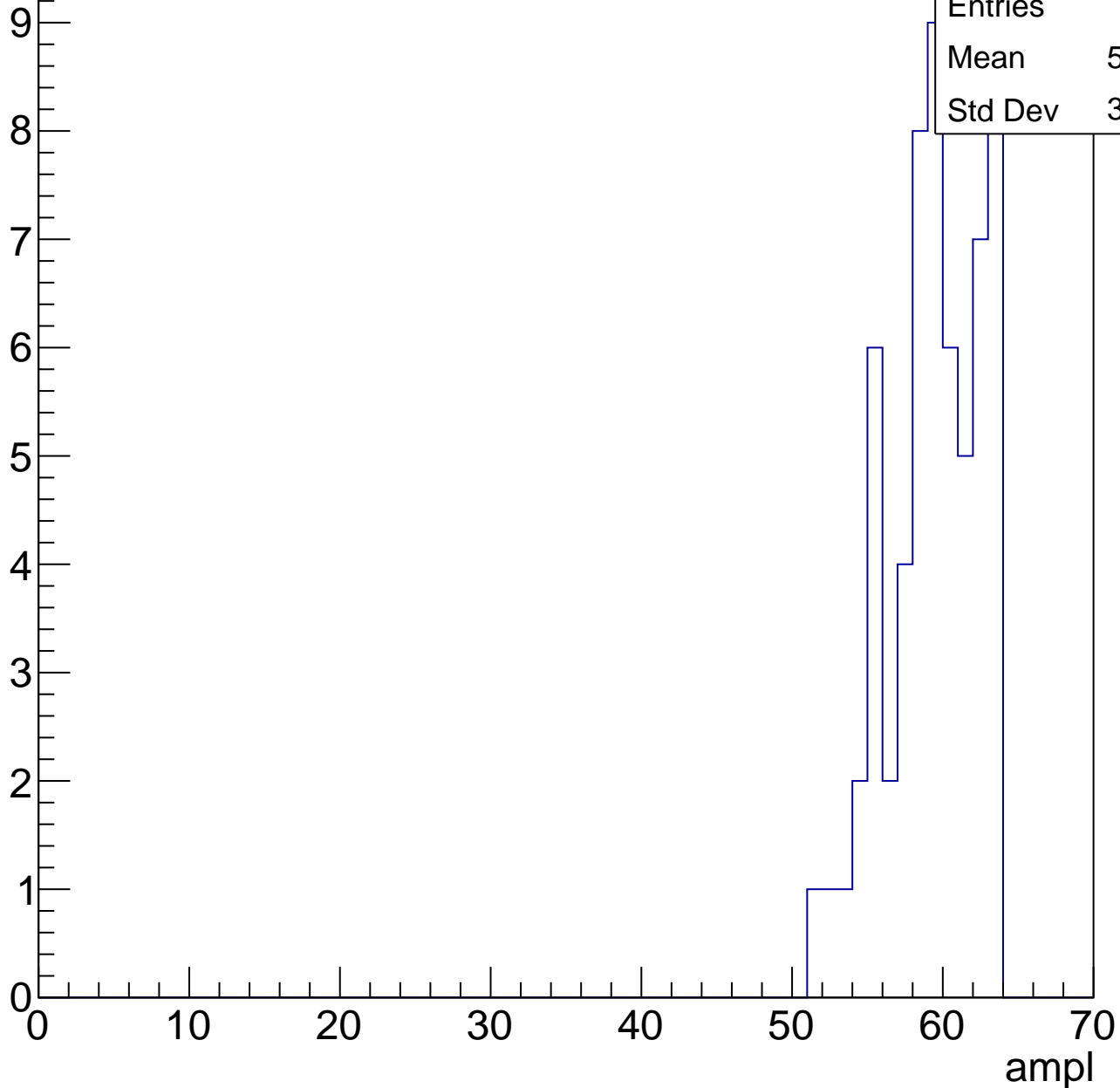
Entries	58
Mean	53.79
Std Dev	3.204



# B0L000S, U18-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

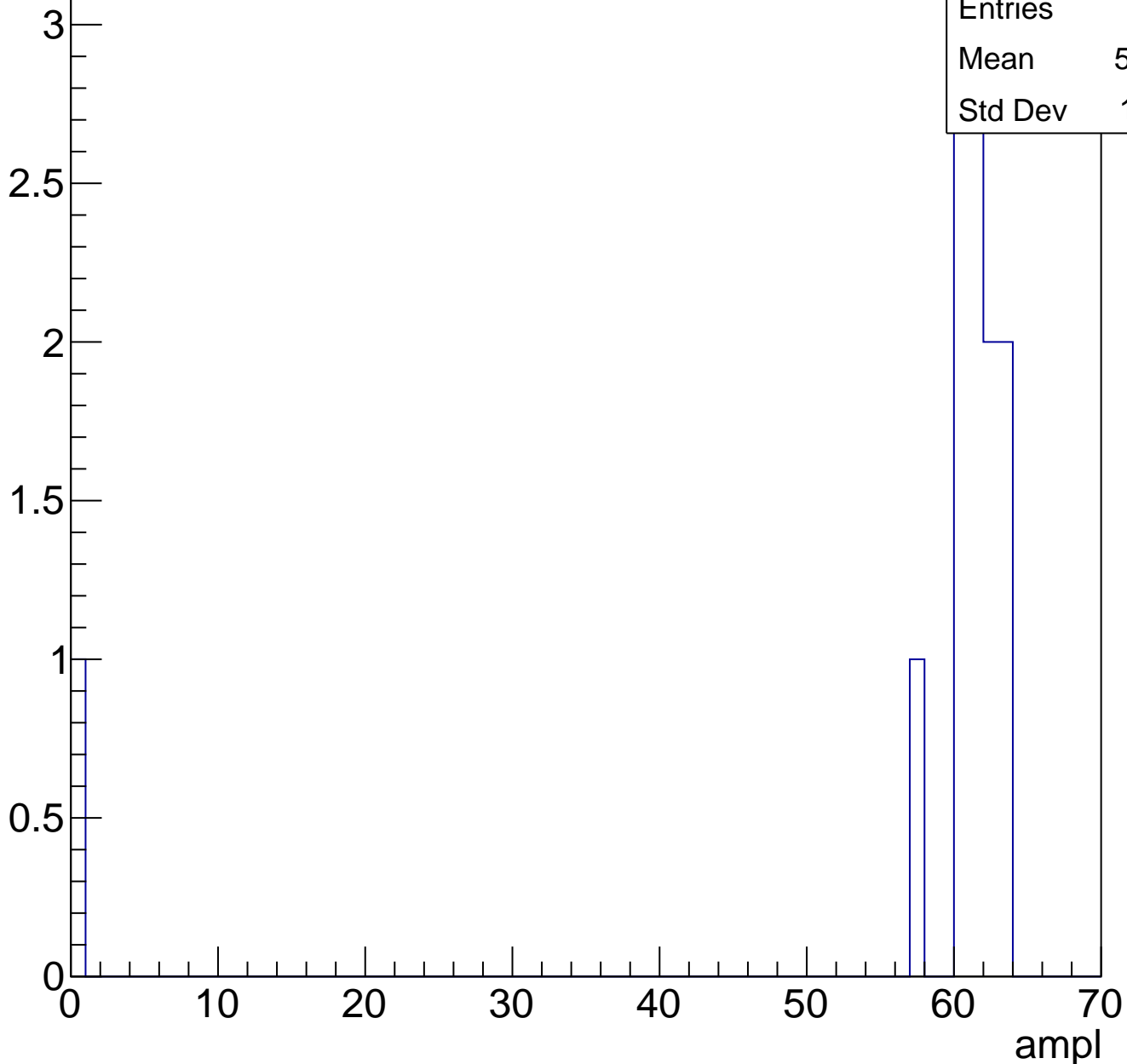


Entries	60
Mean	58.87
Std Dev	3.036

# B0L000S, U18-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch51, adc0

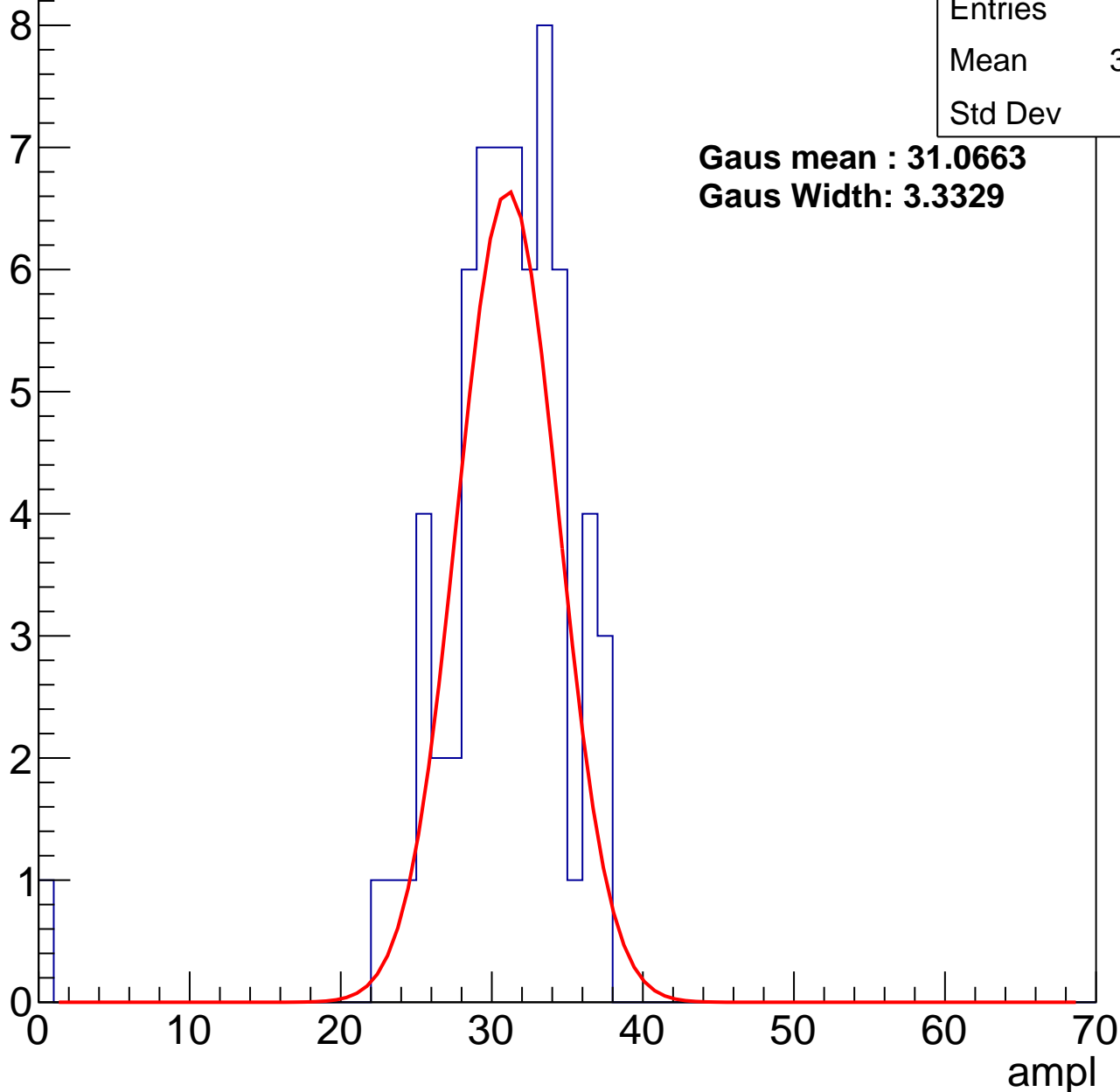
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.19
Std Dev	5.1

**Gaus mean : 31.0663**

**Gaus Width: 3.3329**



# B0L000S, U18-ch51, adc1

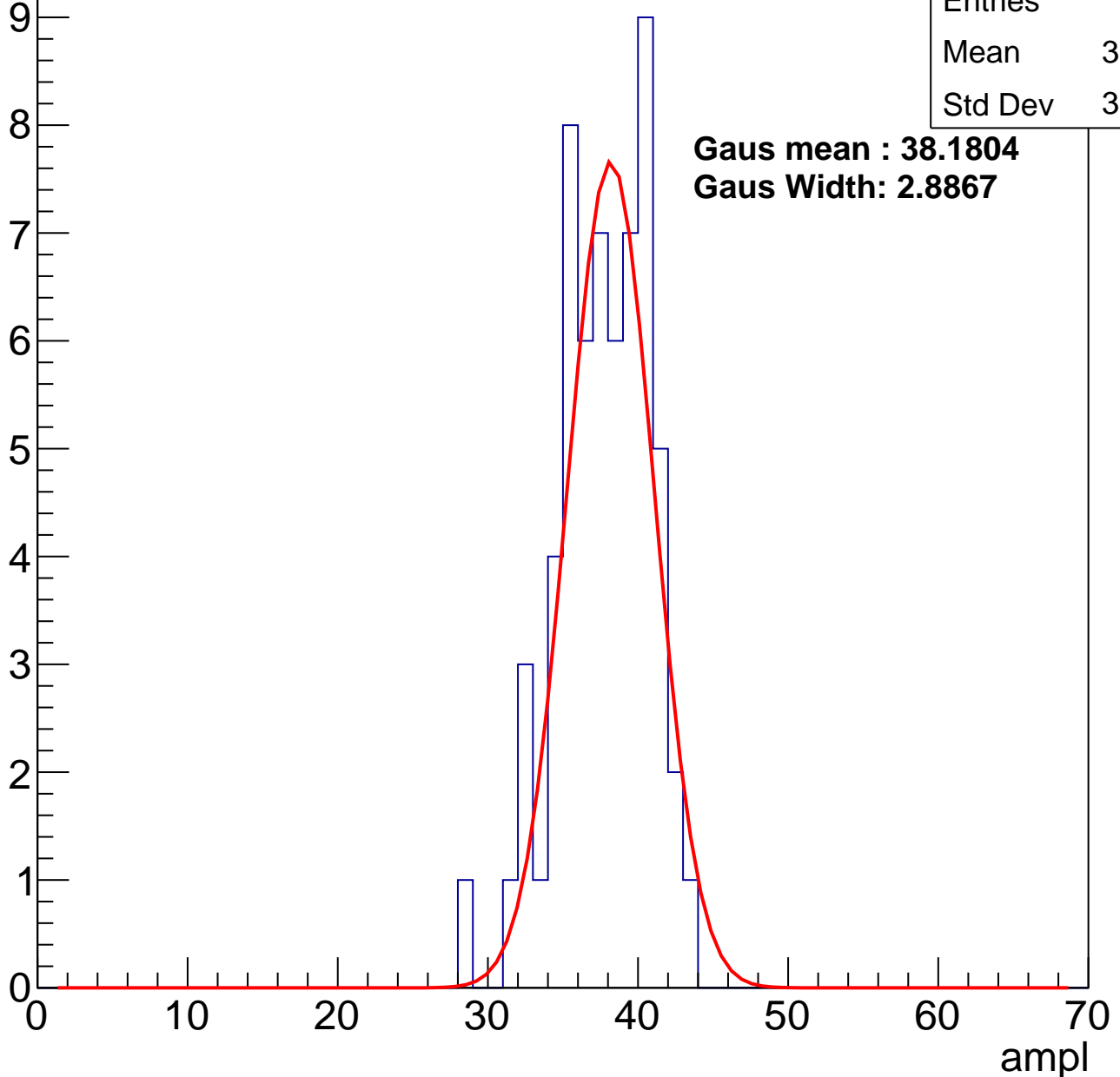
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.25
Std Dev	3.028

**Gaus mean : 38.1804**

**Gaus Width: 2.8867**



# B0L000S, U18-ch51, adc2

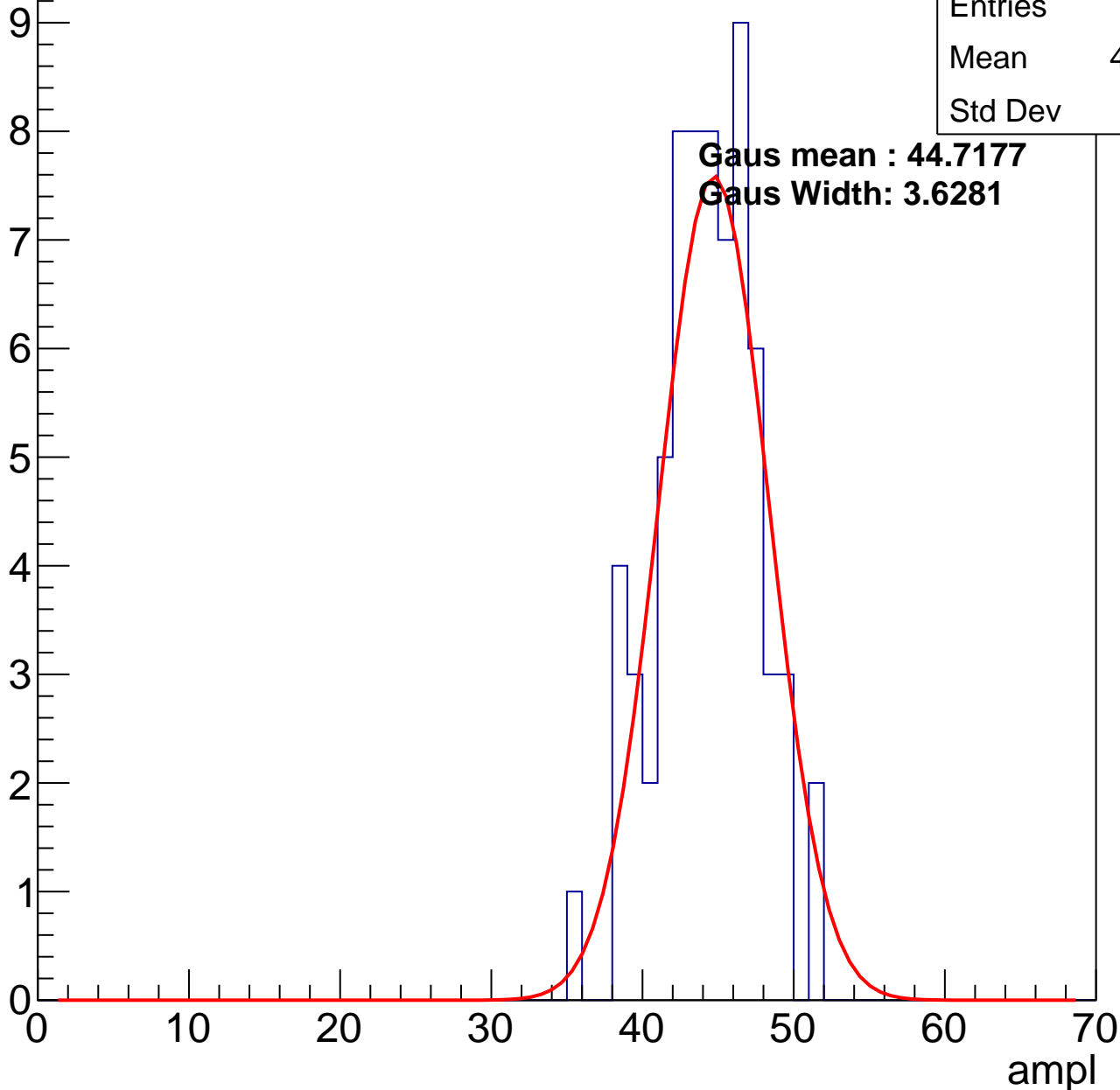
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.84
Std Dev	3.26

**Gaus mean : 44.7177**

**Gaus Width: 3.6281**

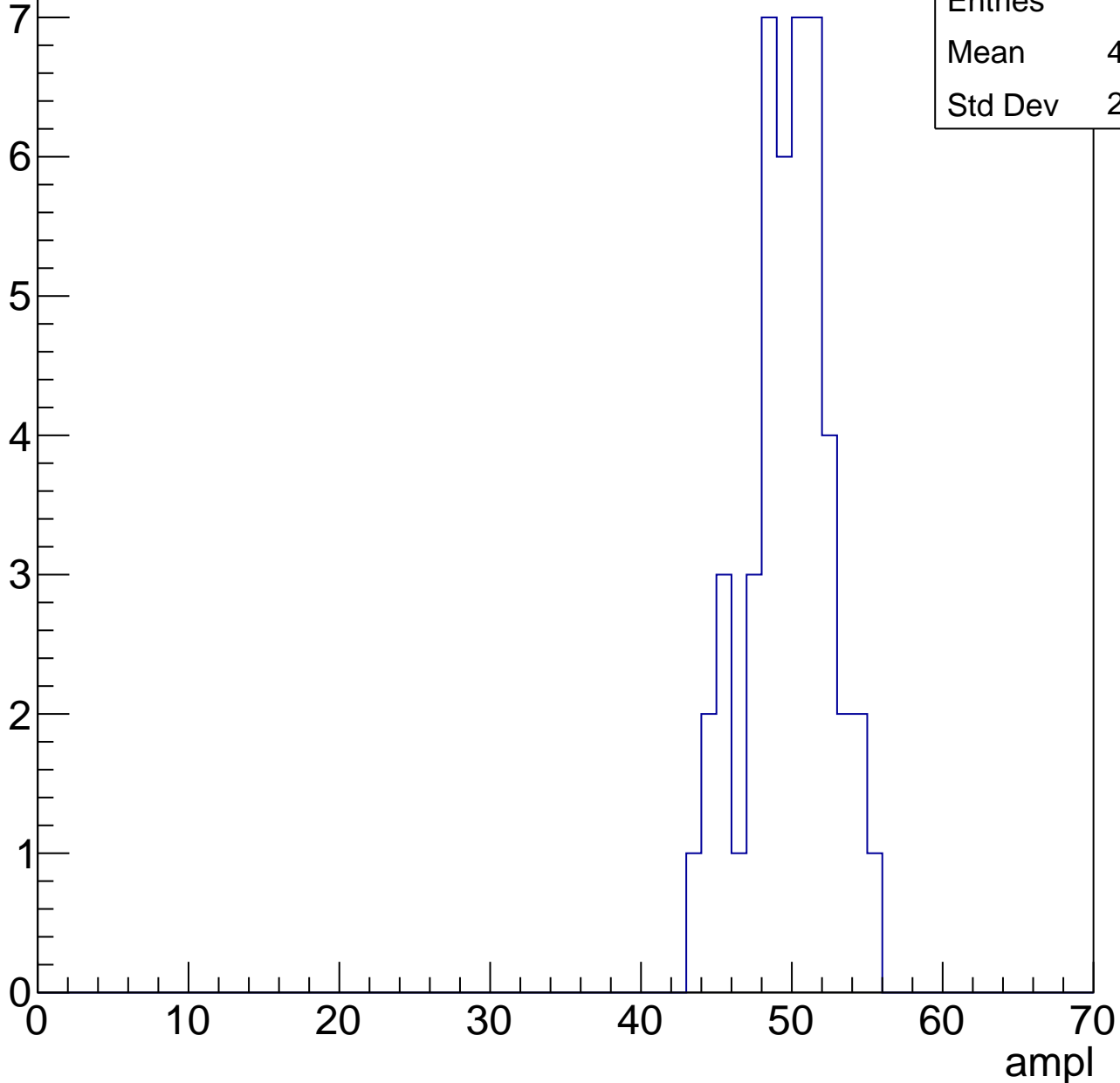


# B0L000S, U18-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	49.28
Std Dev	2.748



# B0L000S, U18-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	55.31
Std Dev	3.173

Entry

10

8

6

4

2

0

0

10

20

30

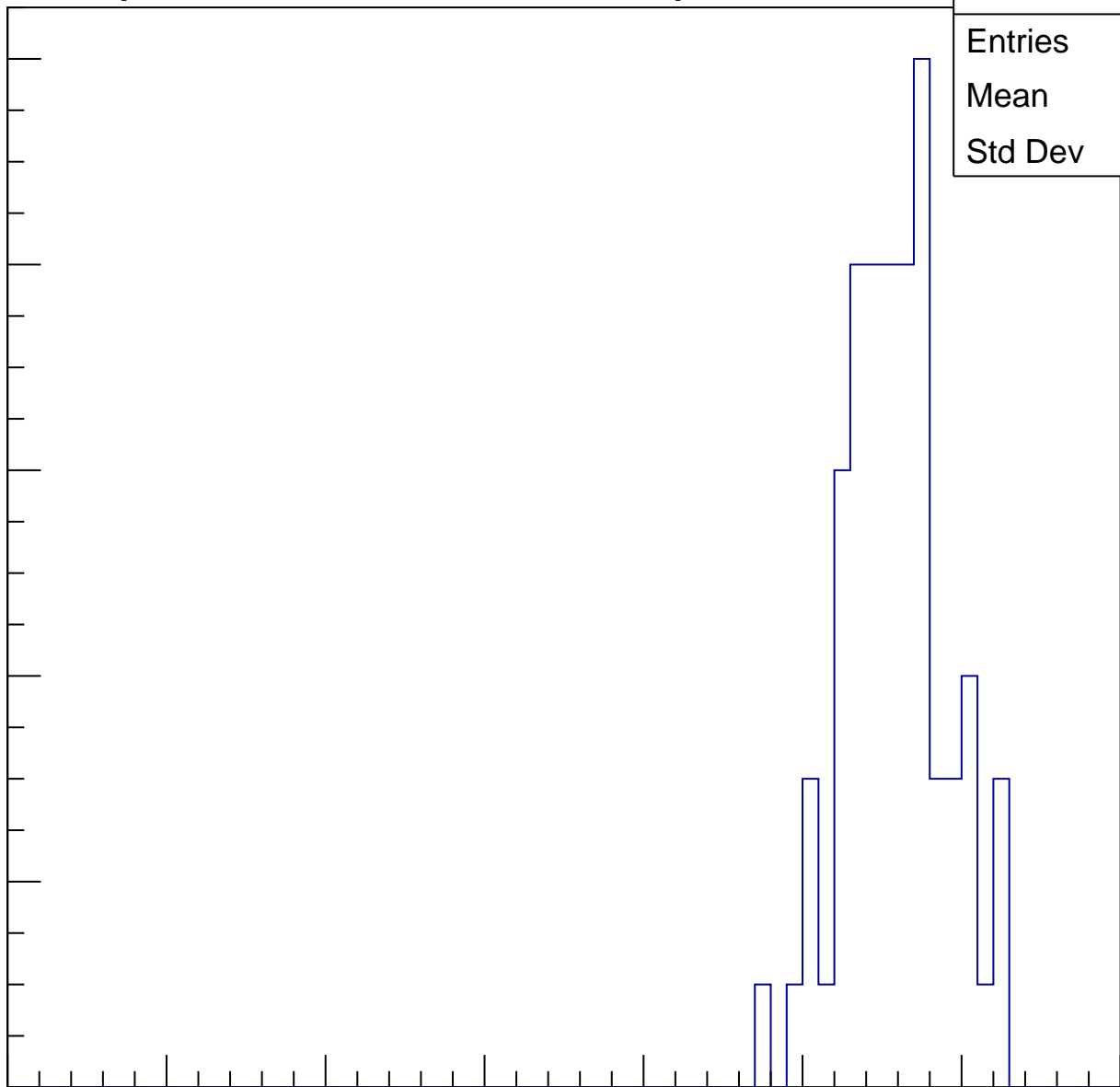
40

50

60

70

ampl

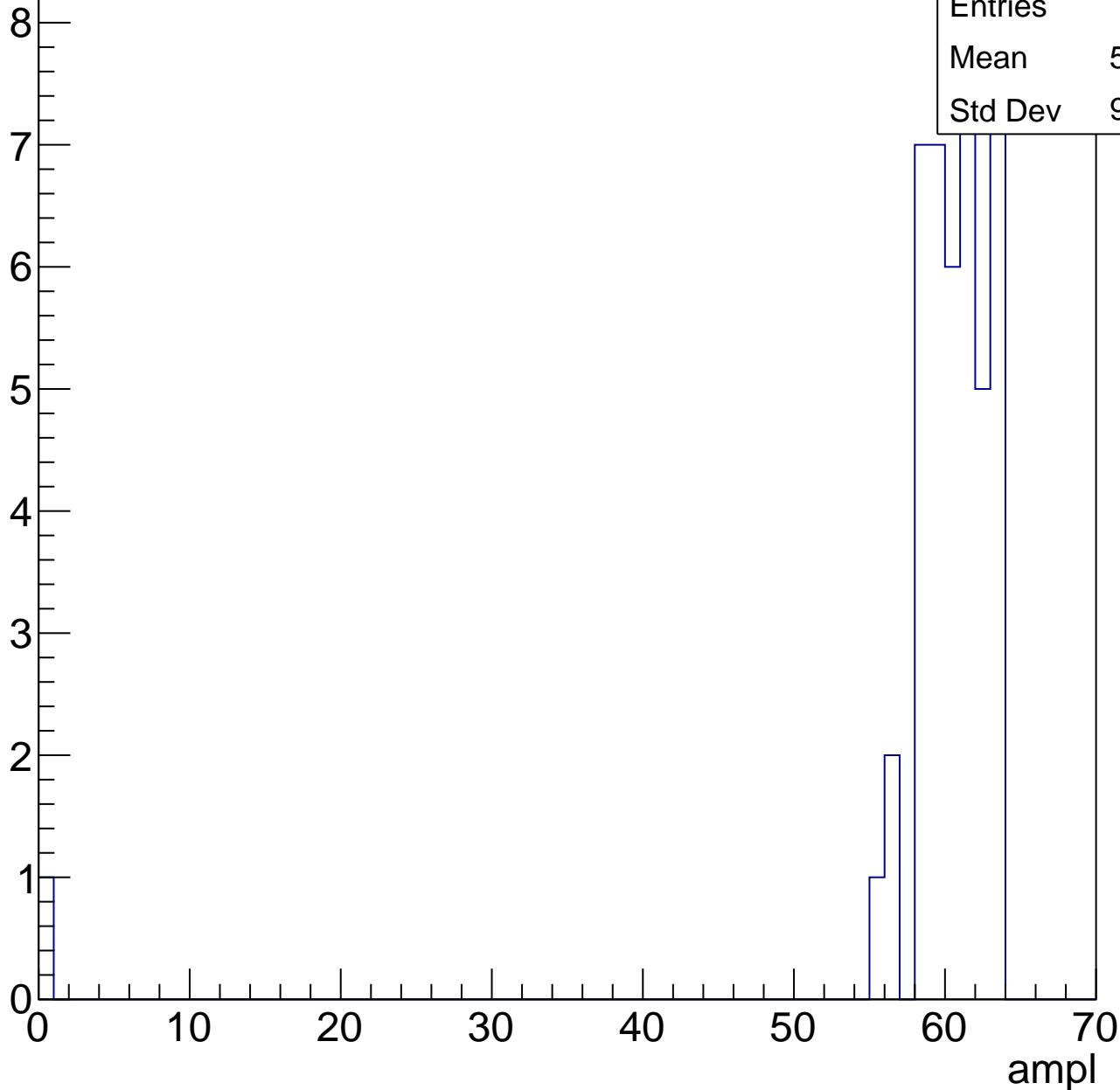


# B0L000S, U18-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.84
Std Dev	9.107



# B0L000S, U18-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch52, adc0

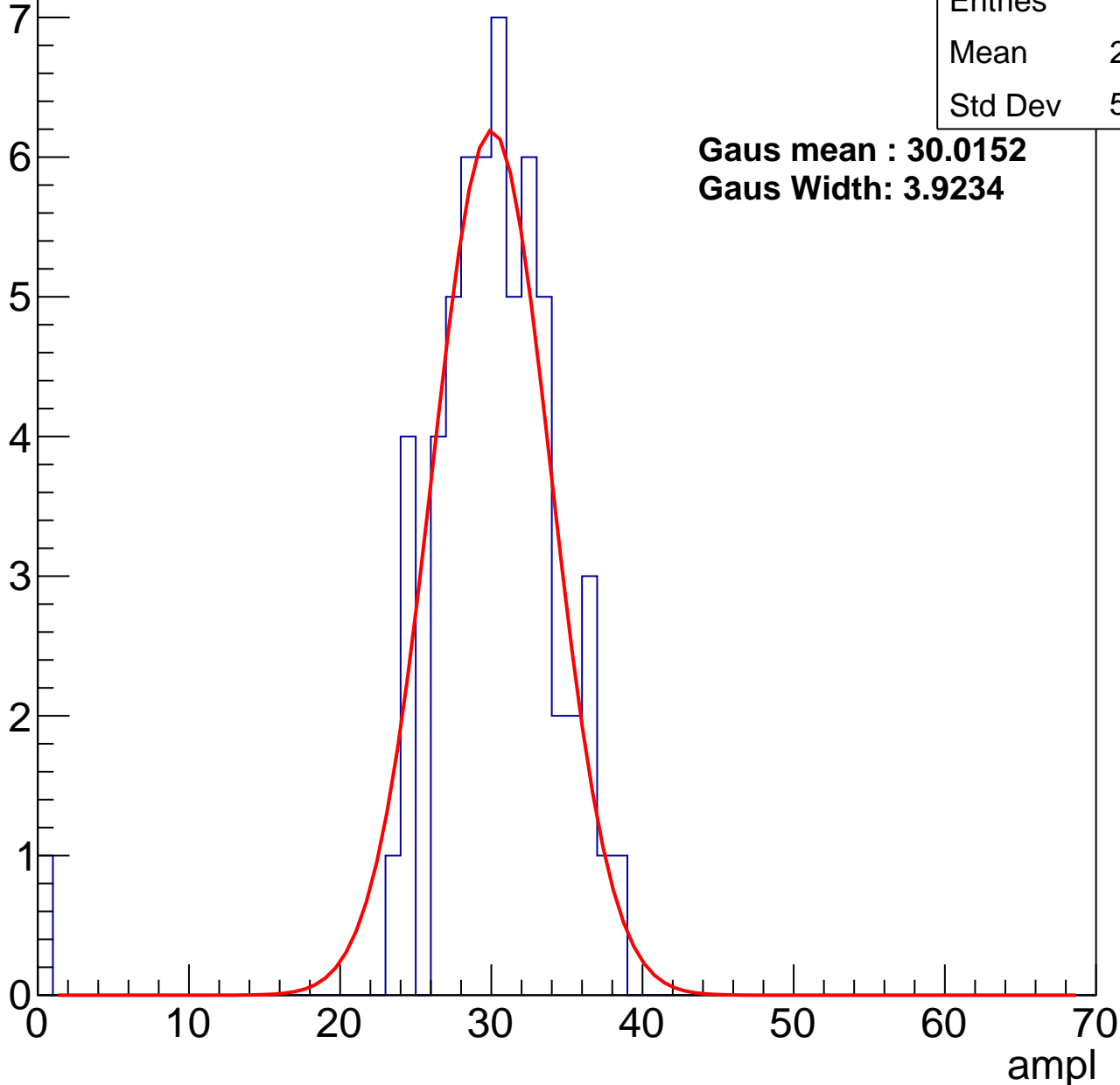
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.54
Std Dev	5.202

**Gaus mean : 30.0152**

**Gaus Width: 3.9234**



# B0L000S, U18-ch52, adc1

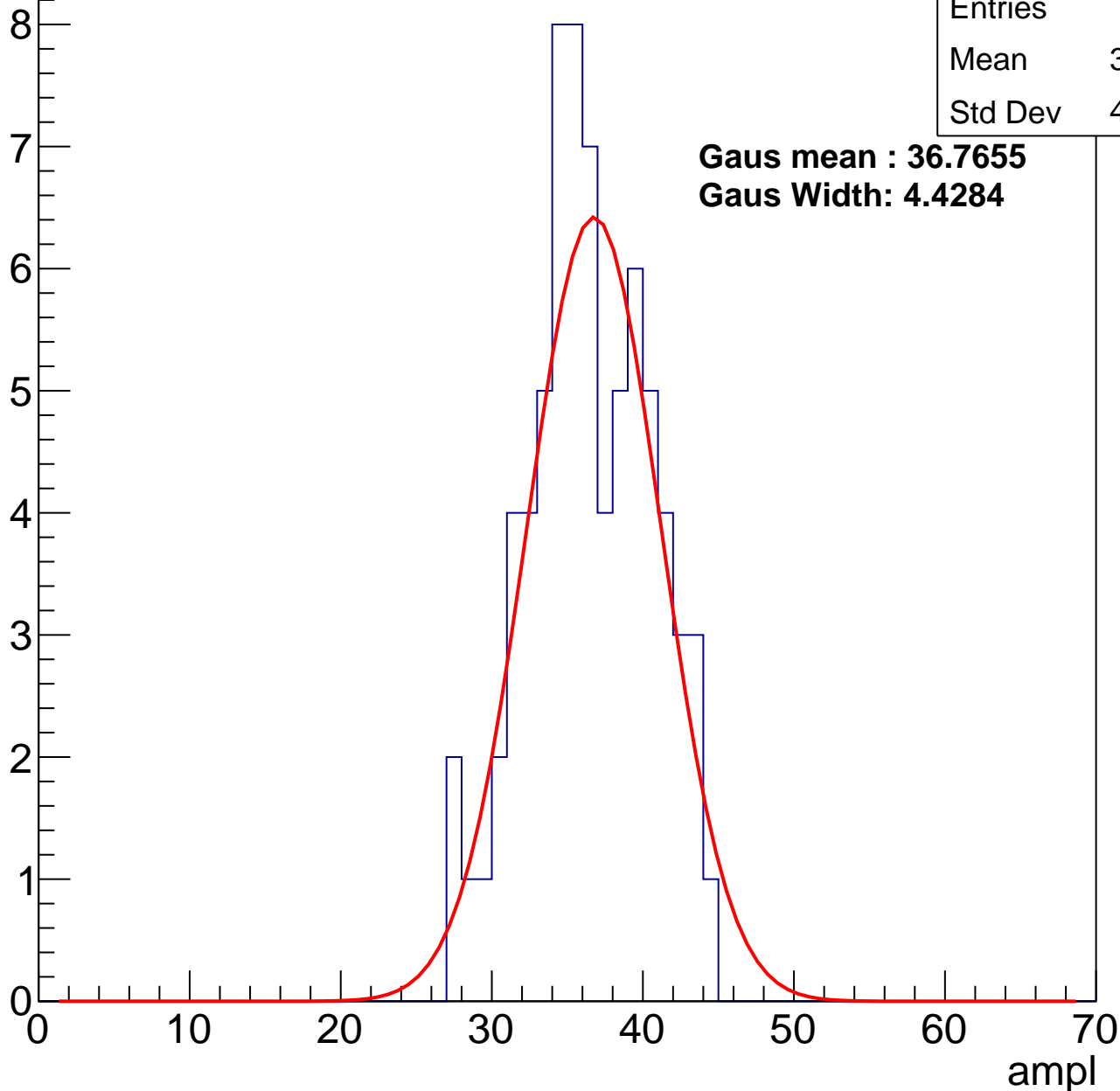
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	35.99
Std Dev	4.009

**Gaus mean : 36.7655**

**Gaus Width: 4.4284**



# B0L000S, U18-ch52, adc2

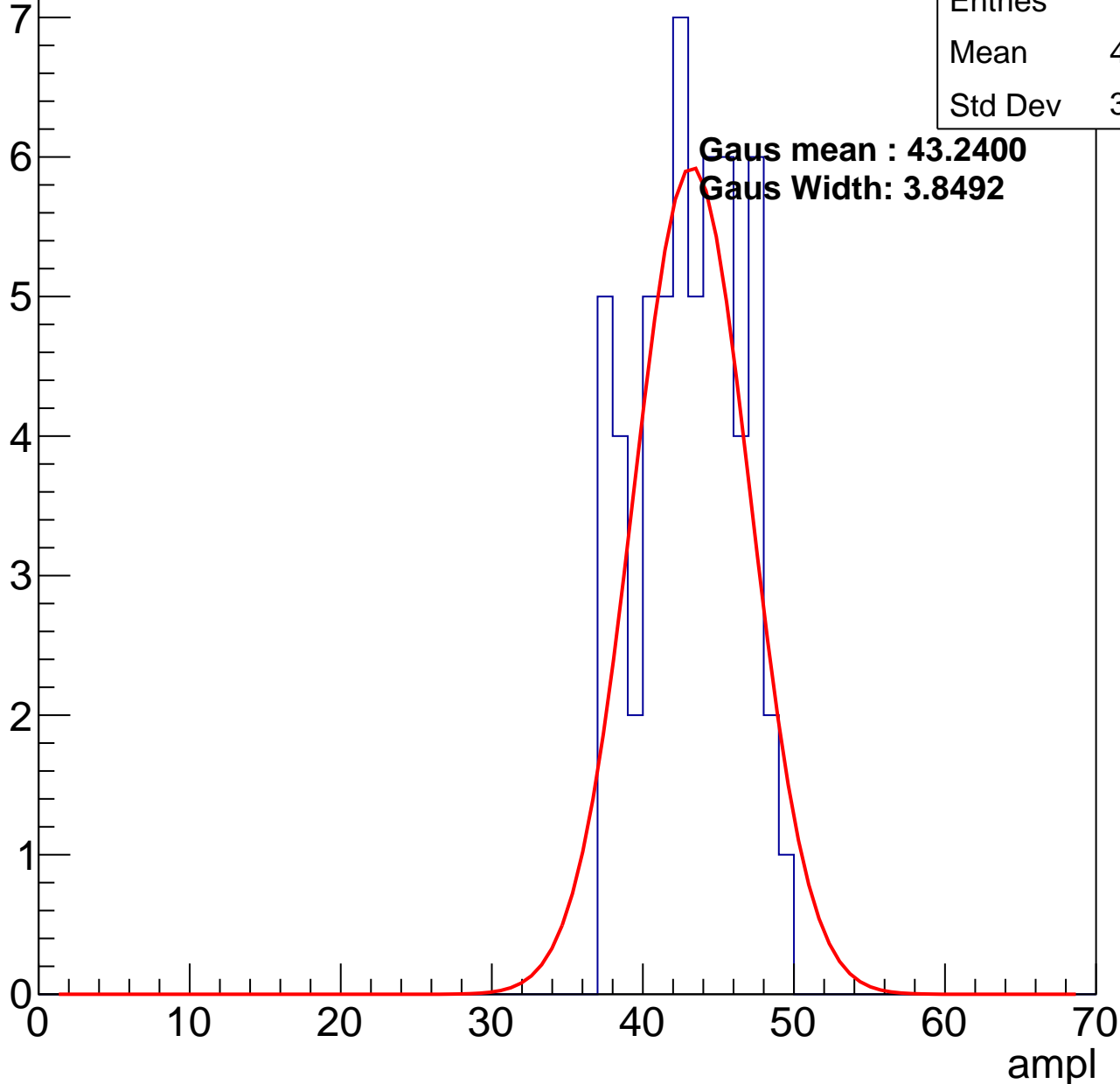
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.66
Std Dev	3.283

**Gaus mean : 43.2400**

**Gaus Width: 3.8492**



# B0L000S, U18-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

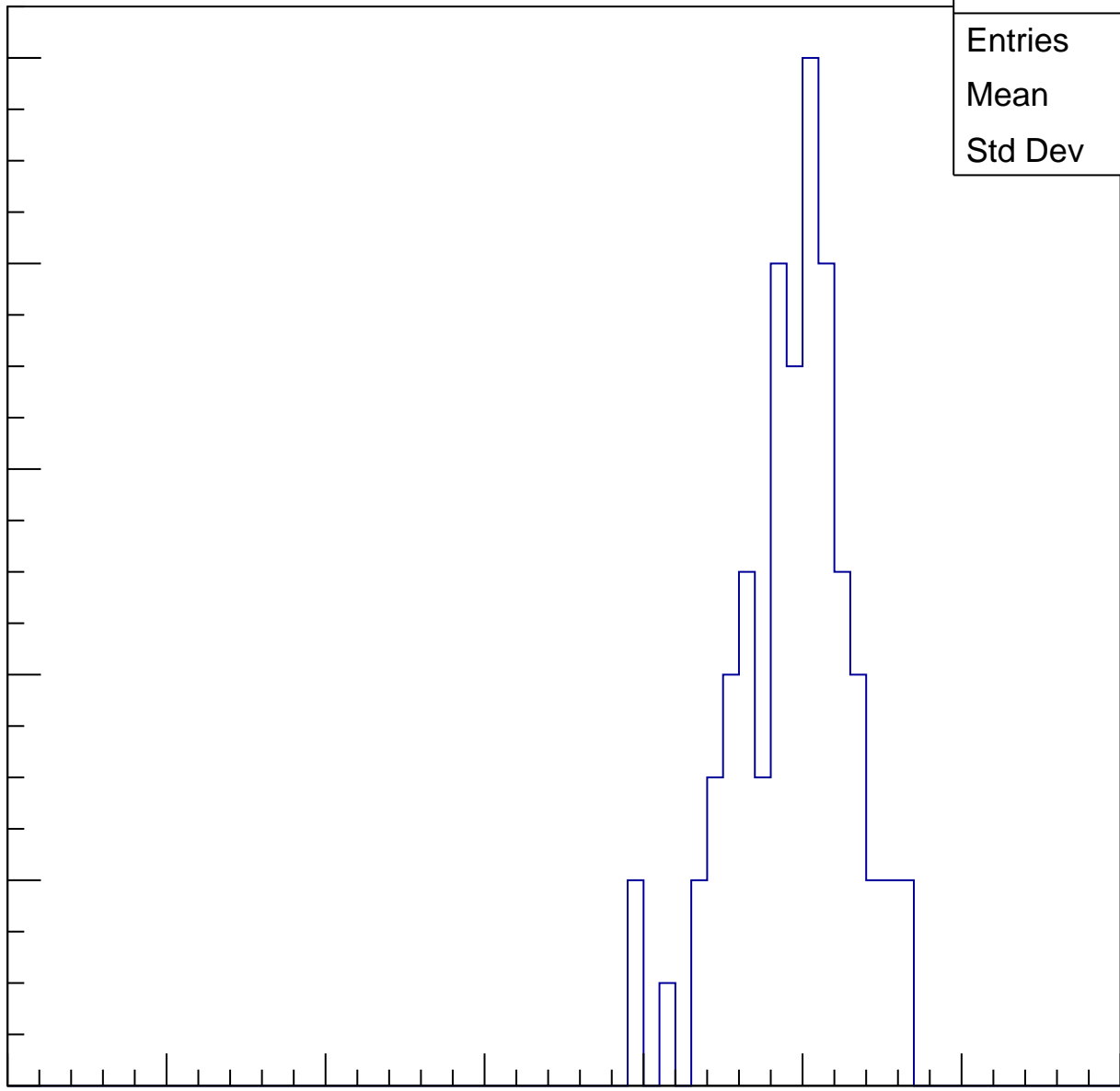
Entries	68
Mean	48.9
Std Dev	3.659

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

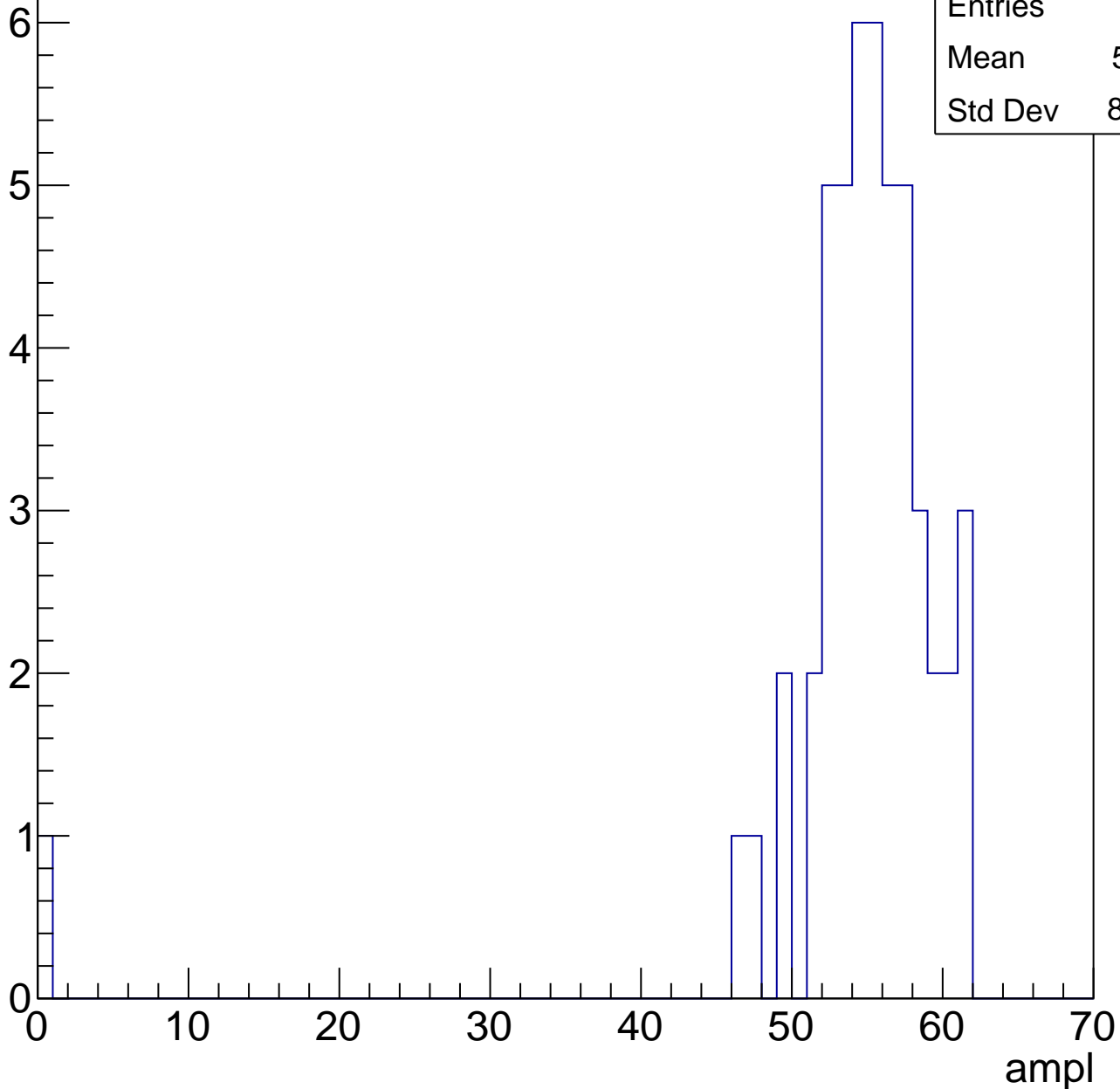


# B0L000S, U18-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

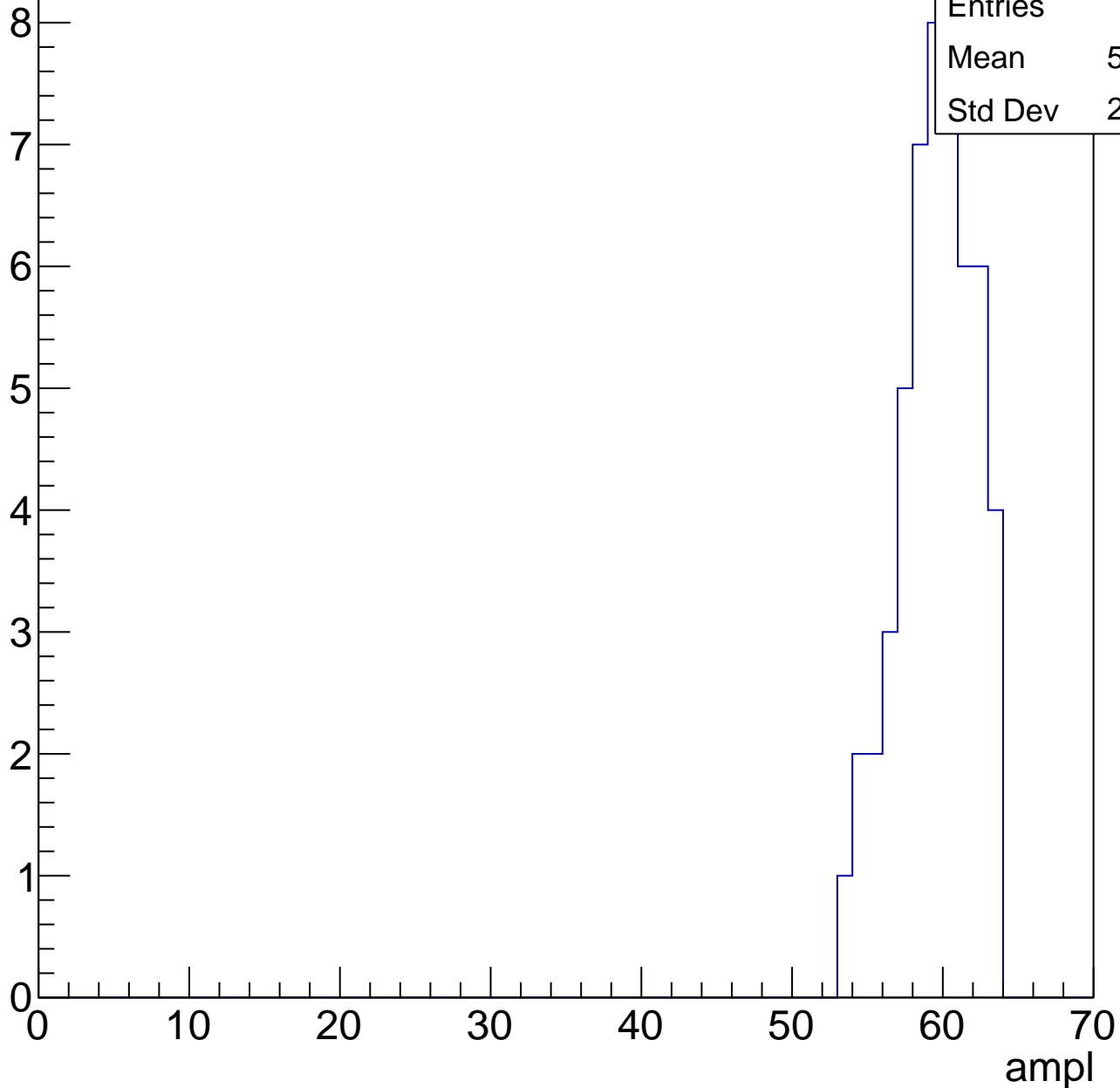
Entries	49
Mean	53.71
Std Dev	8.459



# B0L000S, U18-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

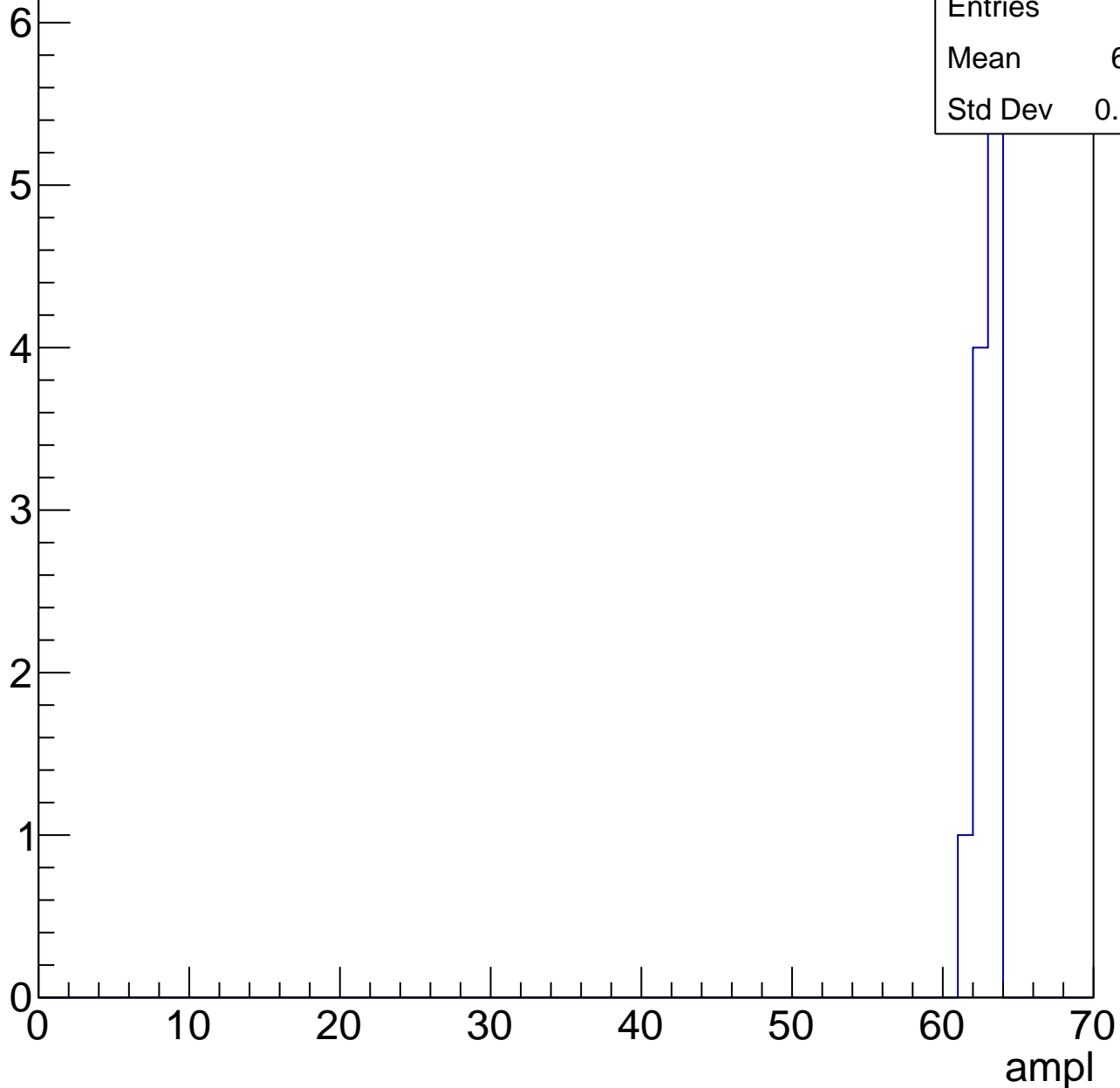


# B0L000S, U18-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	62.45
Std Dev	0.6556





# B0L000S, U18-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch53, adc0

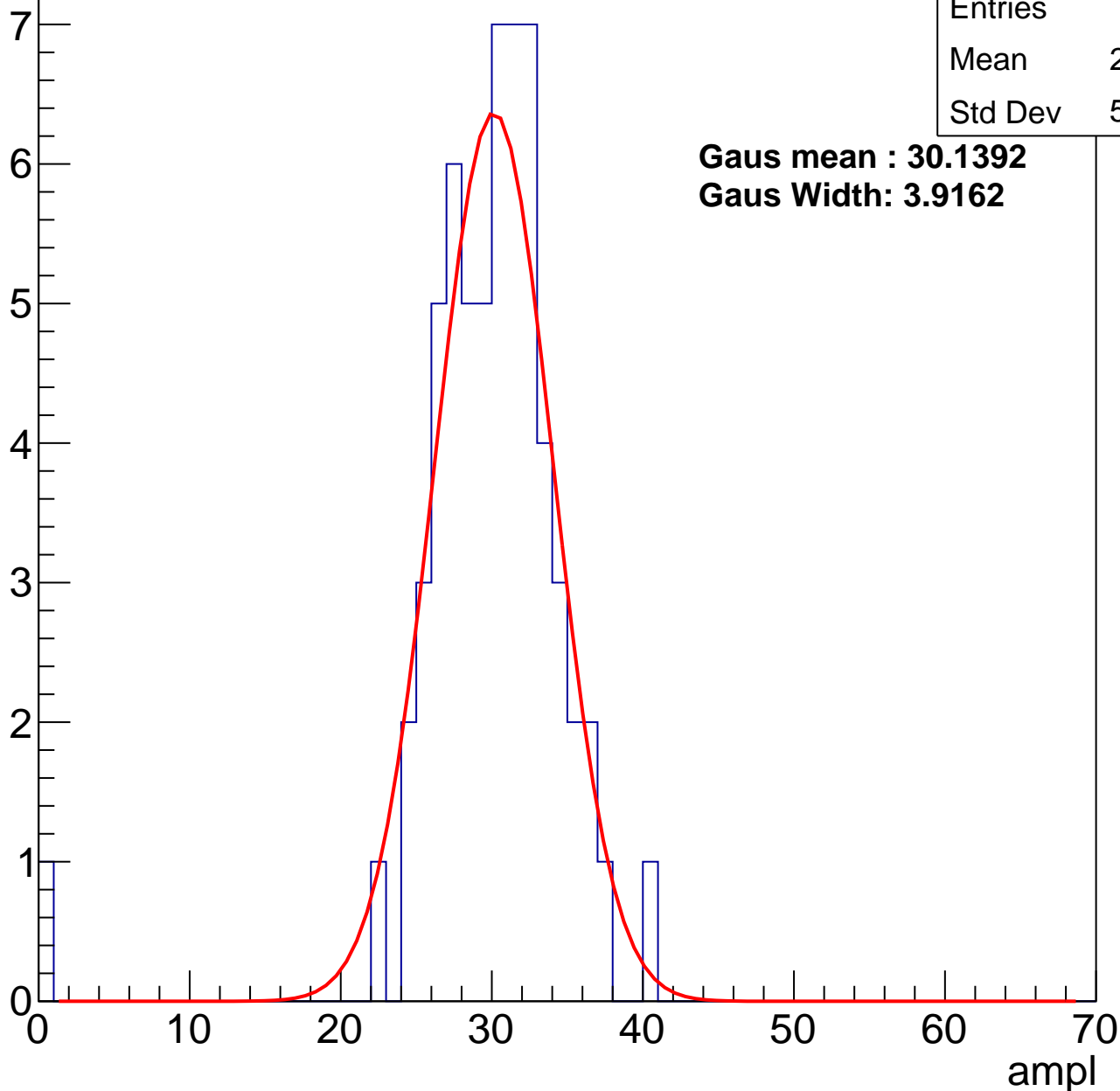
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	29.45
Std Dev	5.145

**Gaus mean : 30.1392**

**Gaus Width: 3.9162**



# B0L000S, U18-ch53, adc1

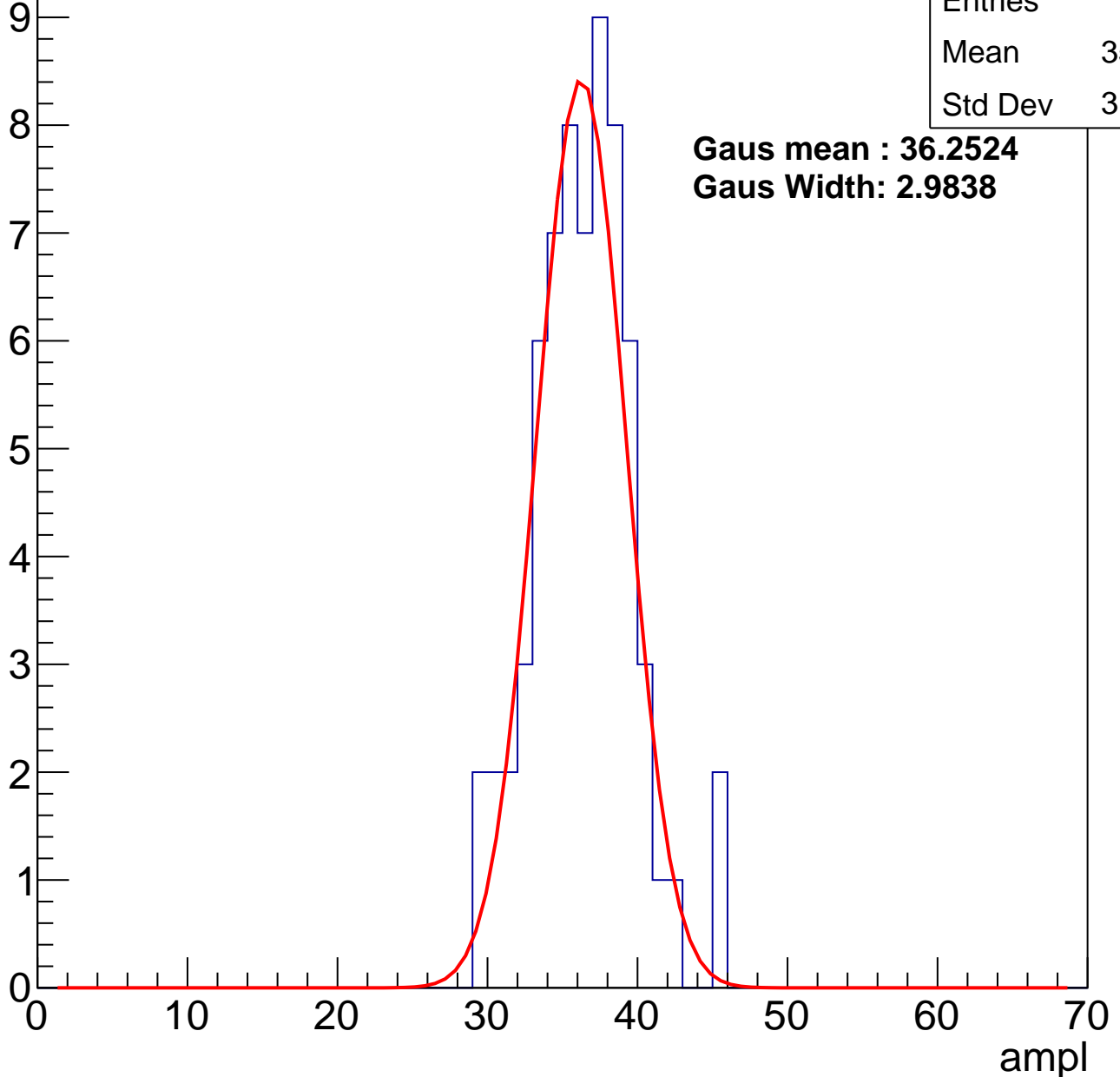
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.94
Std Dev	3.287

**Gaus mean : 36.2524**

**Gaus Width: 2.9838**



# B0L000S, U18-ch53, adc2

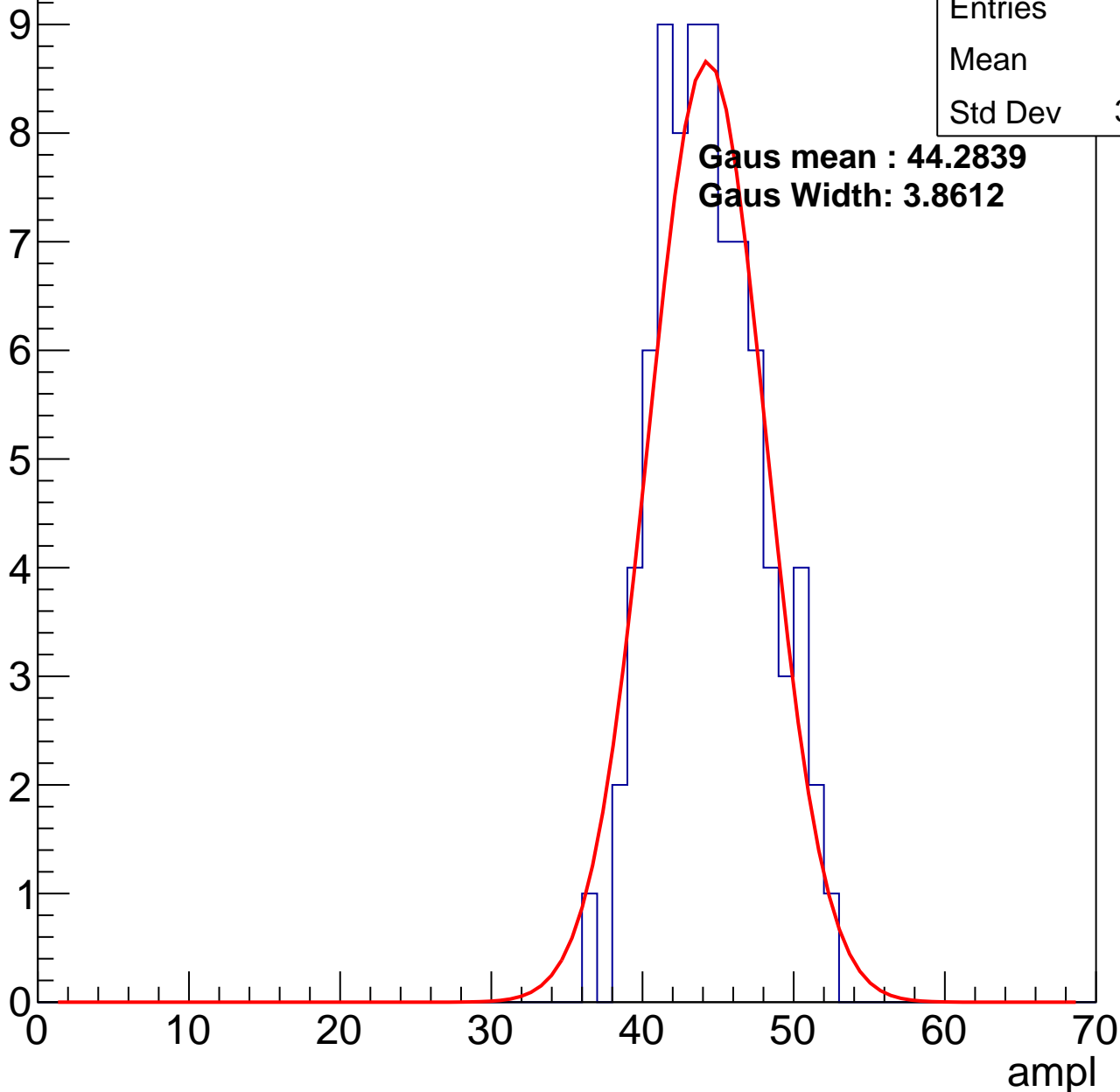
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	44
Std Dev	3.471

**Gaus mean : 44.2839**

**Gaus Width: 3.8612**

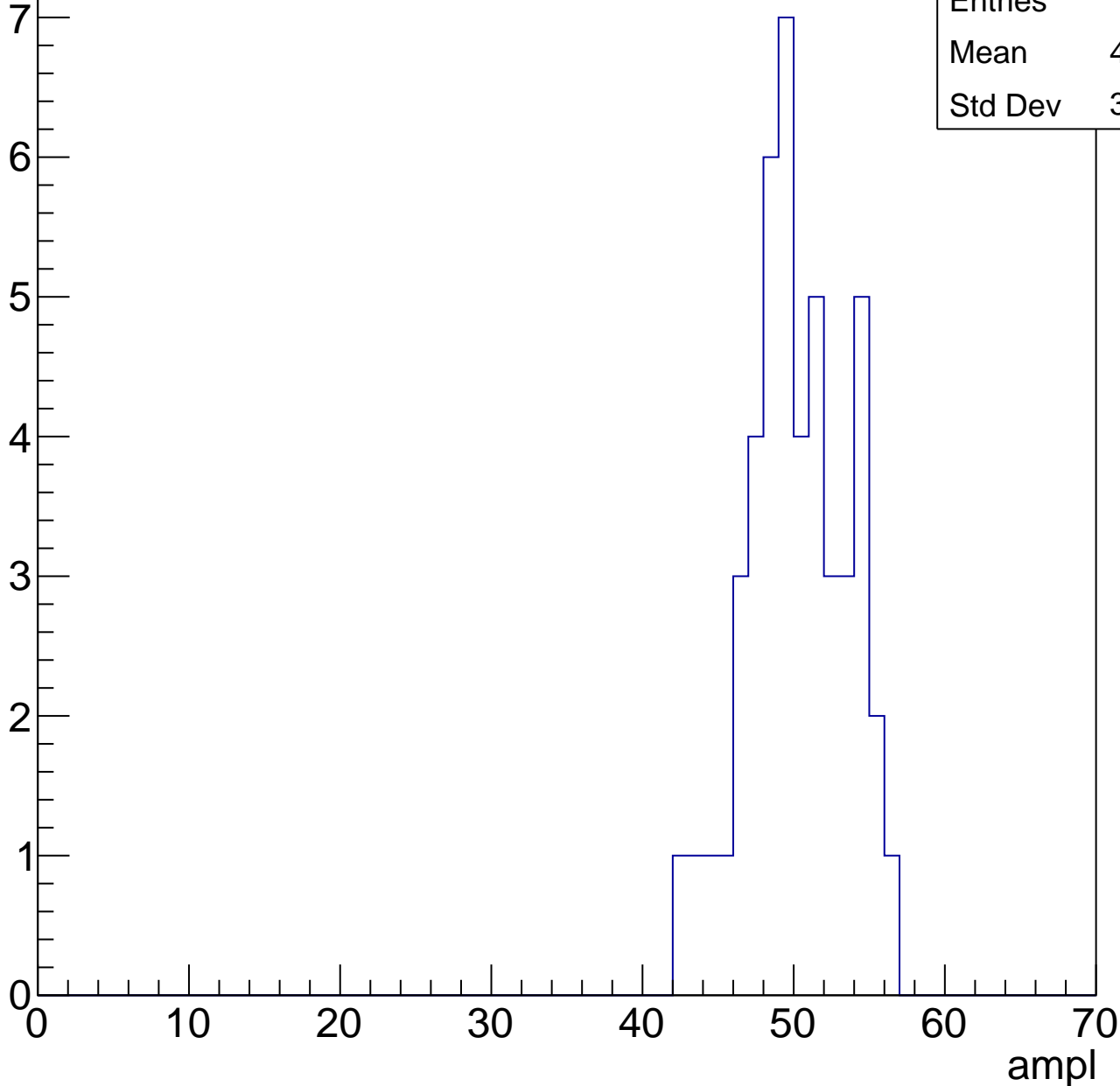


# B0L000S, U18-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	49.72
Std Dev	3.247

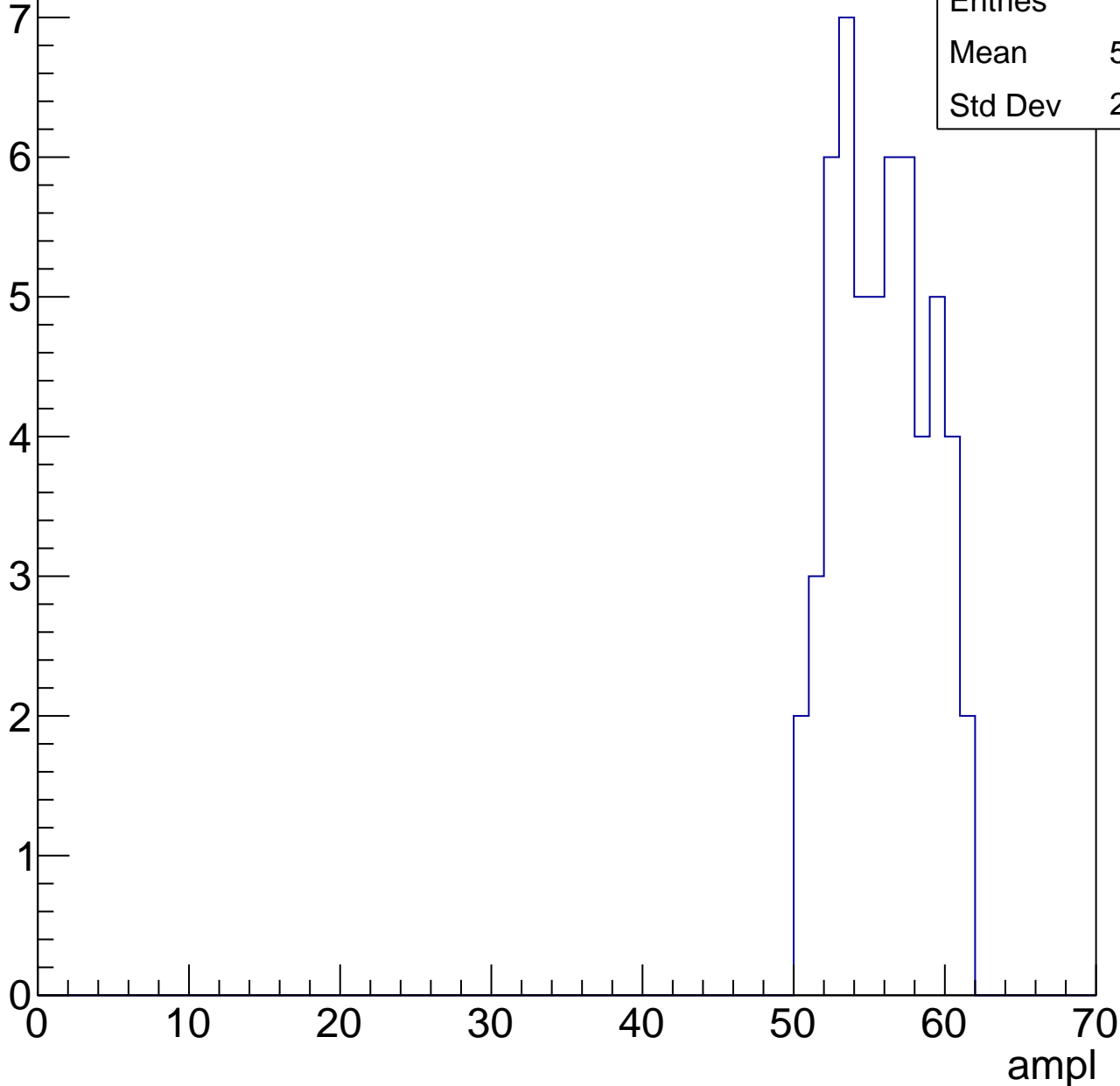


# B0L000S, U18-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.42
Std Dev	2.995

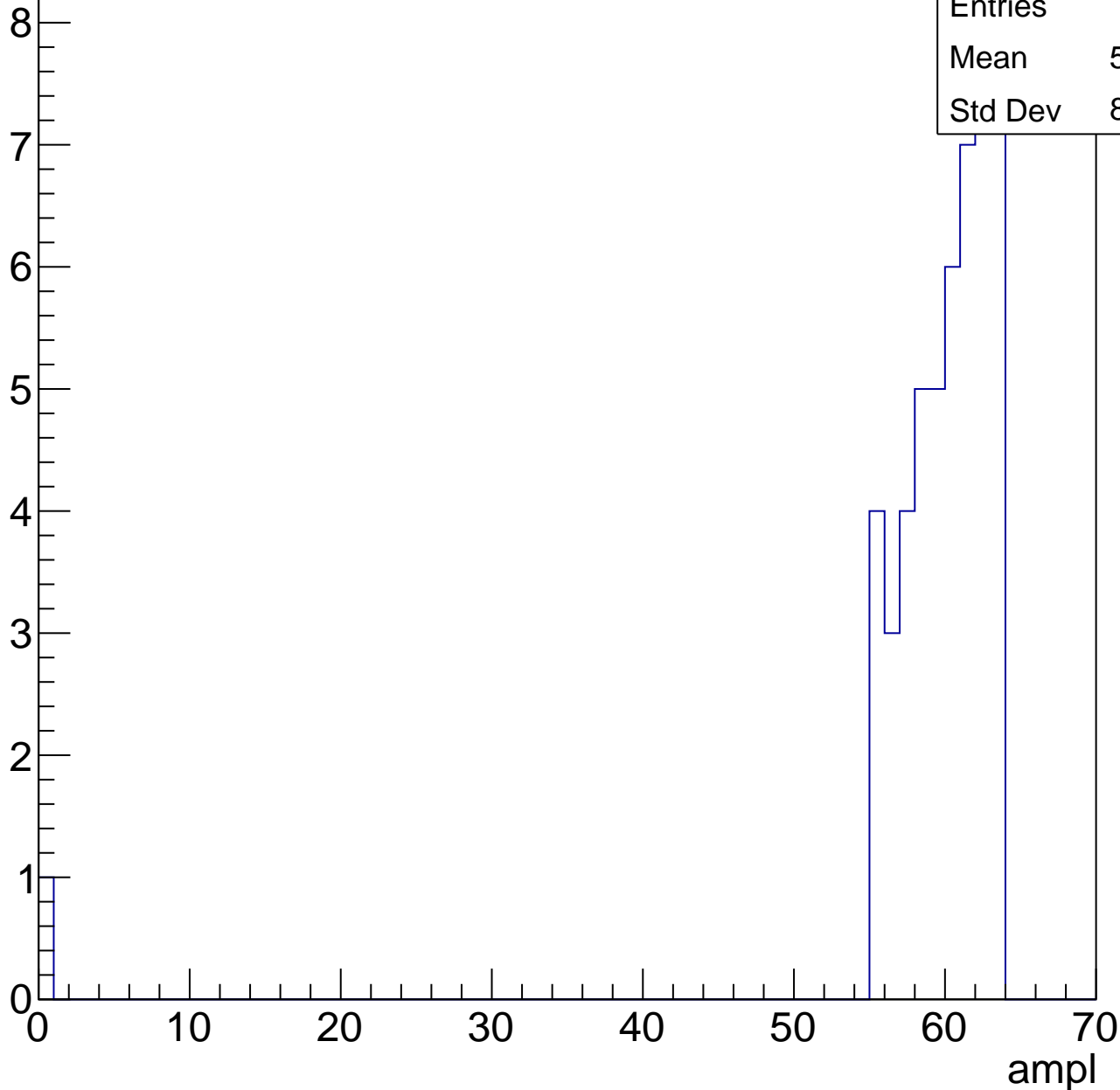


# B0L000S, U18-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

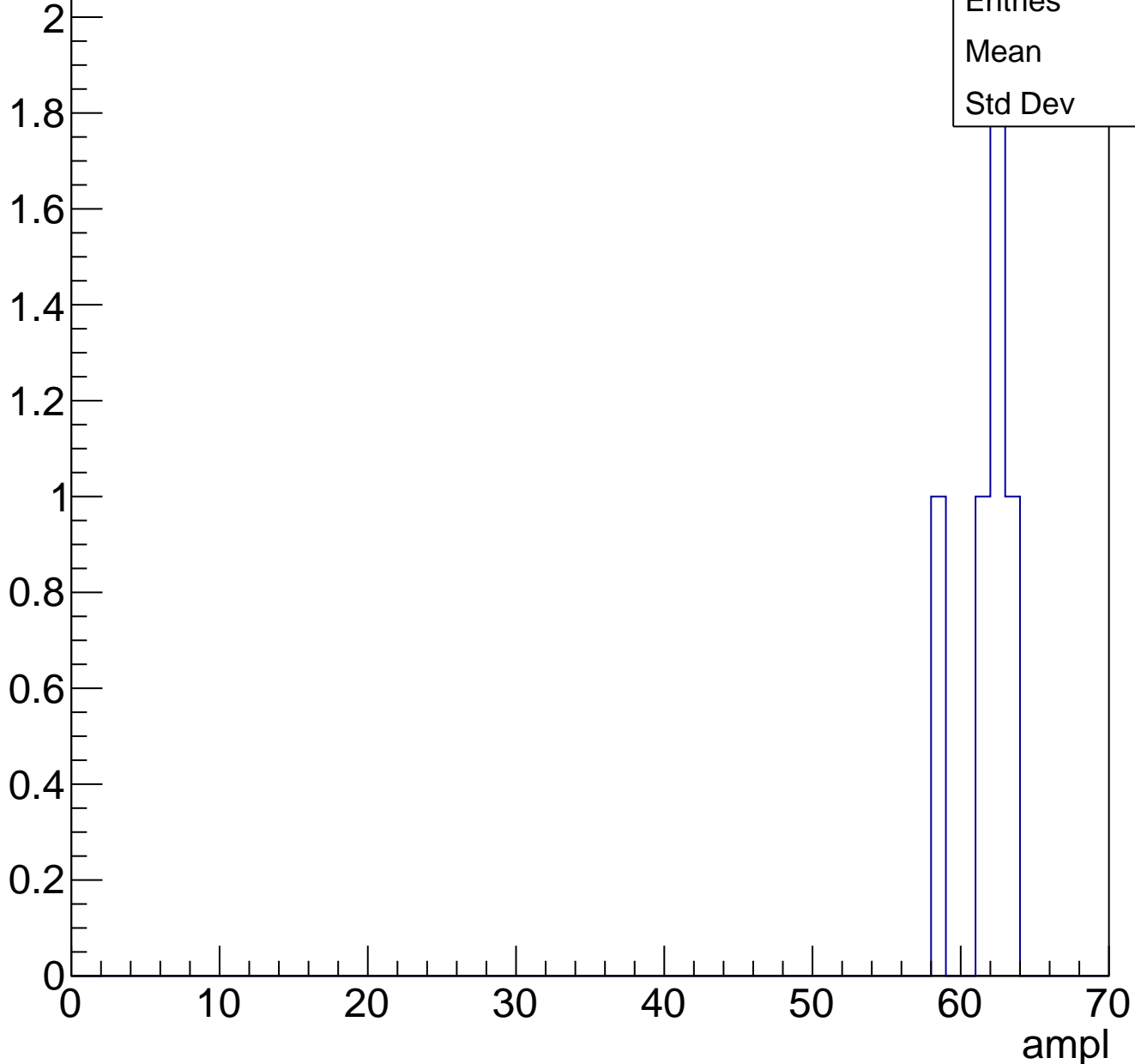
Entries	51
Mean	58.59
Std Dev	8.653



# B0L000S, U18-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch54, adc0

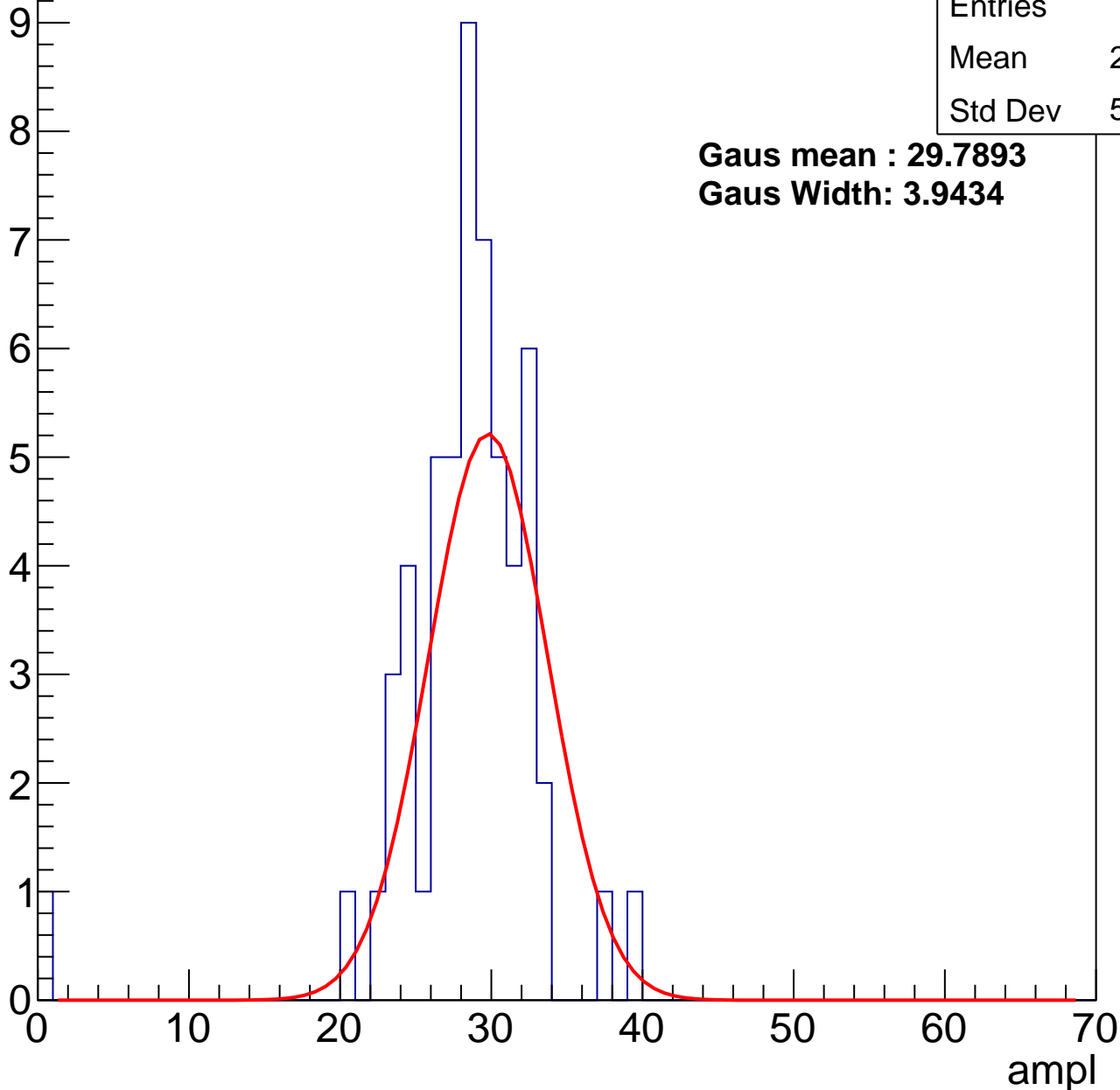
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	27.86
Std Dev	5.118

**Gaus mean : 29.7893**

**Gaus Width: 3.9434**



# B0L000S, U18-ch54, adc1

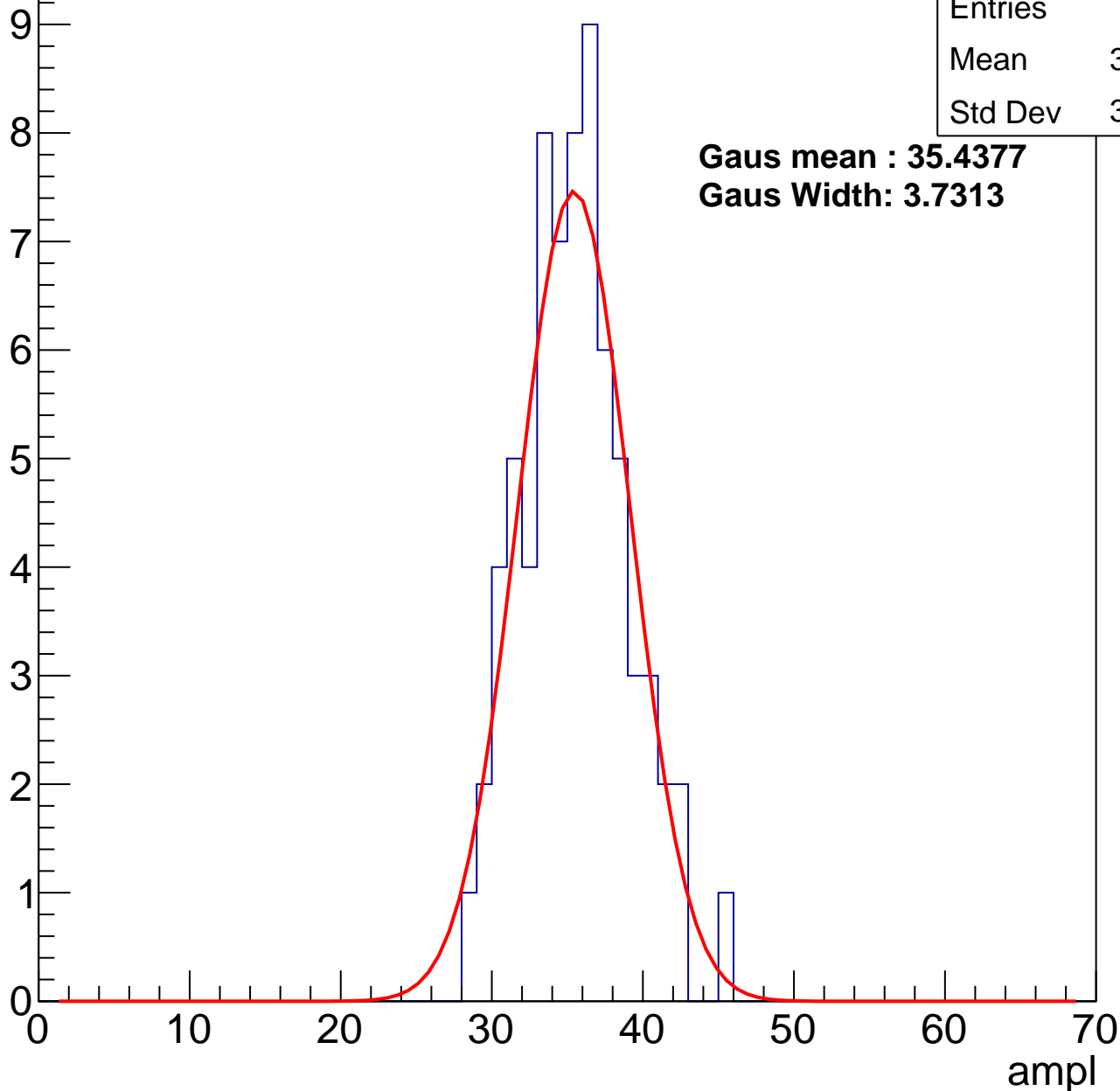
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.07
Std Dev	3.478

**Gaus mean : 35.4377**

**Gaus Width: 3.7313**



# B0L000S, U18-ch54, adc2

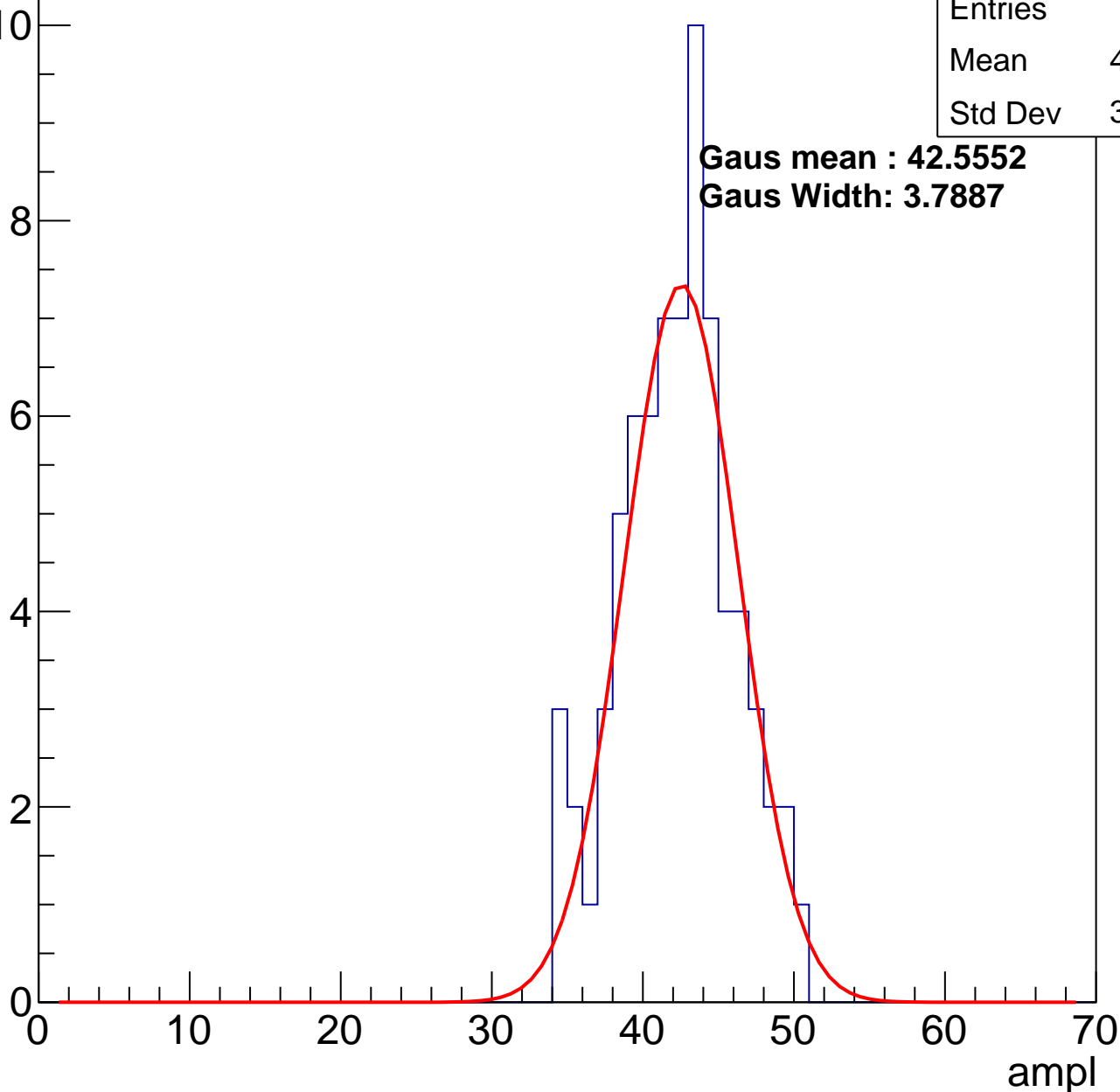
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	41.79
Std Dev	3.719

**Gaus mean : 42.5552**

**Gaus Width: 3.7887**

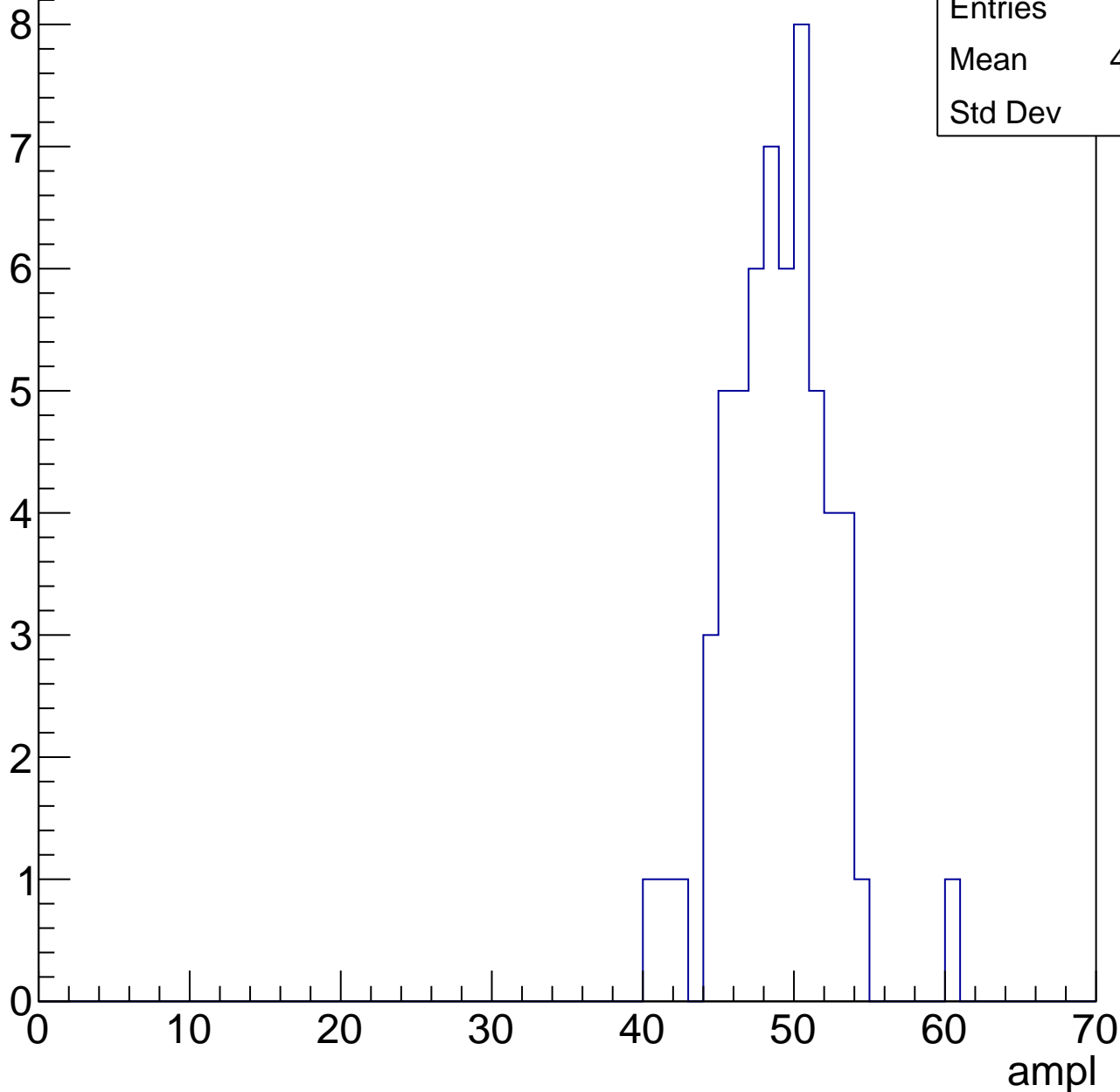


# B0L000S, U18-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	48.47
Std Dev	3.43

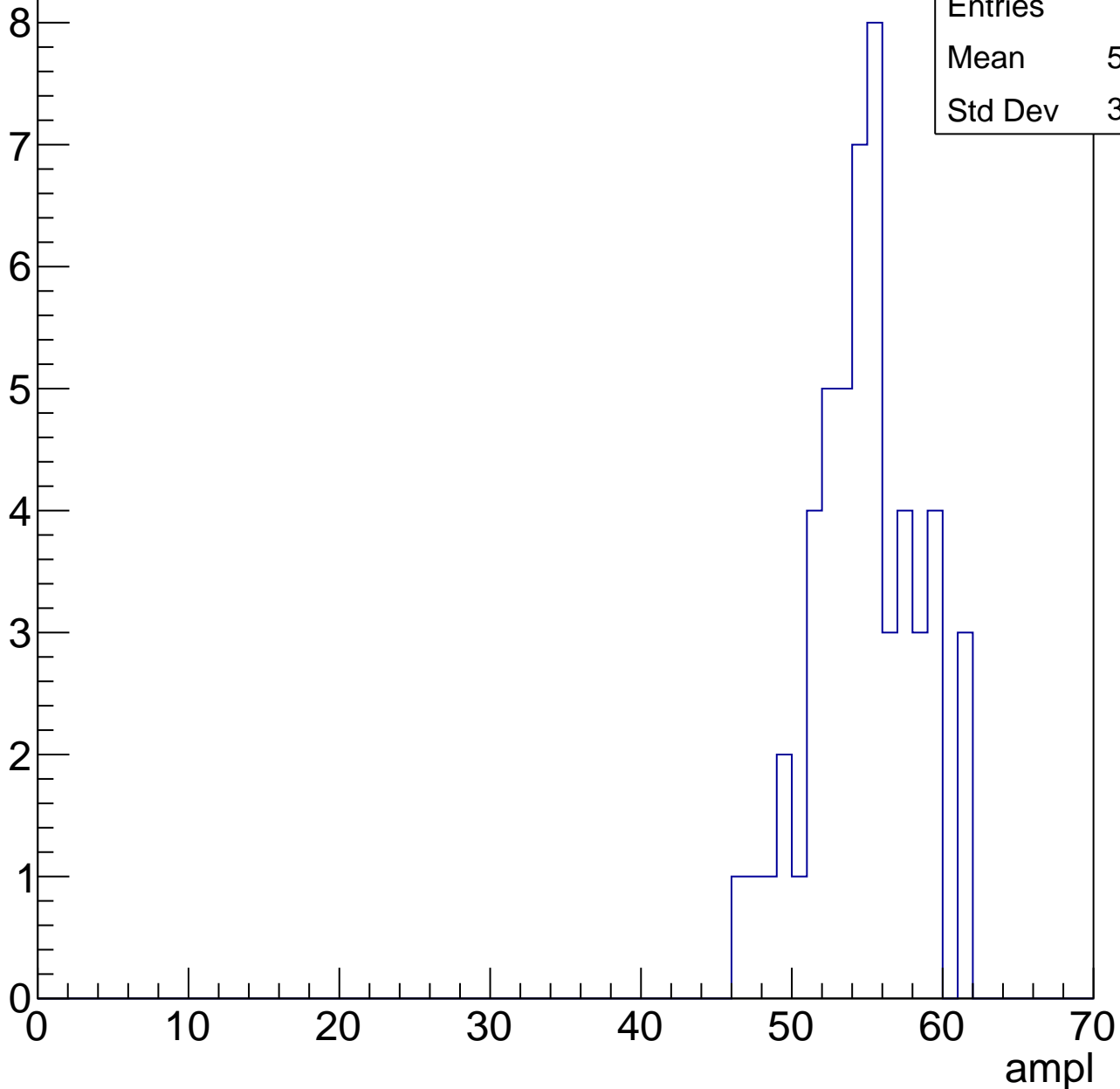


# B0L000S, U18-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

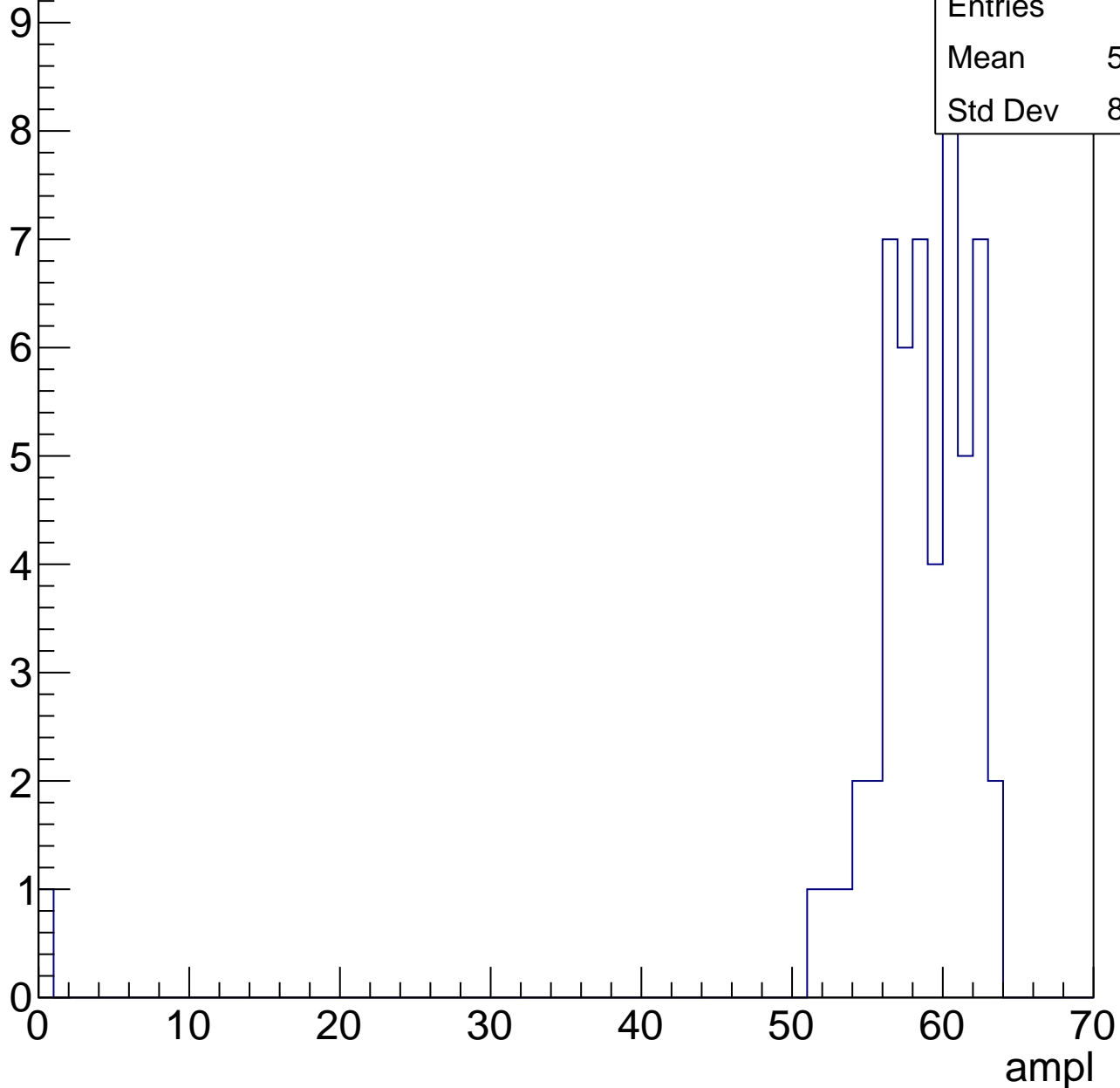
Entries	52
Mean	54.33
Std Dev	3.457



# B0L000S, U18-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

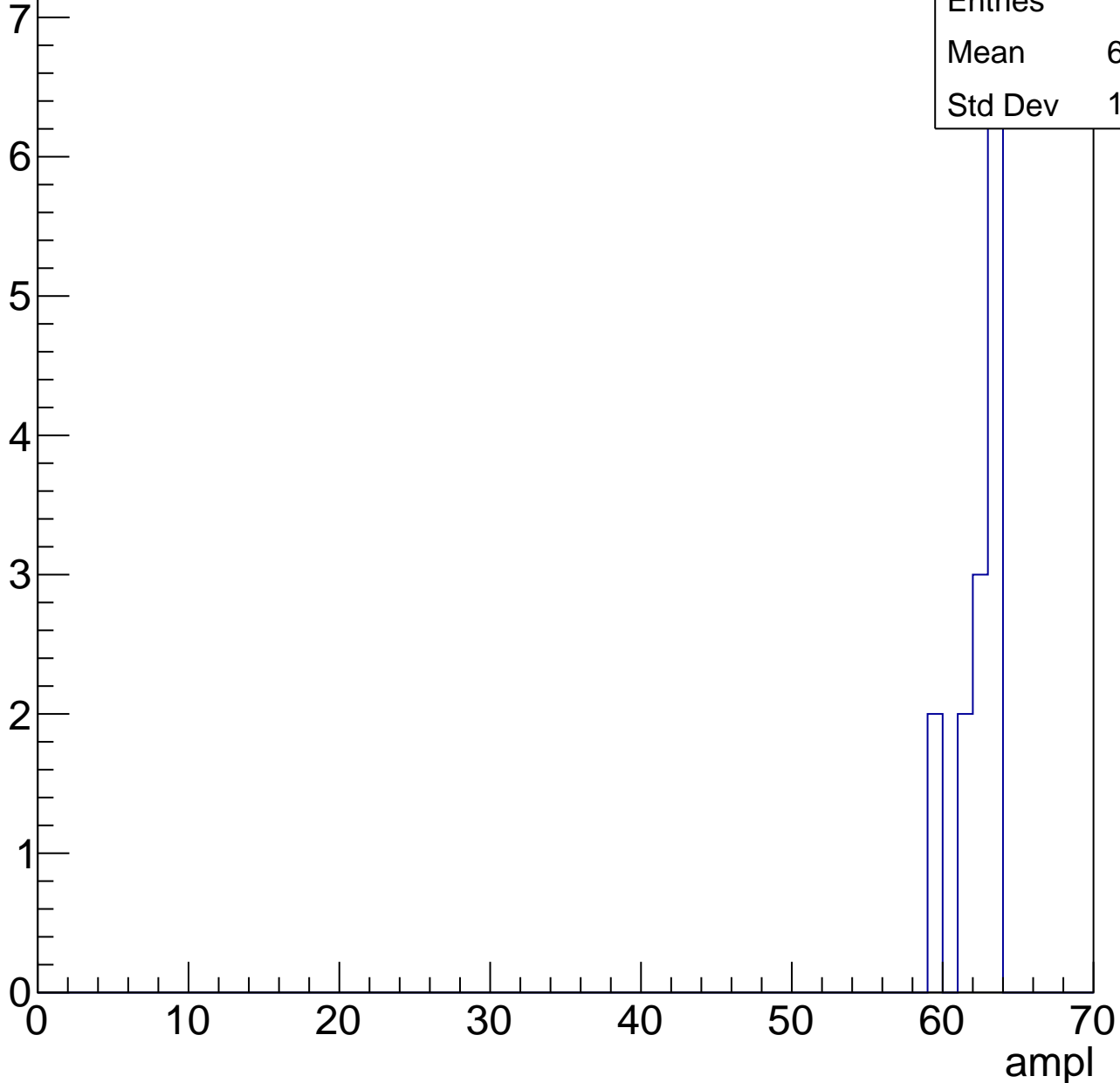


# B0L000S, U18-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.93
Std Dev	1.387





# B0L000S, U18-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch55, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	29.37
Std Dev	3.686

**Gaus mean : 30.0239**

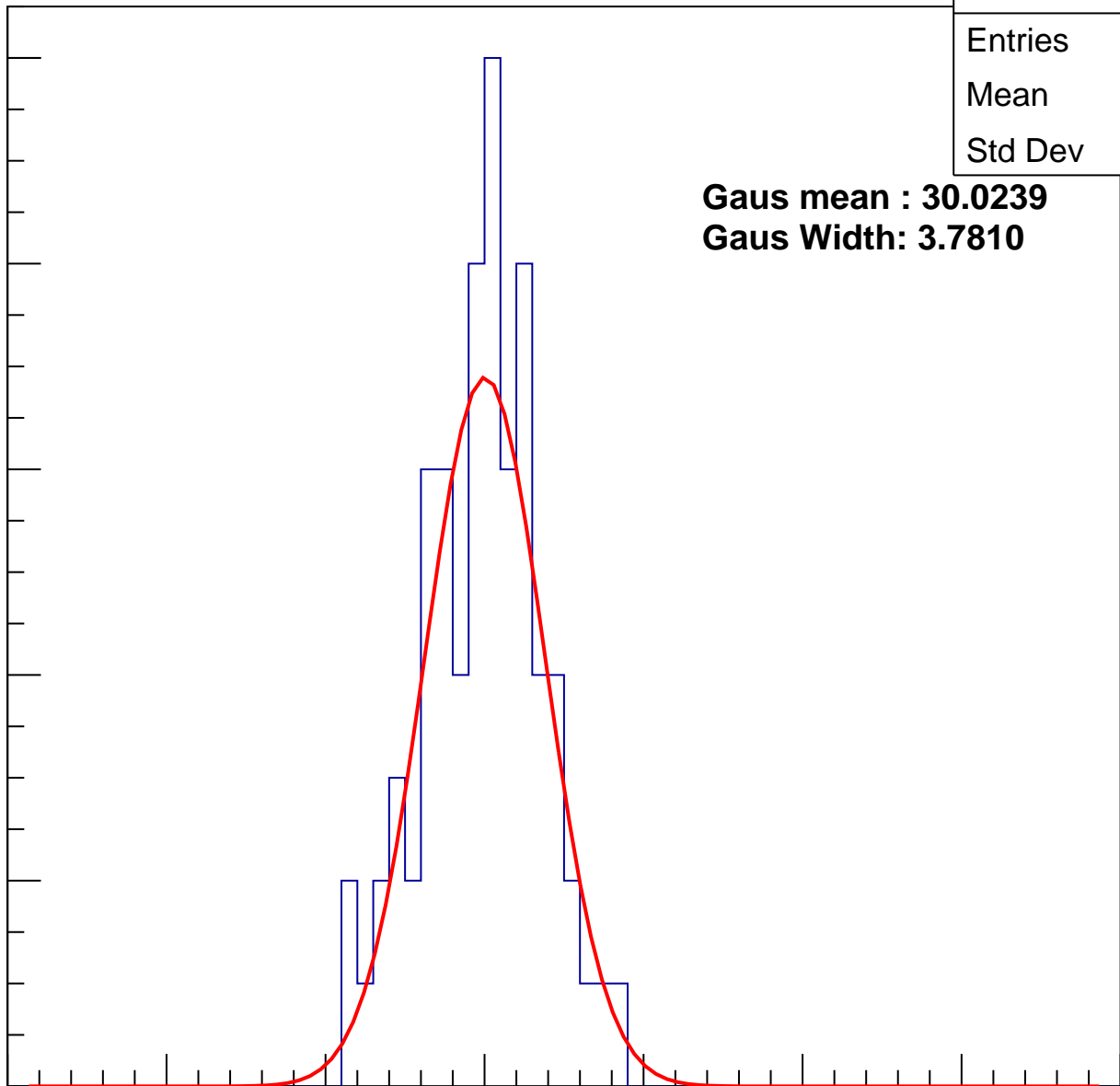
**Gaus Width: 3.7810**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch55, adc1

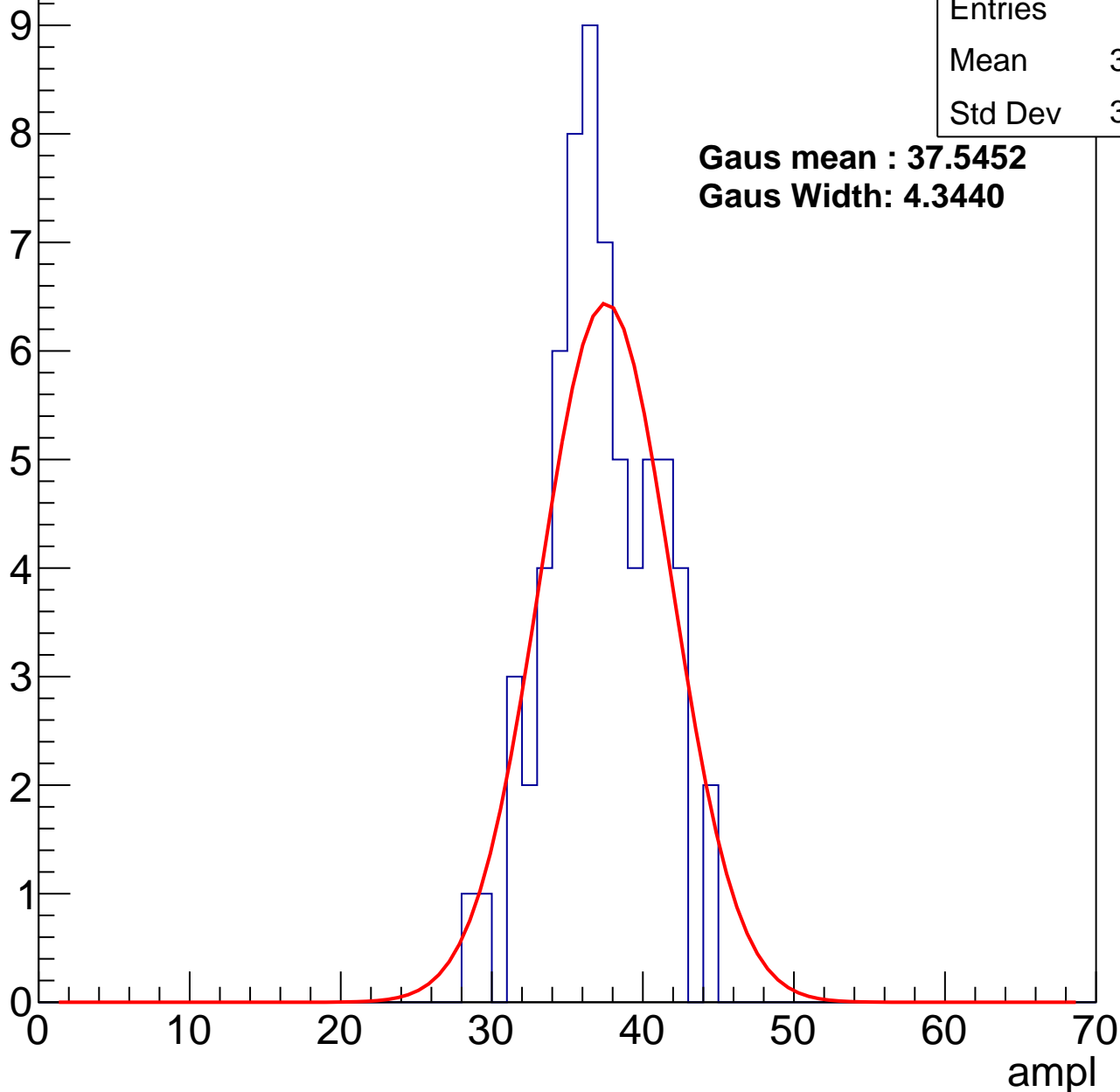
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	36.67
Std Dev	3.479

**Gaus mean : 37.5452**

**Gaus Width: 4.3440**



# B0L000S, U18-ch55, adc2

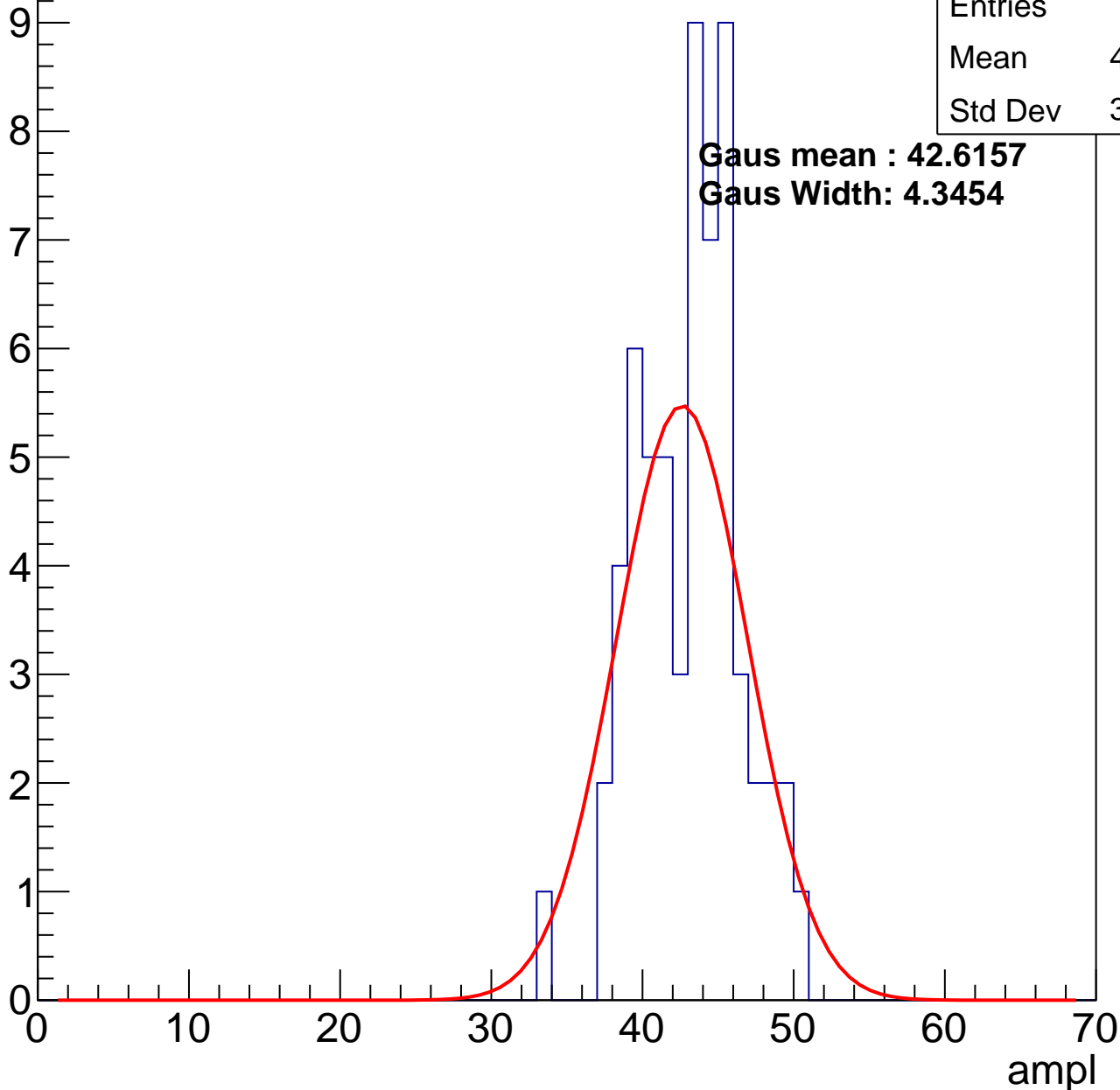
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.62
Std Dev	3.388

**Gaus mean : 42.6157**

**Gaus Width: 4.3454**

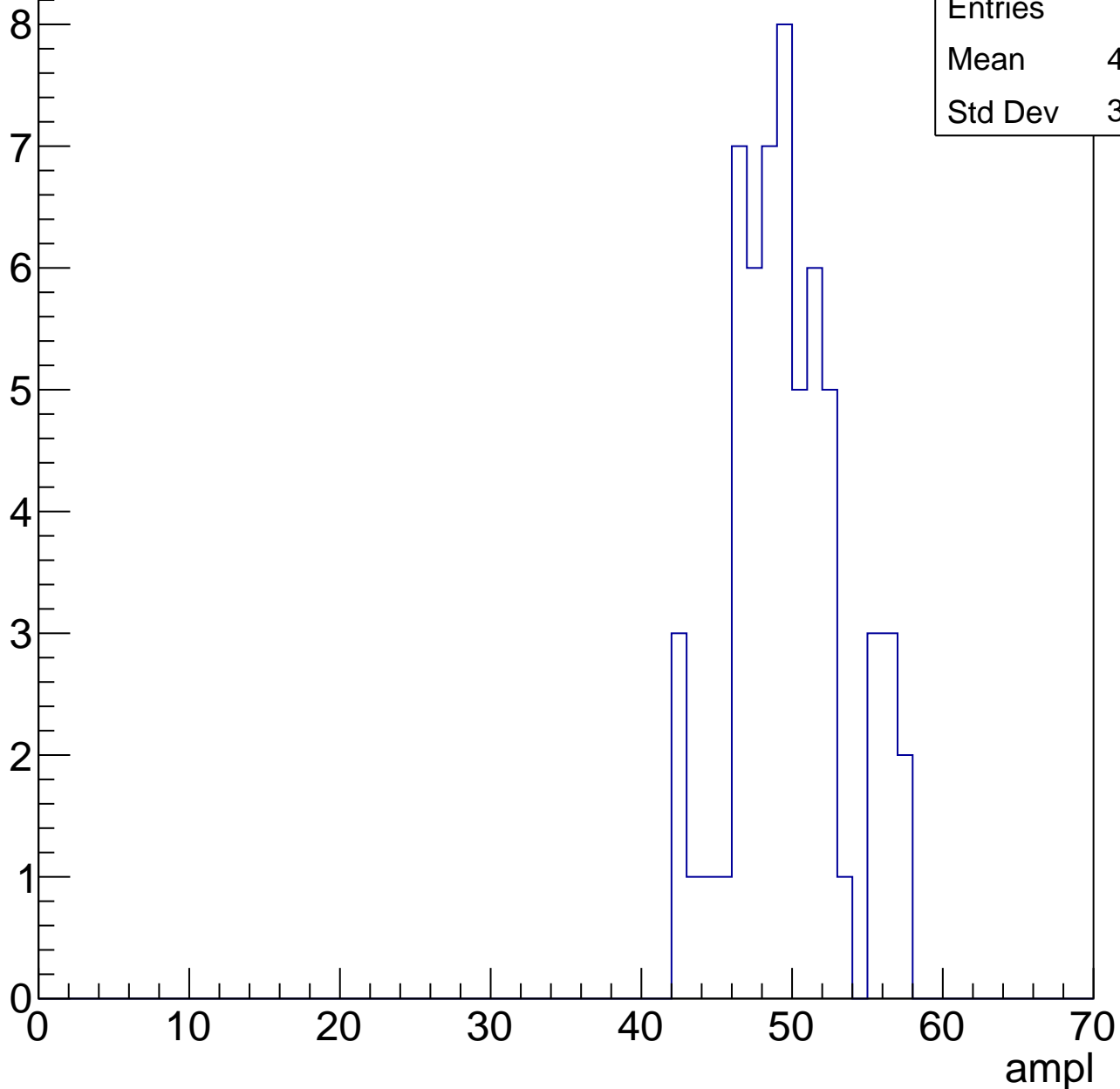


# B0L000S, U18-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.25
Std Dev	3.653

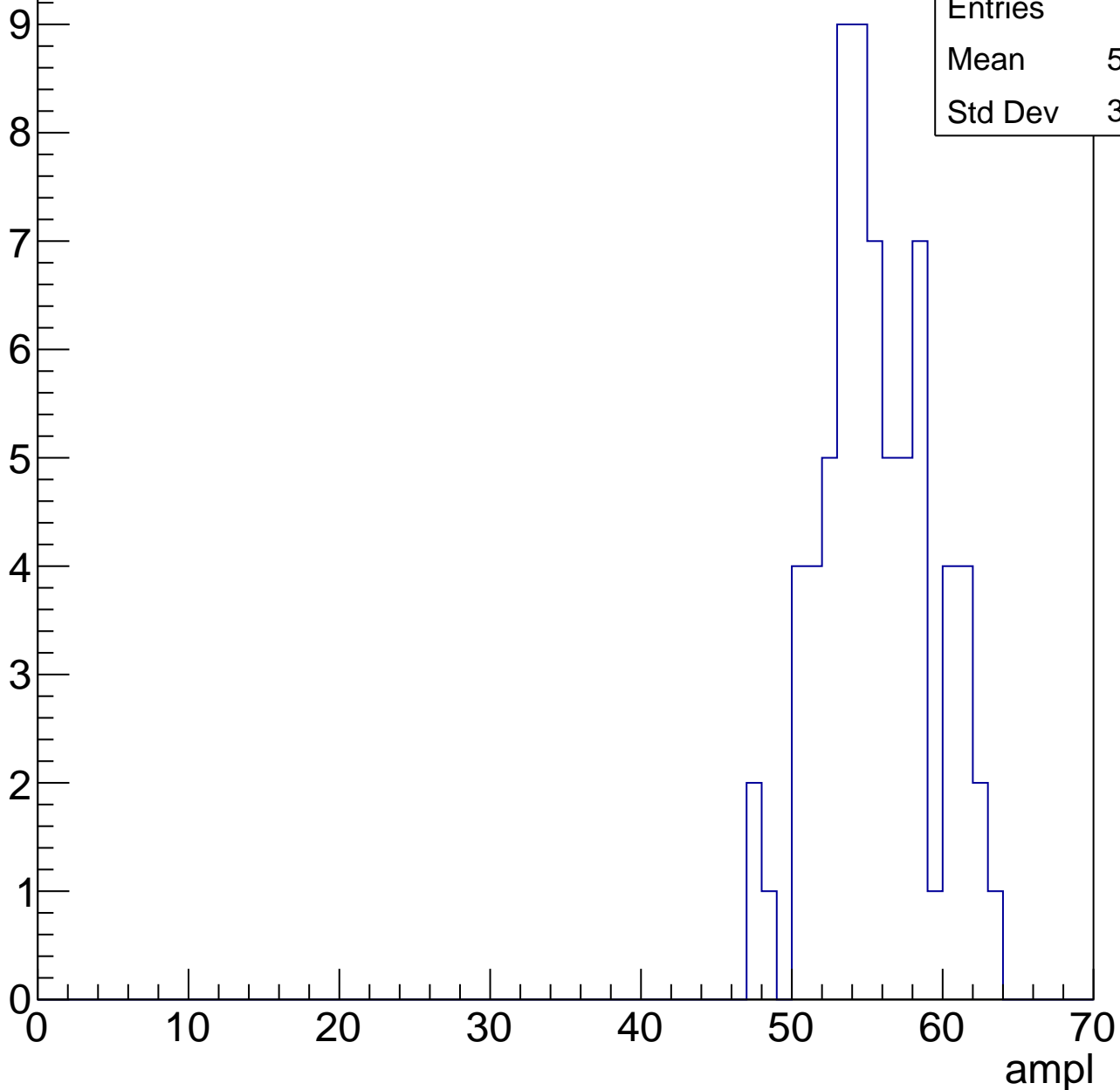


# B0L000S, U18-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	55.07
Std Dev	3.666



# B0L000S, U18-ch55, adc5

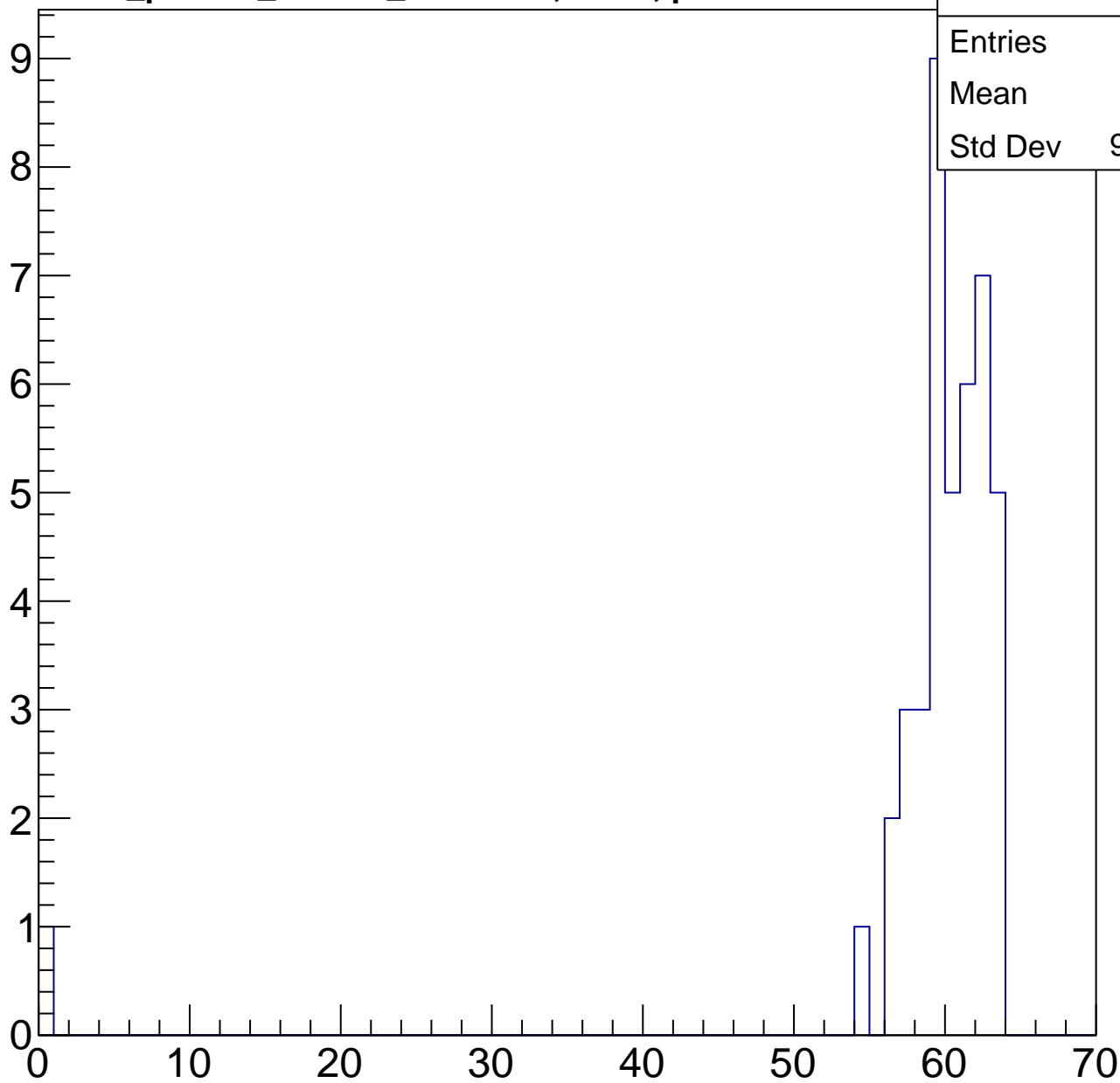
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	58.5
Std Dev	9.387

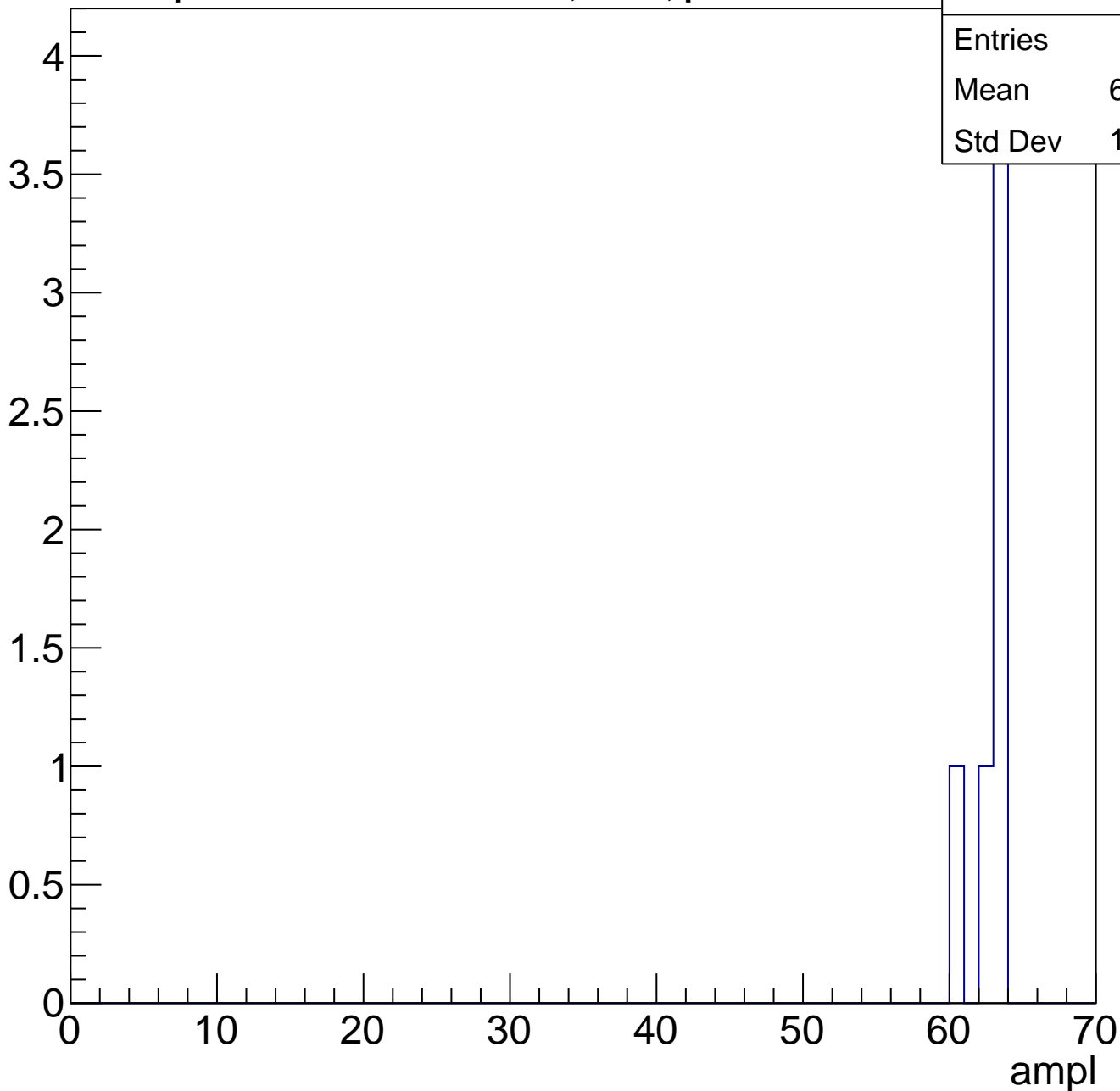
ampl



# B0L000S, U18-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch56, adc0

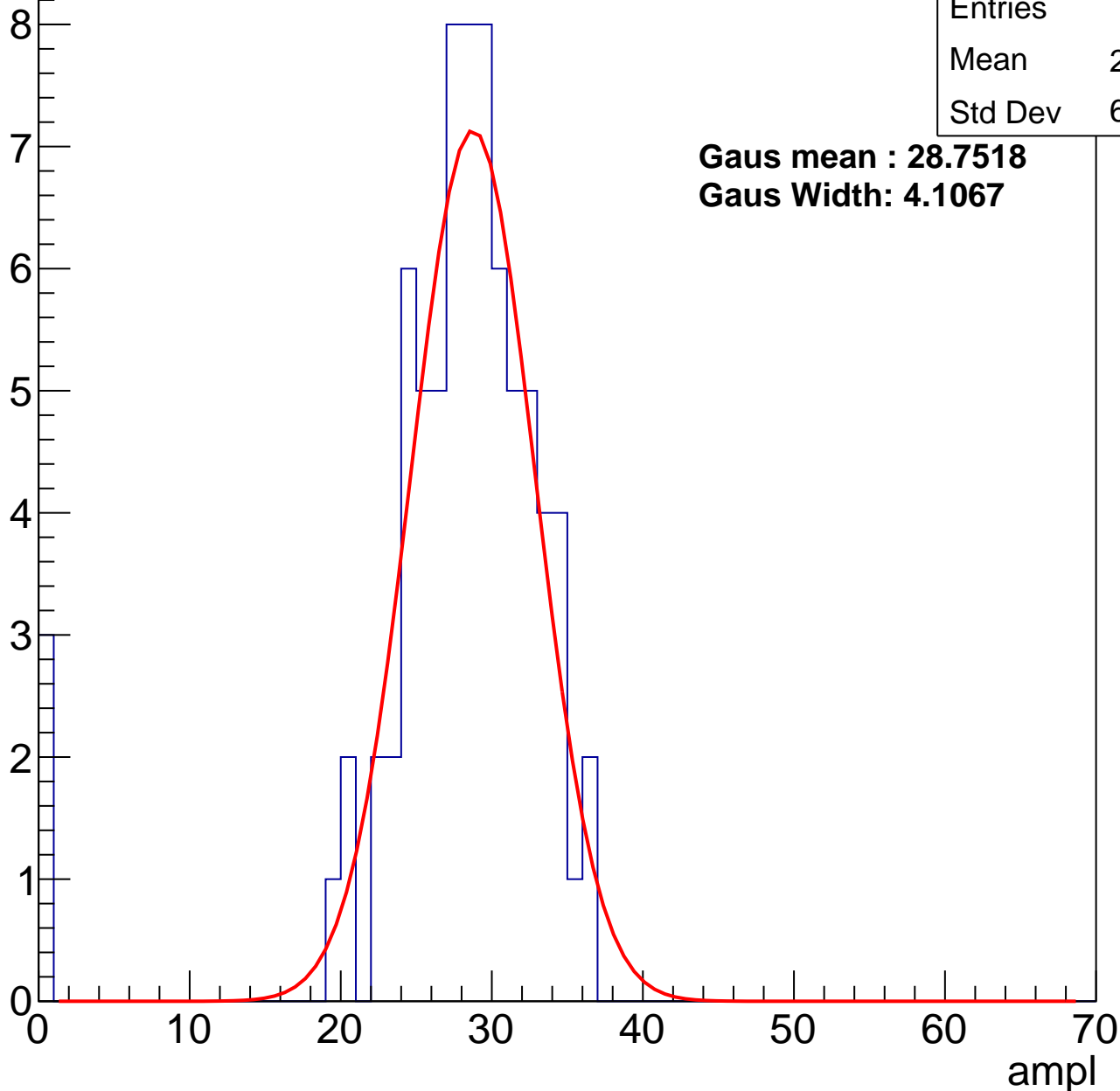
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	27.14
Std Dev	6.619

**Gaus mean : 28.7518**

**Gaus Width: 4.1067**



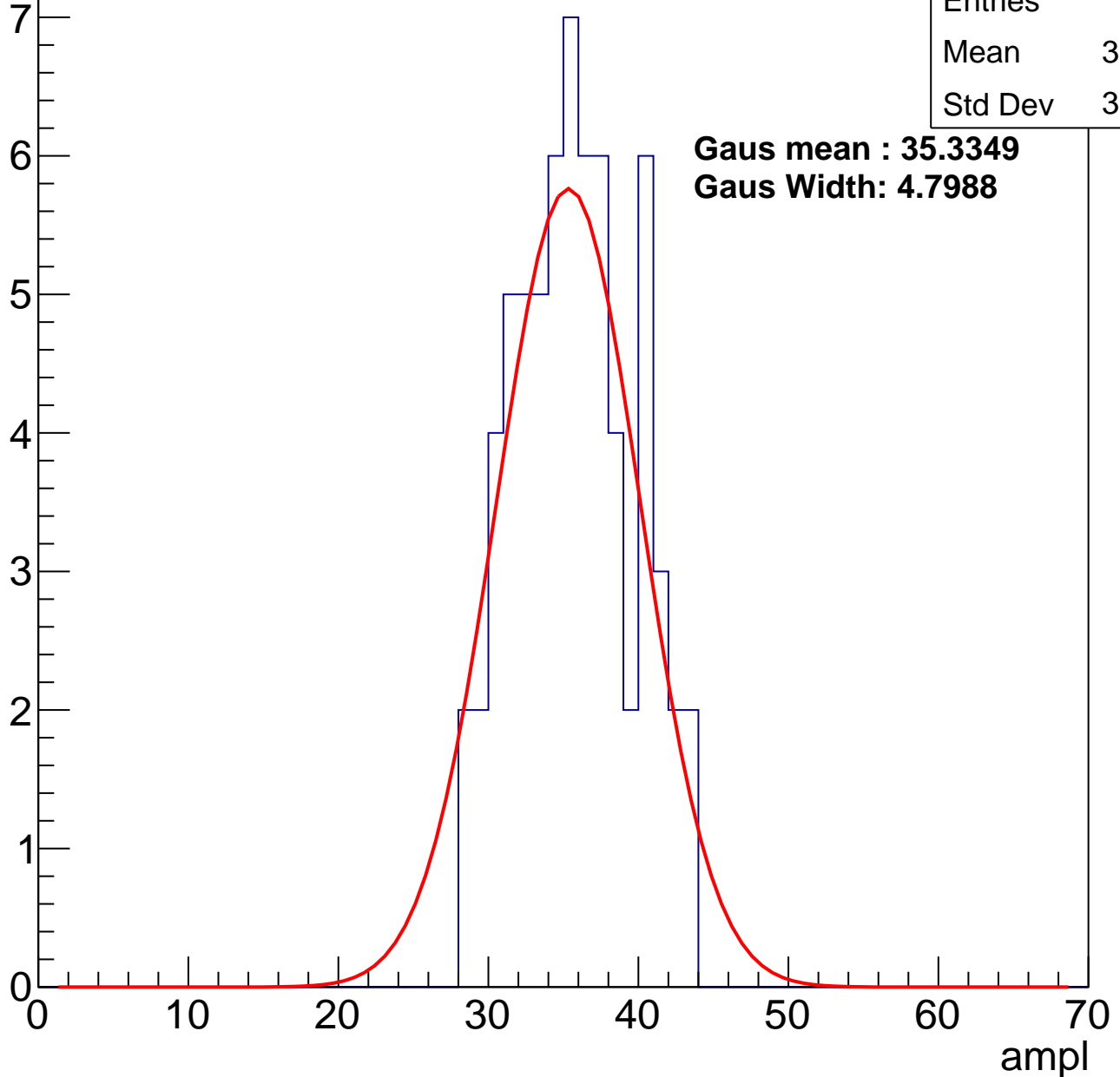
# B0L000S, U18-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.28
Std Dev	3.859

**Gaus mean : 35.3349**  
**Gaus Width: 4.7988**



# B0L000S, U18-ch56, adc2

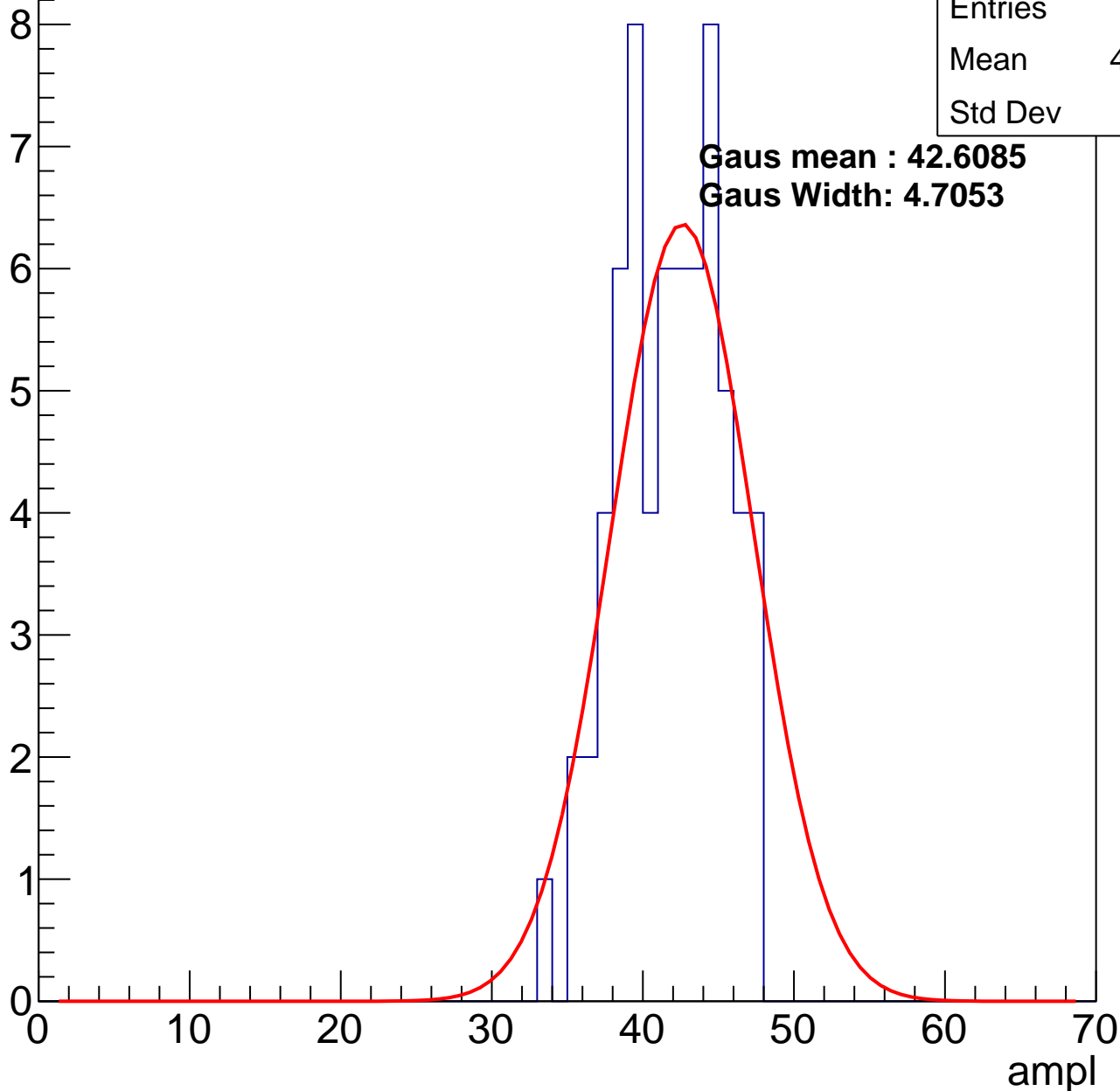
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	41.33
Std Dev	3.39

**Gaus mean : 42.6085**

**Gaus Width: 4.7053**

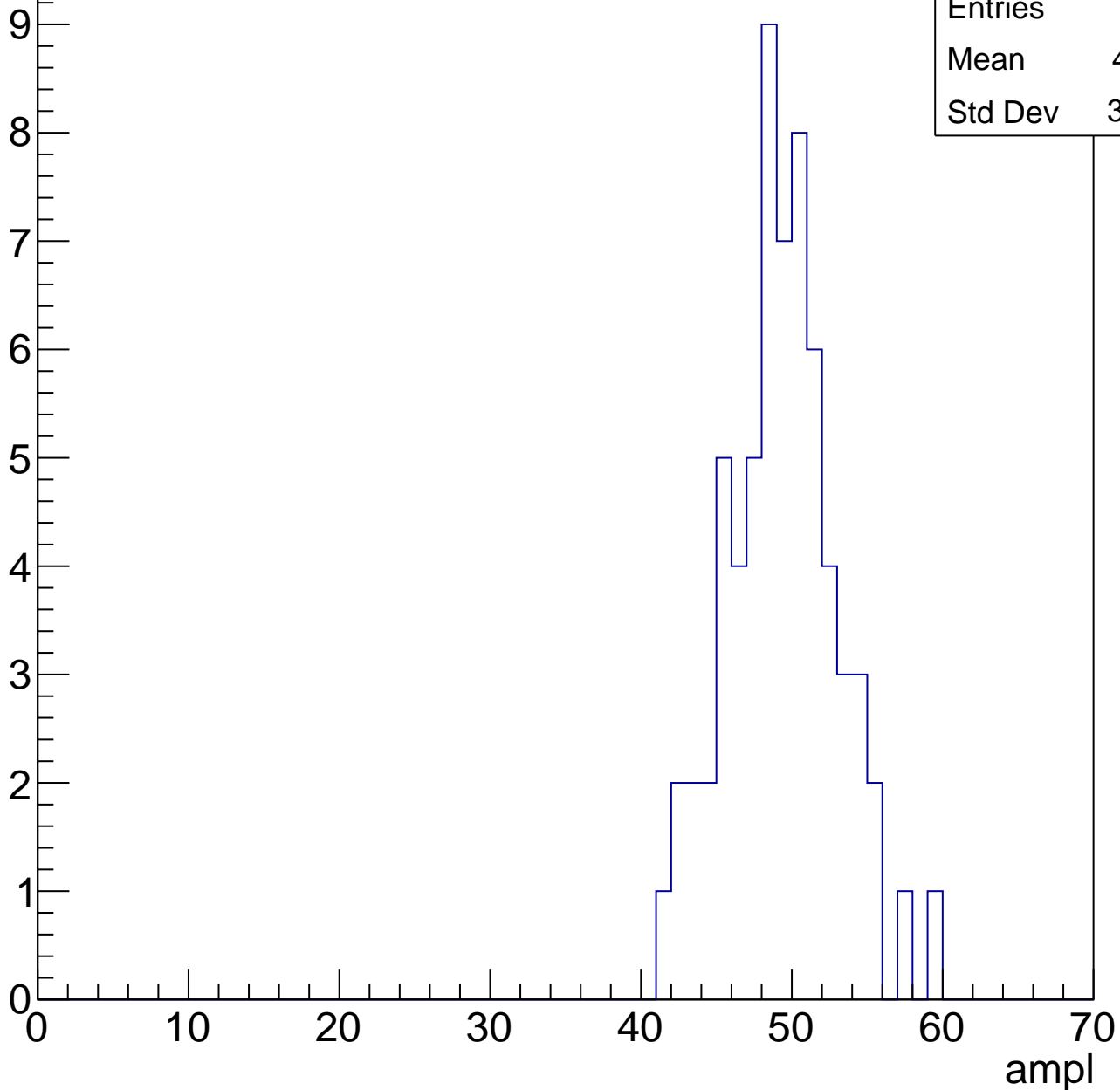


# B0L000S, U18-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	48.91
Std Dev	3.628

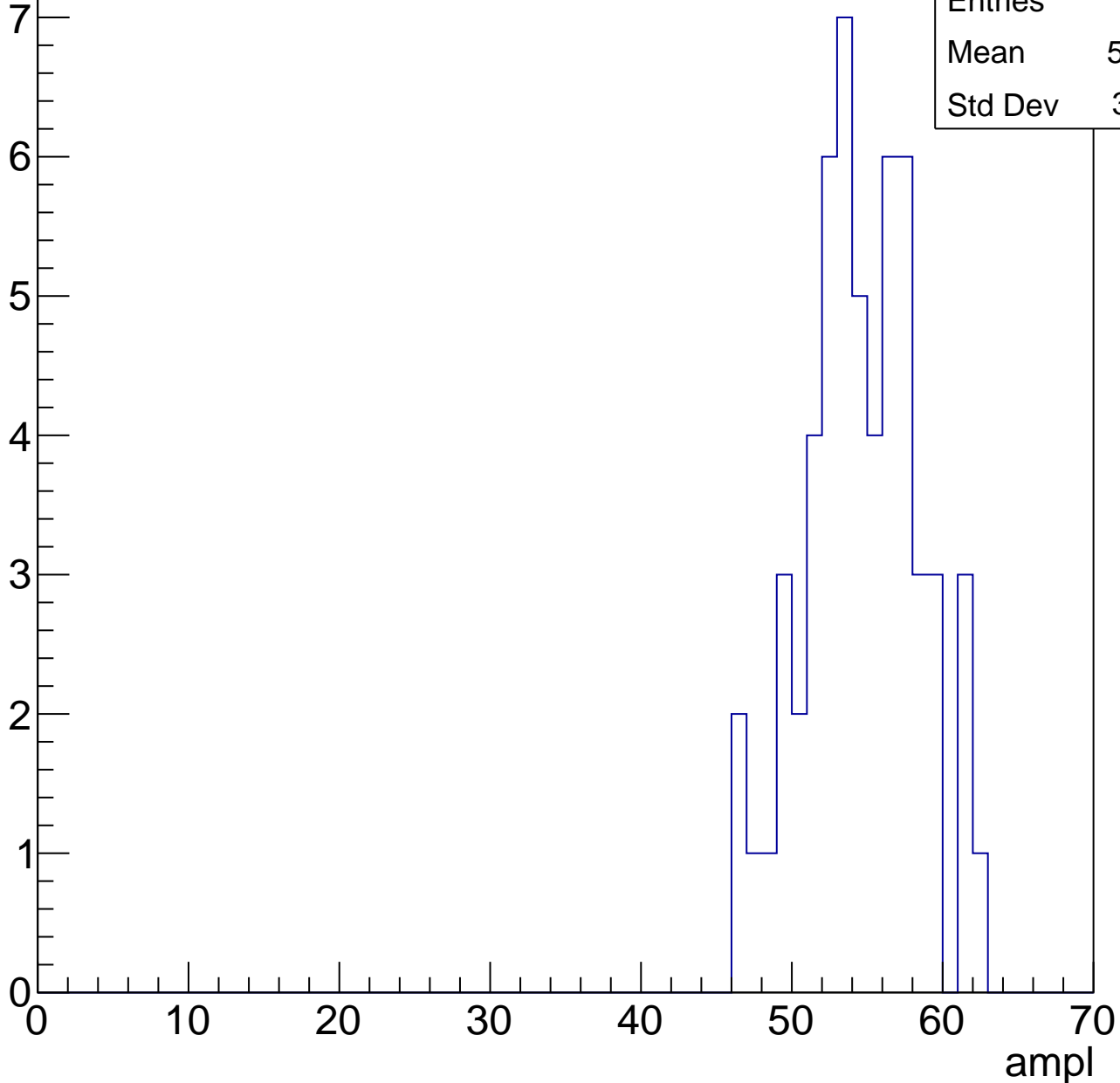


# B0L000S, U18-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

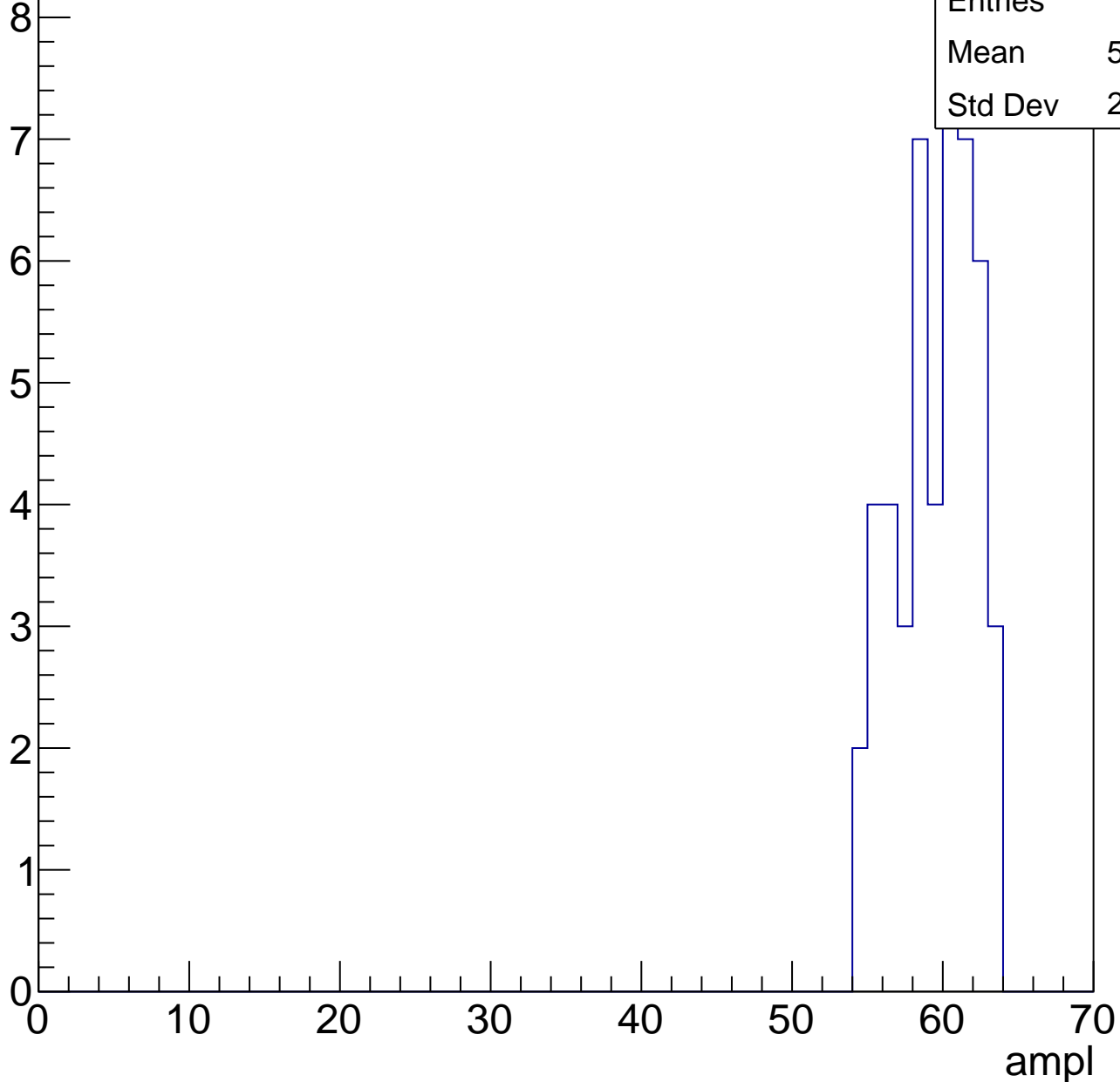
Entries	57
Mean	54.12
Std Dev	3.751



# B0L000S, U18-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



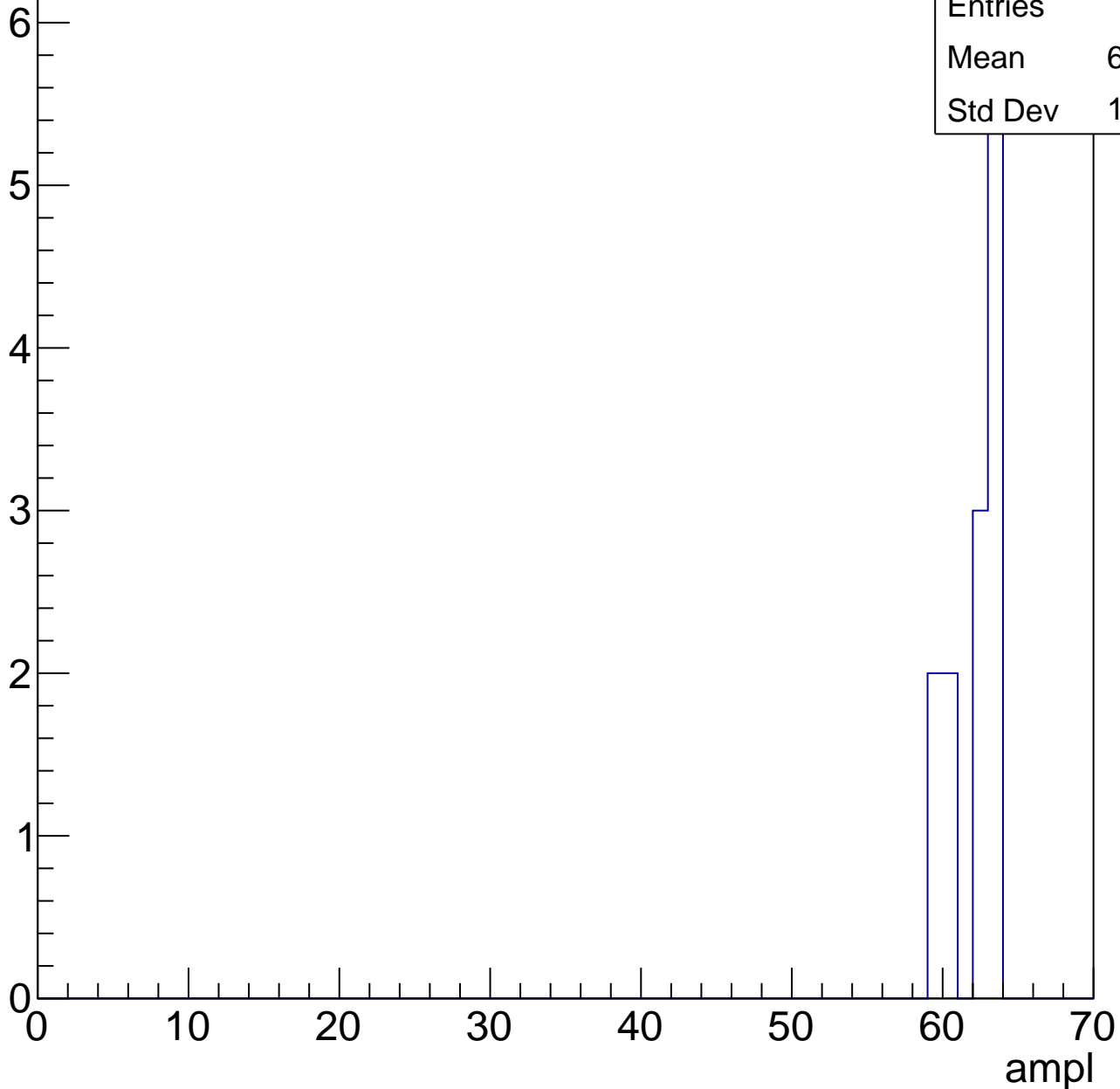
Entries	48
Mean	59.02
Std Dev	2.529

# B0L000S, U18-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.69
Std Dev	1.538

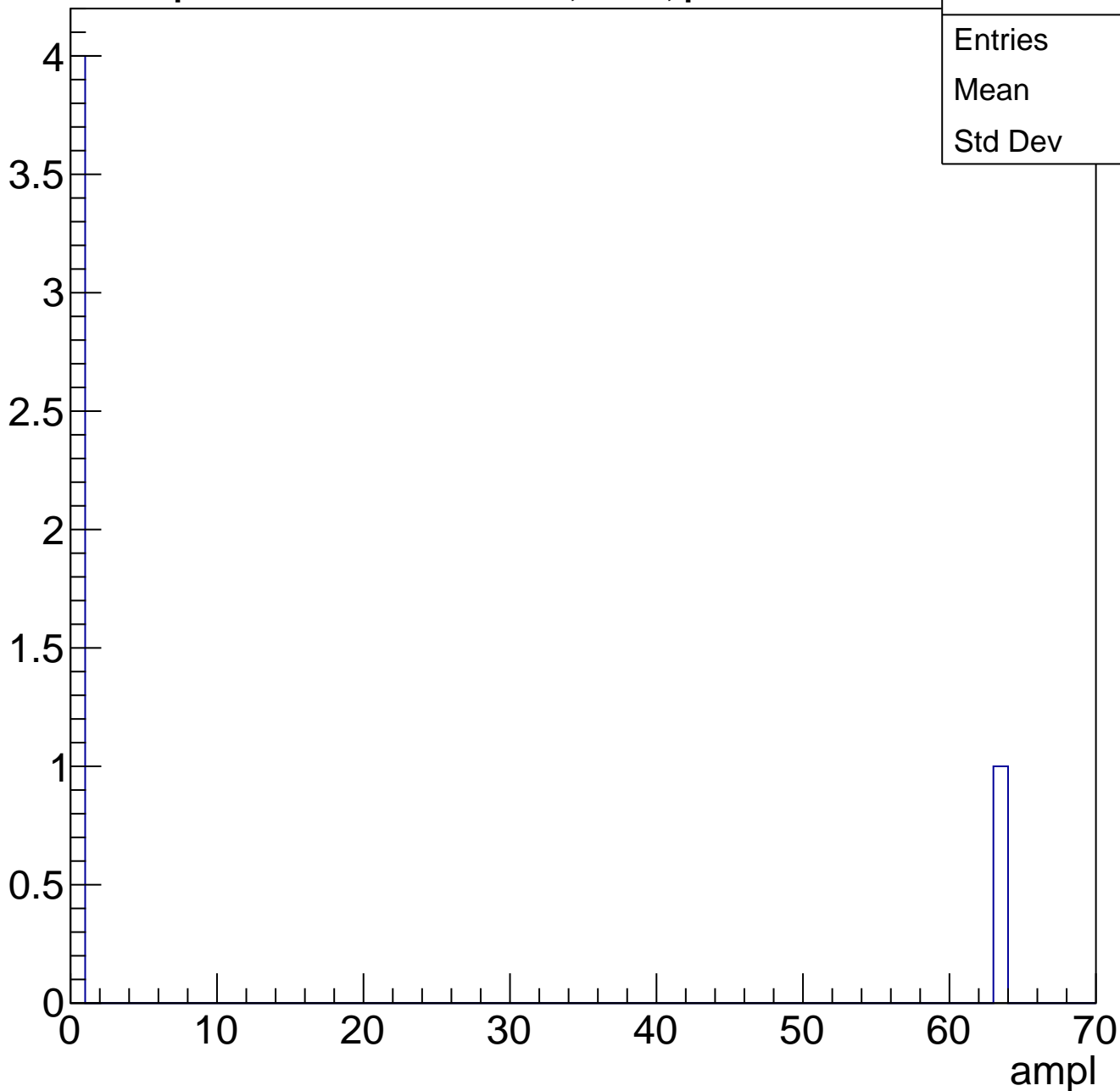




# B0L000S, U18-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch57, adc0

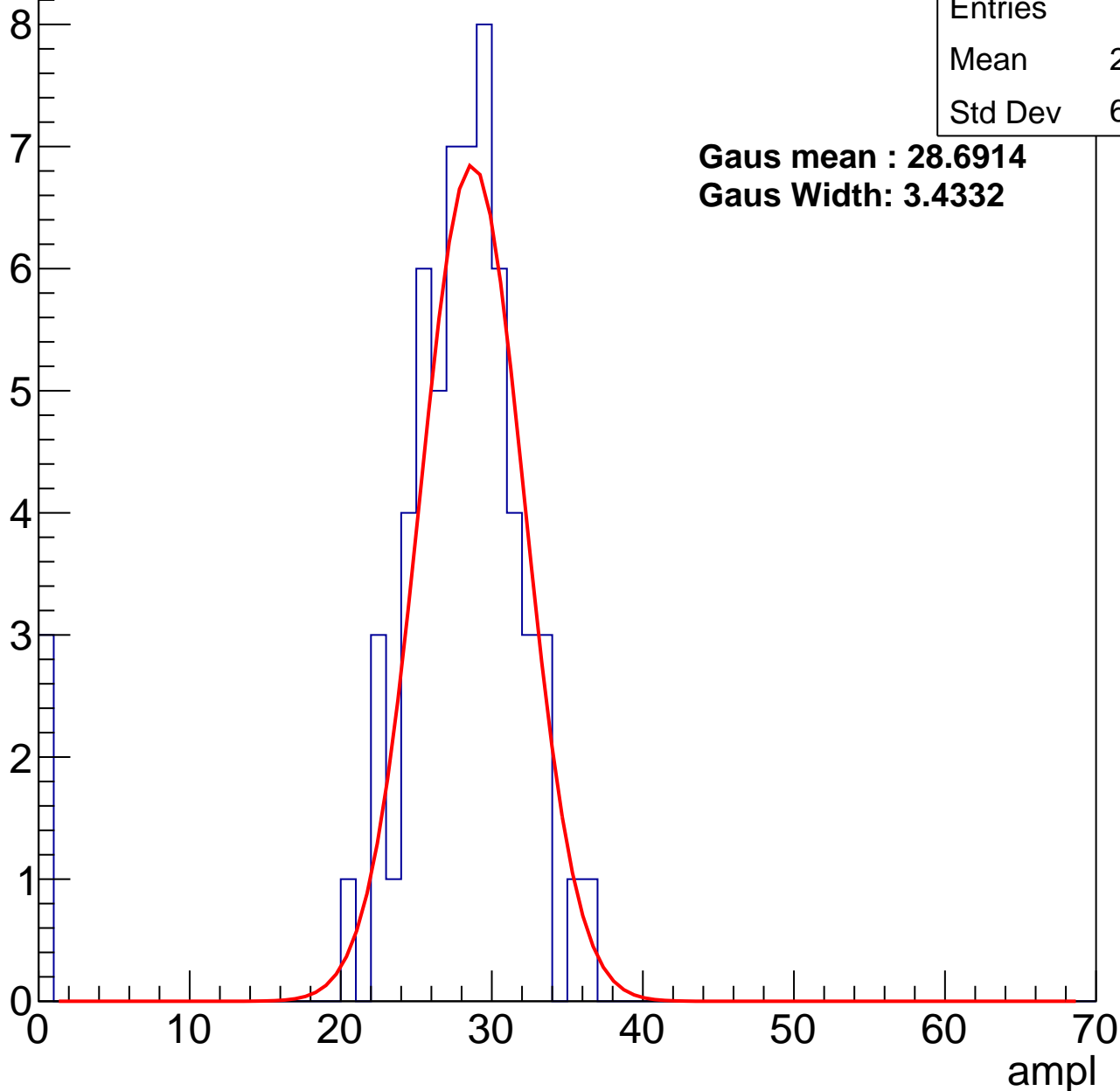
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	26.54
Std Dev	6.742

**Gaus mean : 28.6914**

**Gaus Width: 3.4332**



# B0L000S, U18-ch57, adc1

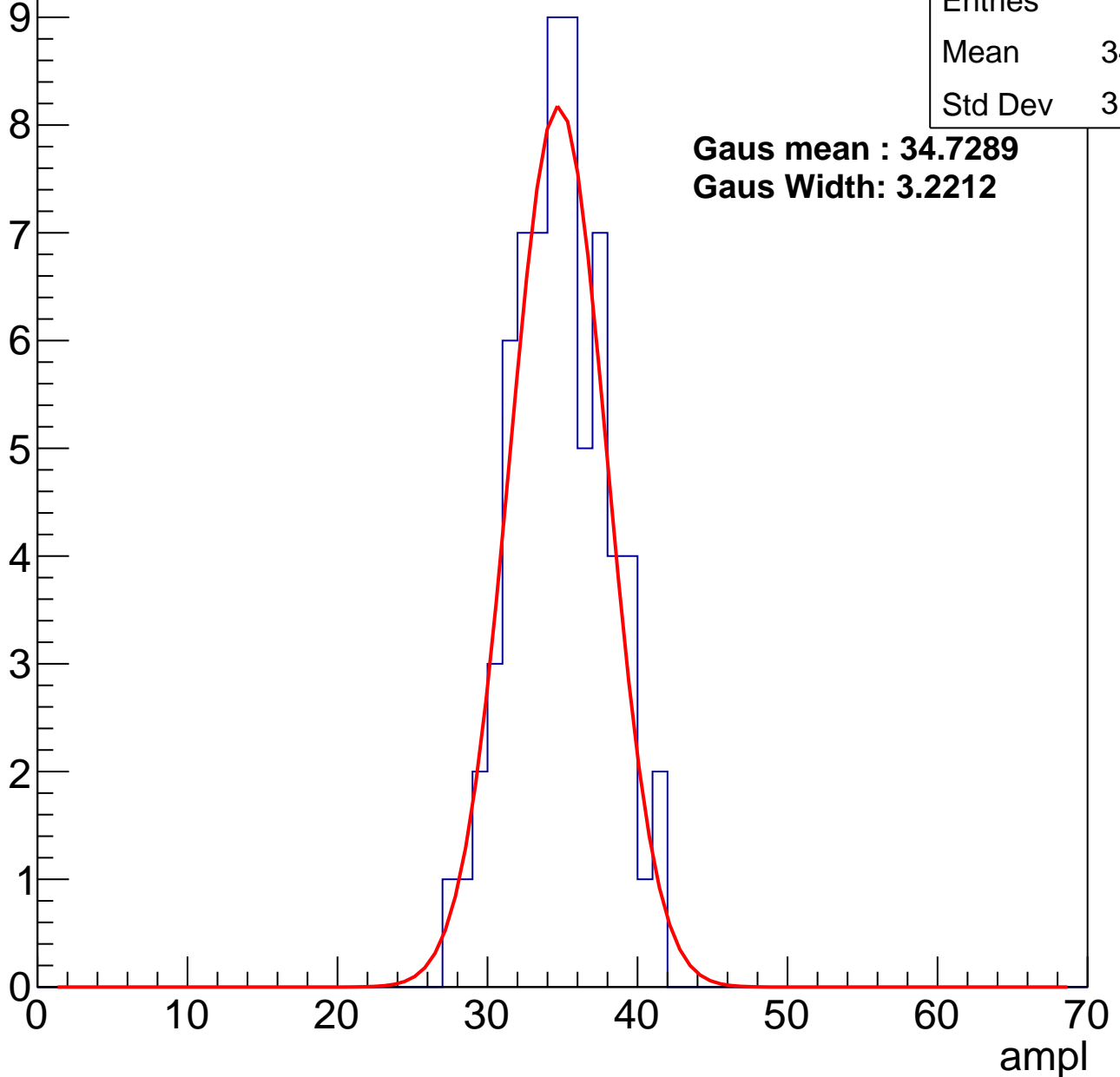
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	34.32
Std Dev	3.103

**Gaus mean : 34.7289**

**Gaus Width: 3.2212**



# B0L000S, U18-ch57, adc2

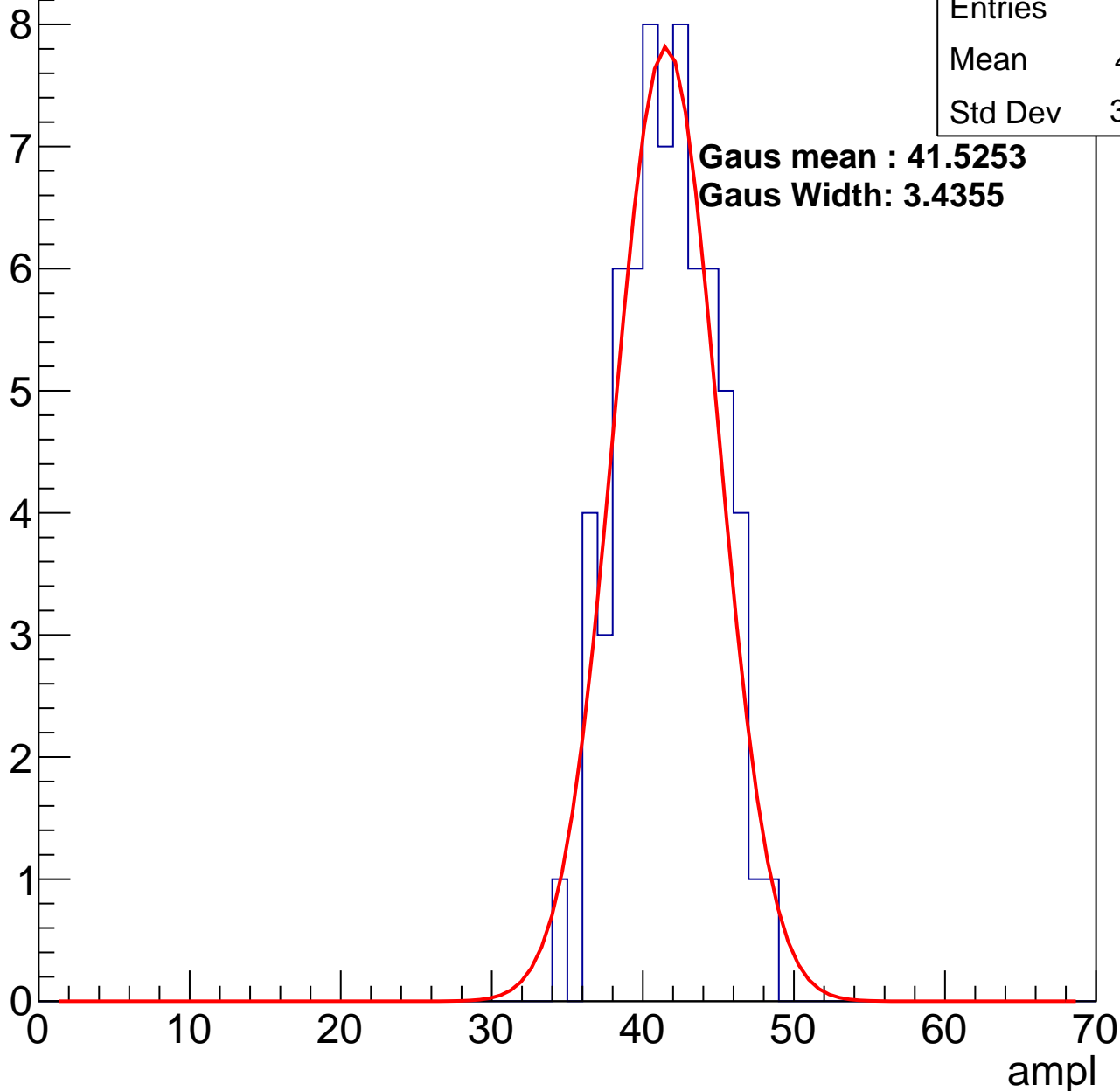
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	41.21
Std Dev	3.092

**Gaus mean : 41.5253**

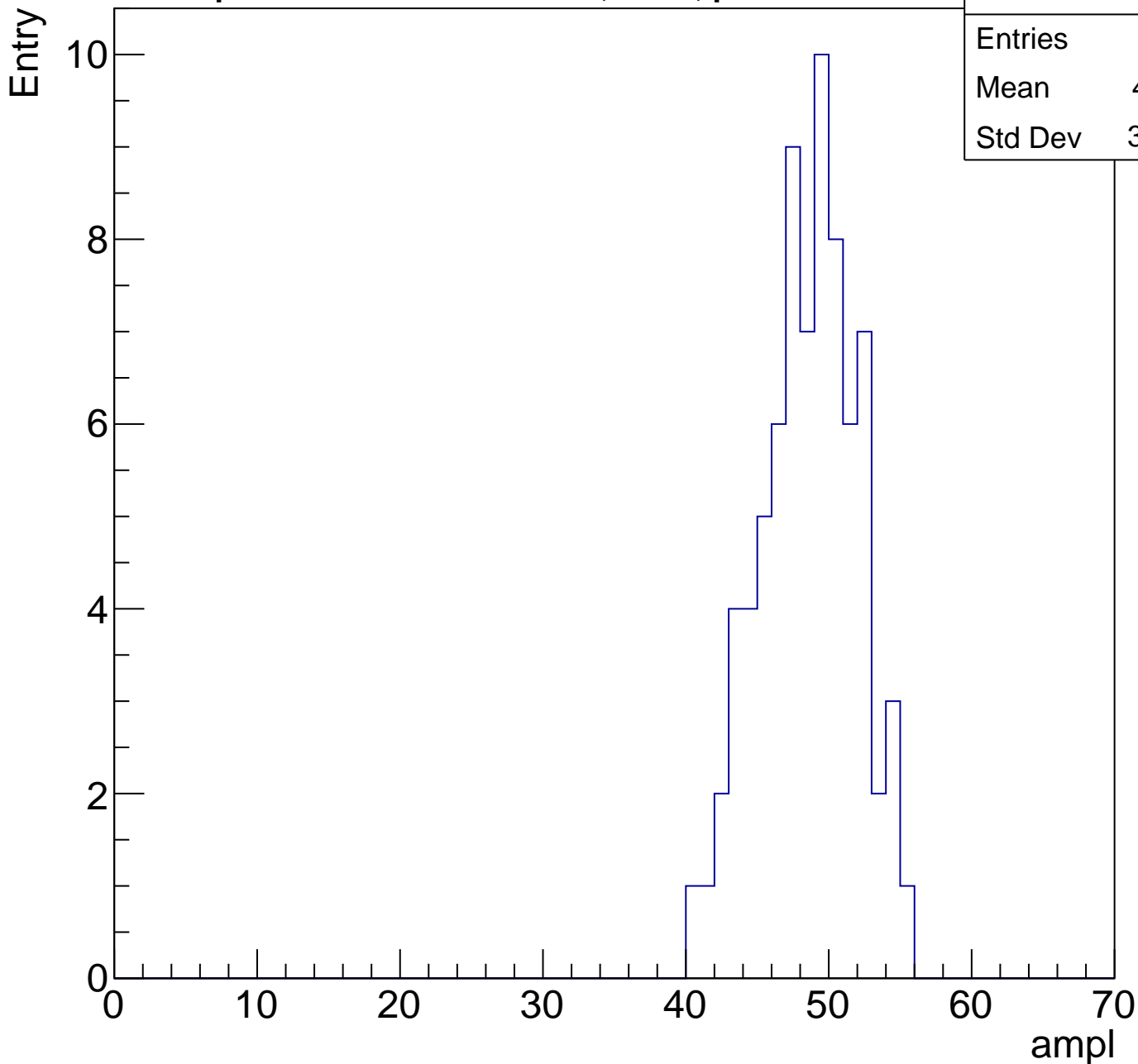
**Gaus Width: 3.4355**



# B0L000S, U18-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	48.11
Std Dev	3.327

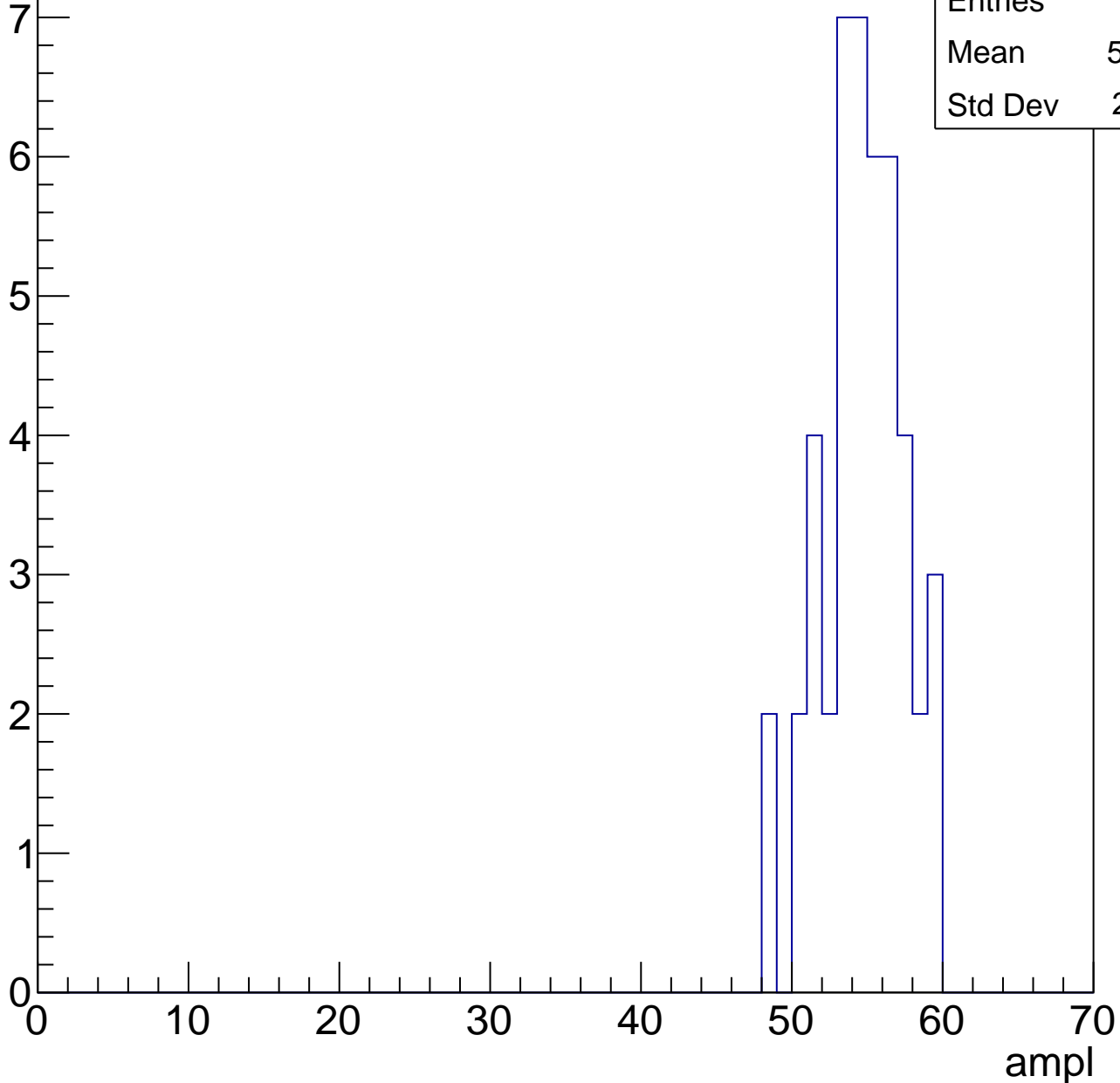


# B0L000S, U18-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	54.22
Std Dev	2.691

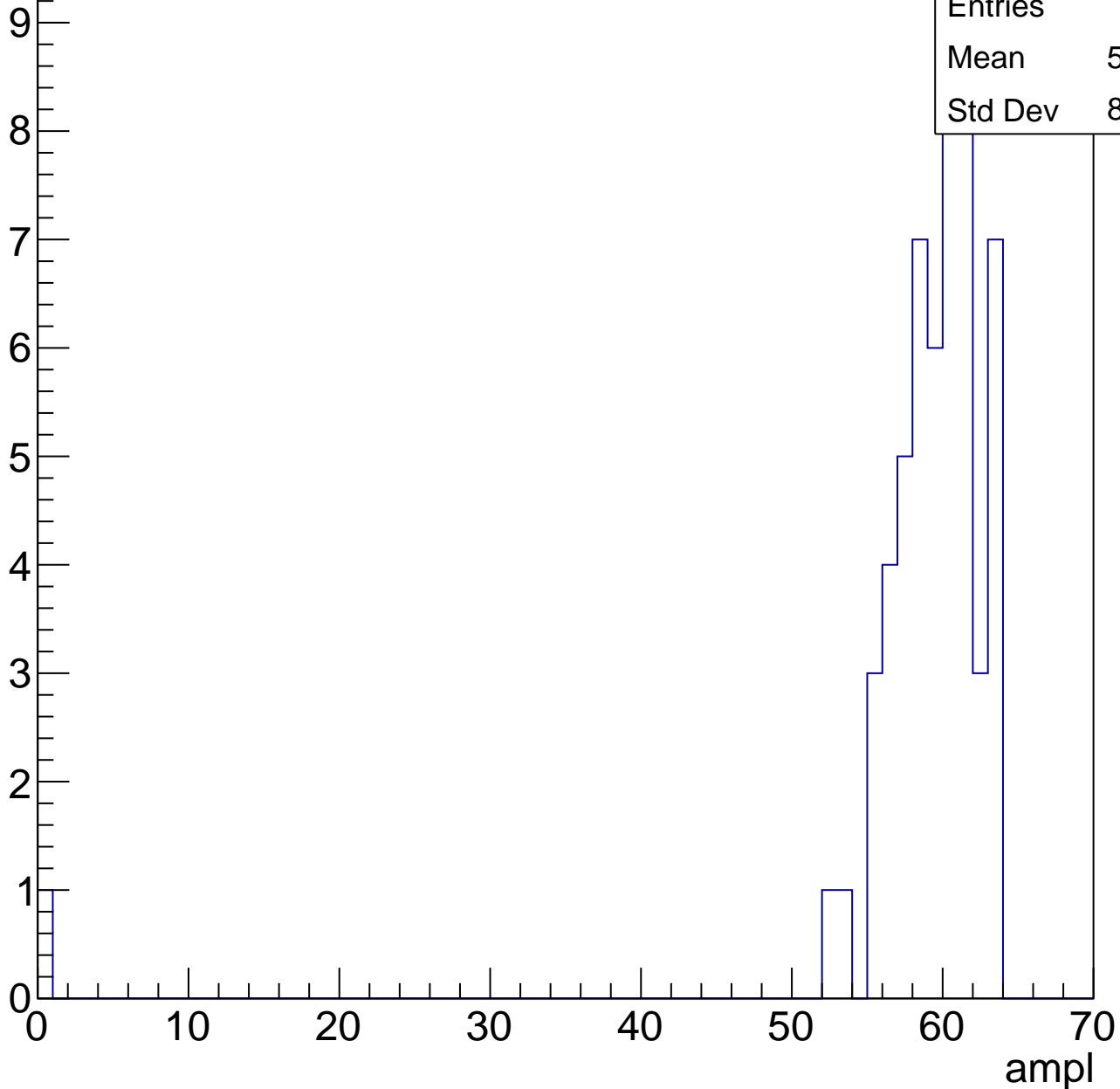


# B0L000S, U18-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.07
Std Dev	8.323

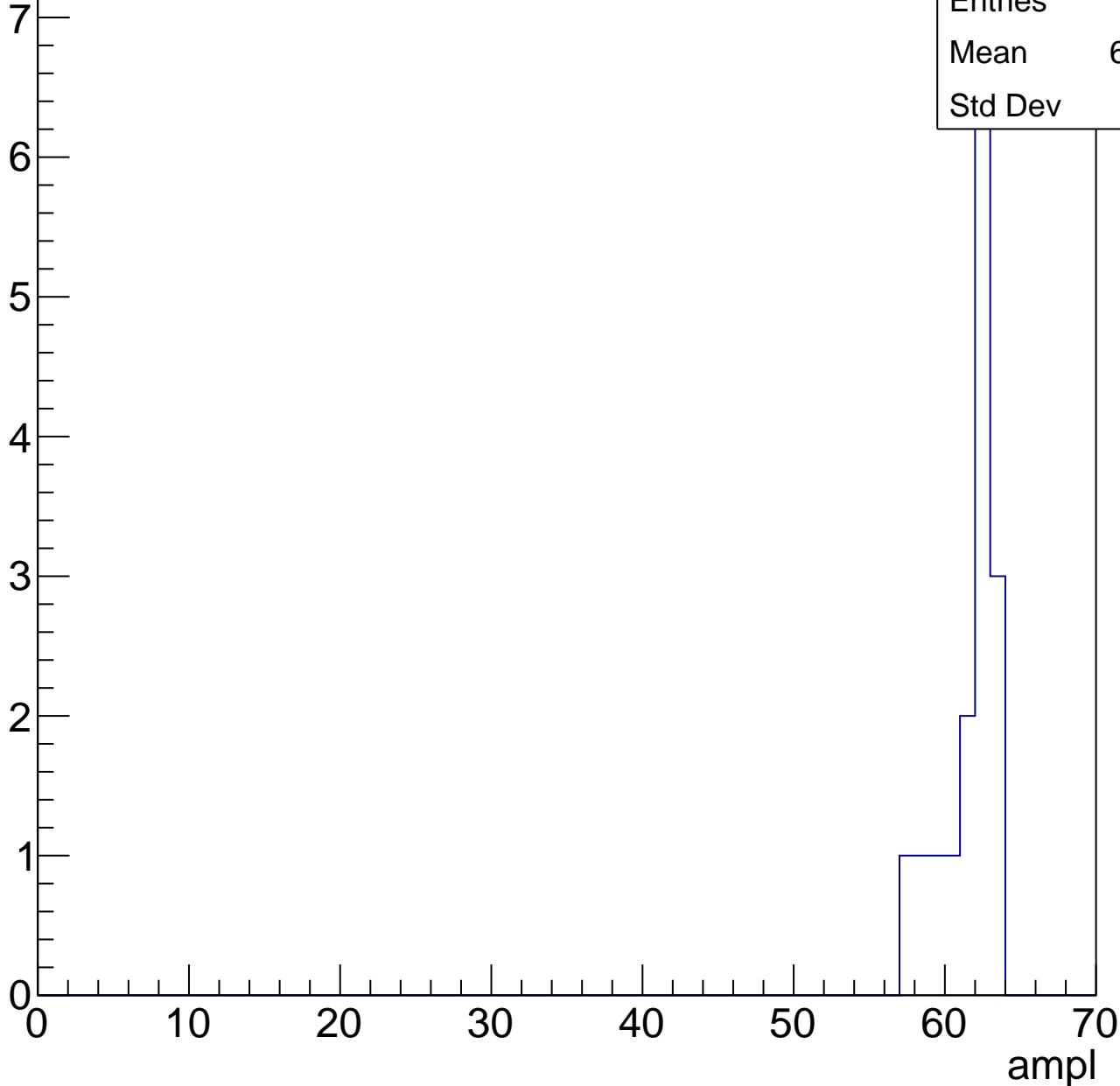


# B0L000S, U18-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.19
Std Dev	1.74

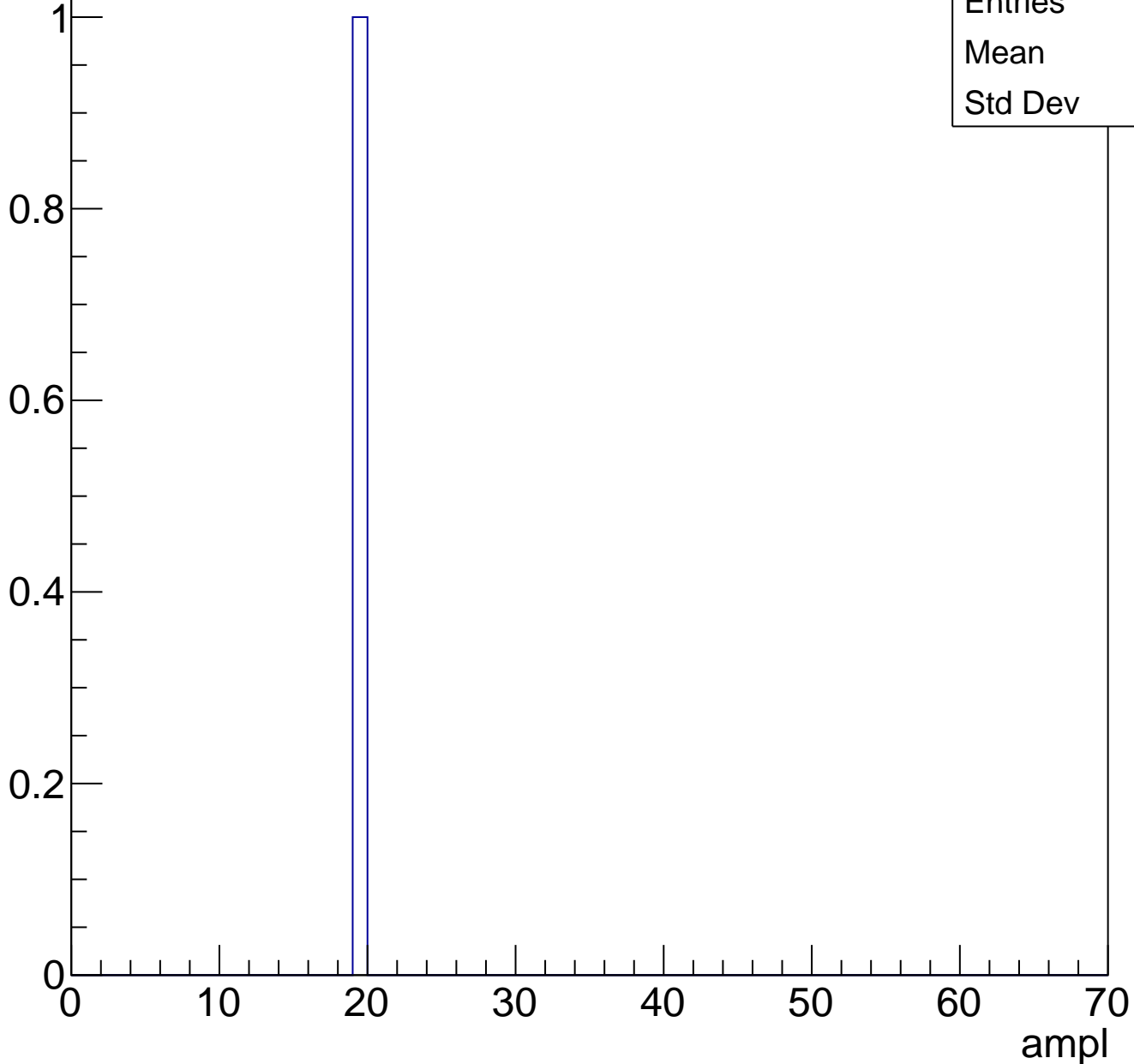




# B0L000S, U18-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch58, adc0

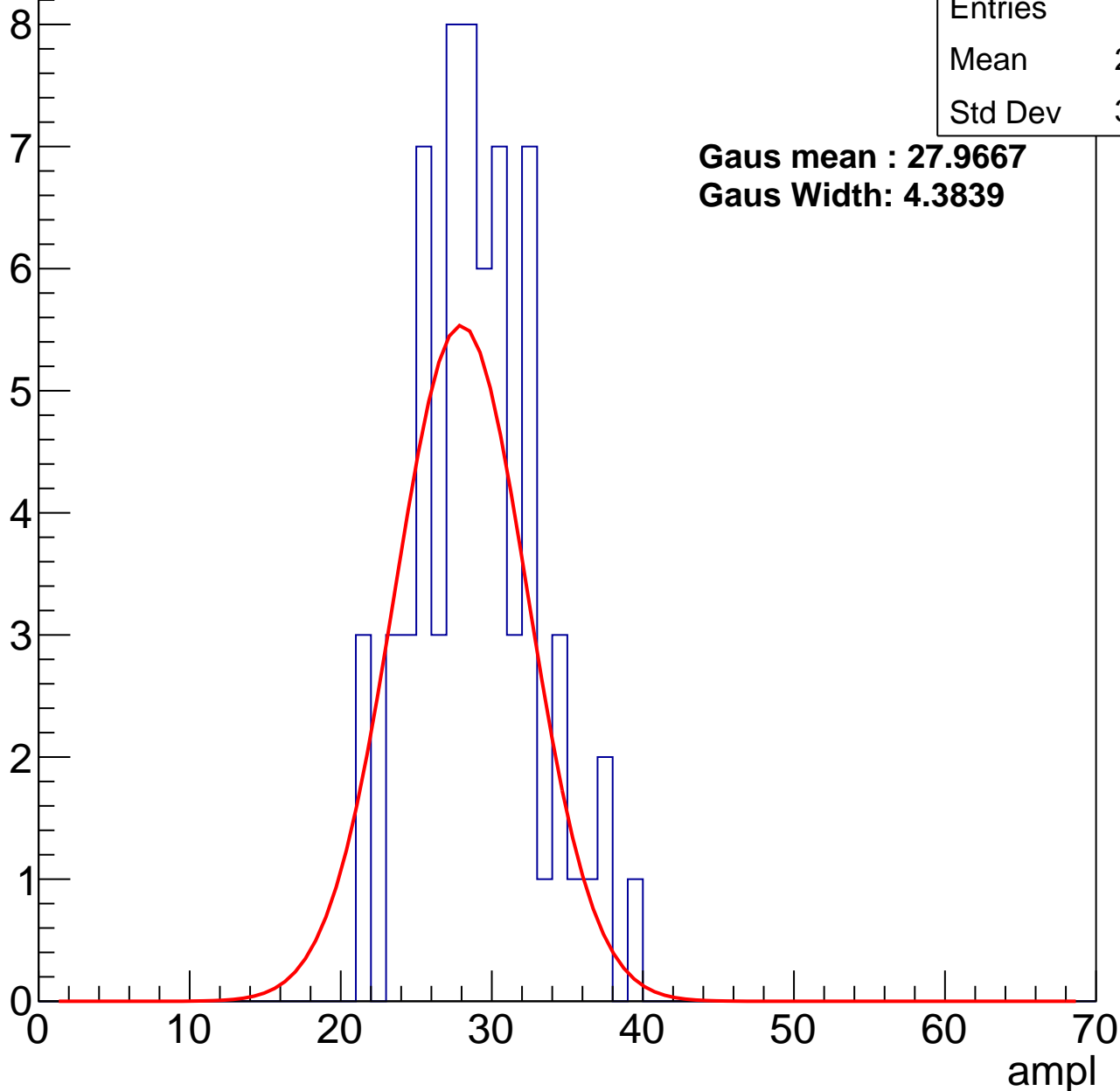
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.61
Std Dev	3.921

**Gaus mean : 27.9667**

**Gaus Width: 4.3839**



# B0L000S, U18-ch58, adc1

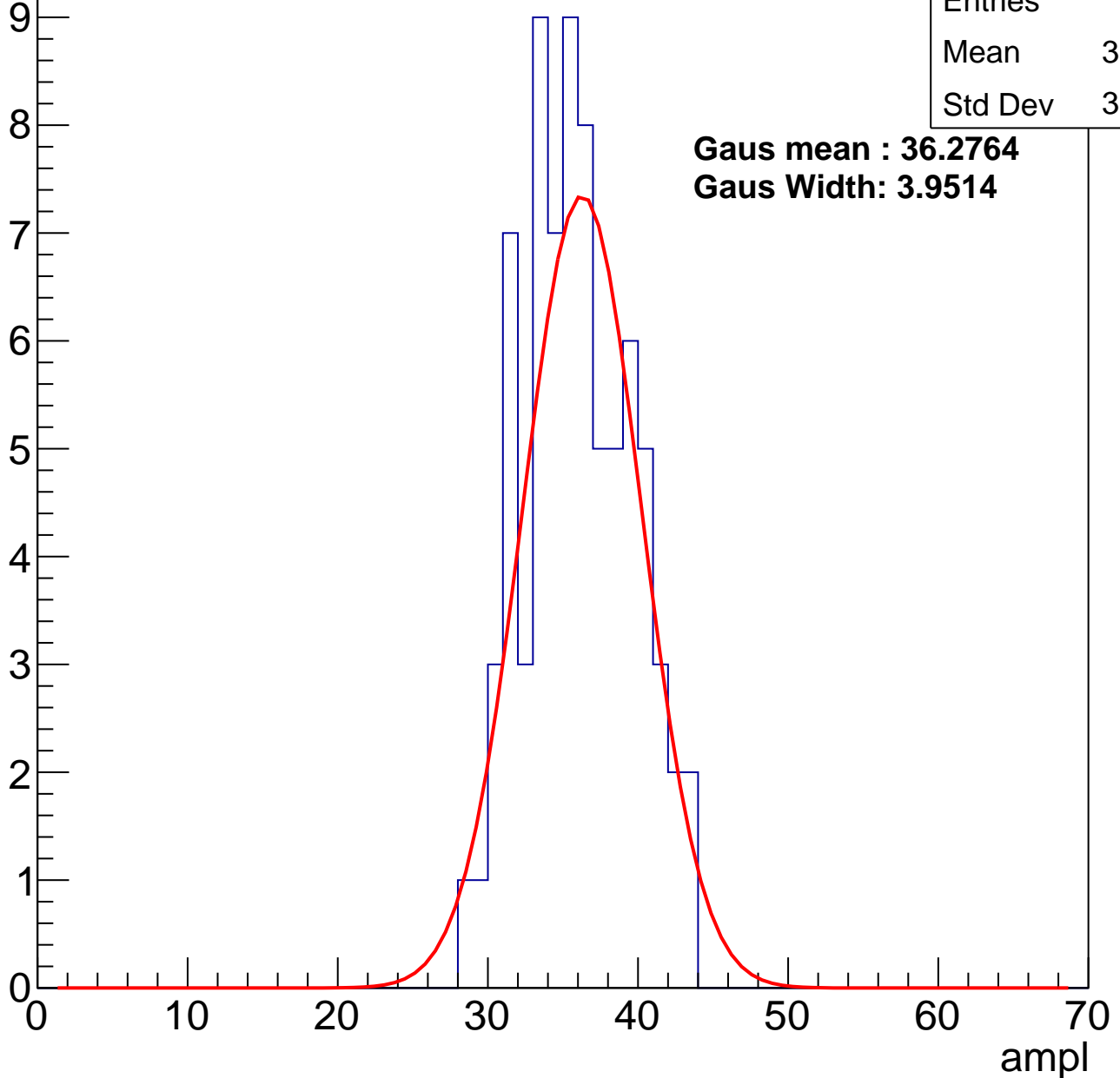
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	35.53
Std Dev	3.534

**Gaus mean : 36.2764**

**Gaus Width: 3.9514**



# B0L000S, U18-ch58, adc2

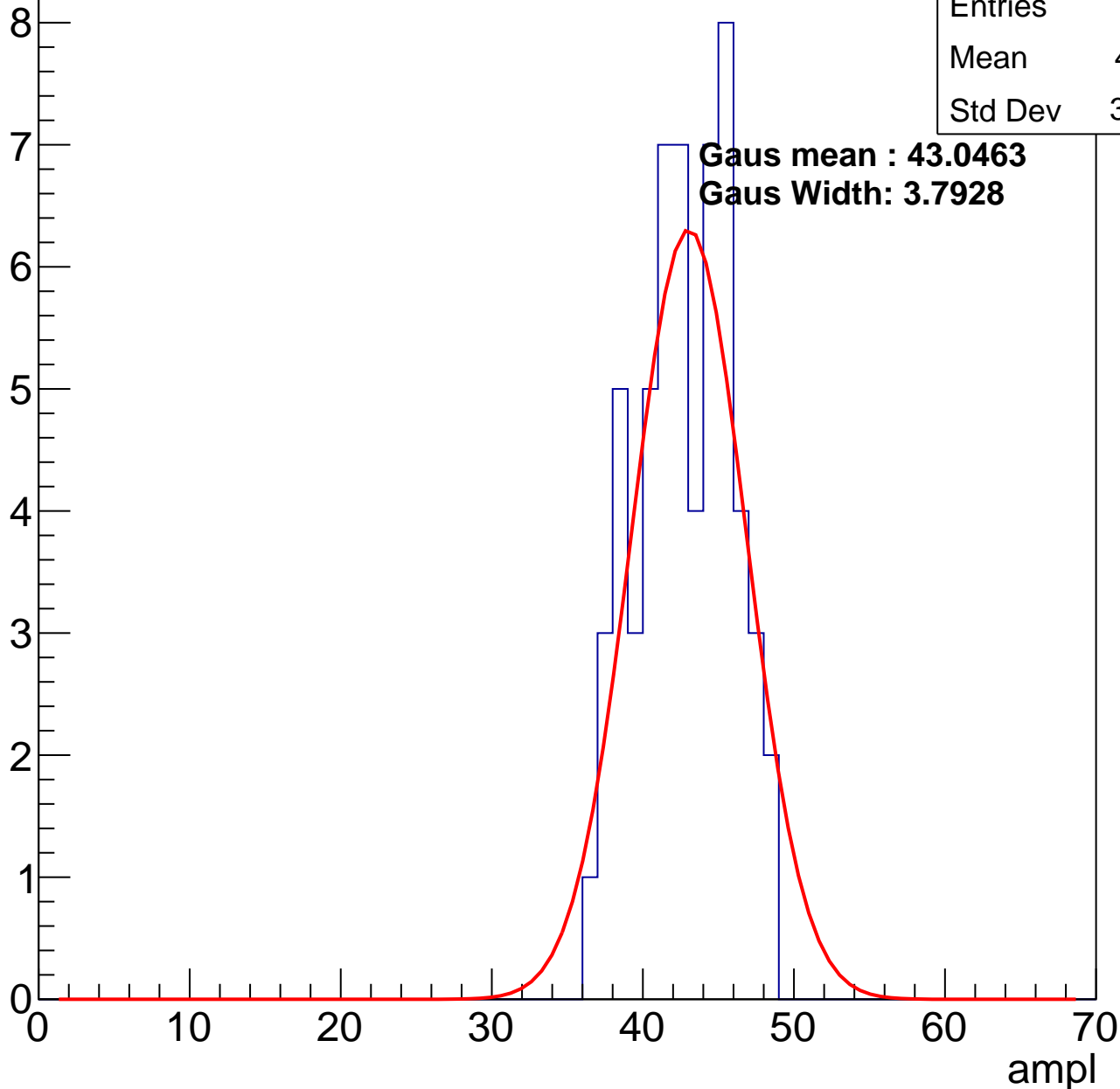
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.31
Std Dev	3.066

**Gaus mean : 43.0463**

**Gaus Width: 3.7928**

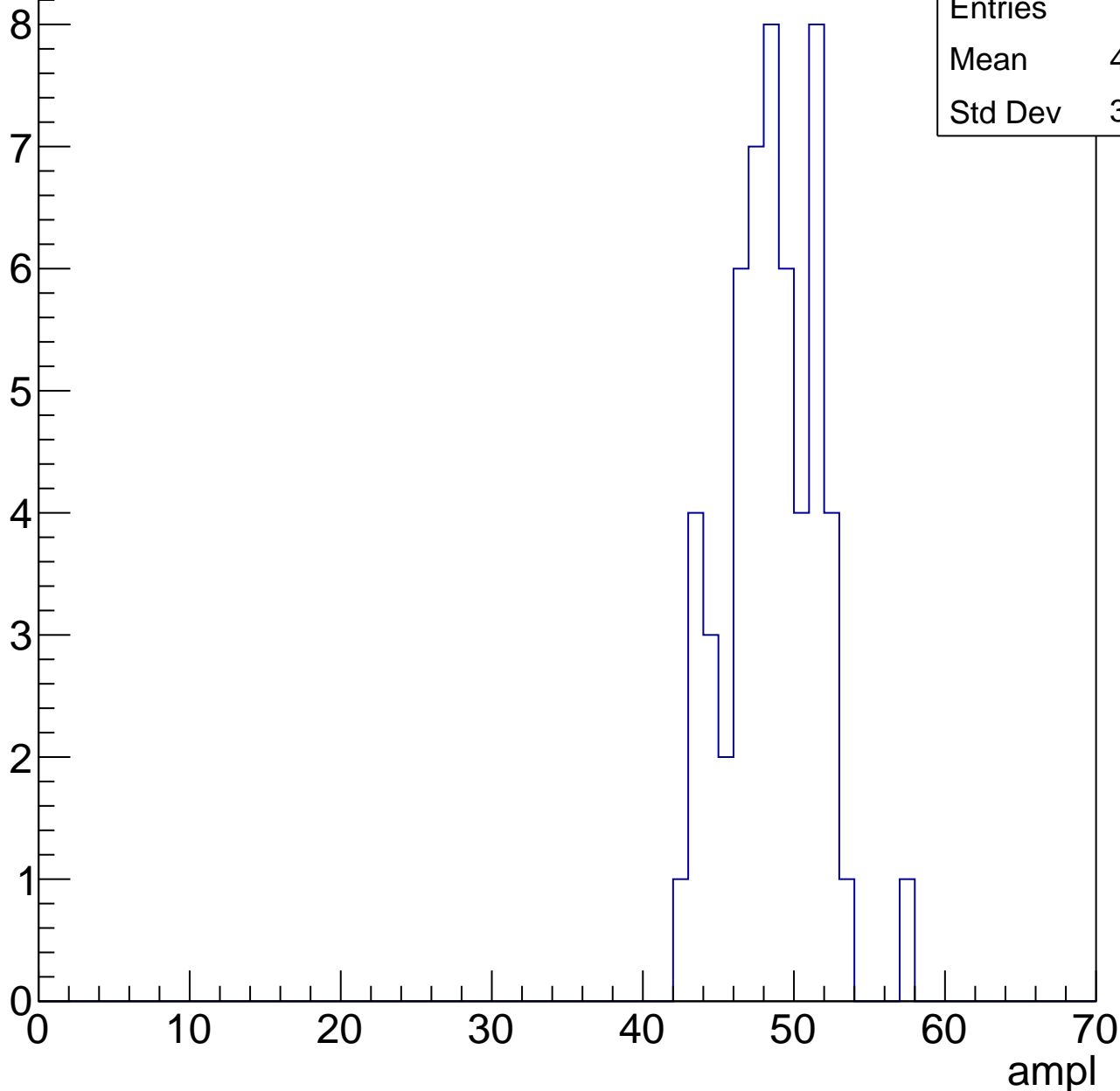


# B0L000S, U18-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	48.09
Std Dev	3.005

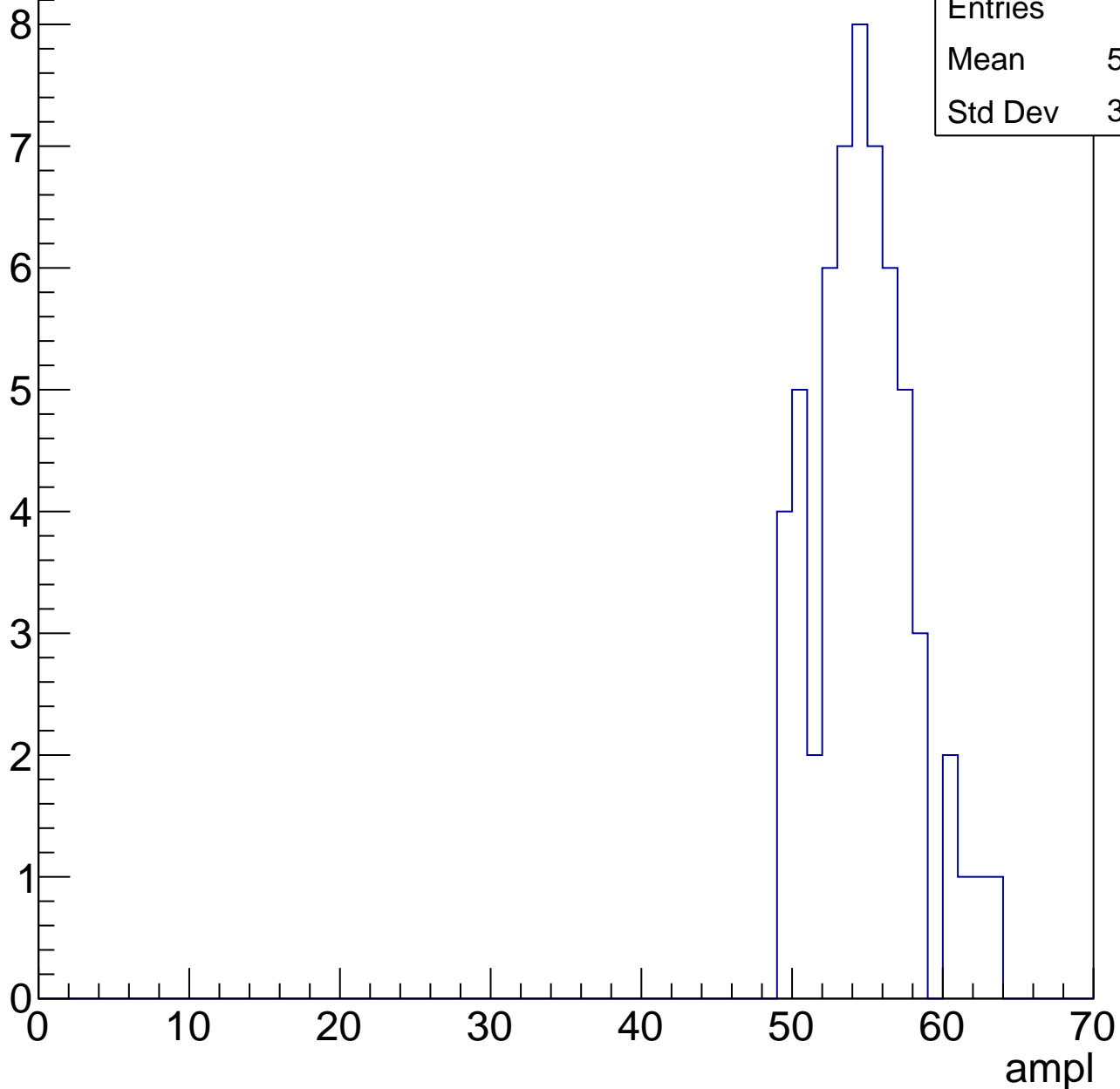


# B0L000S, U18-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

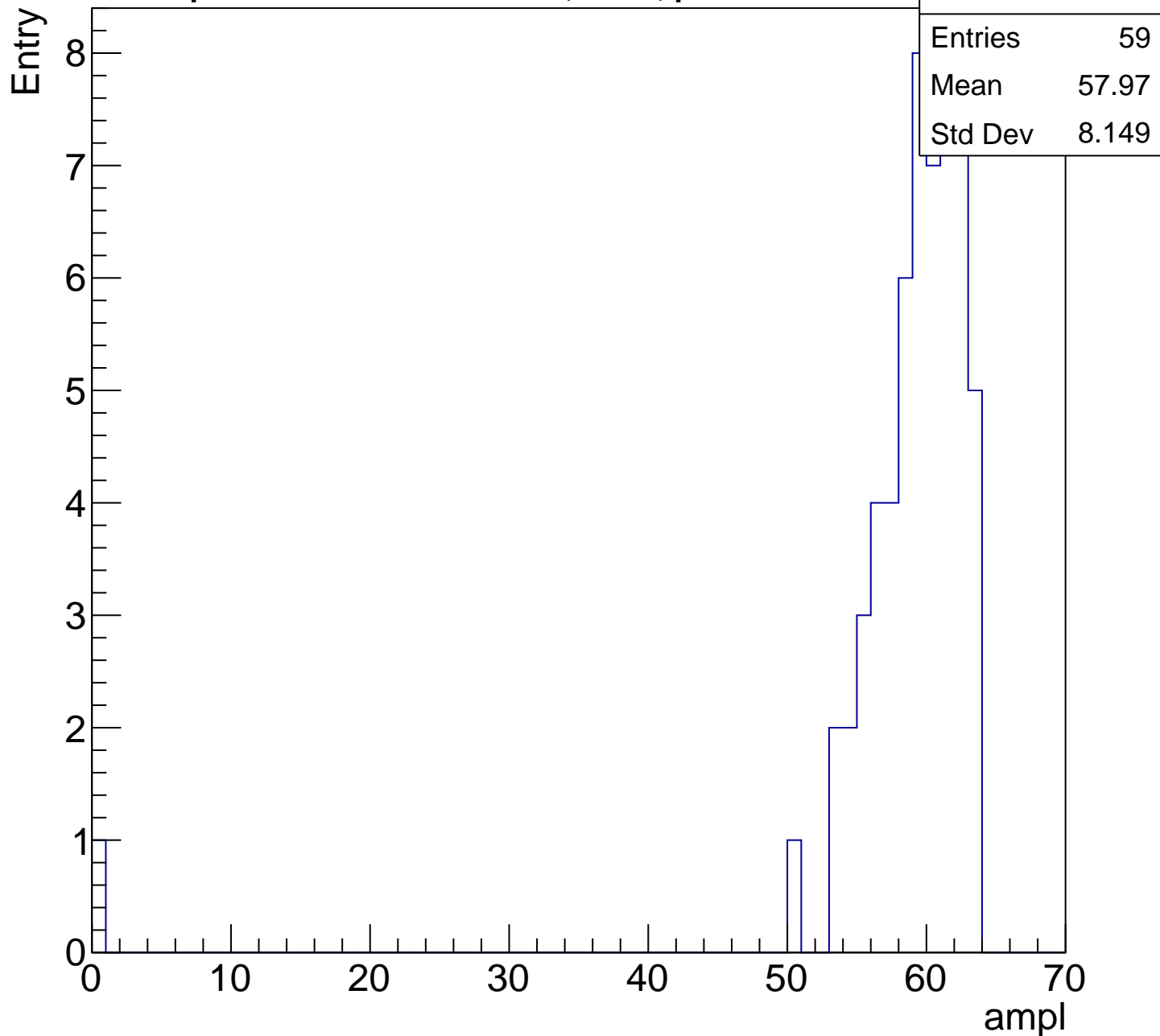
Entry

Entries	58
Mean	54.29
Std Dev	3.254



# B0L000S, U18-ch58, adc5

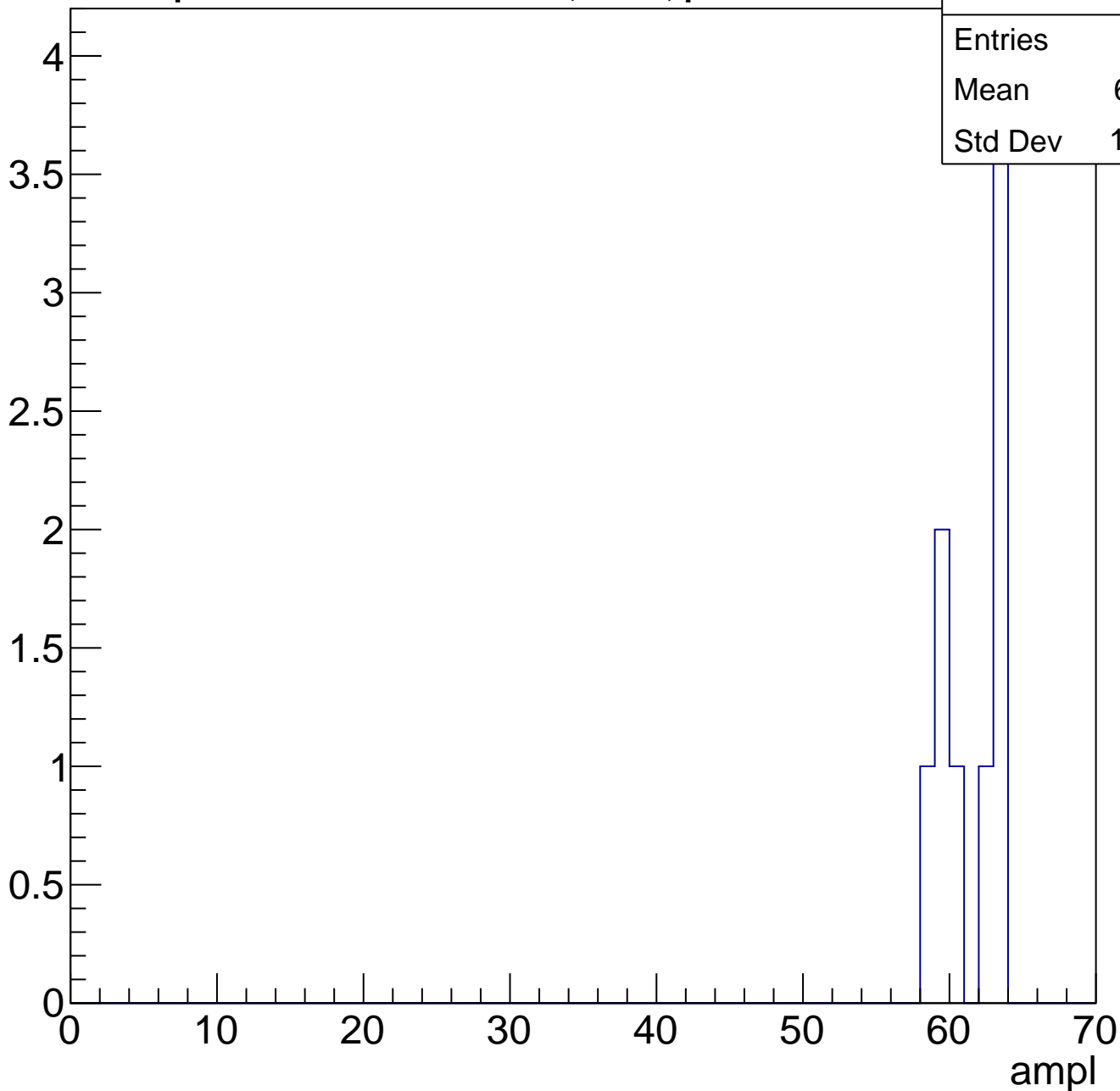
calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U18-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

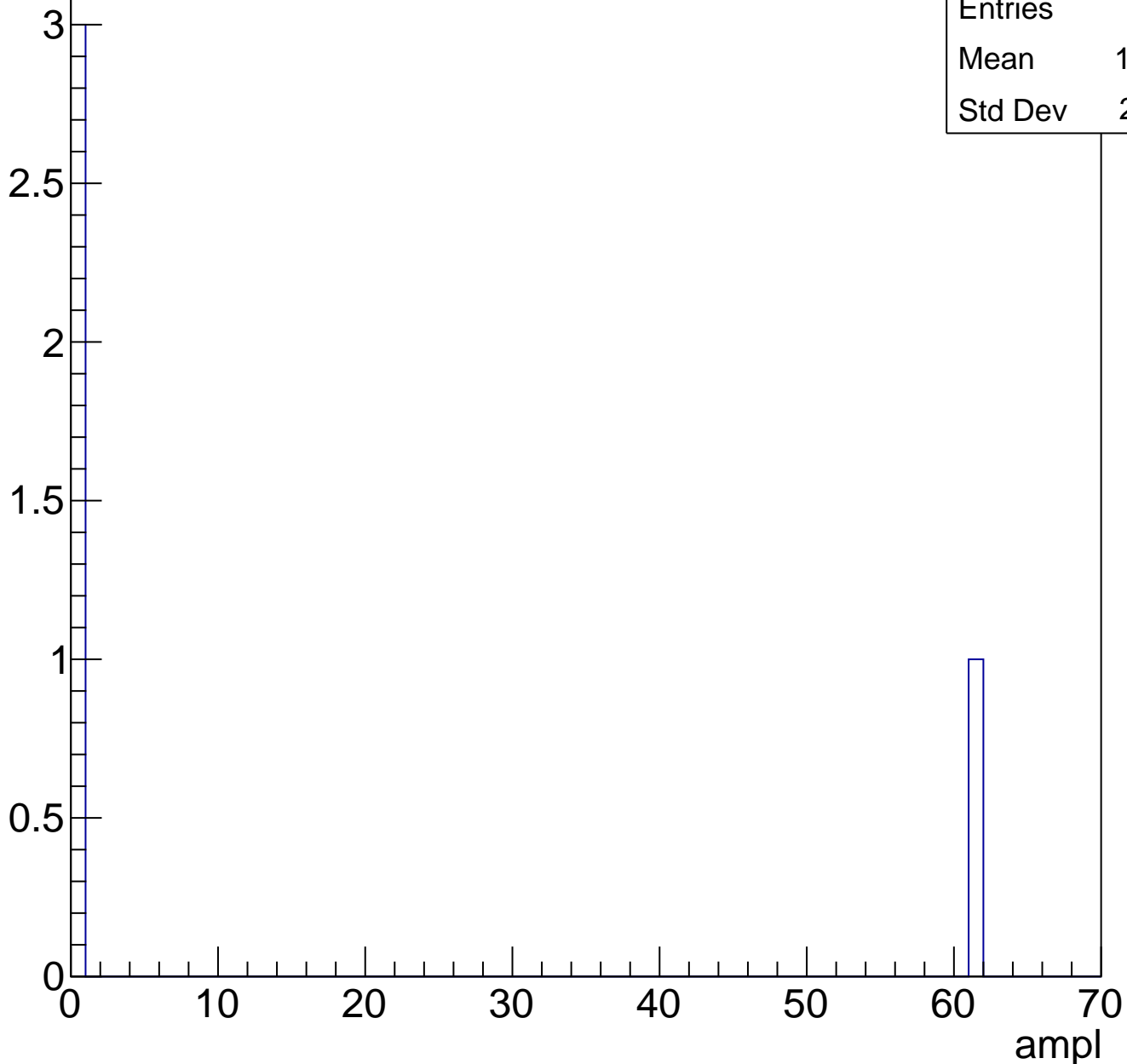




# B0L000S, U18-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch59, adc0

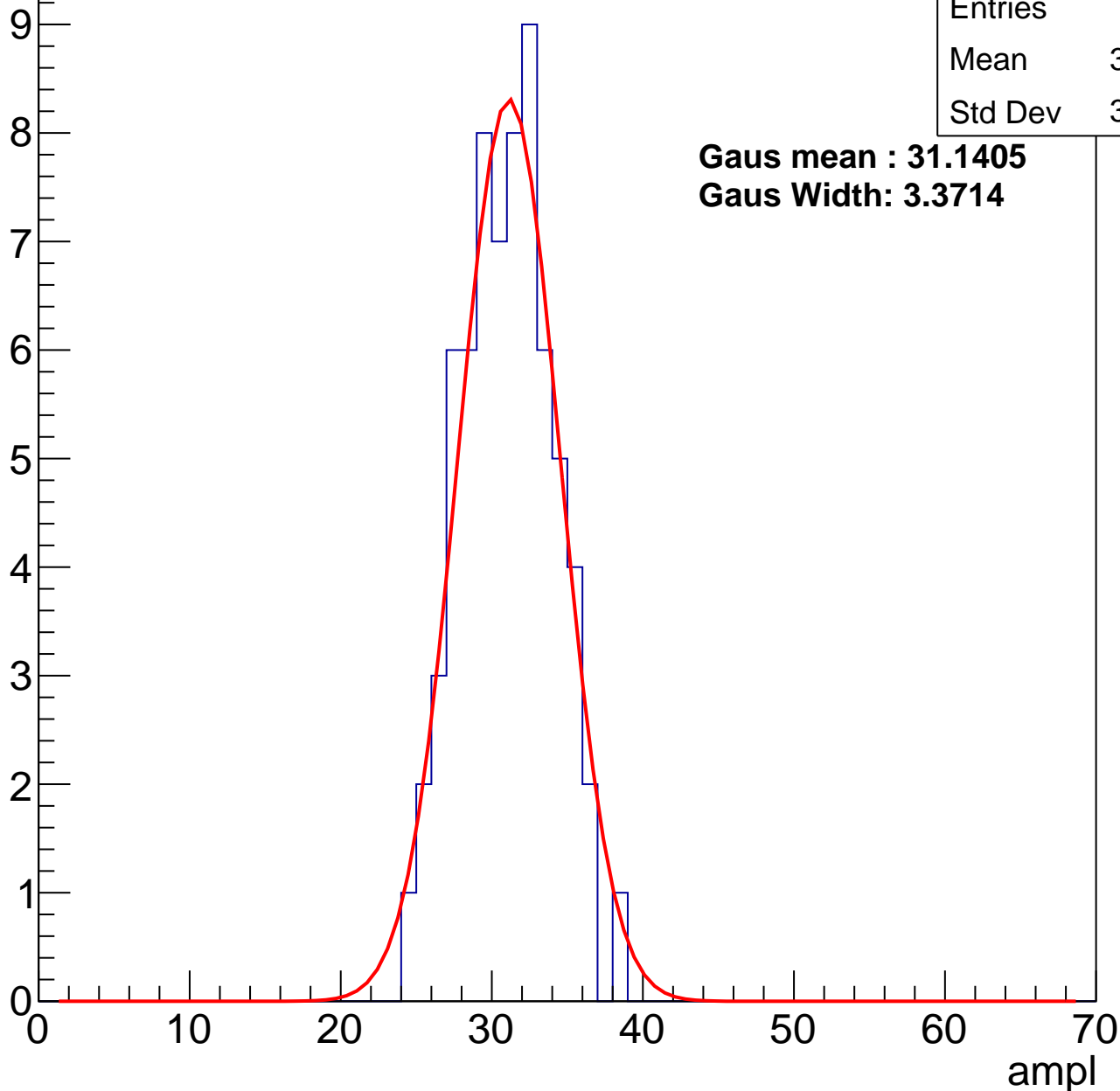
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.56
Std Dev	3.002

**Gaus mean : 31.1405**

**Gaus Width: 3.3714**



# B0L000S, U18-ch59, adc1

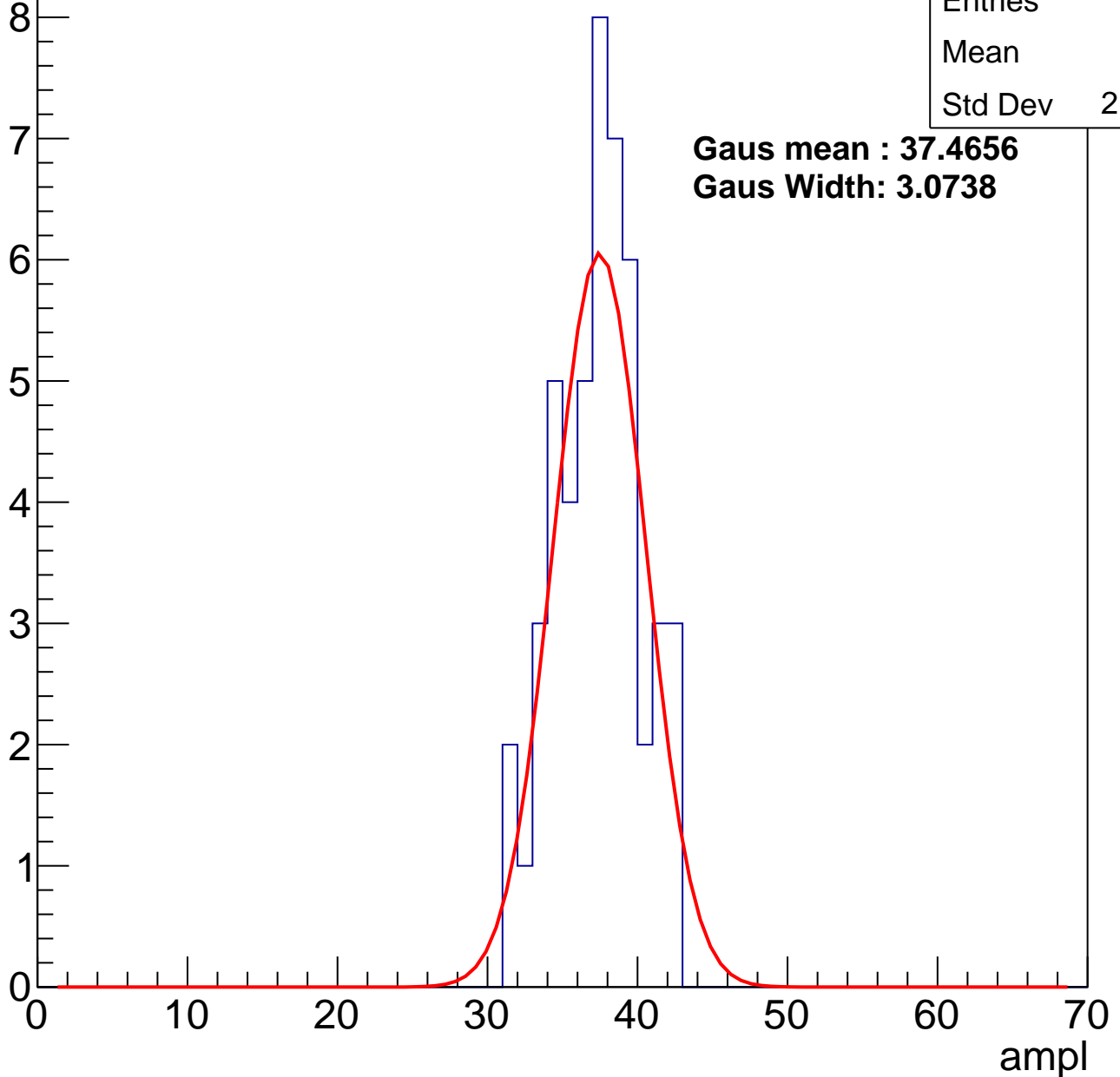
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.9
Std Dev	2.794

**Gaus mean : 37.4656**

**Gaus Width: 3.0738**



# B0L000S, U18-ch59, adc2

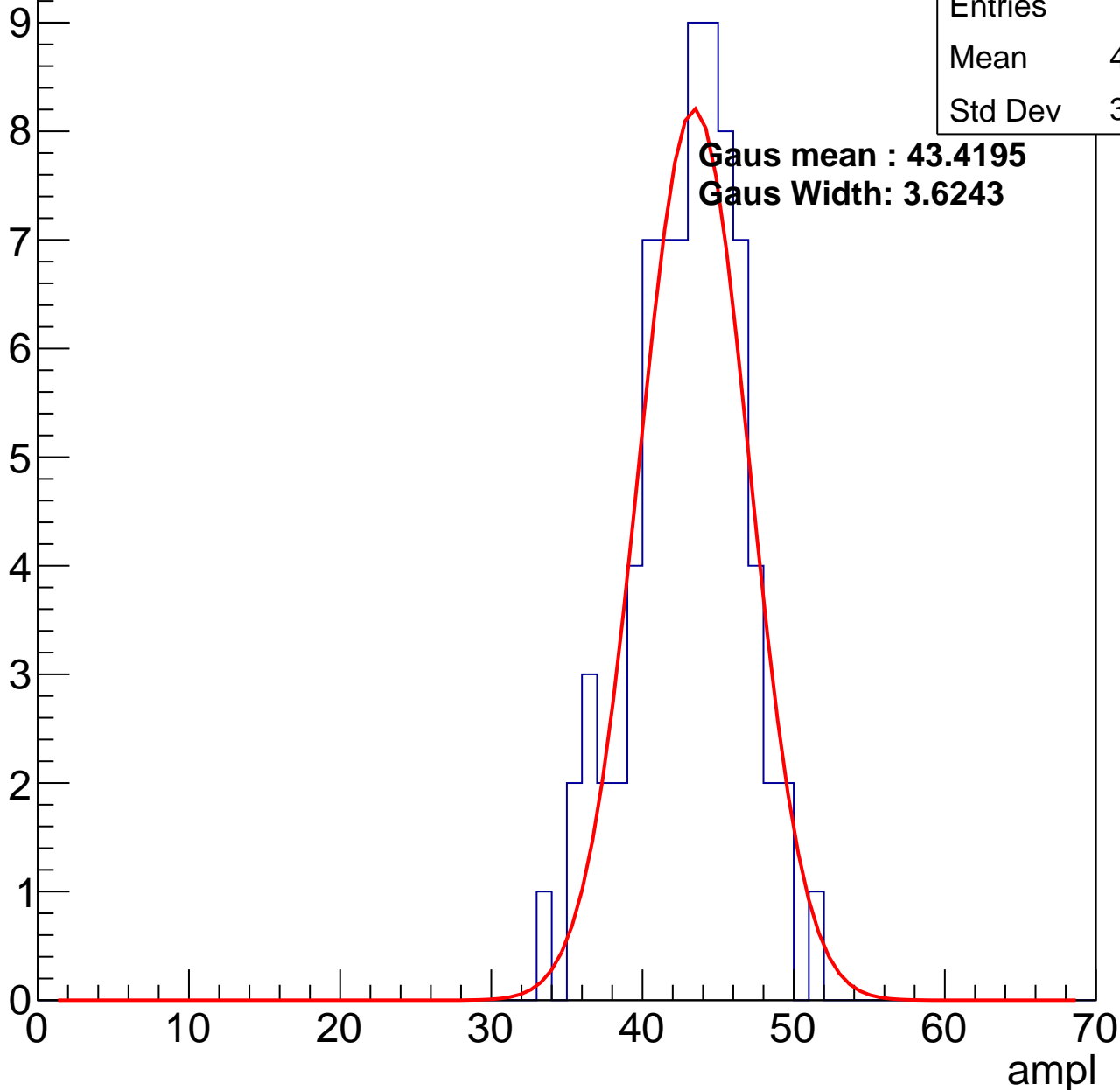
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	42.55
Std Dev	3.599

**Gaus mean : 43.4195**

**Gaus Width: 3.6243**

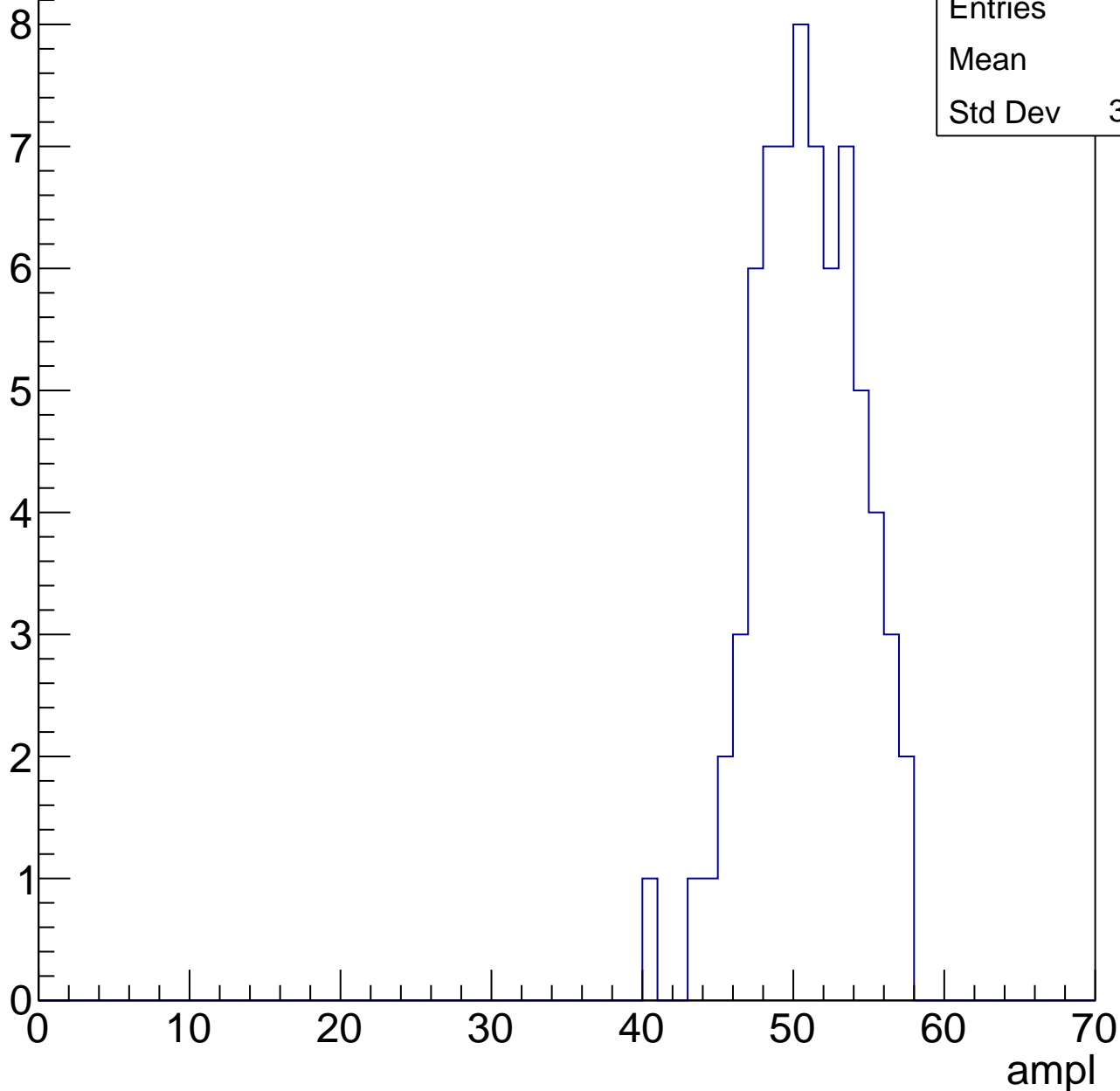


# B0L000S, U18-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	50.4
Std Dev	3.466

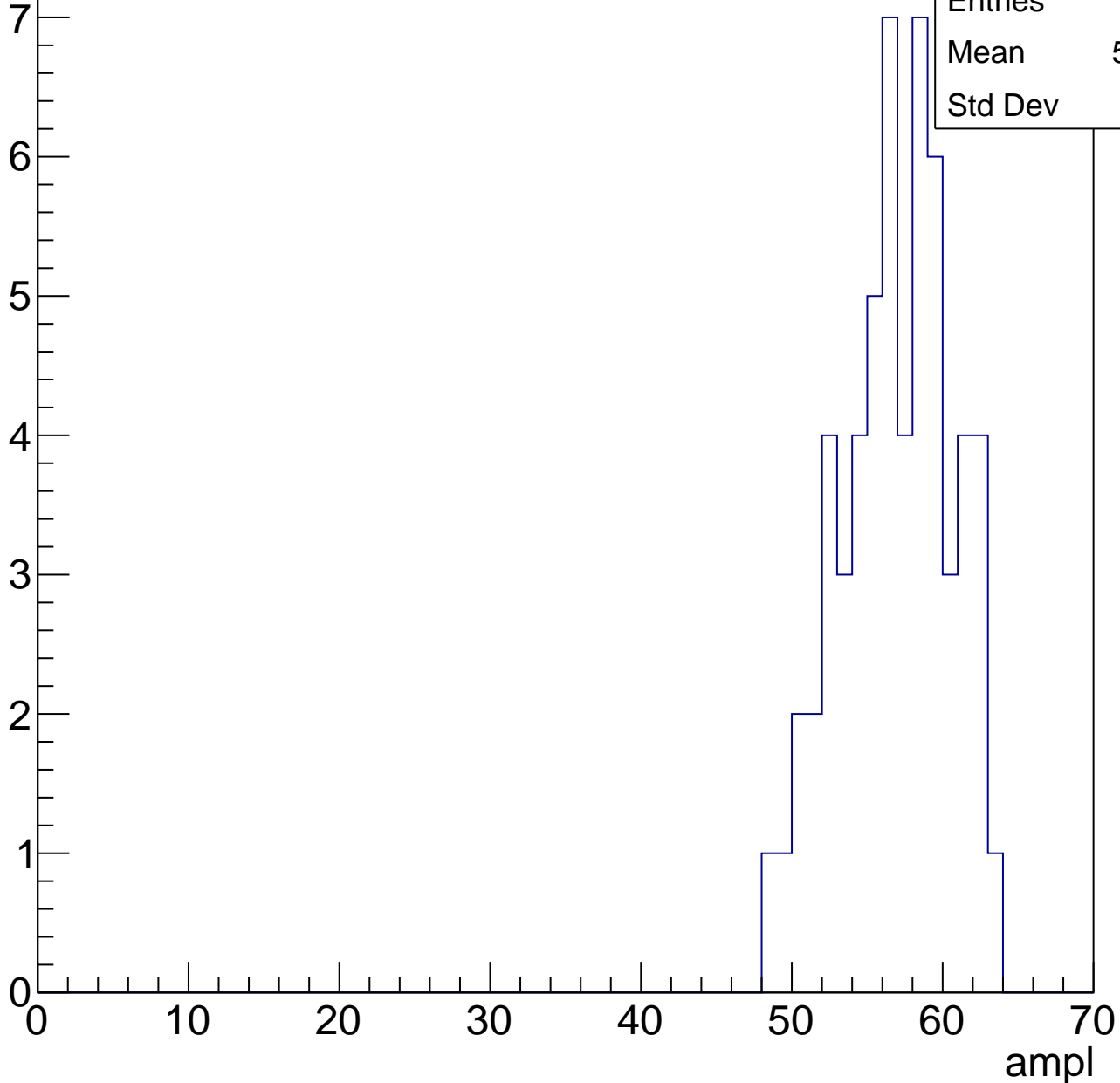


# B0L000S, U18-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

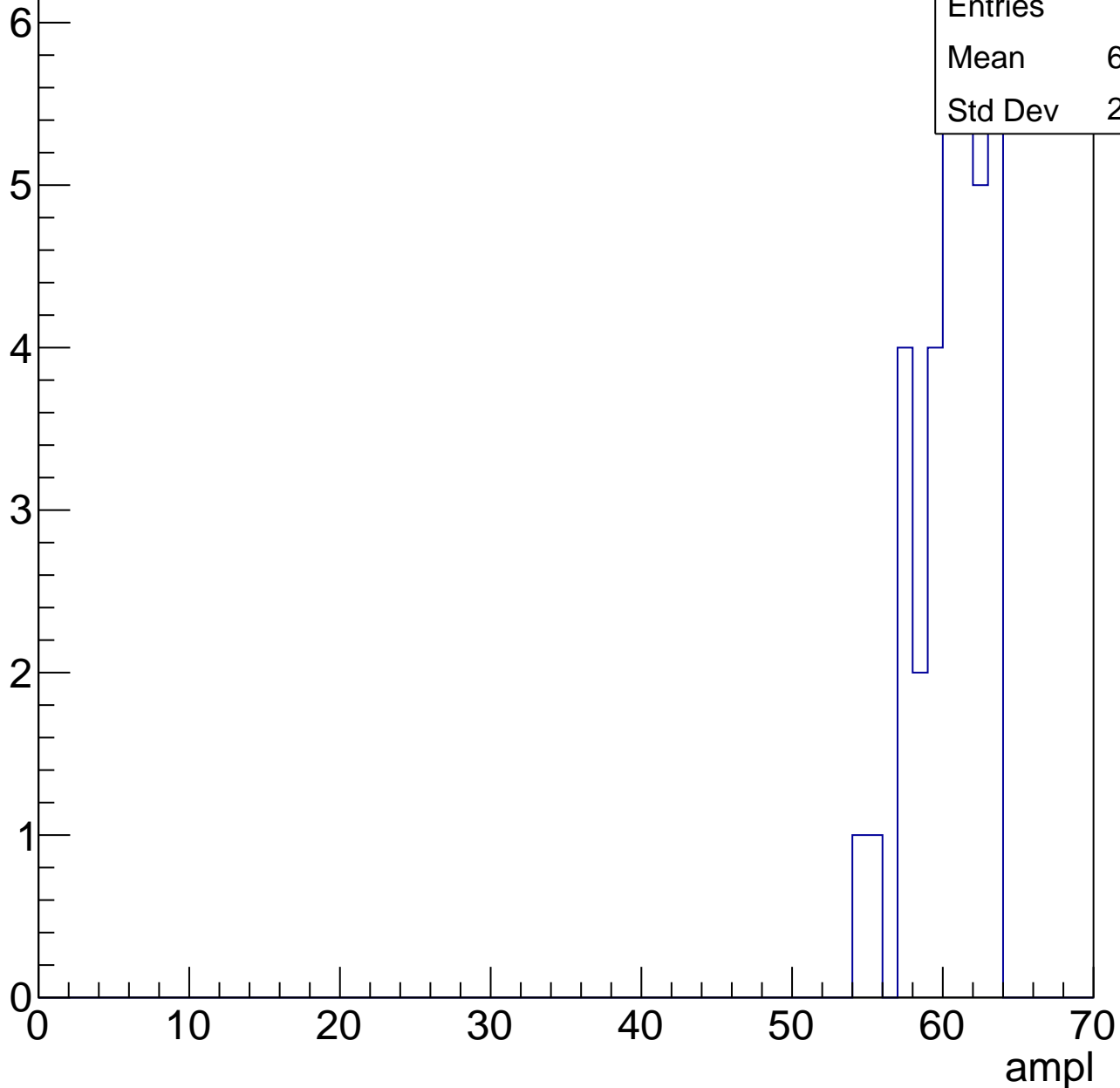
Entries	58
Mean	56.41
Std Dev	3.63



# B0L000S, U18-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

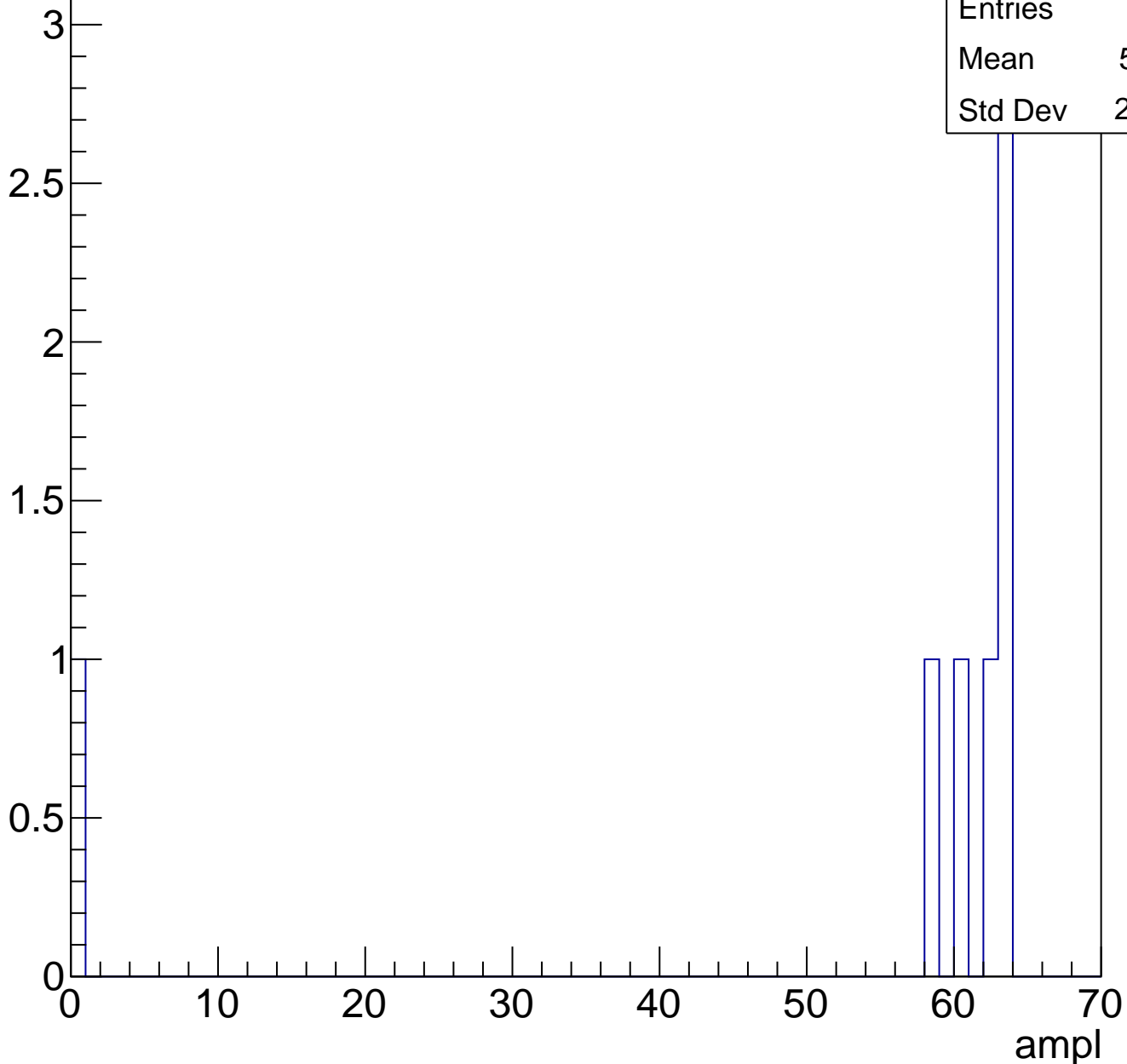


Entries	35
Mean	60.09
Std Dev	2.322

# B0L000S, U18-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	7
Mean	52.71
Std Dev	21.59



# B0L000S, U18-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch60, adc0

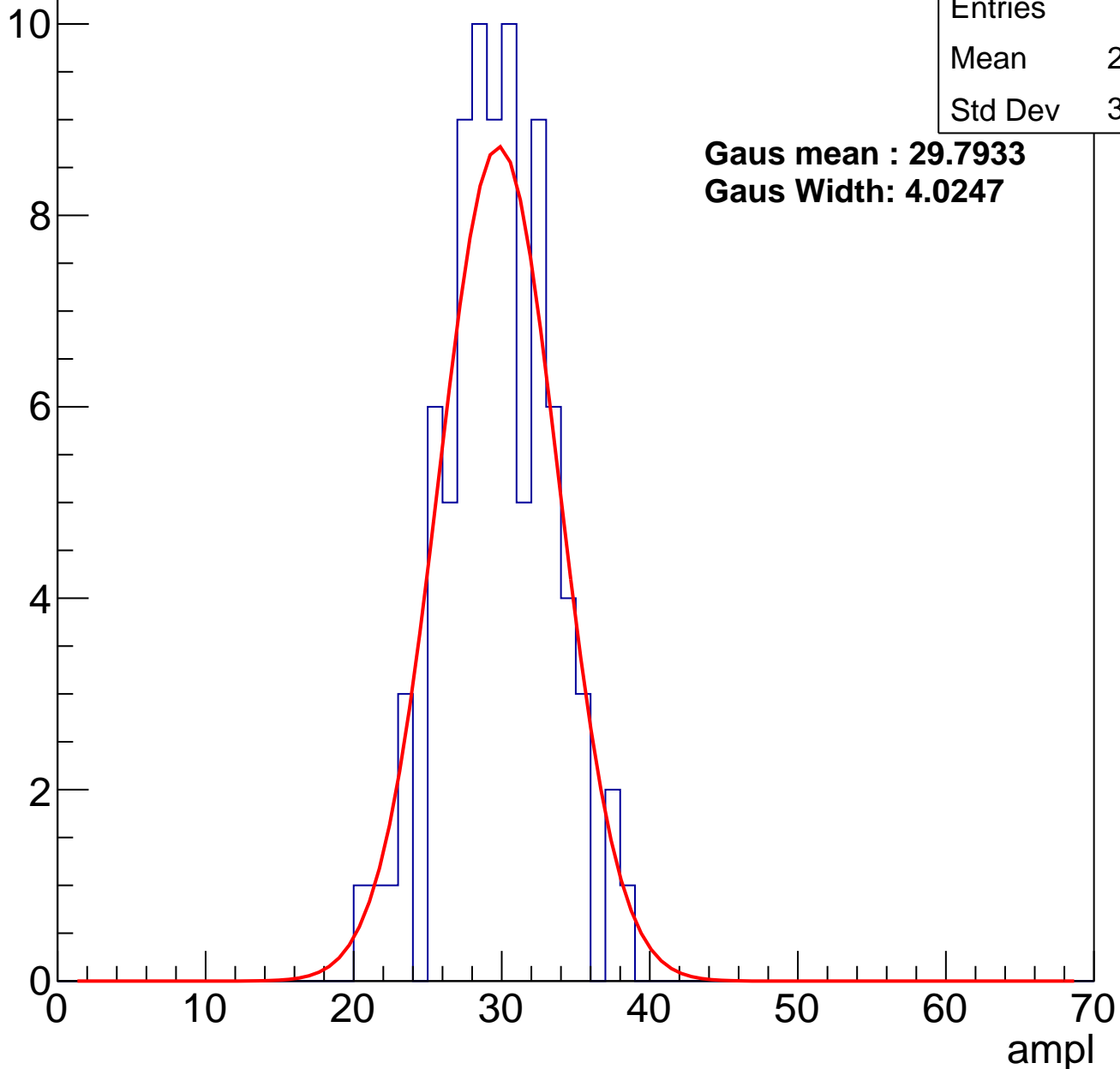
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	29.29
Std Dev	3.607

**Gaus mean : 29.7933**

**Gaus Width: 4.0247**

Entry



# B0L000S, U18-ch60, adc1

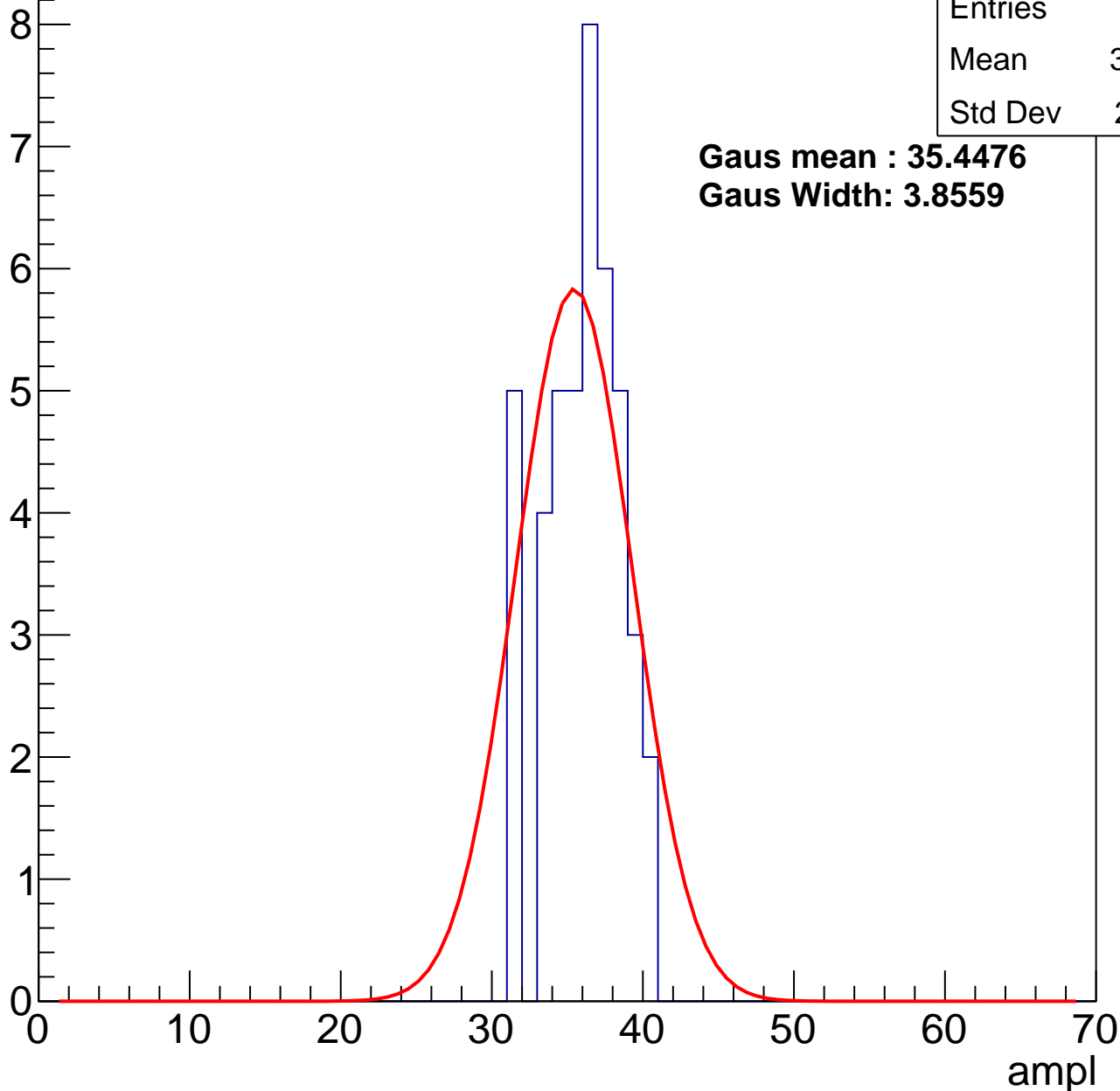
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	35.56
Std Dev	2.471

**Gaus mean : 35.4476**

**Gaus Width: 3.8559**



# B0L000S, U18-ch60, adc2

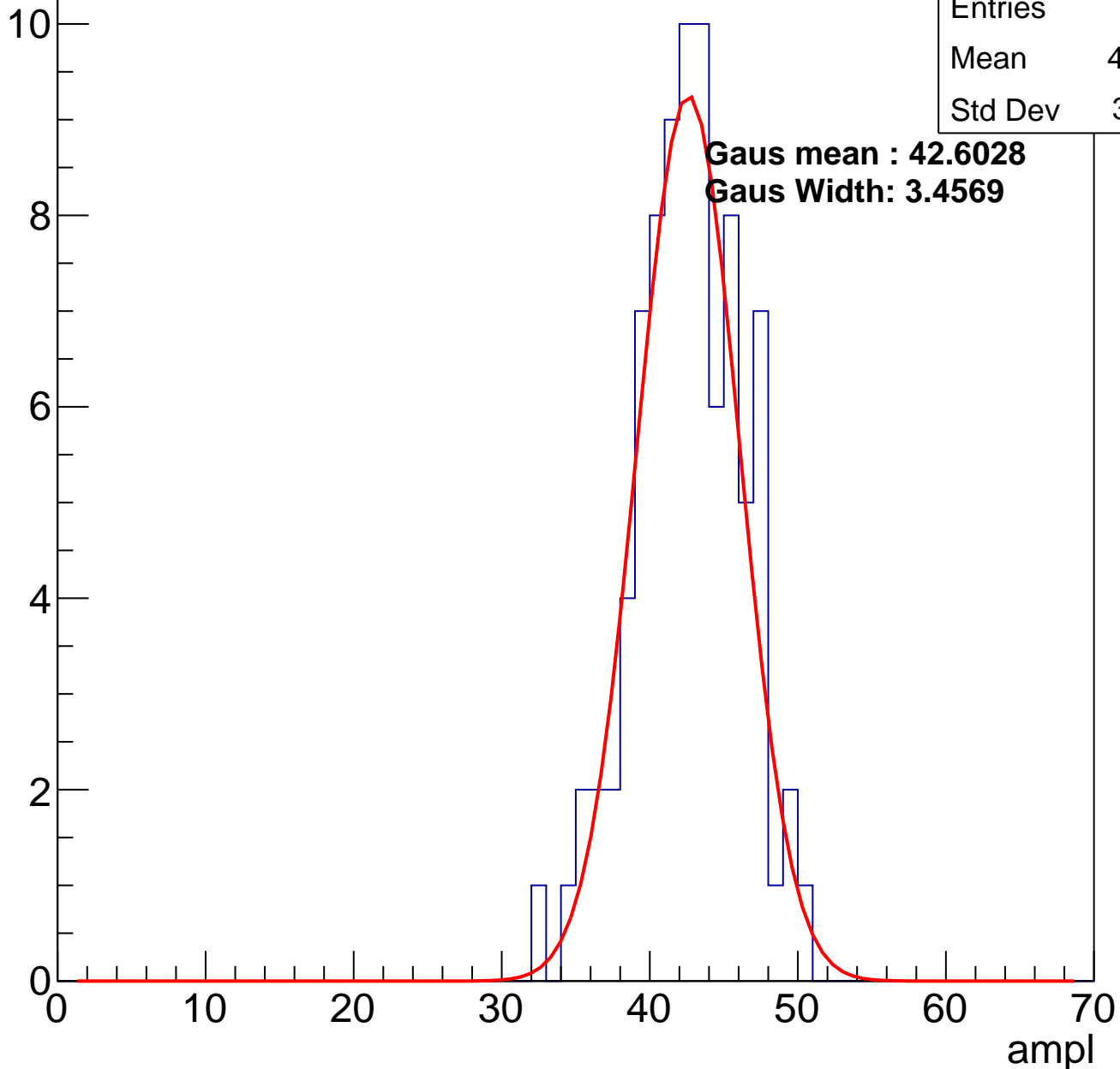
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	86
Mean	42.15
Std Dev	3.591

**Gaus mean : 42.6028**

**Gaus Width: 3.4569**

Entry

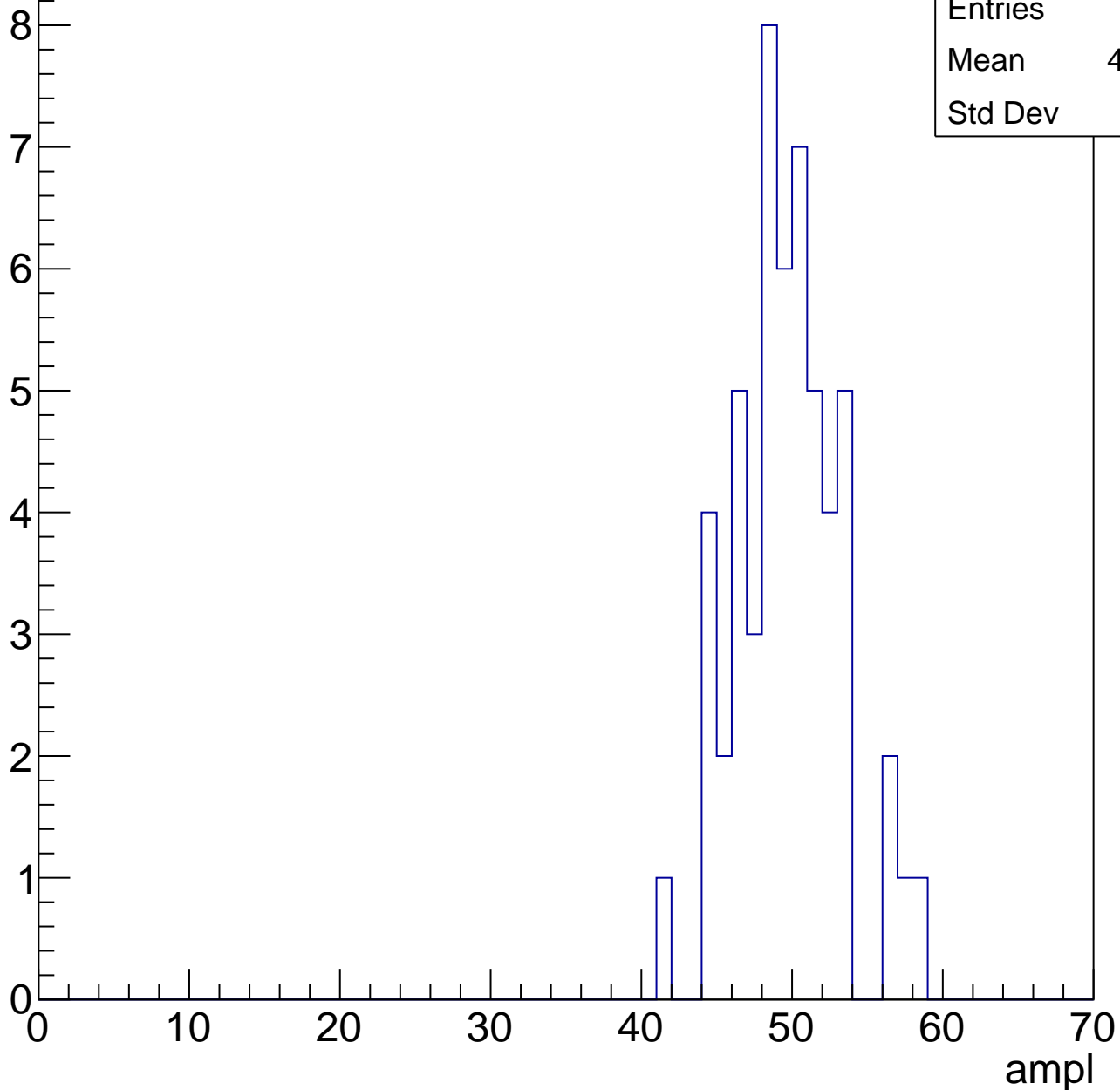


# B0L000S, U18-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

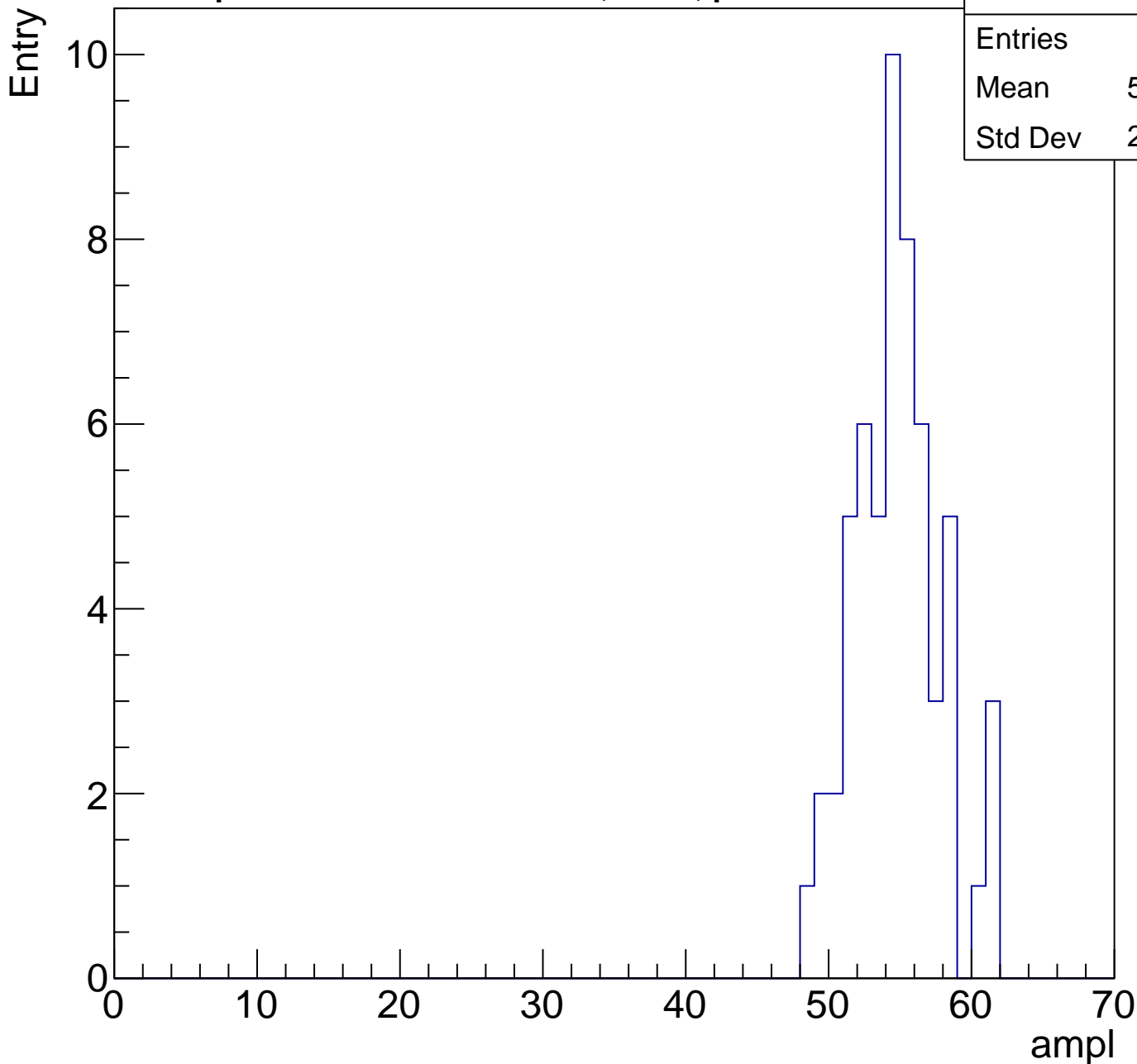
Entries	54
Mean	49.28
Std Dev	3.45



# B0L000S, U18-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

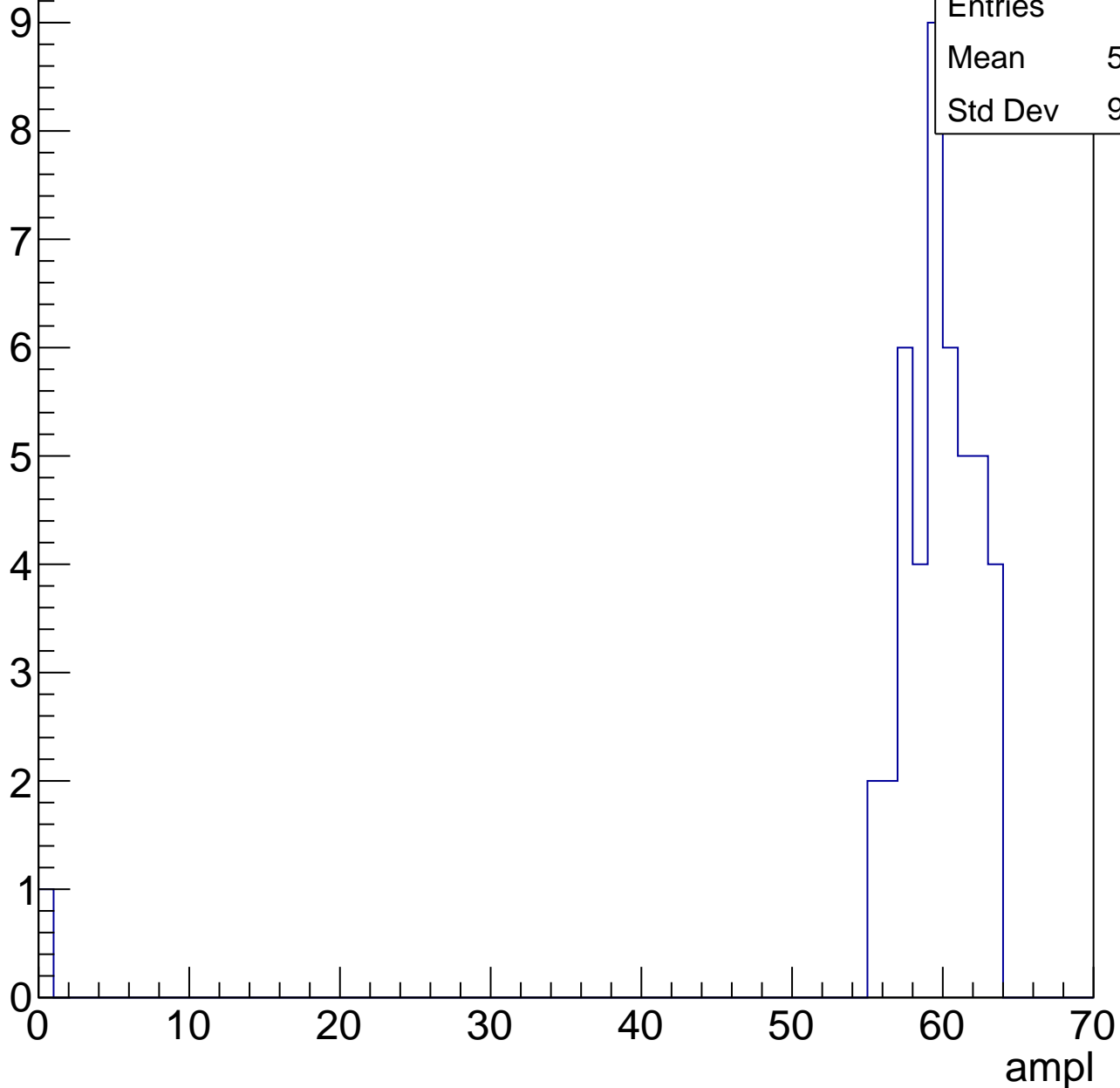
Entries	57
Mean	54.35
Std Dev	2.982



# B0L000S, U18-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

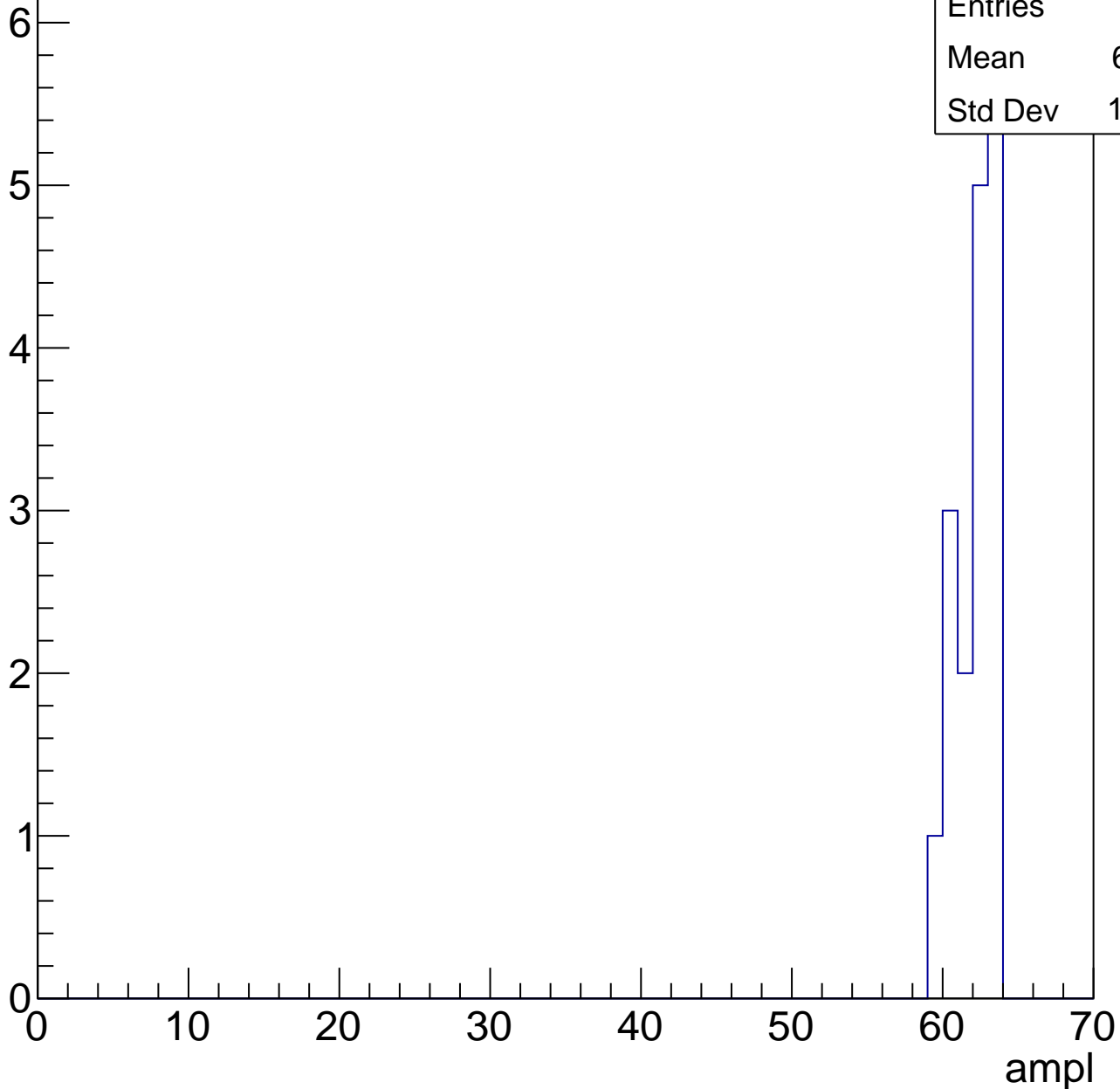


# B0L000S, U18-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.71
Std Dev	1.273





# B0L000S, U18-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch61, adc0

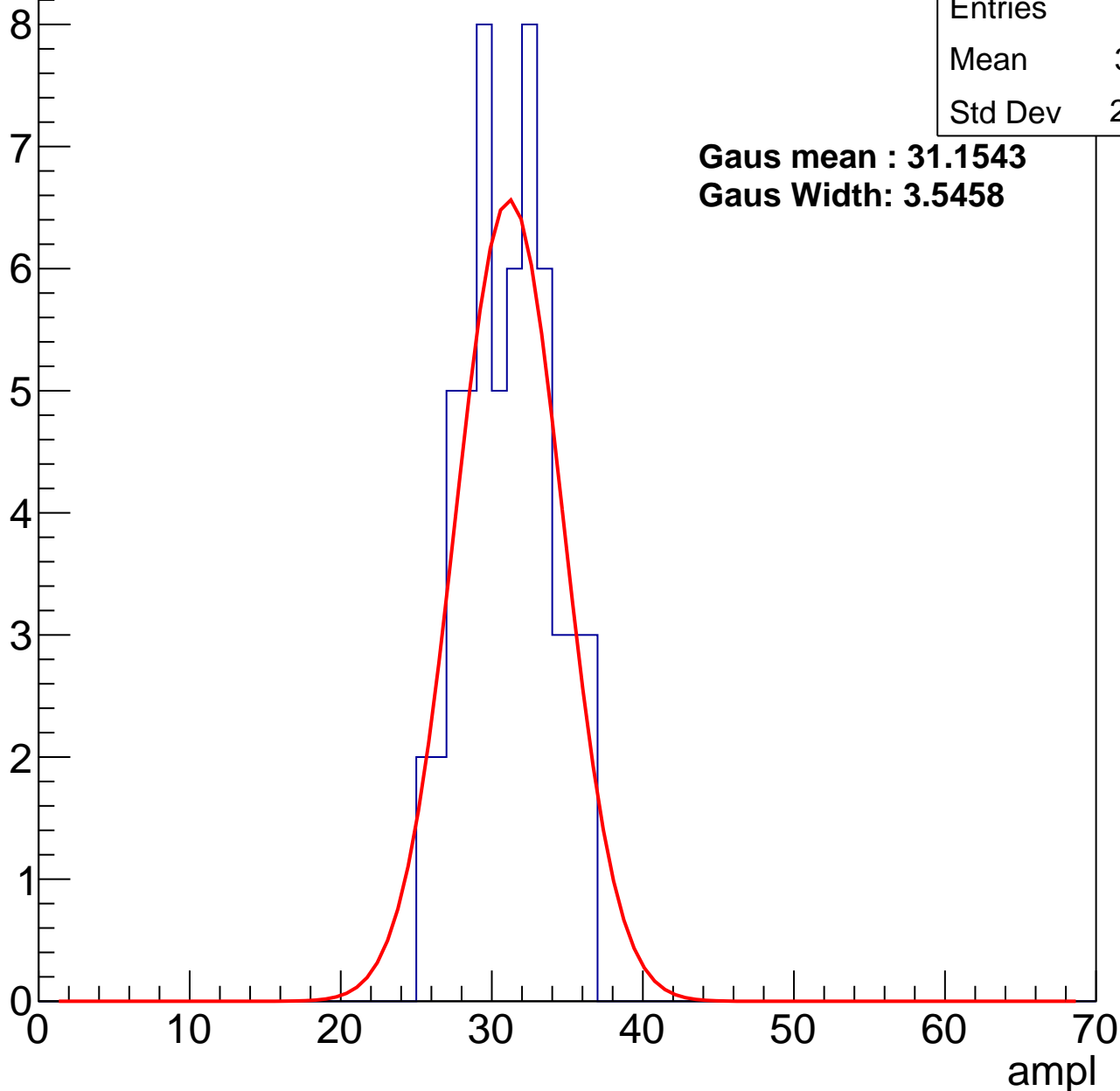
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	30.61
Std Dev	2.858

**Gaus mean : 31.1543**

**Gaus Width: 3.5458**



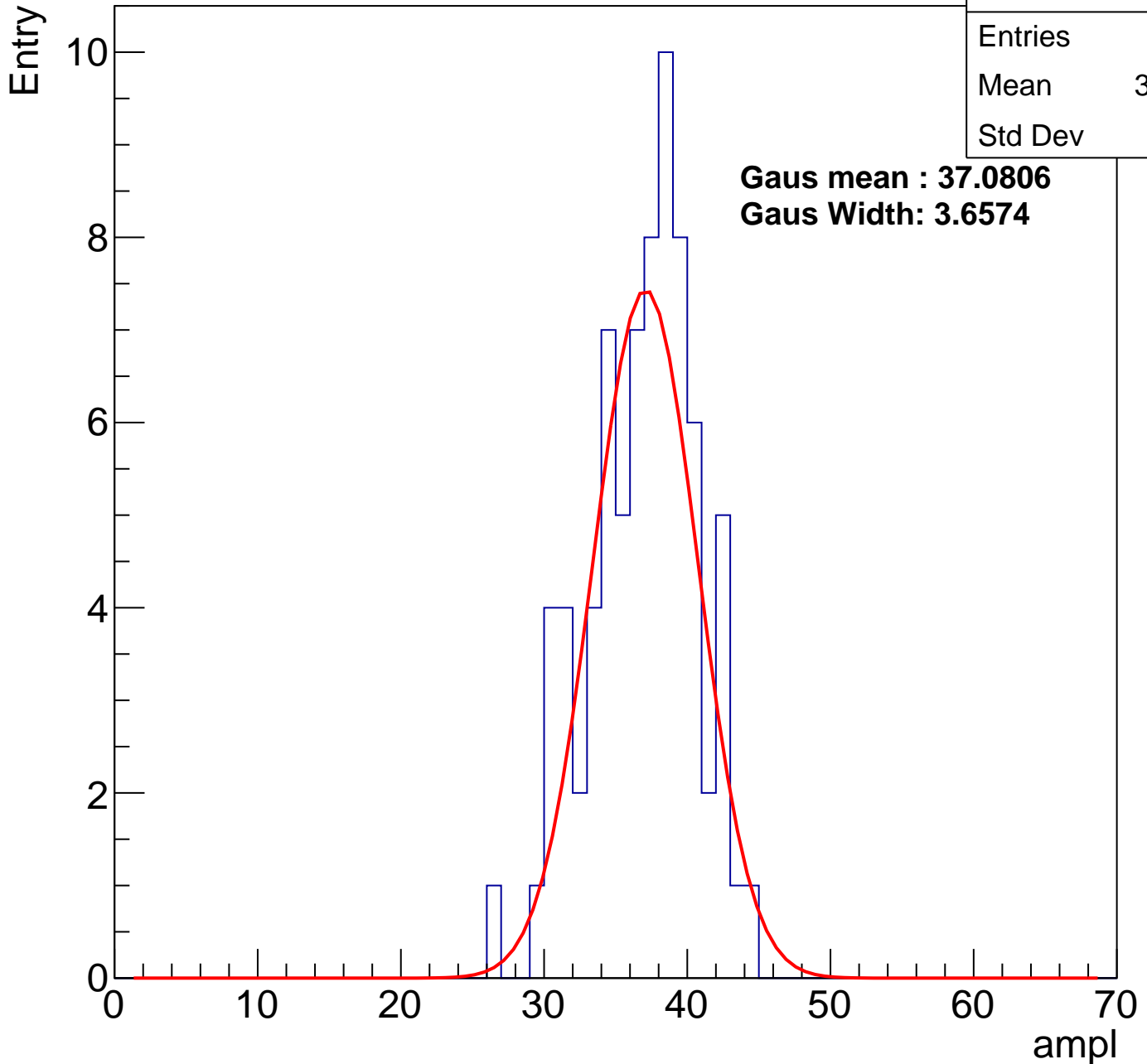
# B0L000S, U18-ch61, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	36.41
Std Dev	3.7

**Gaus mean : 37.0806**

**Gaus Width: 3.6574**



# B0L000S, U18-ch61, adc2

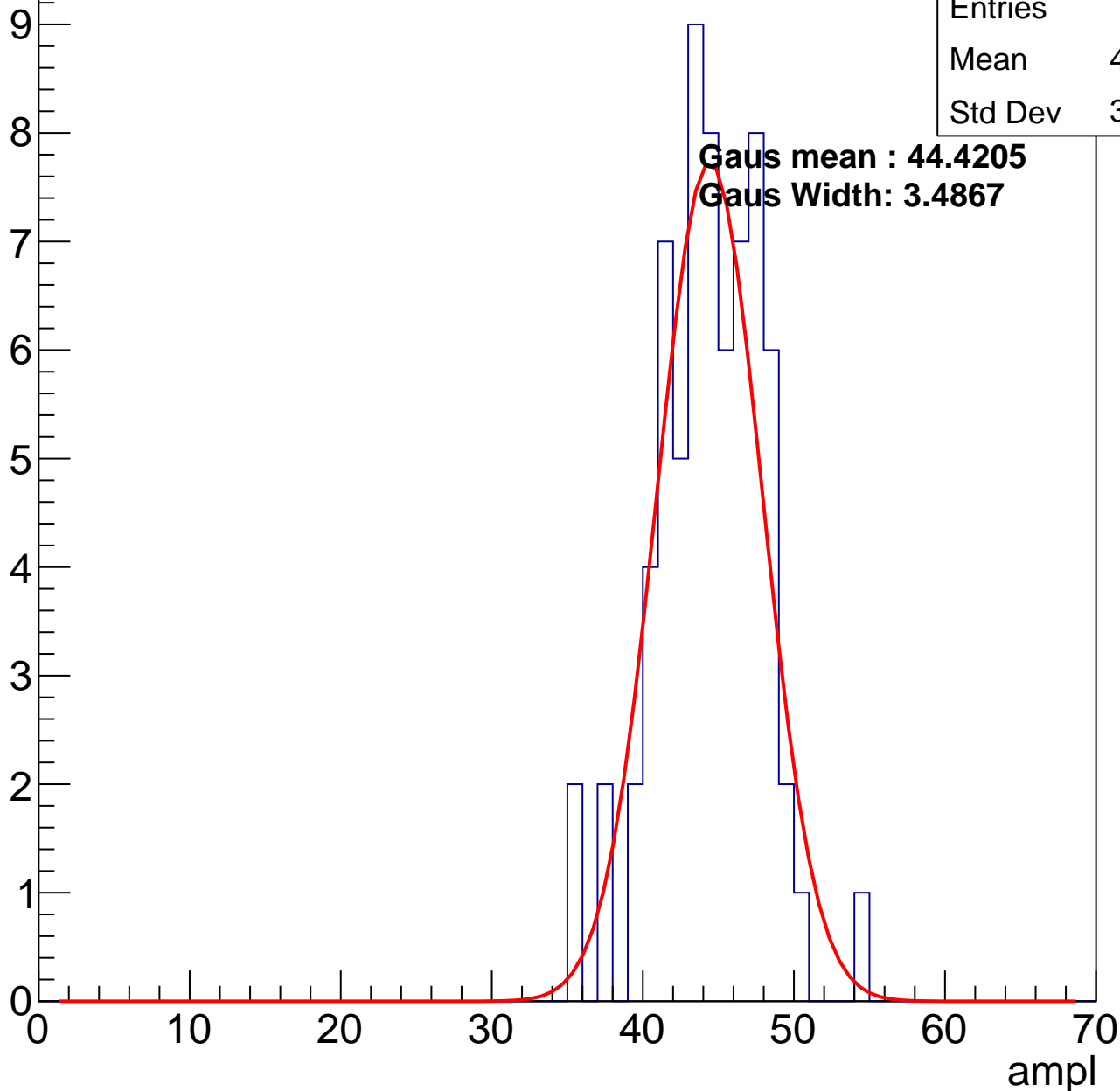
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.94
Std Dev	3.492

**Gaus mean : 44.4205**

**Gaus Width: 3.4867**

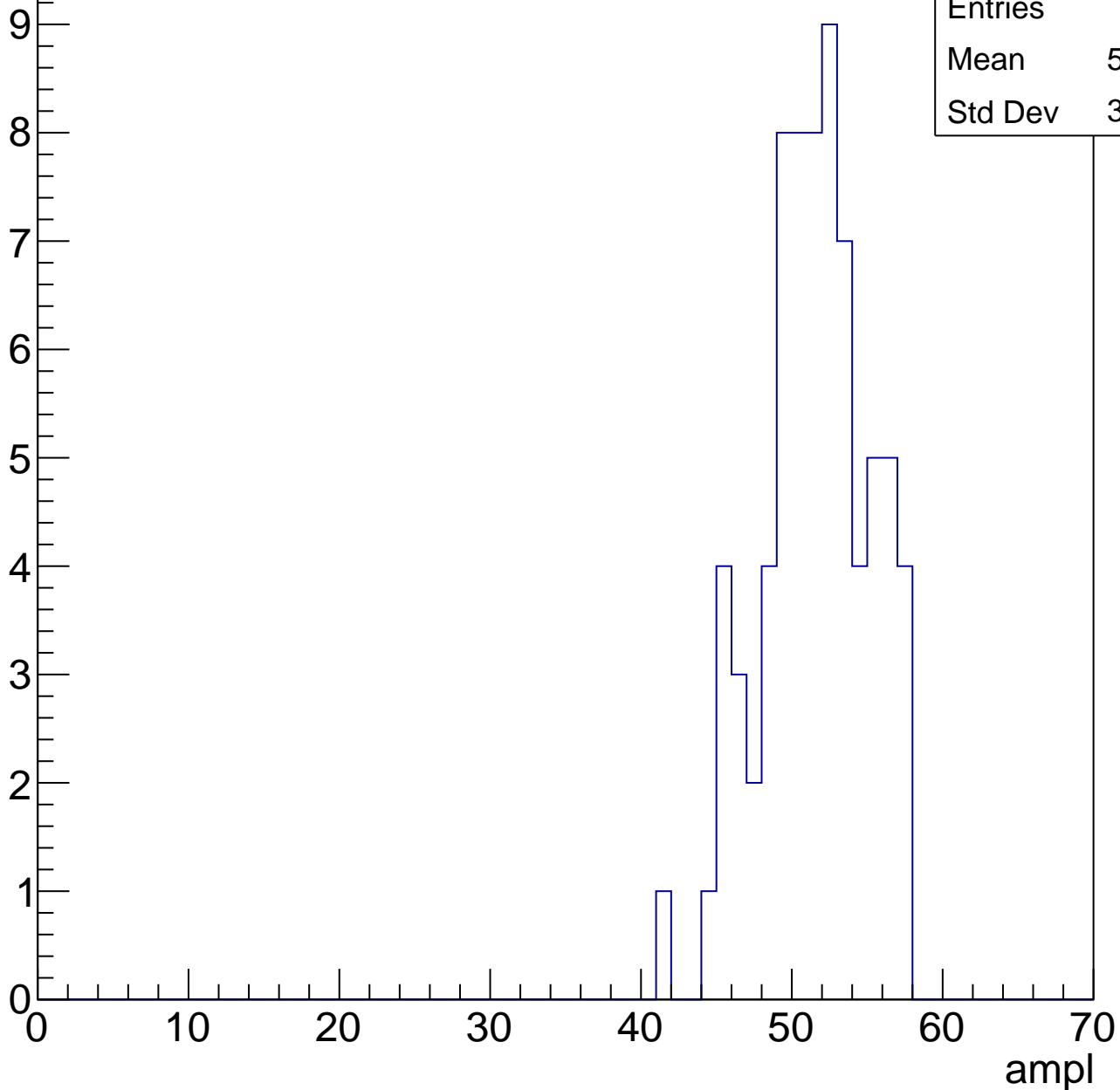


# B0L000S, U18-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	51.05
Std Dev	3.507

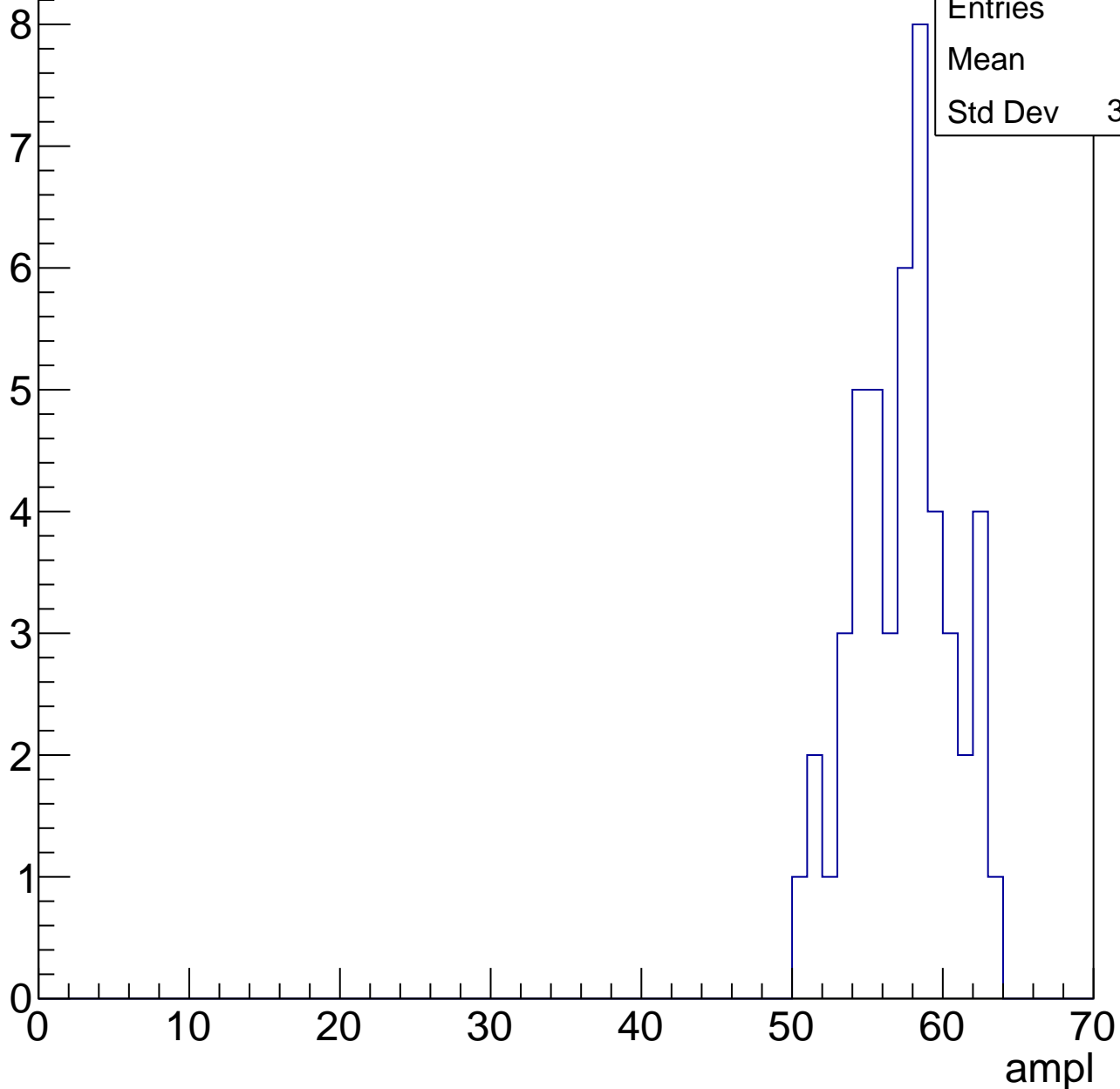


# B0L000S, U18-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	56.9
Std Dev	3.164

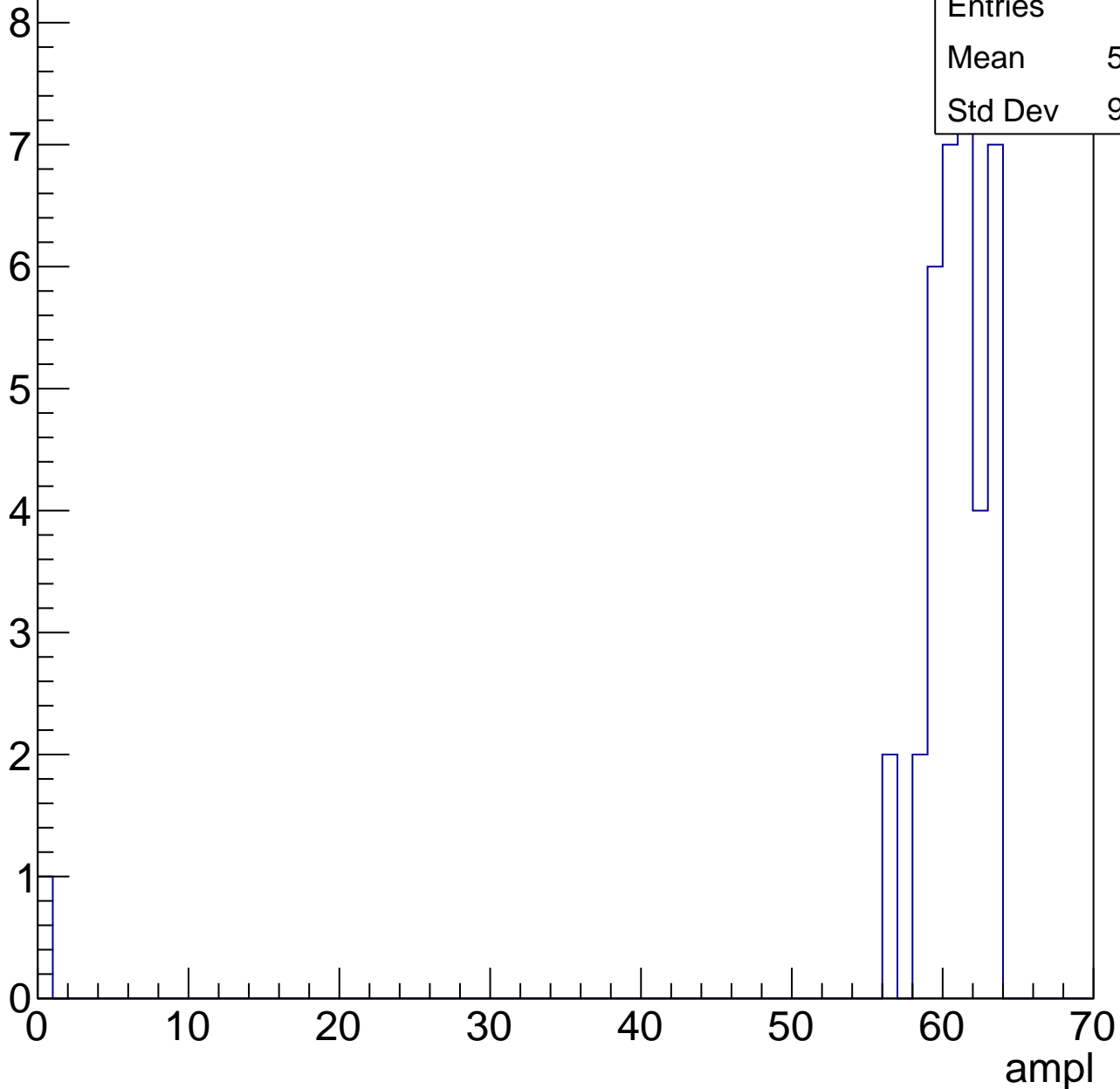


# B0L000S, U18-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.89
Std Dev	9.983



# B0L000S, U18-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch62, adc0

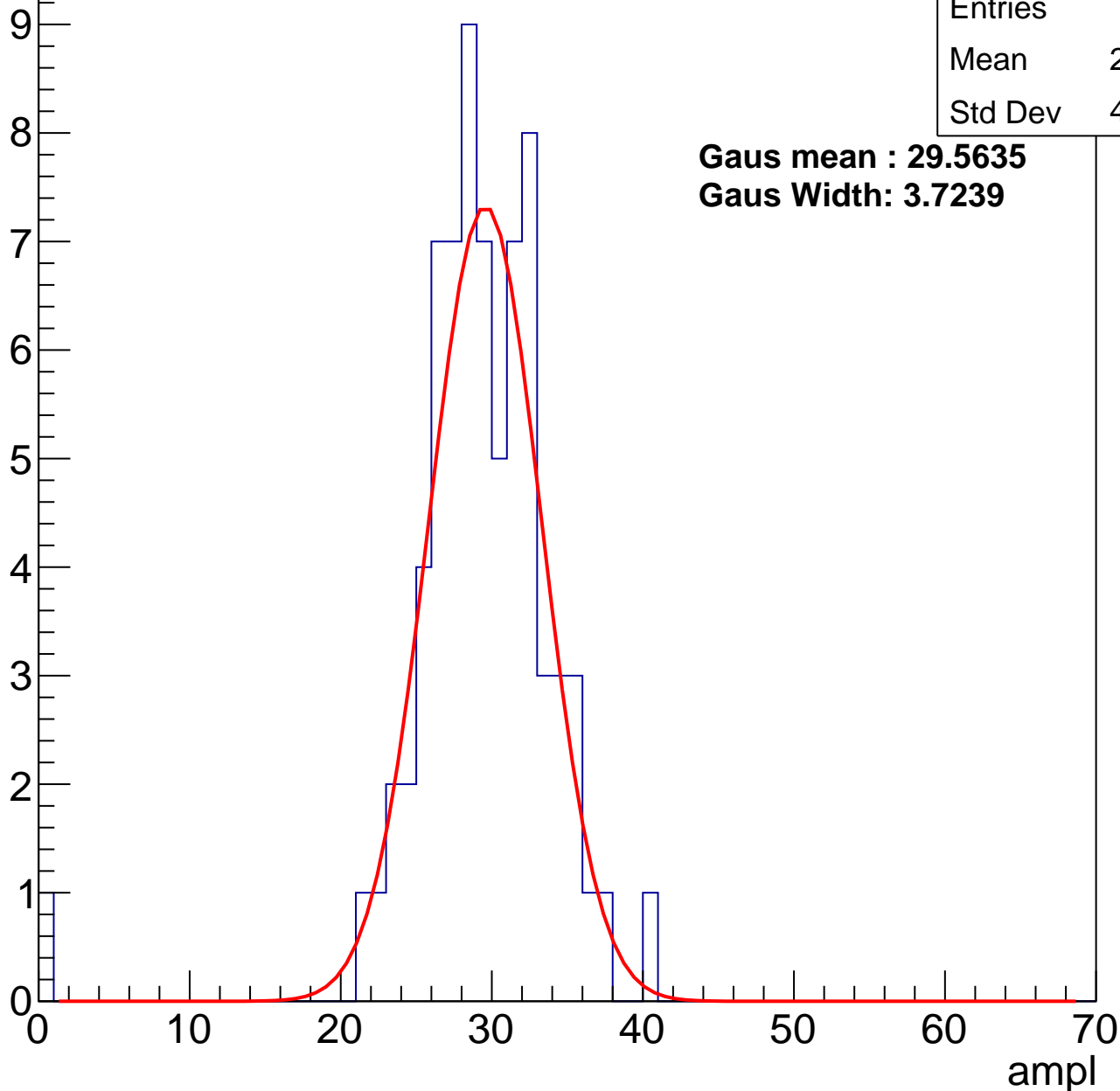
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	28.84
Std Dev	4.977

**Gaus mean : 29.5635**

**Gaus Width: 3.7239**



# B0L000S, U18-ch62, adc1

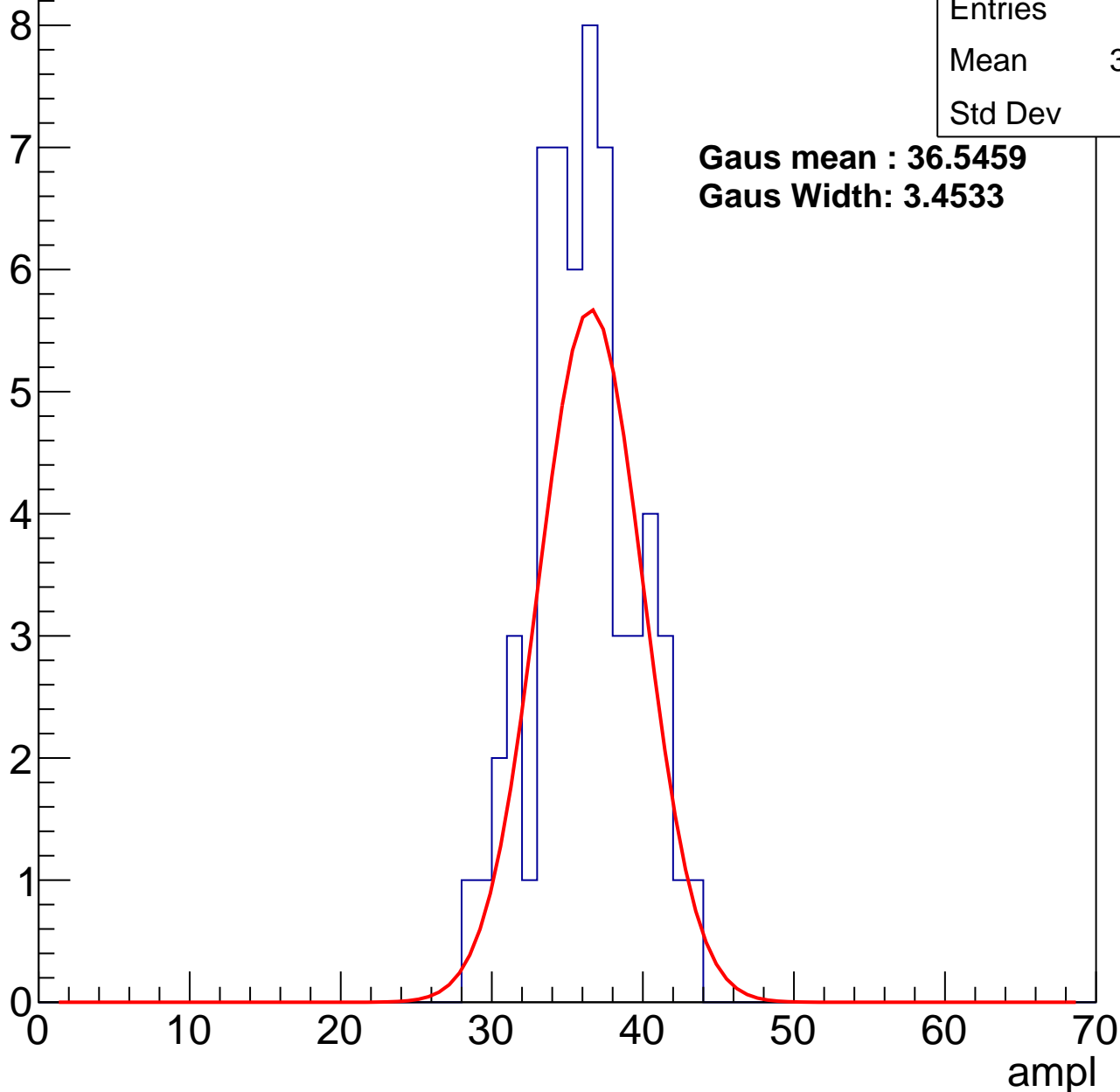
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.64
Std Dev	3.31

**Gaus mean : 36.5459**

**Gaus Width: 3.4533**



# B0L000S, U18-ch62, adc2

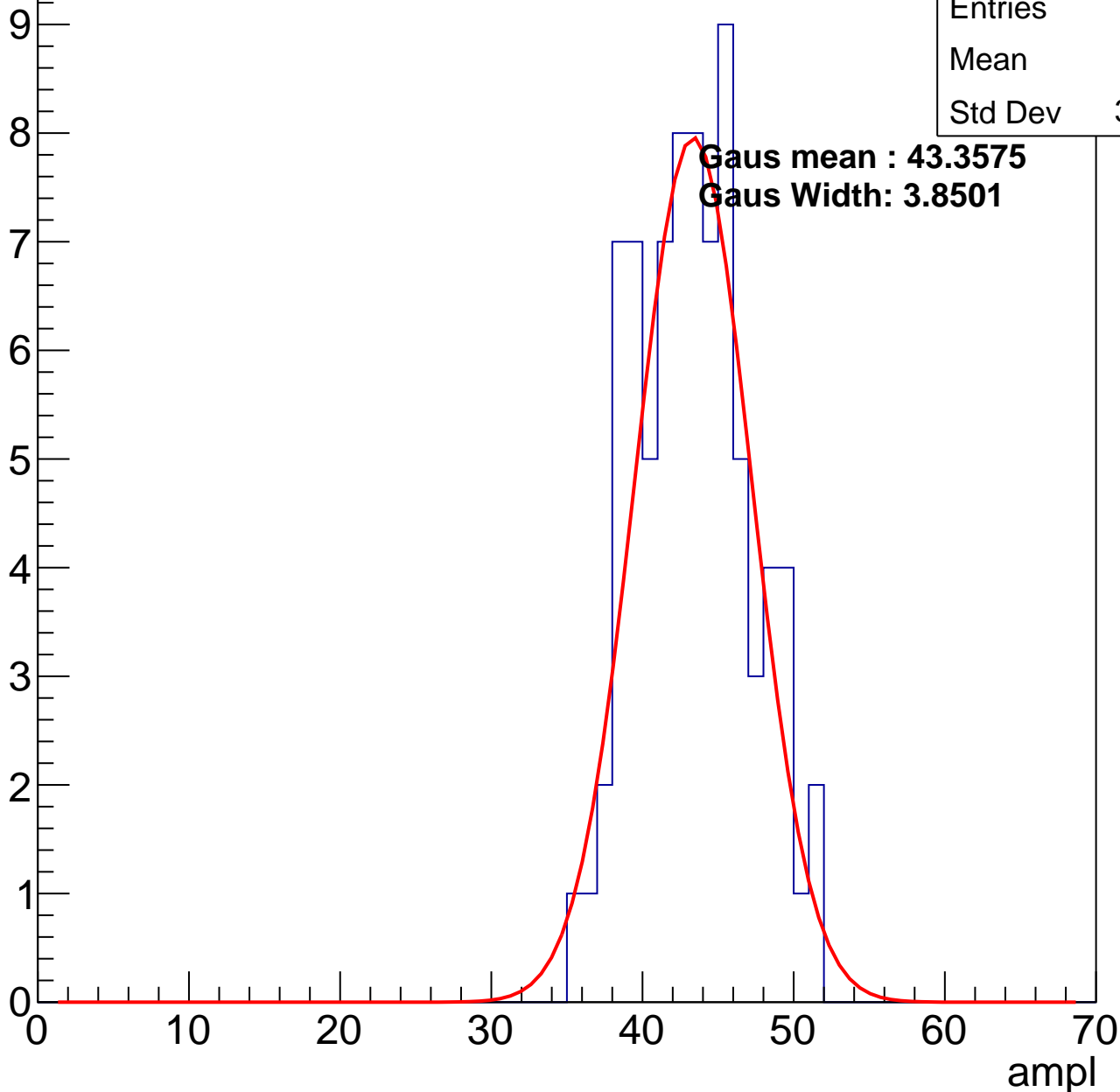
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	42.9
Std Dev	3.701

**Gaus mean : 43.3575**

**Gaus Width: 3.8501**

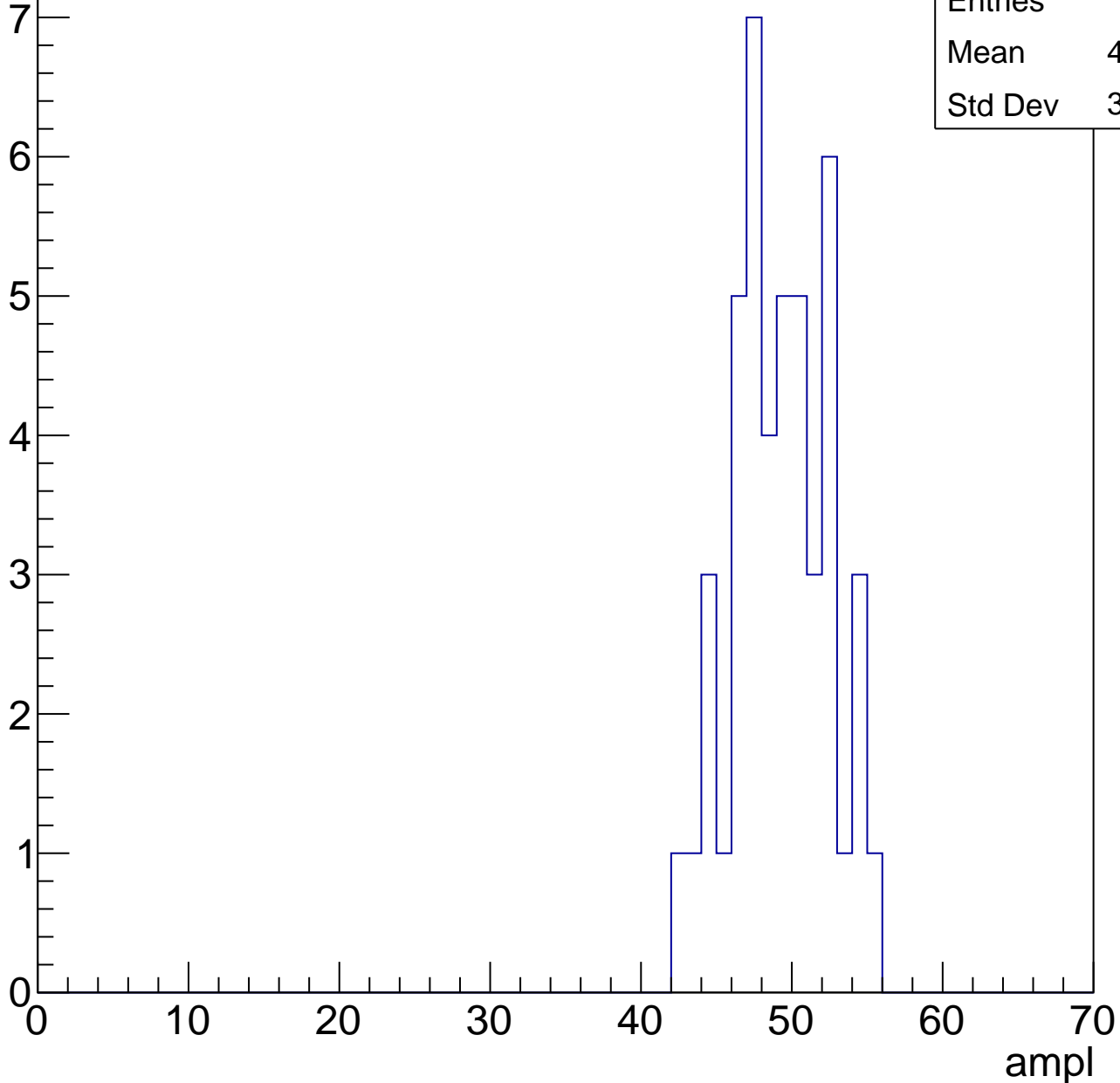


# B0L000S, U18-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	48.76
Std Dev	3.122

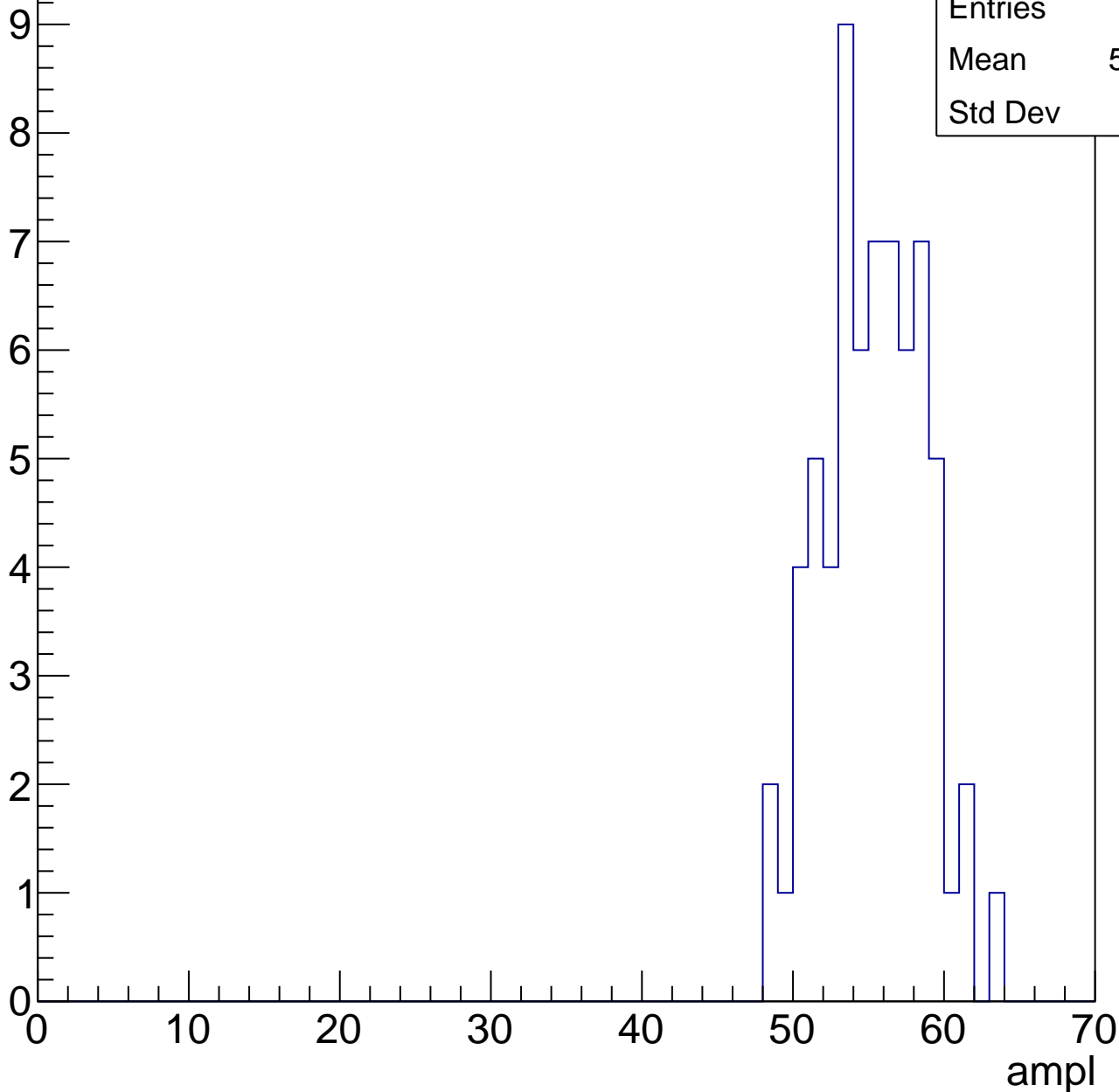


# B0L000S, U18-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

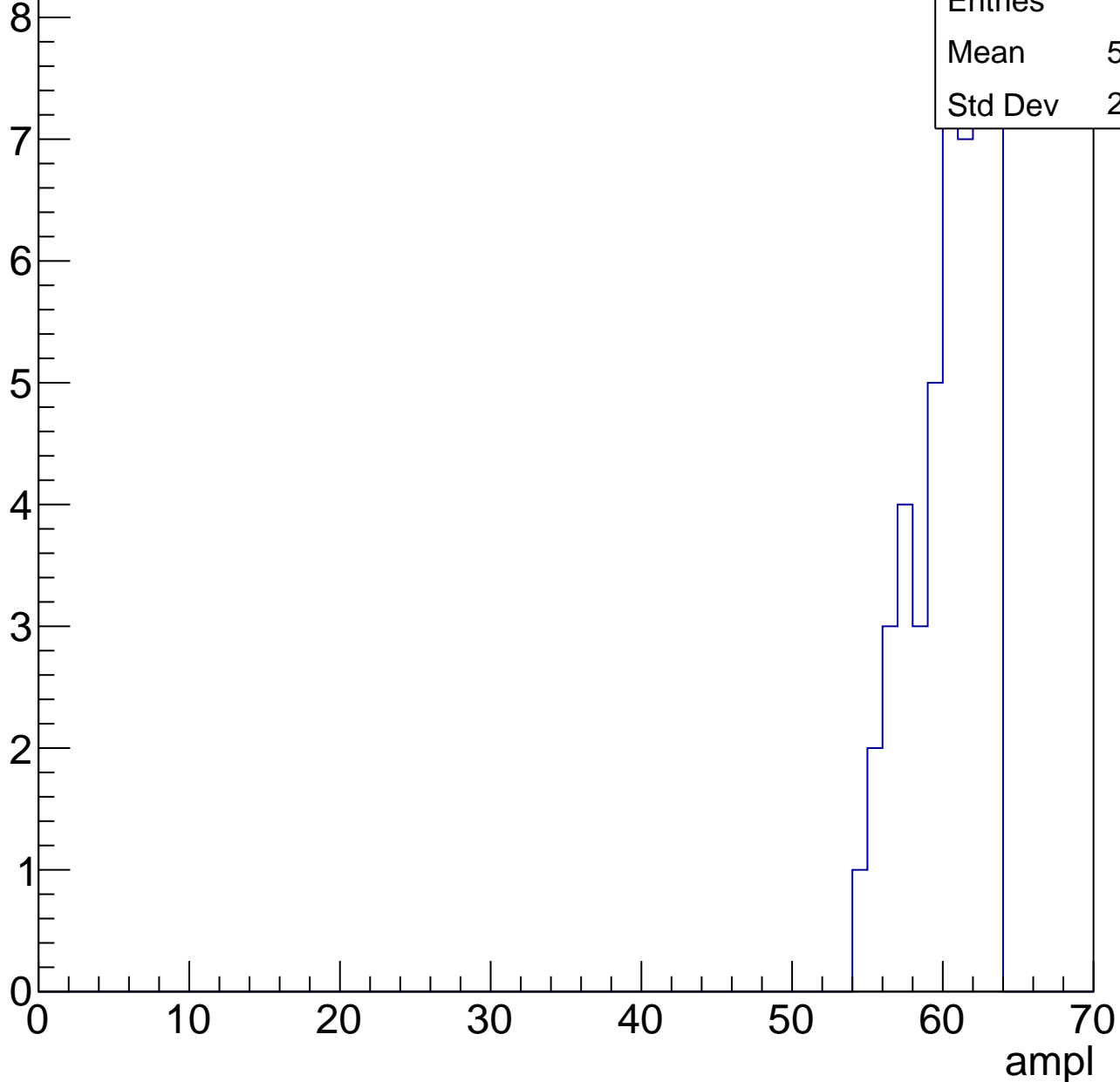
Entries	67
Mean	54.84
Std Dev	3.29



# B0L000S, U18-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

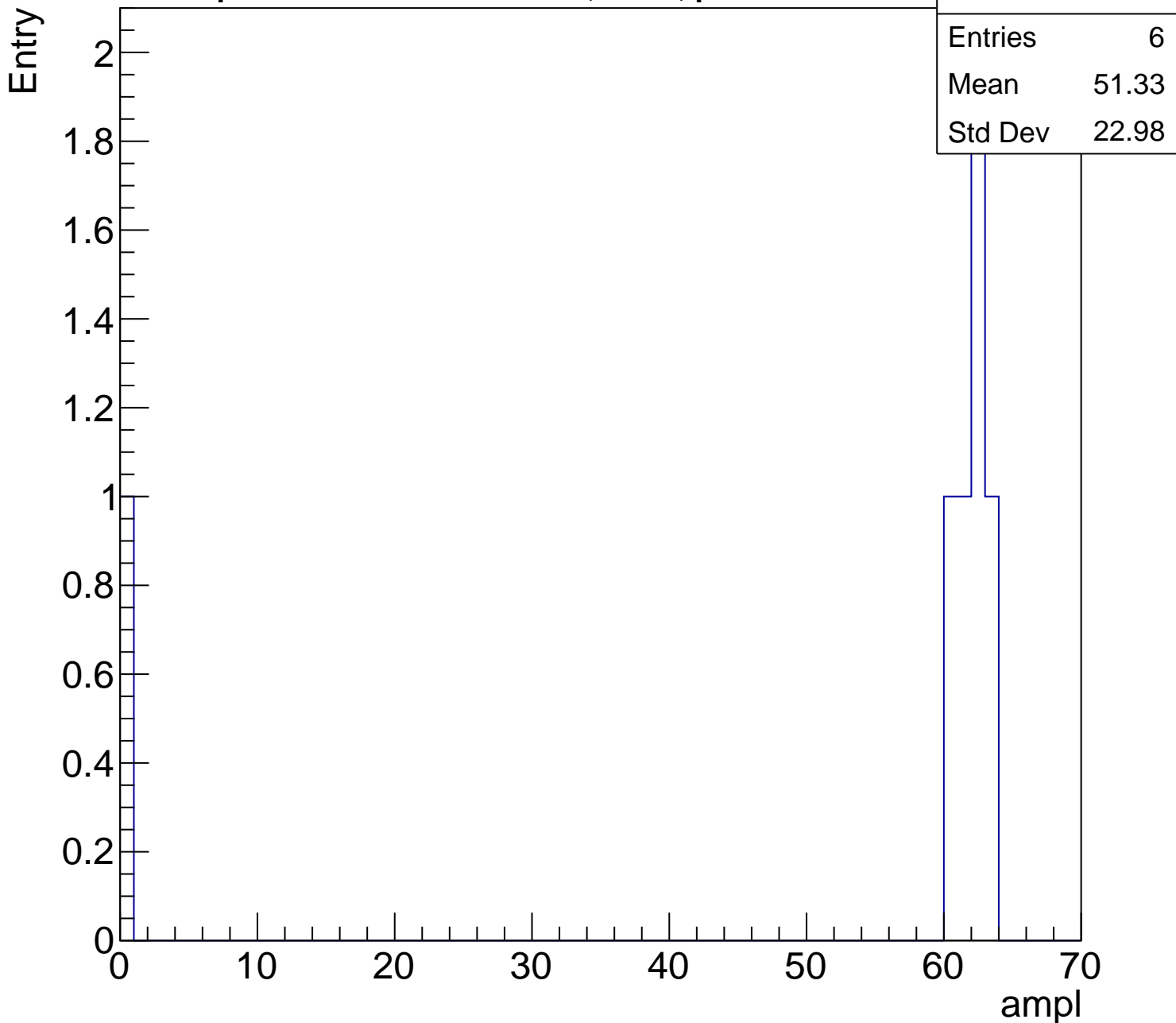
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	51.33
Std Dev	22.98

0 10 20 30 40 50 60 70

ampl





# B0L000S, U18-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch63, adc0

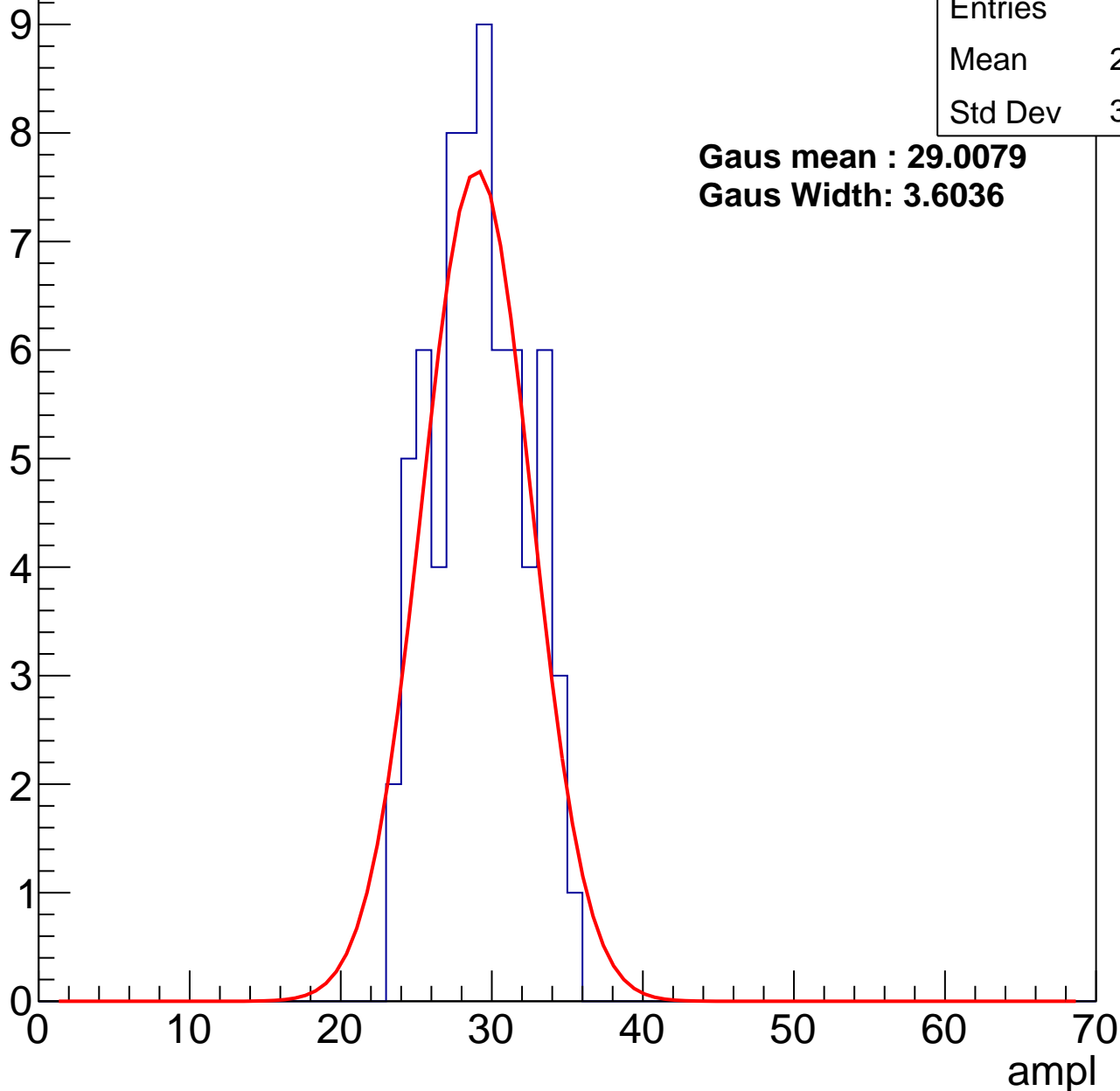
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28.68
Std Dev	3.056

**Gaus mean : 29.0079**

**Gaus Width: 3.6036**



# B0L000S, U18-ch63, adc1

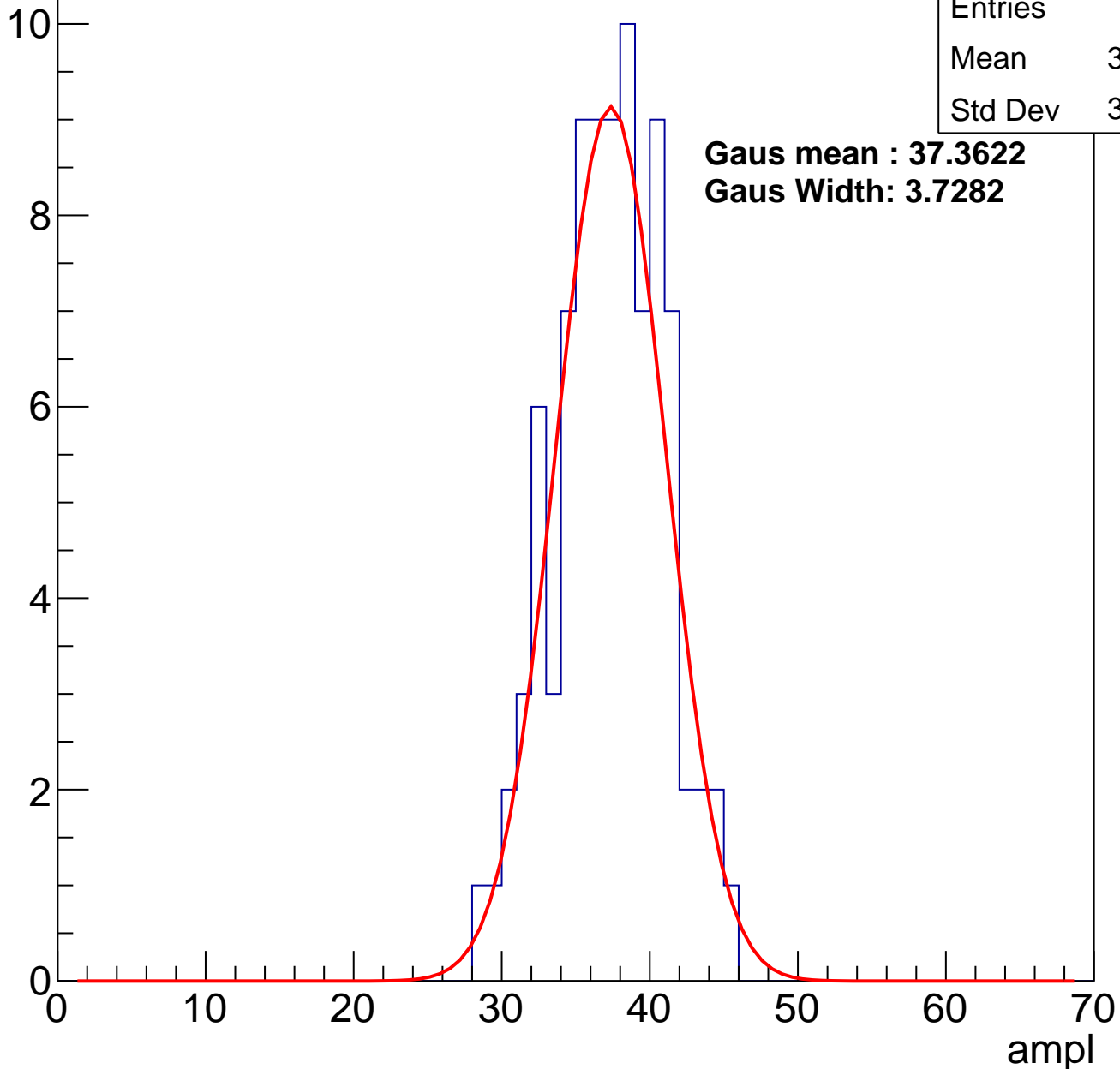
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	90
Mean	36.82
Std Dev	3.604

**Gaus mean : 37.3622**

**Gaus Width: 3.7282**

Entry



# B0L000S, U18-ch63, adc2

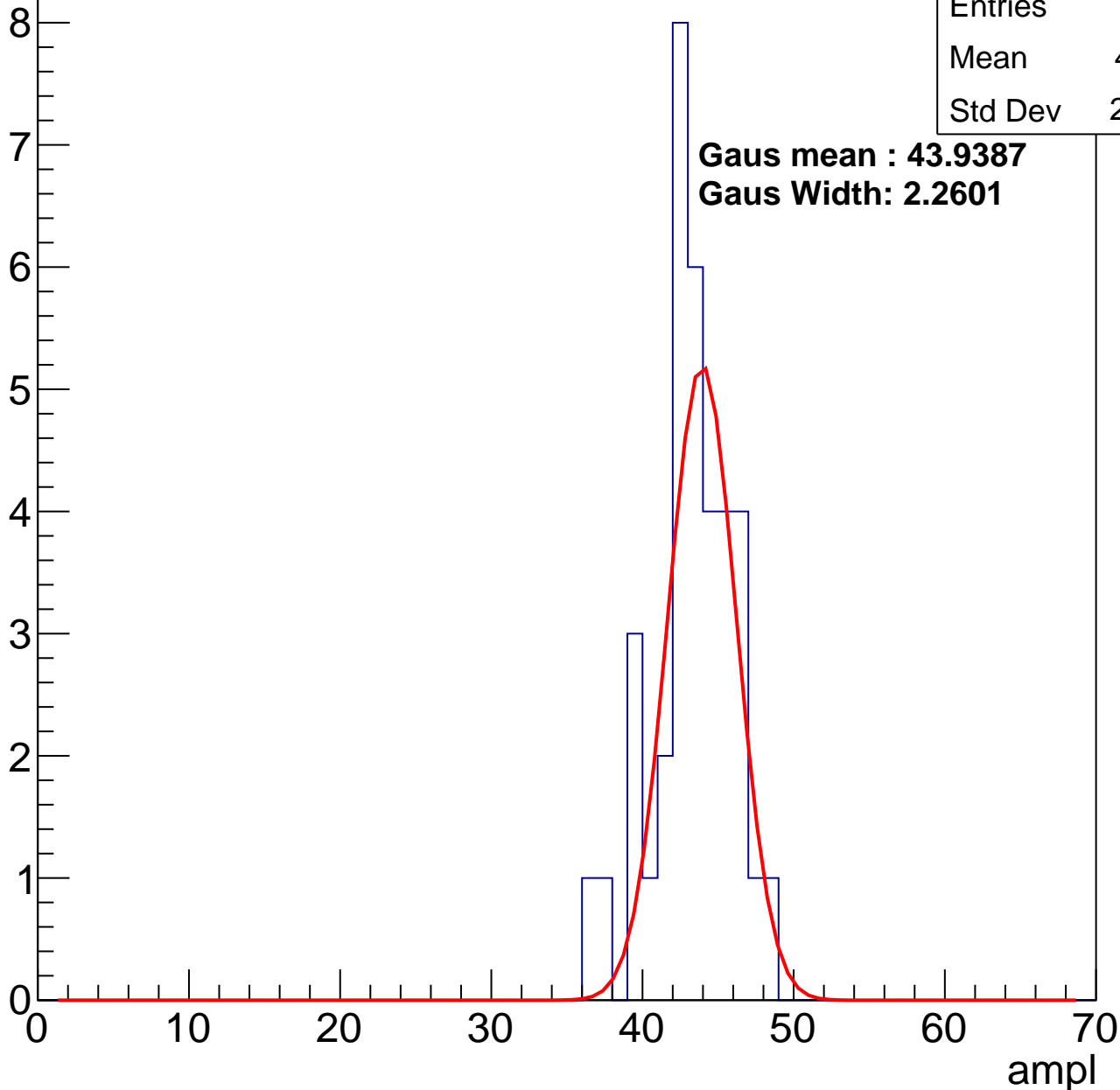
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	42.81
Std Dev	2.654

**Gaus mean : 43.9387**

**Gaus Width: 2.2601**



# B0L000S, U18-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

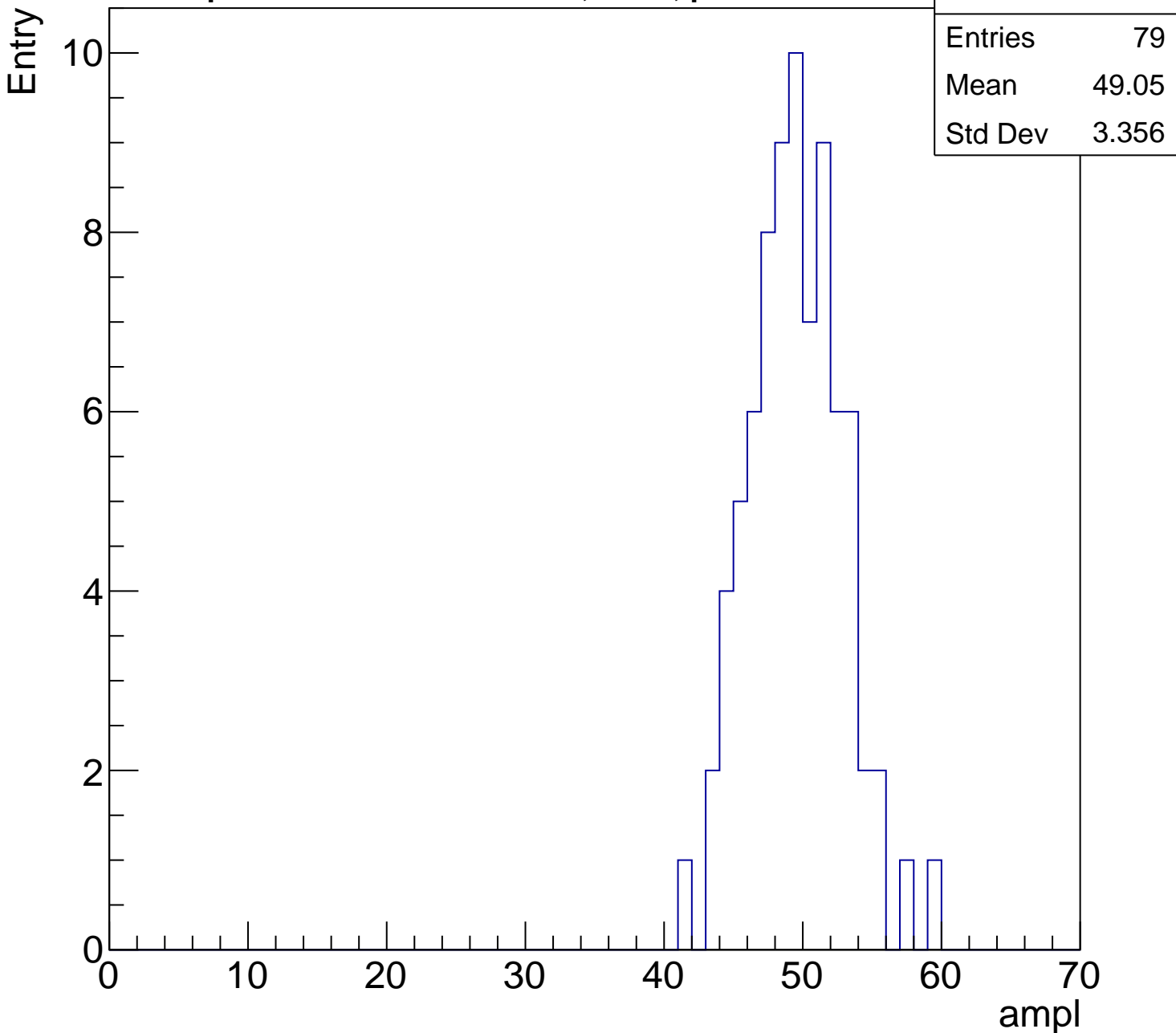
Entries	79
Mean	49.05
Std Dev	3.356

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	56
Mean	55.64
Std Dev	3.193

Entry

10

8

6

4

2

0

0

10

20

30

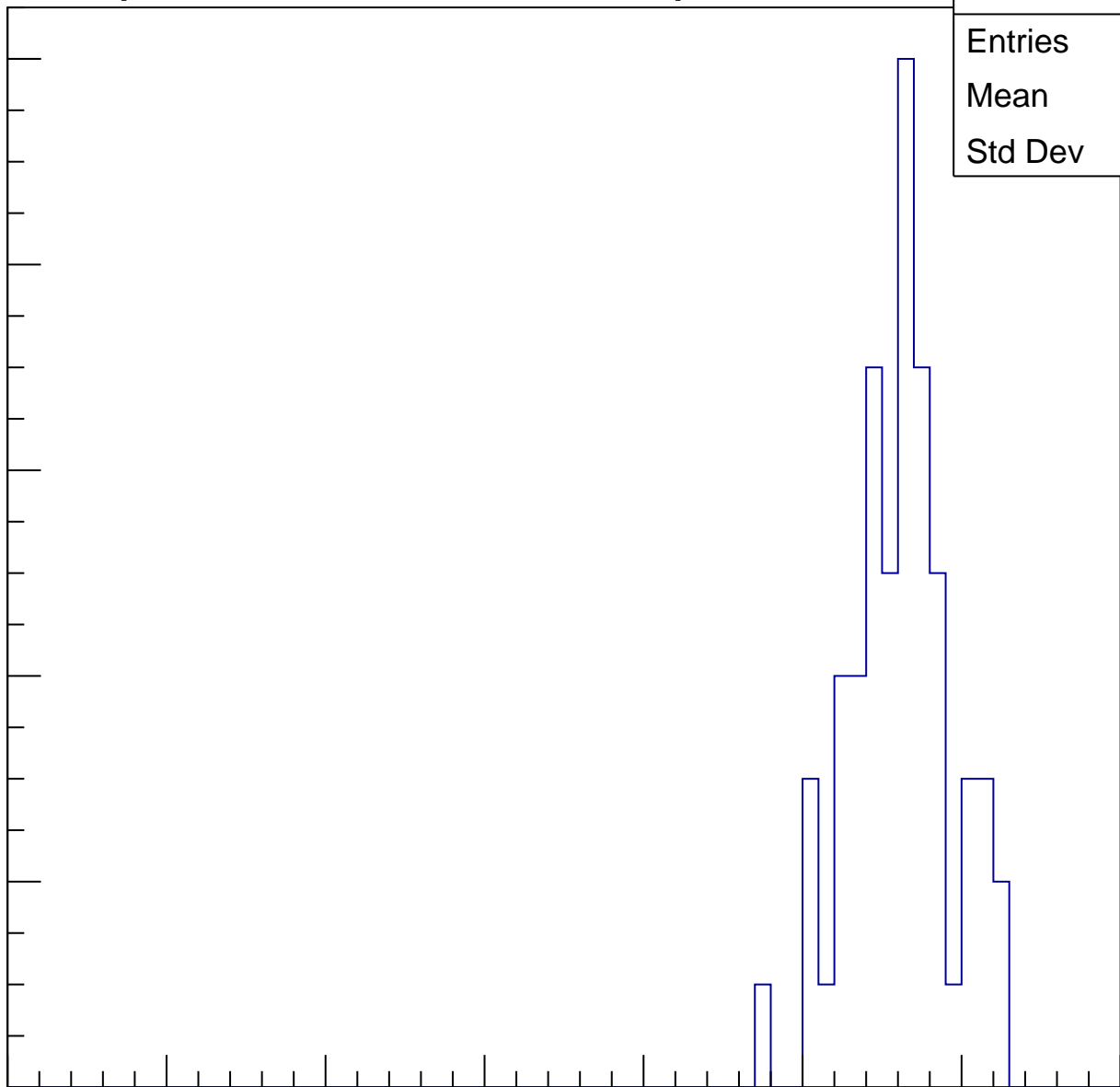
40

50

60

70

ampl

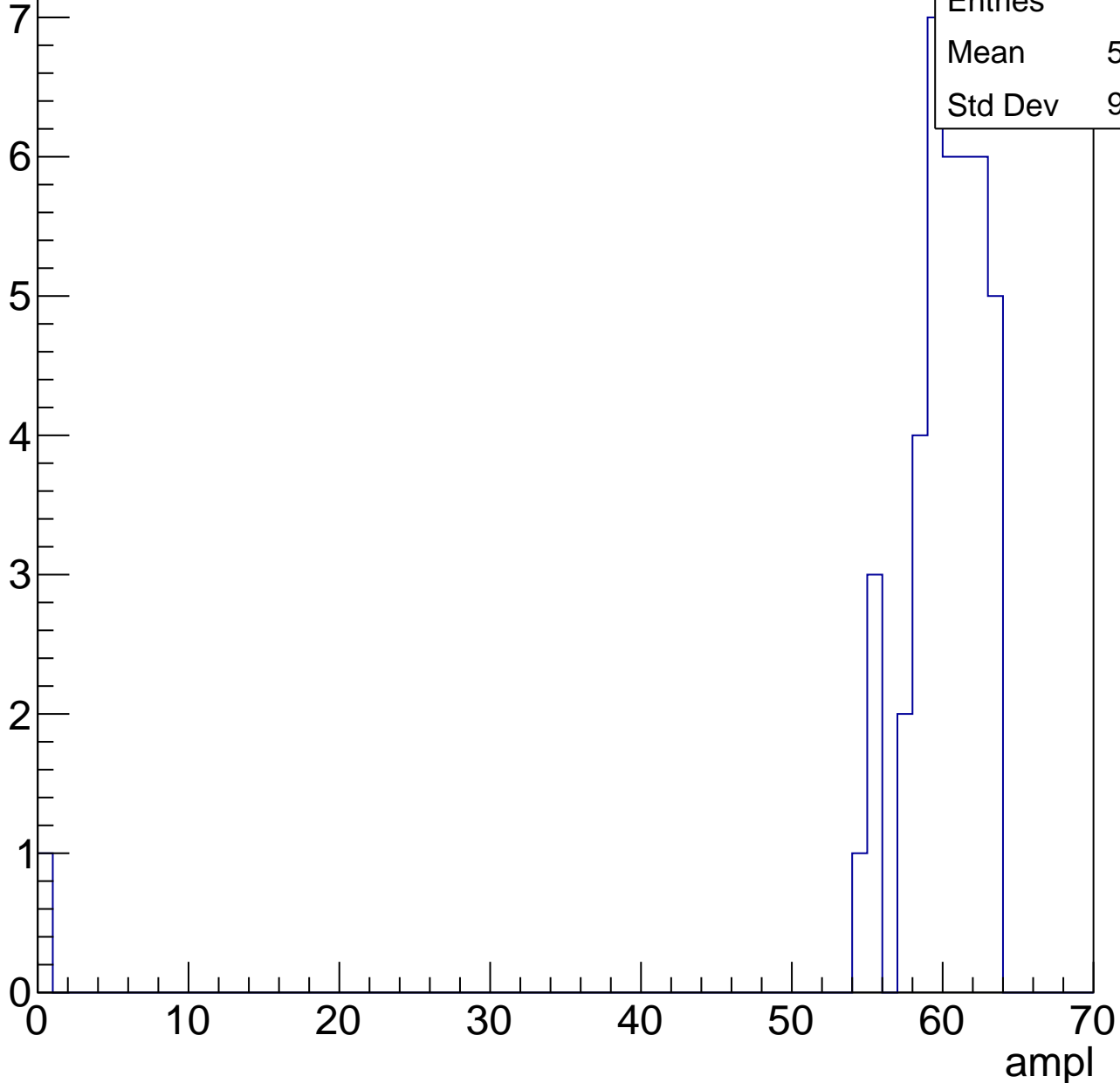


# B0L000S, U18-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.32
Std Dev	9.514

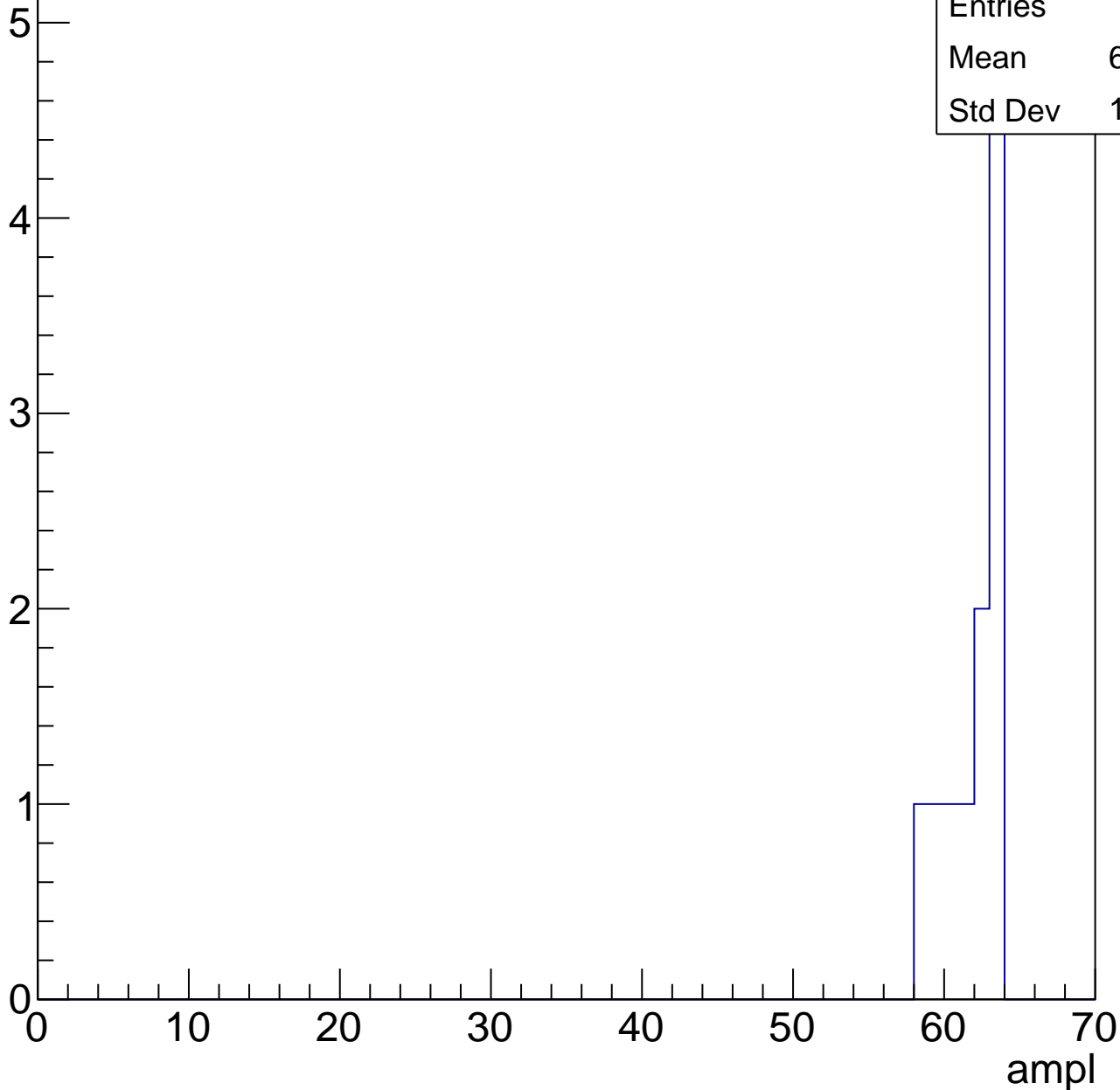


# B0L000S, U18-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	61.55
Std Dev	1.725





# B0L000S, U18-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

# B0L000S, U18-ch64, adc0

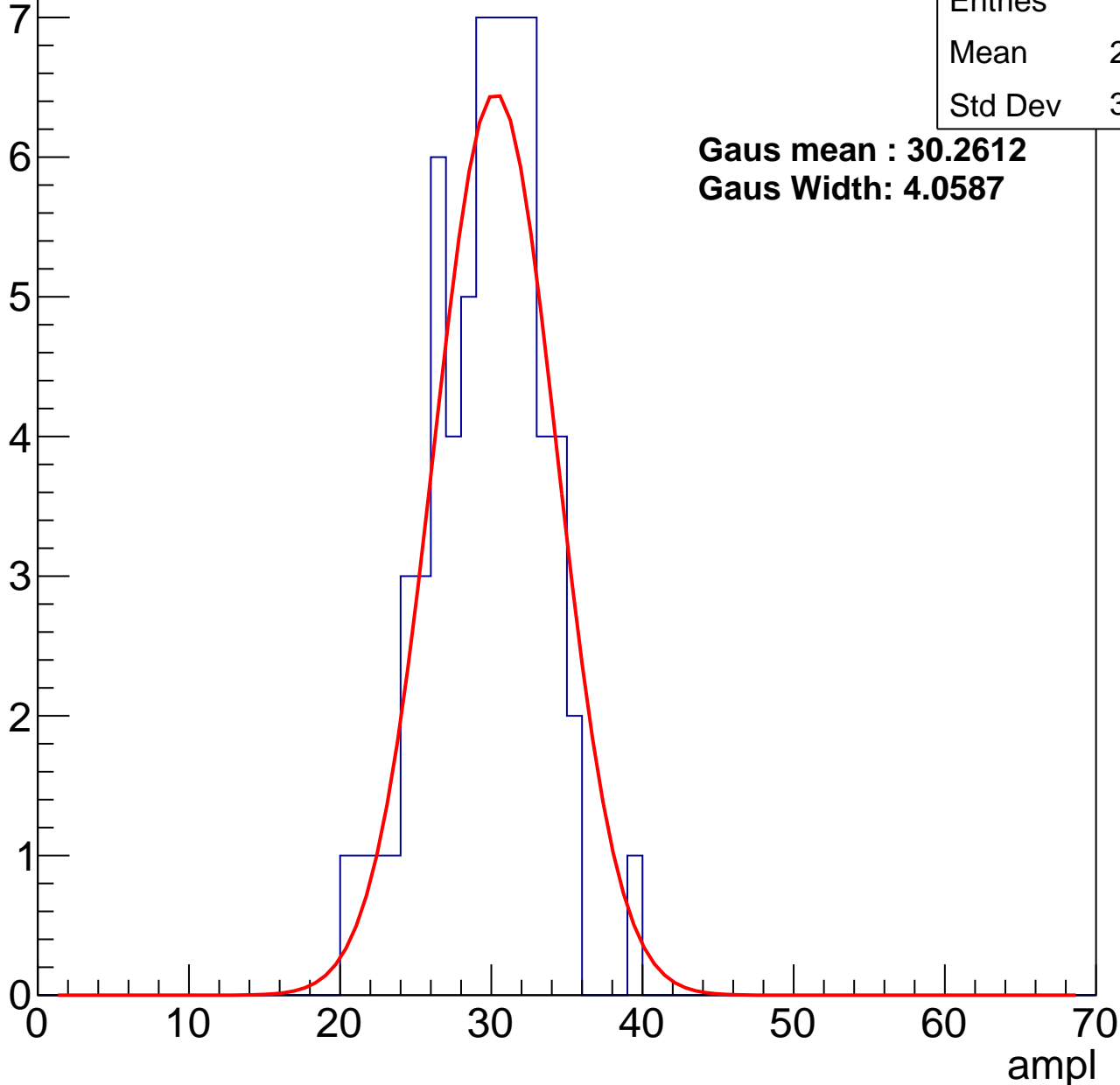
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.19
Std Dev	3.665

**Gaus mean : 30.2612**

**Gaus Width: 4.0587**



# B0L000S, U18-ch64, adc1

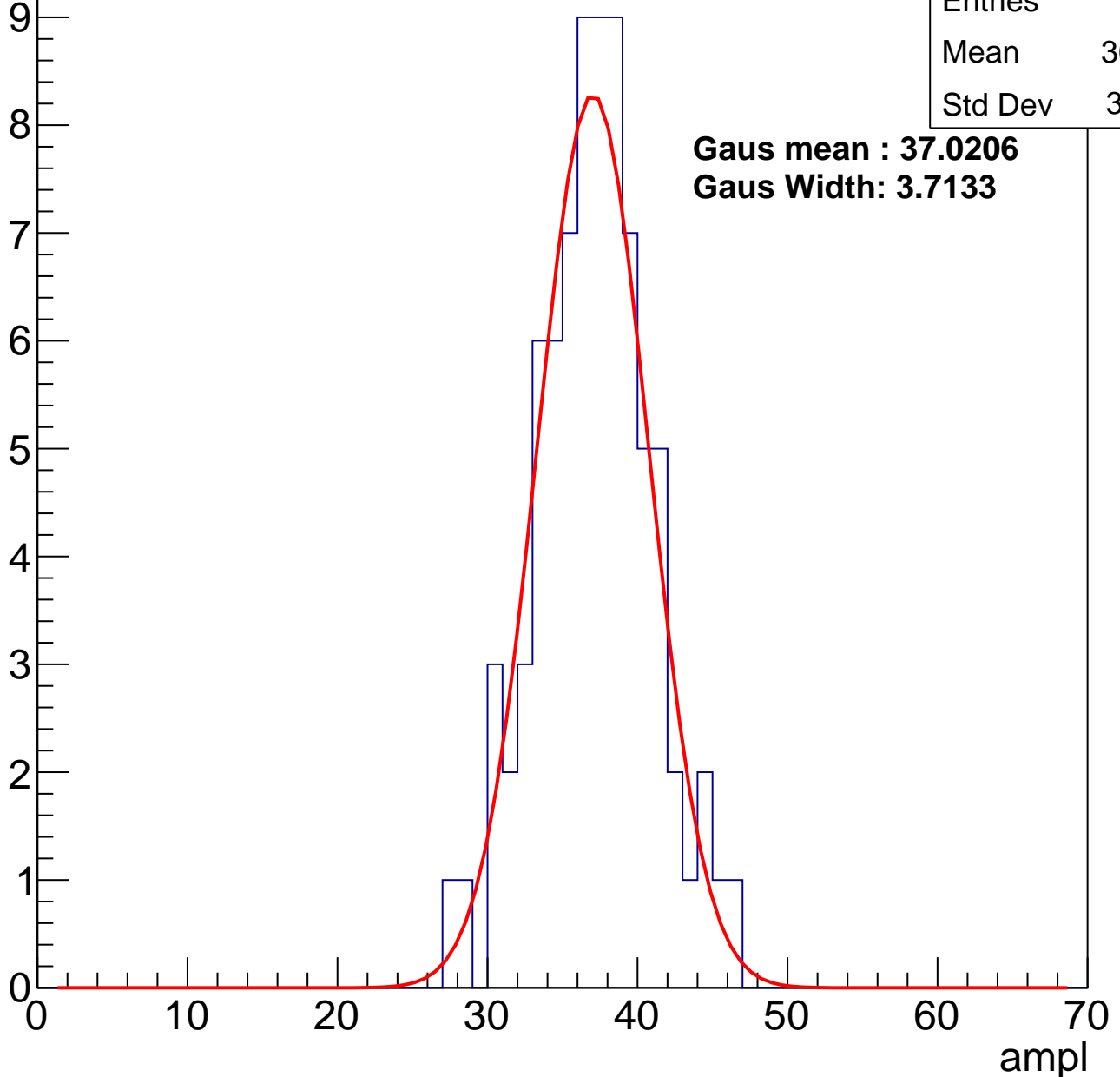
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	36.66
Std Dev	3.781

**Gaus mean : 37.0206**

**Gaus Width: 3.7133**



# B0L000S, U18-ch64, adc2

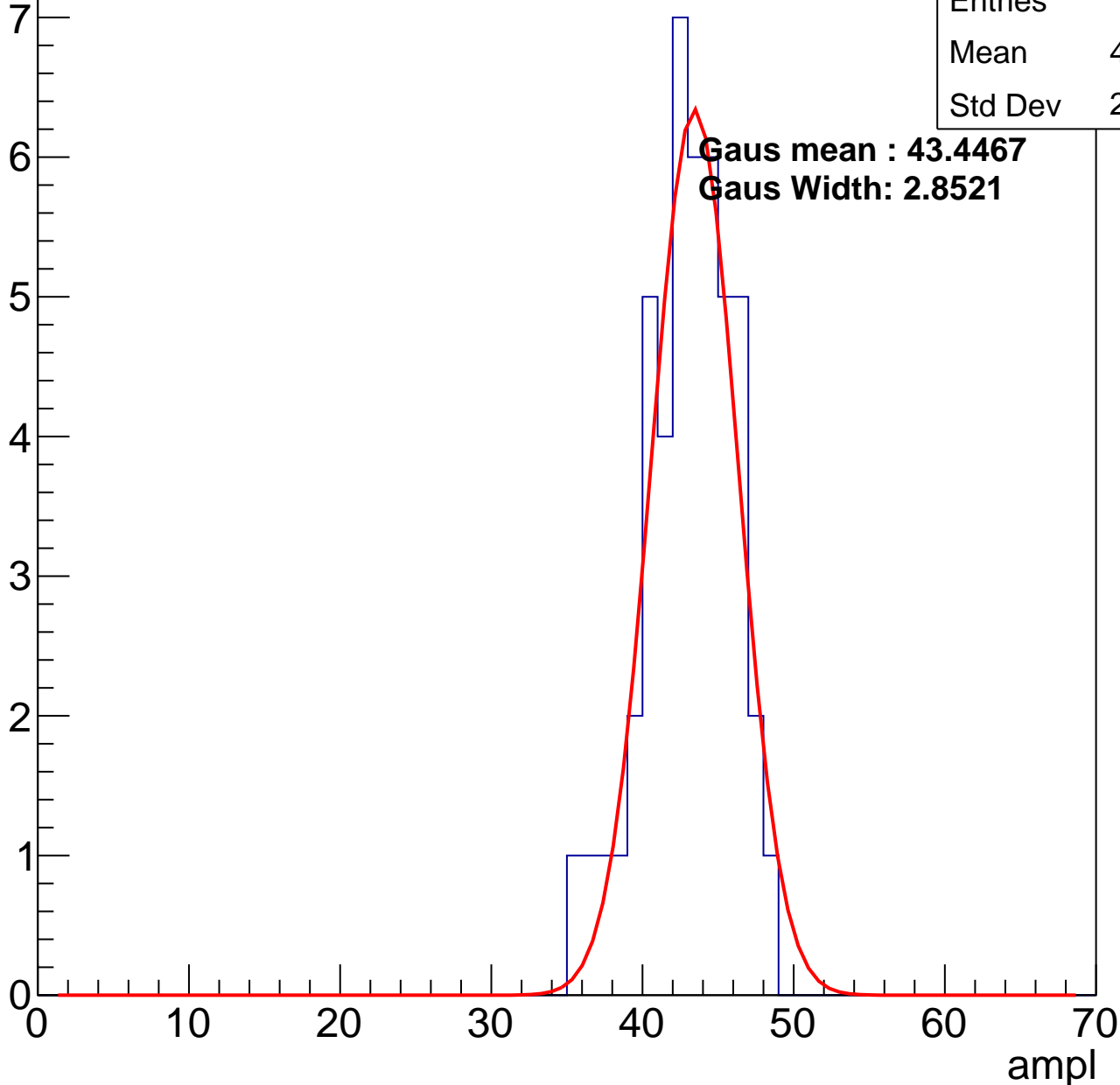
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	42.57
Std Dev	2.893

**Gaus mean : 43.4467**

**Gaus Width: 2.8521**

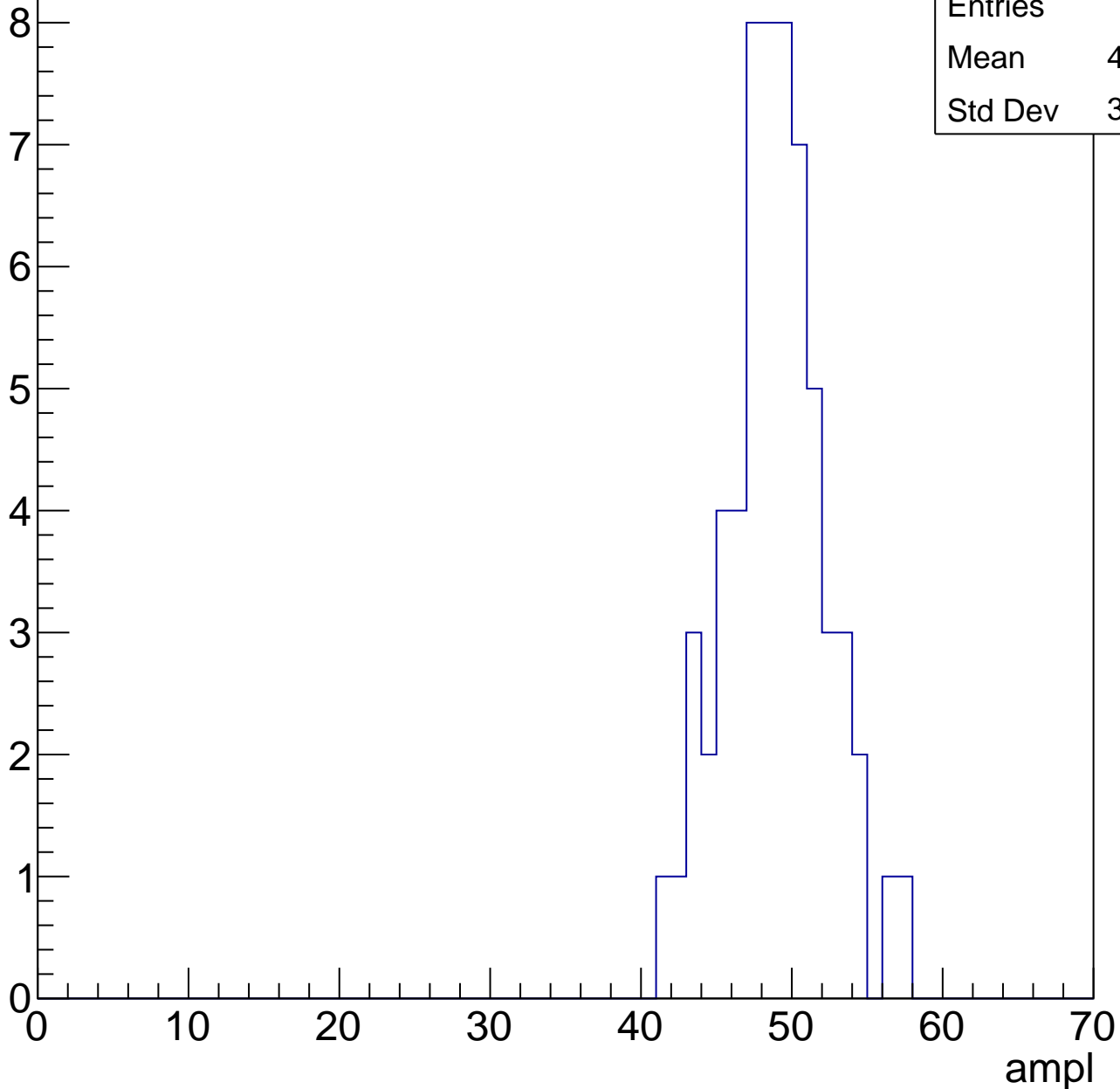


# B0L000S, U18-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	48.48
Std Dev	3.287

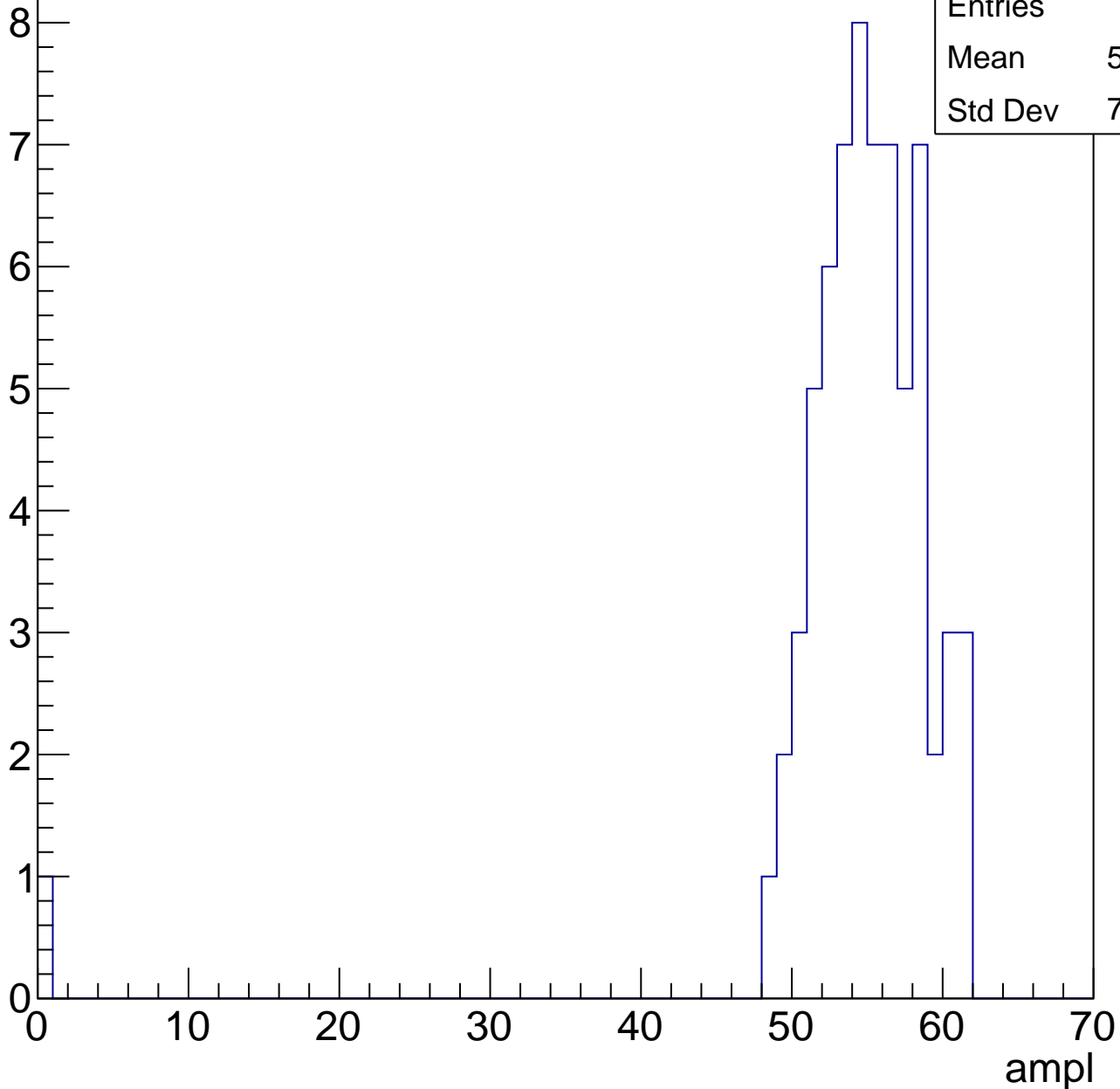


# B0L000S, U18-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

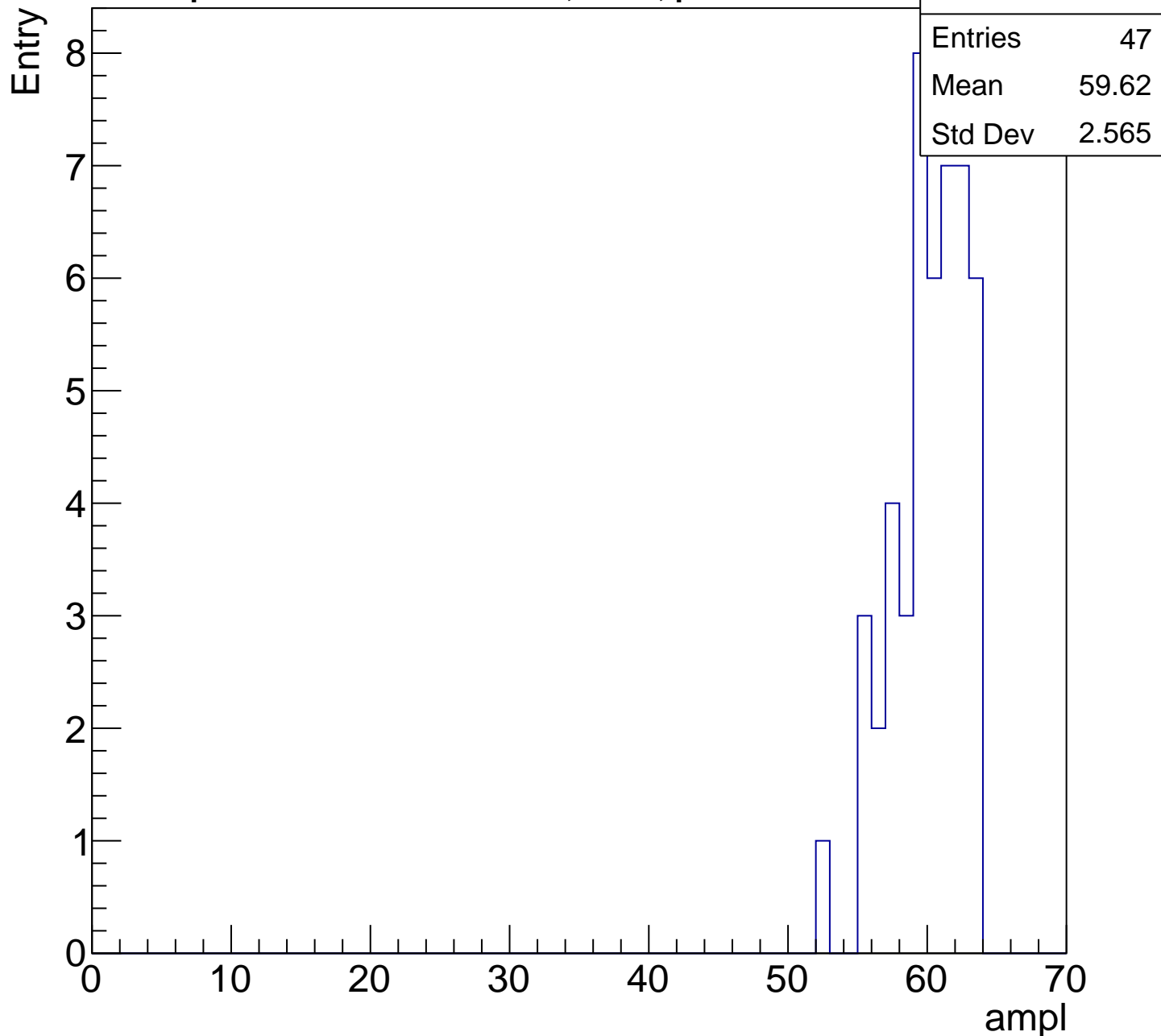
Entry

Entries	67
Mean	53.96
Std Dev	7.353



# B0L000S, U18-ch64, adc5

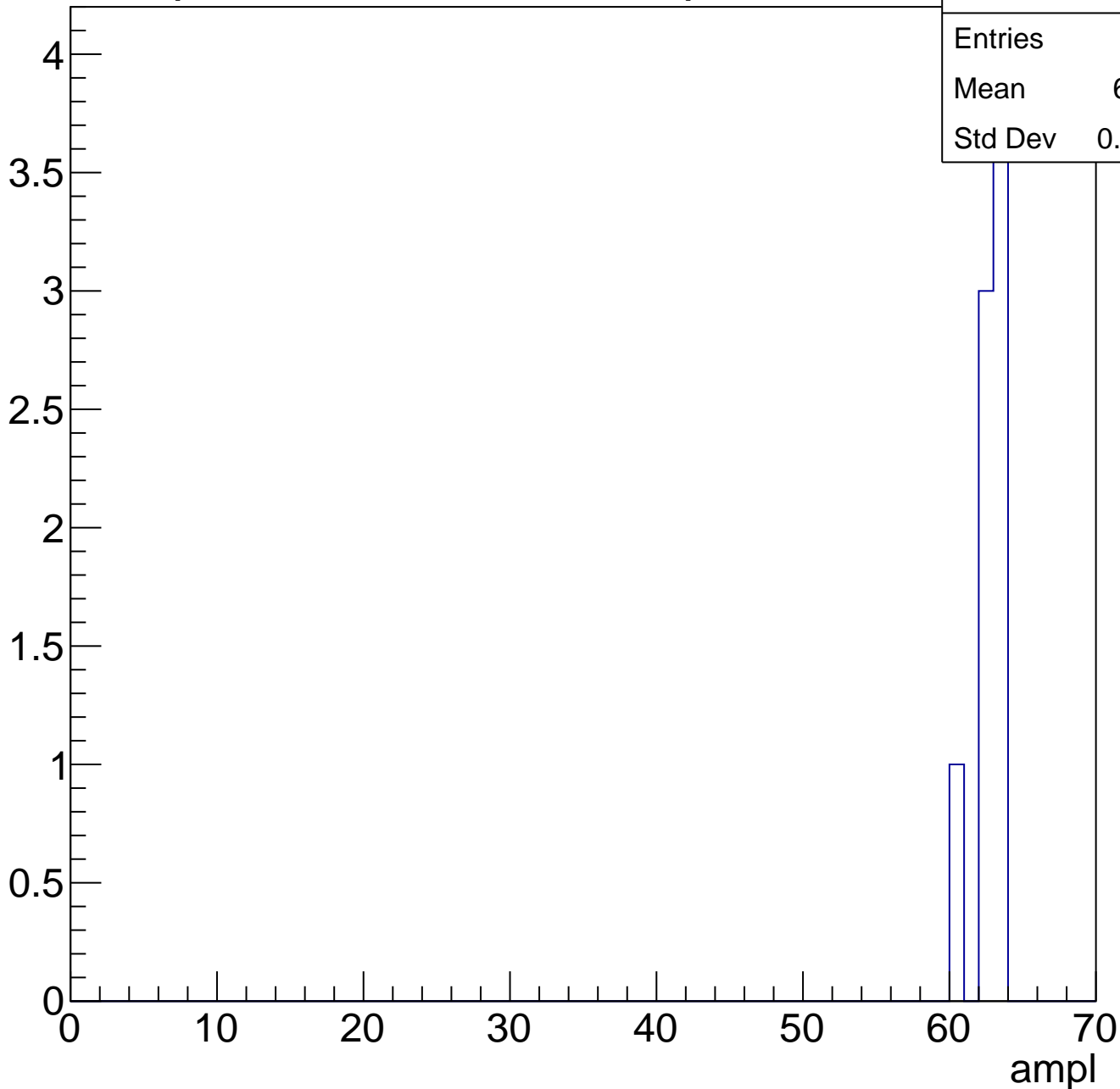
calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U18-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch65, adc0

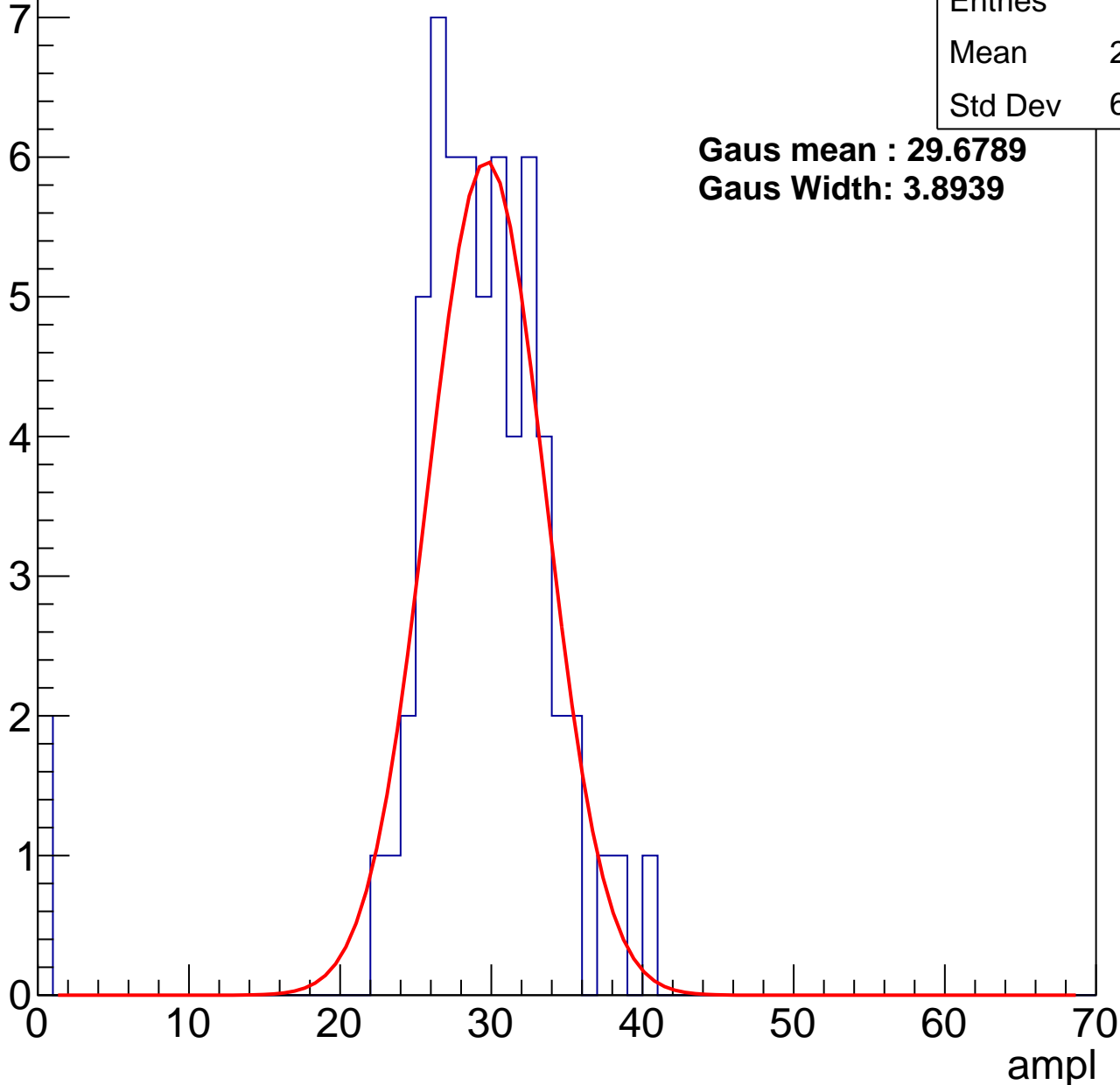
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.32
Std Dev	6.337

**Gaus mean : 29.6789**

**Gaus Width: 3.8939**



# B0L000S, U18-ch65, adc1

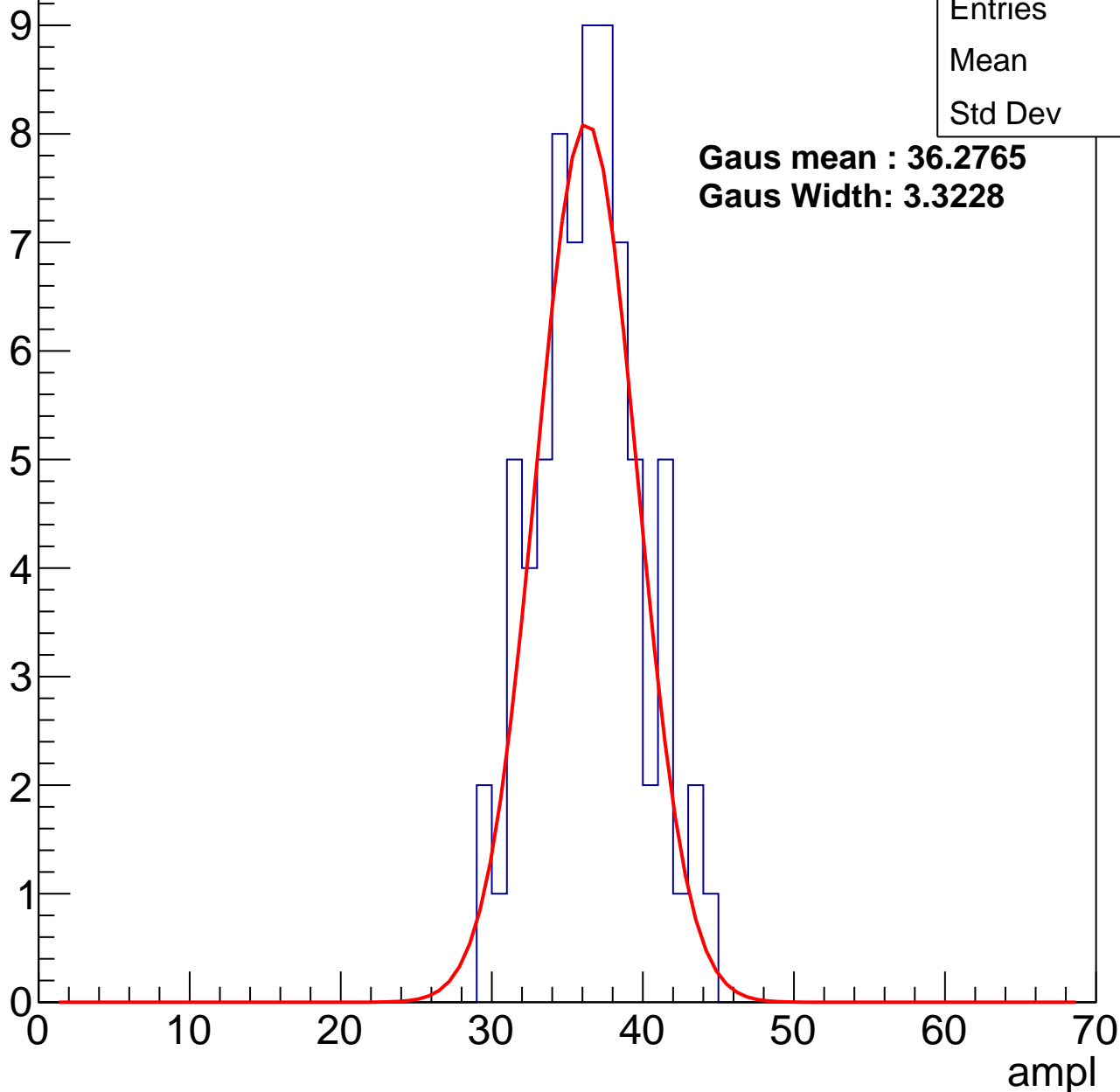
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36
Std Dev	3.4

**Gaus mean : 36.2765**

**Gaus Width: 3.3228**



# B0L000S, U18-ch65, adc2

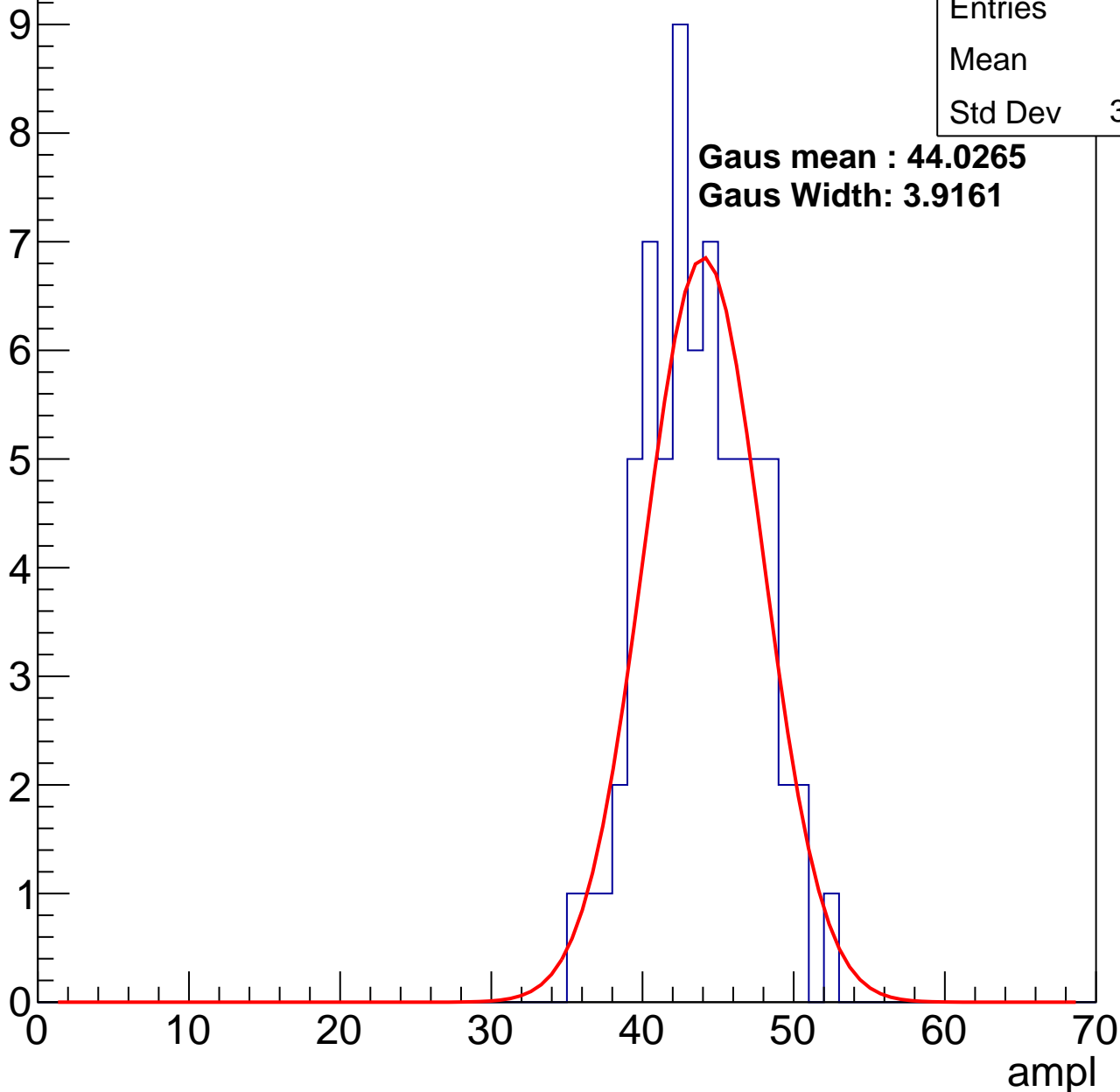
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.3
Std Dev	3.597

**Gaus mean : 44.0265**

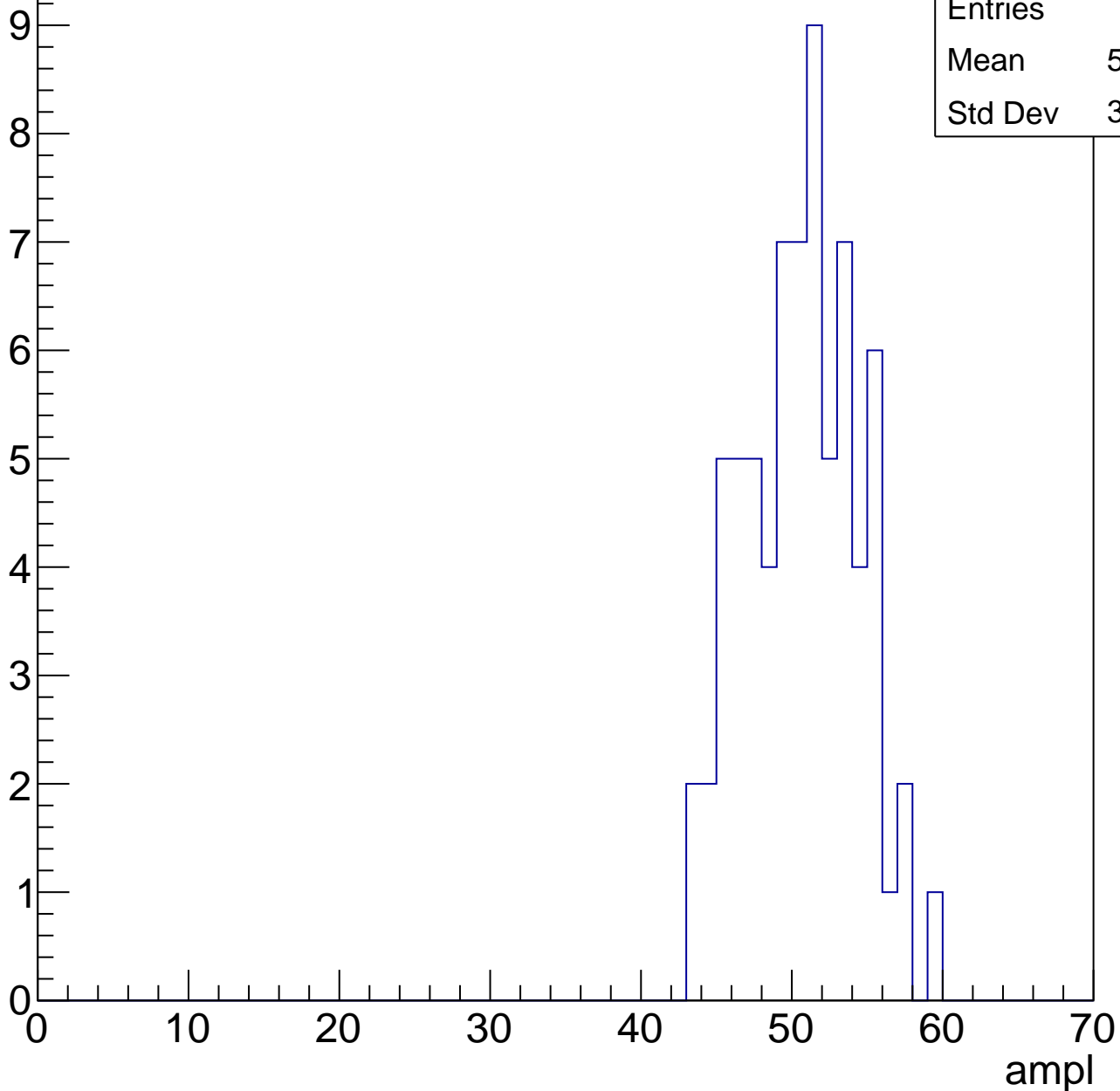
**Gaus Width: 3.9161**



# B0L000S, U18-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

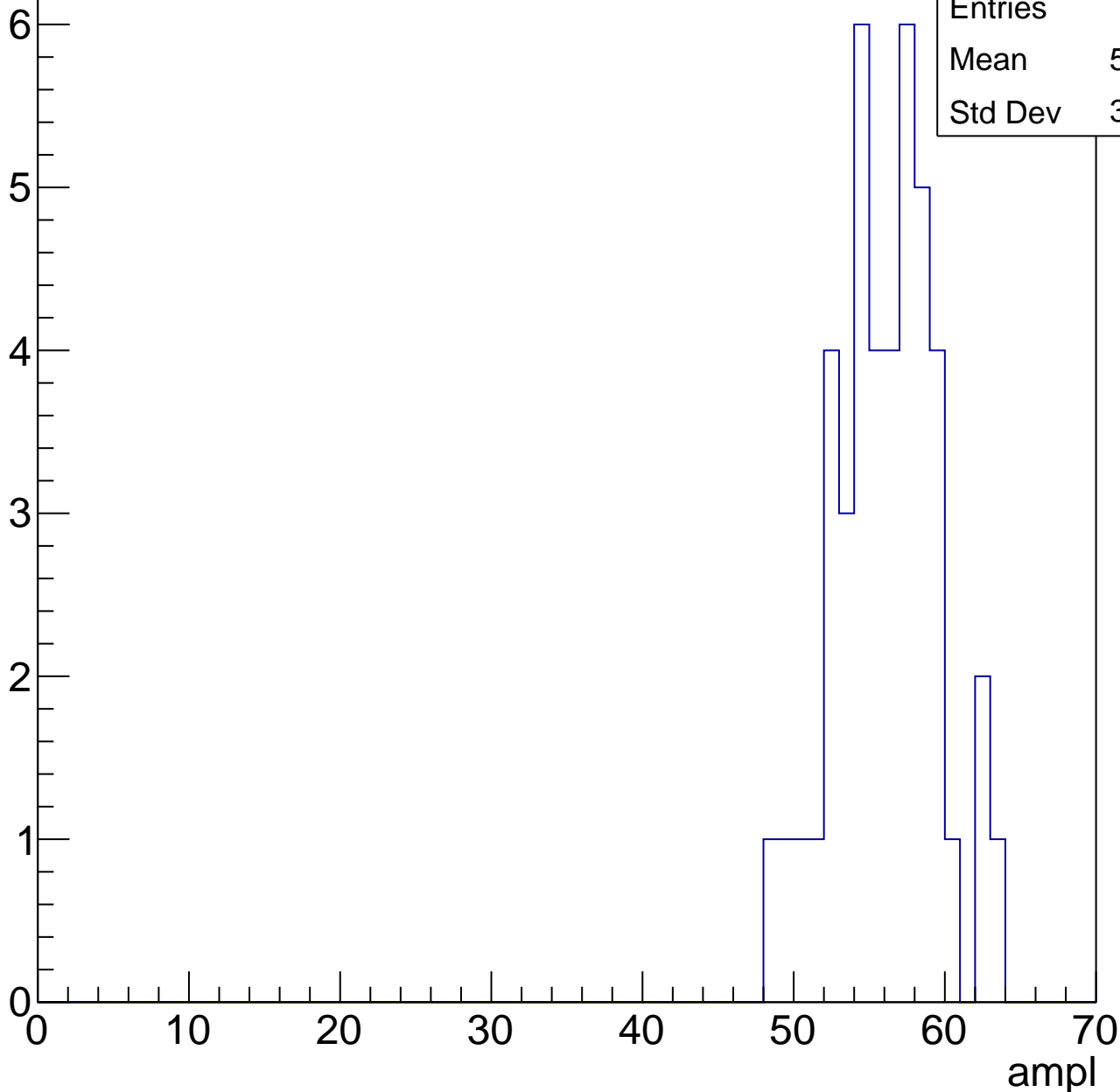


# B0L000S, U18-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

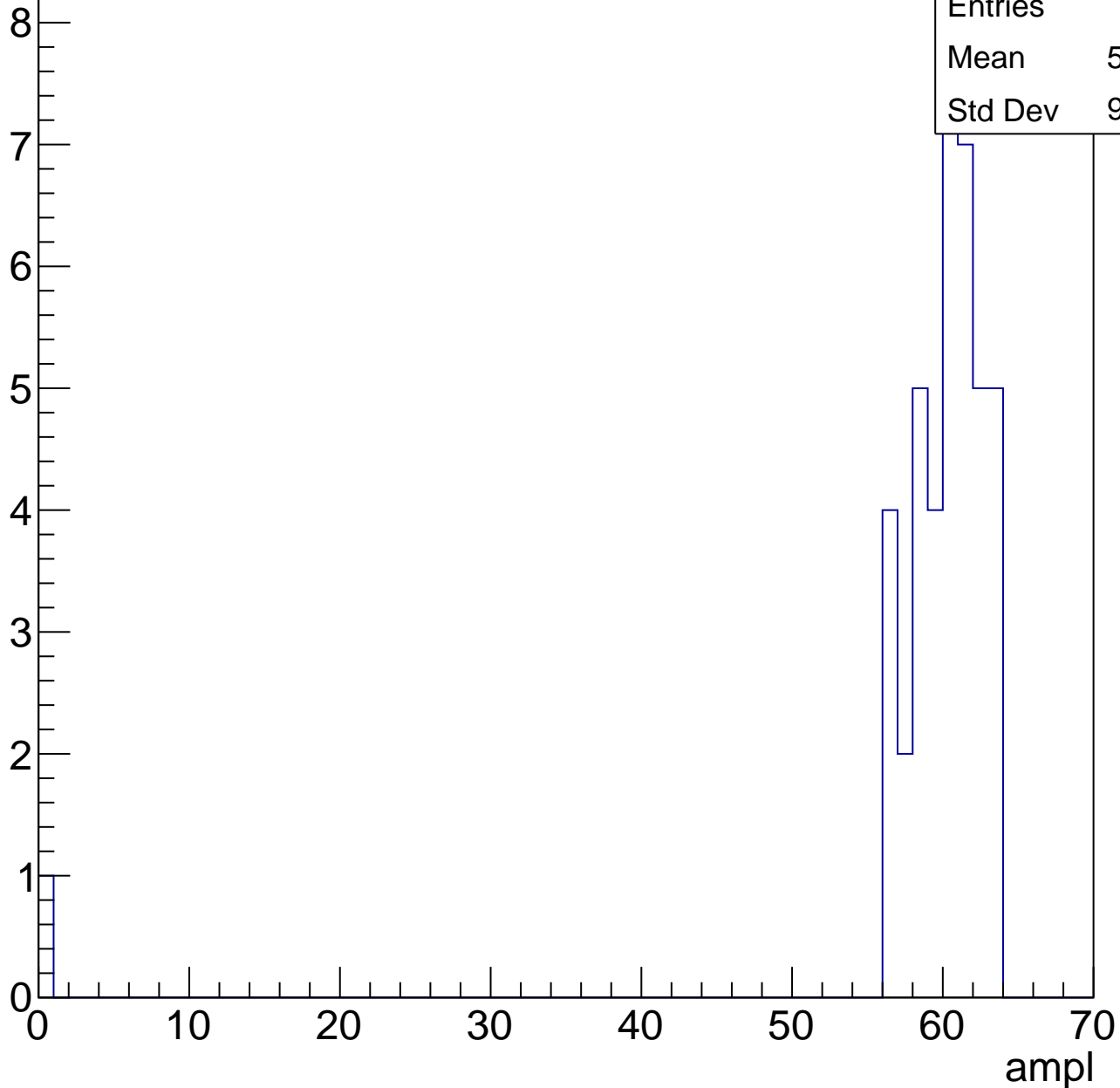
Entries	44
Mean	55.64
Std Dev	3.324



# B0L000S, U18-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

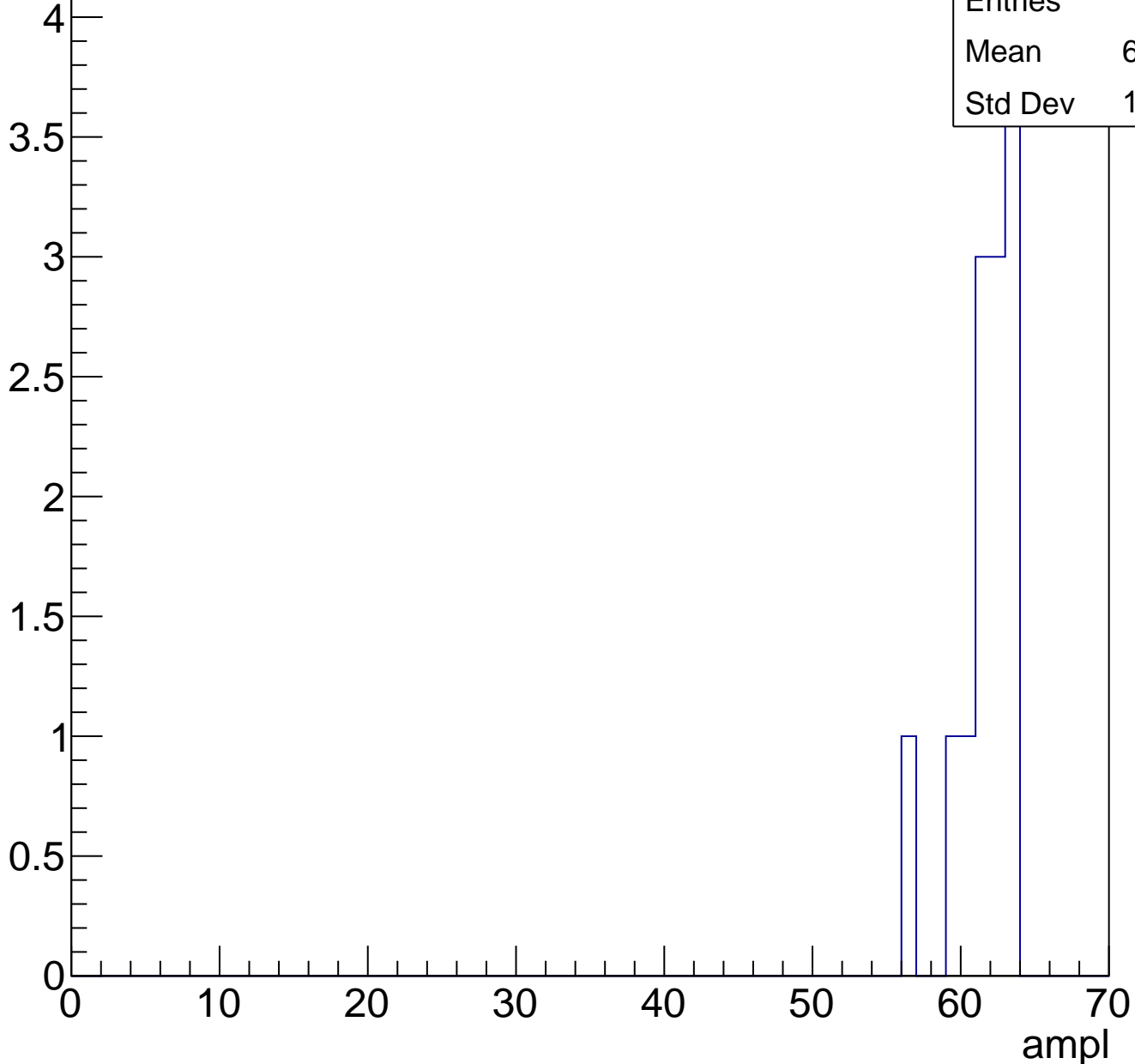
Entry



# B0L000S, U18-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	13
Mean	61.23
Std Dev	1.928



# B0L000S, U18-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch66, adc0

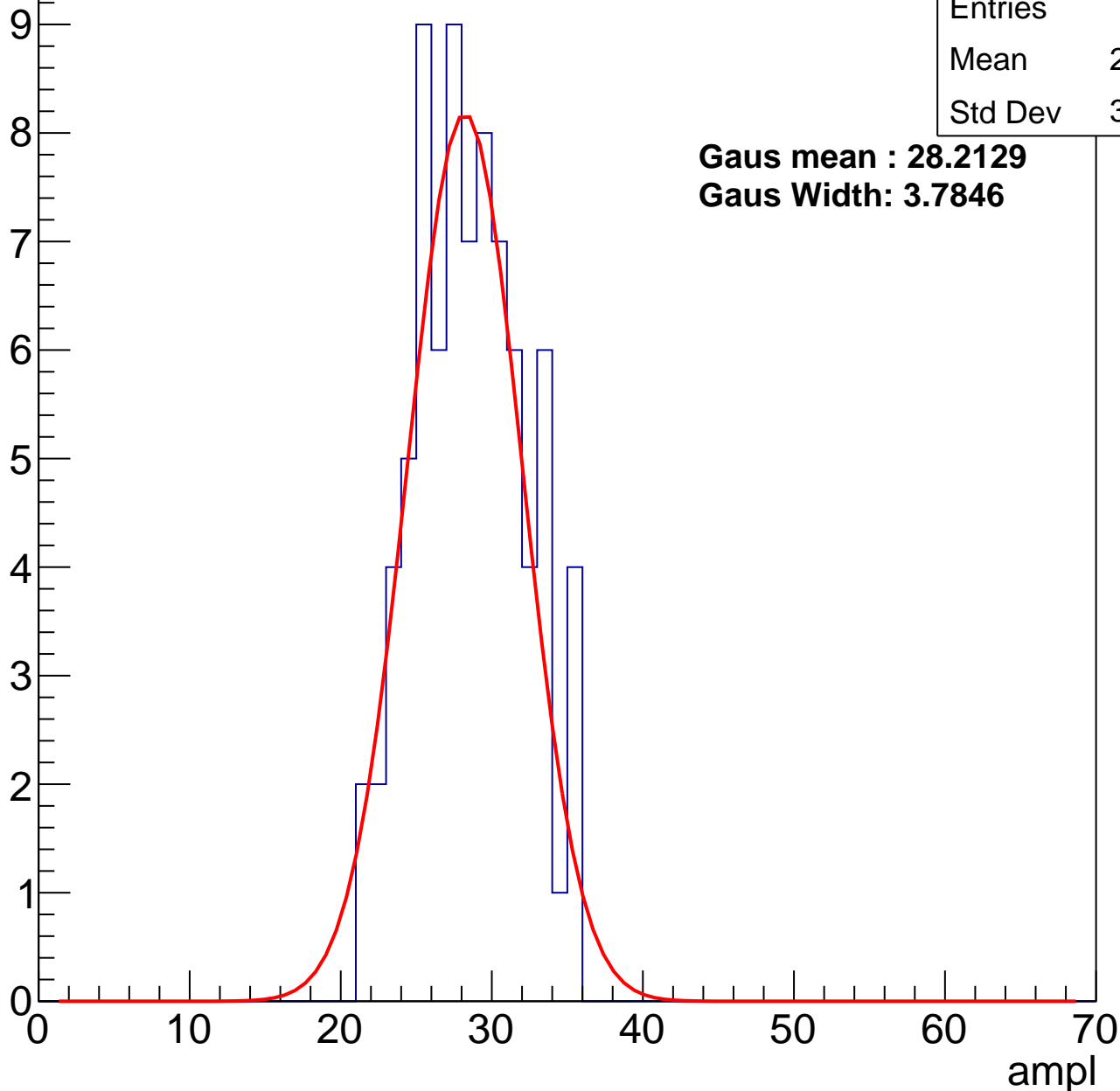
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	28.07
Std Dev	3.535

**Gaus mean : 28.2129**

**Gaus Width: 3.7846**



# B0L000S, U18-ch66, adc1

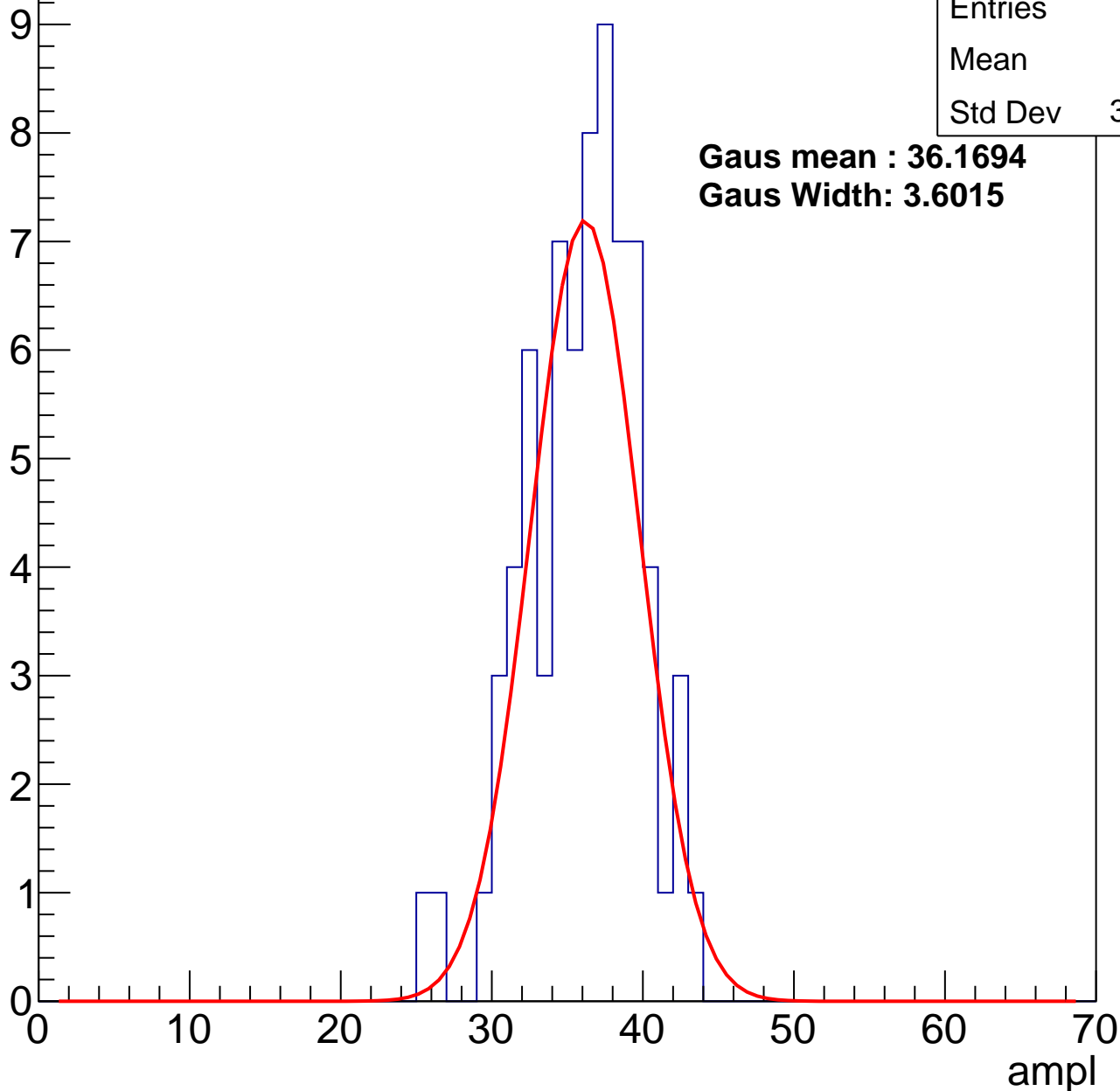
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	35.6
Std Dev	3.669

**Gaus mean : 36.1694**

**Gaus Width: 3.6015**



# B0L000S, U18-ch66, adc2

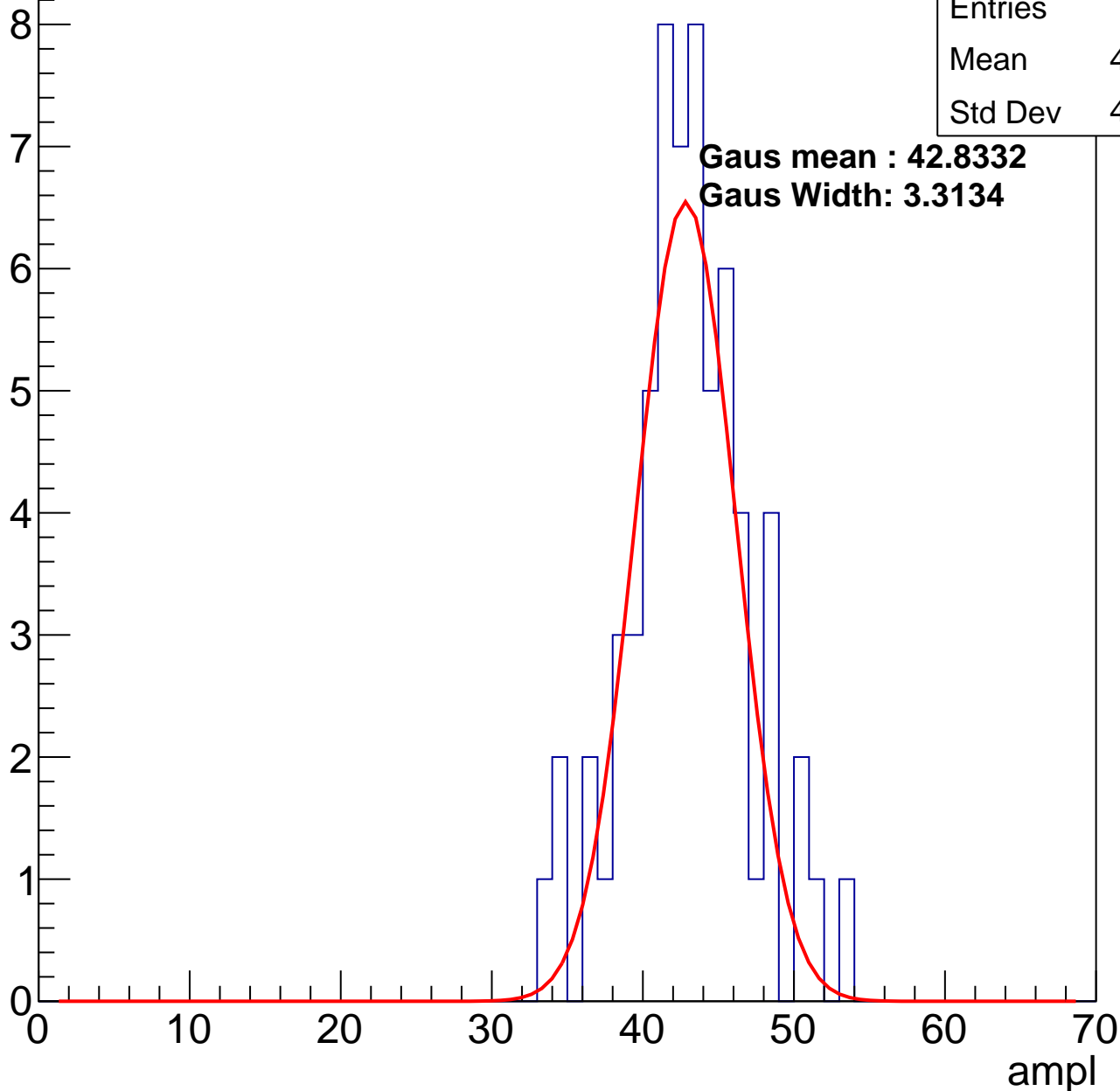
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.56
Std Dev	4.019

**Gaus mean : 42.8332**

**Gaus Width: 3.3134**

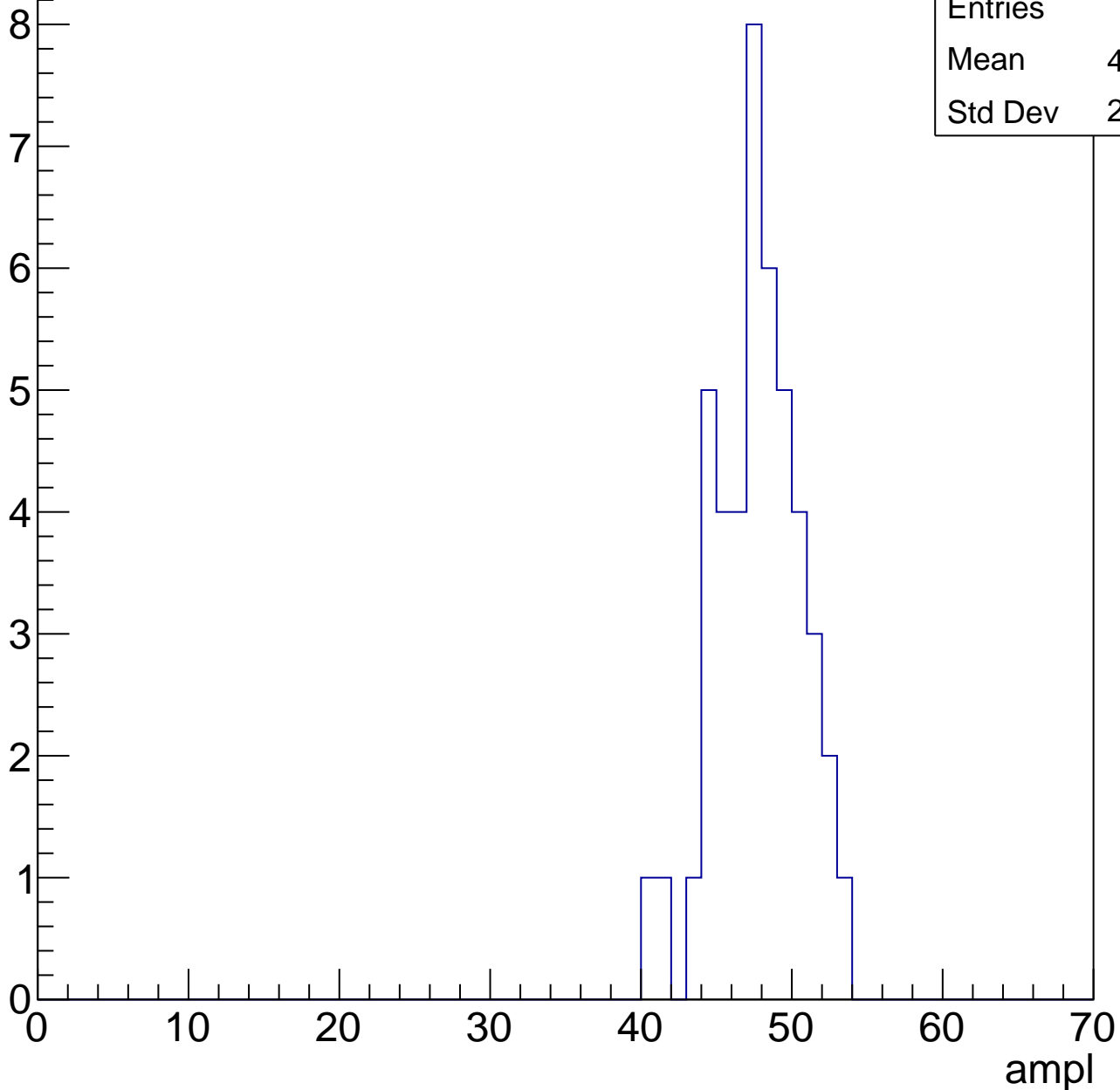


# B0L000S, U18-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

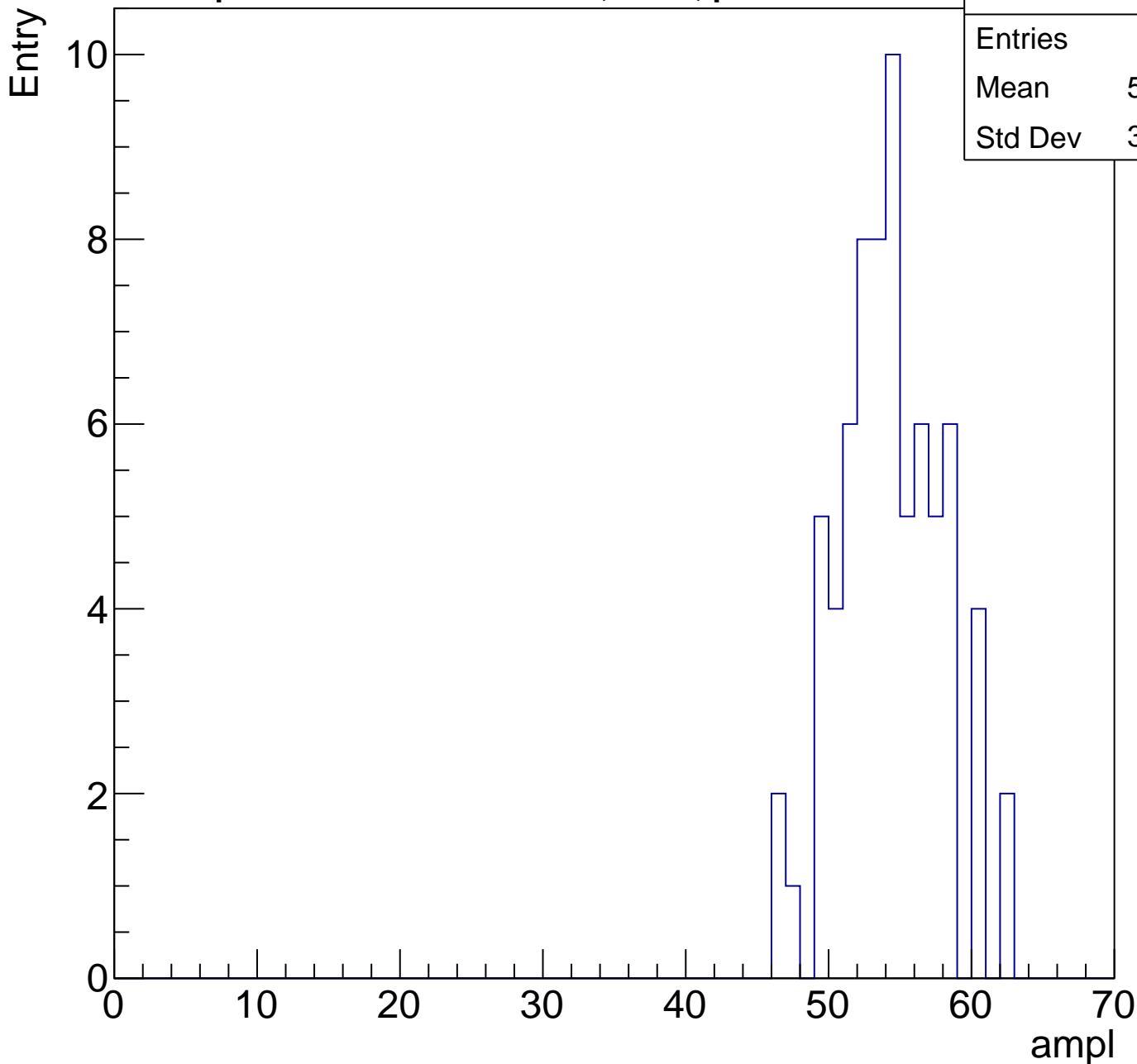
Entries	45
Mean	47.27
Std Dev	2.824



# B0L000S, U18-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

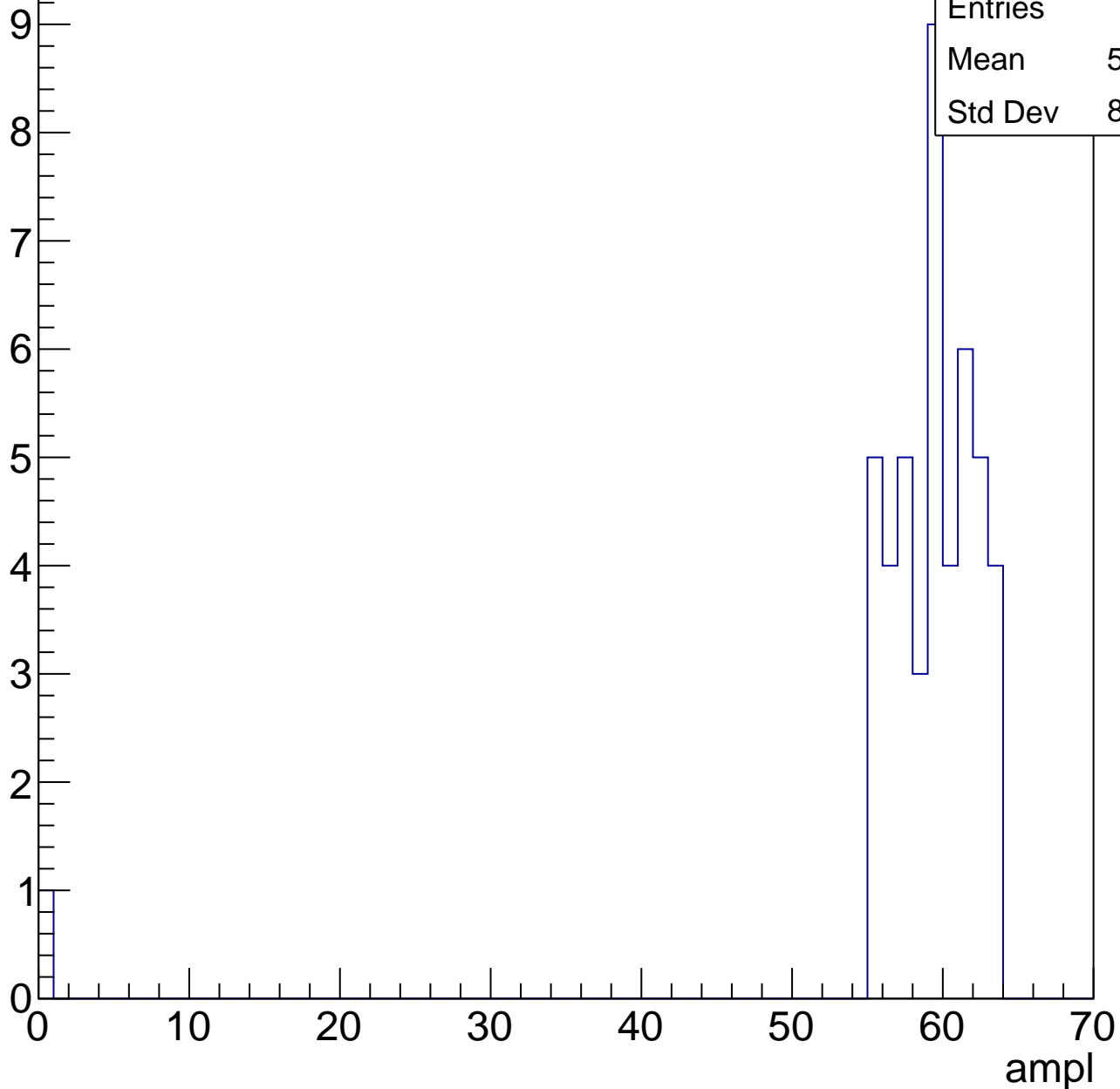
Entries	72
Mean	53.86
Std Dev	3.537



# B0L000S, U18-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

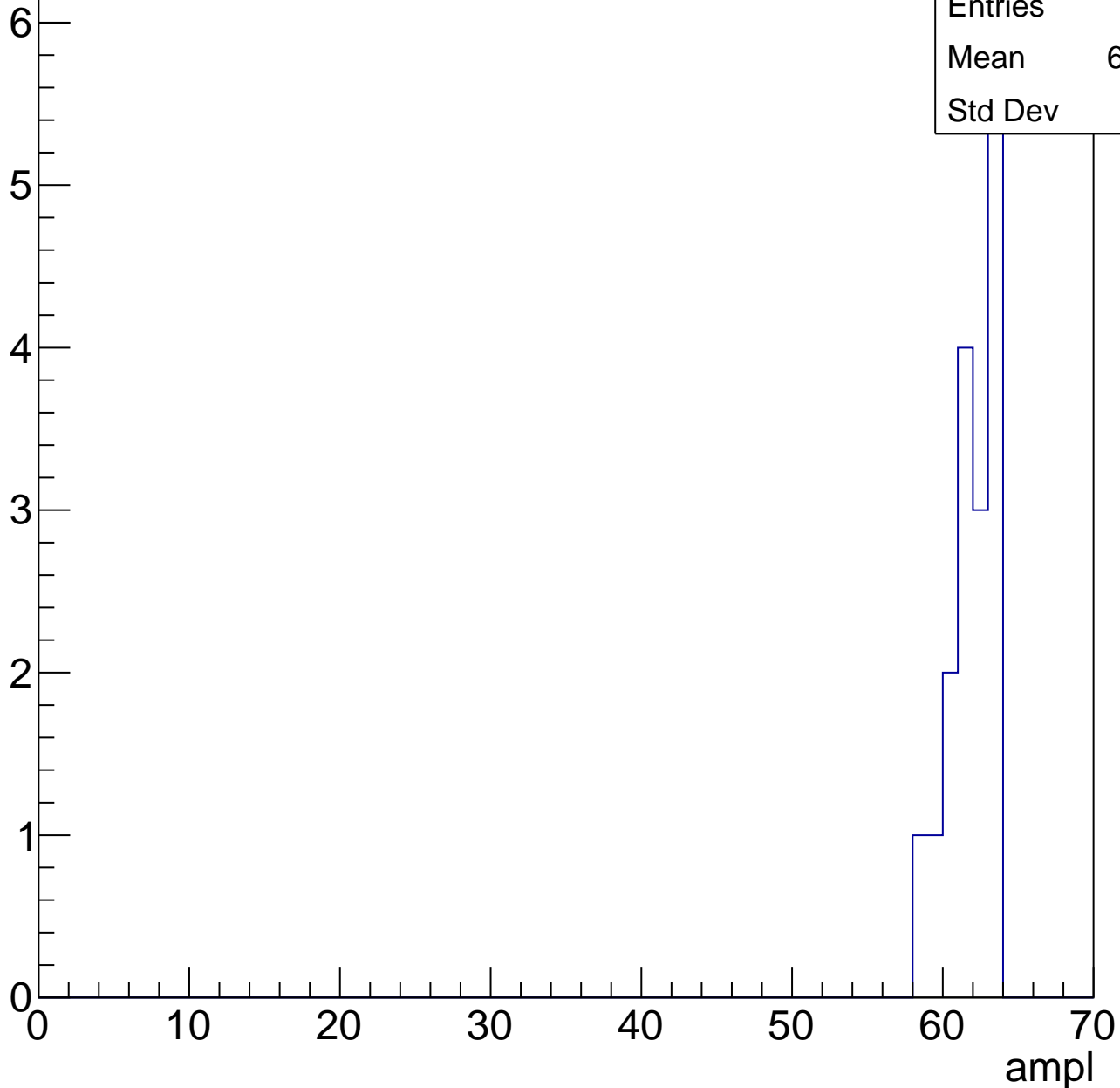
Entry



# B0L000S, U18-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch67, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	91
Mean	28.24
Std Dev	3.599

**Gaus mean : 29.2171**

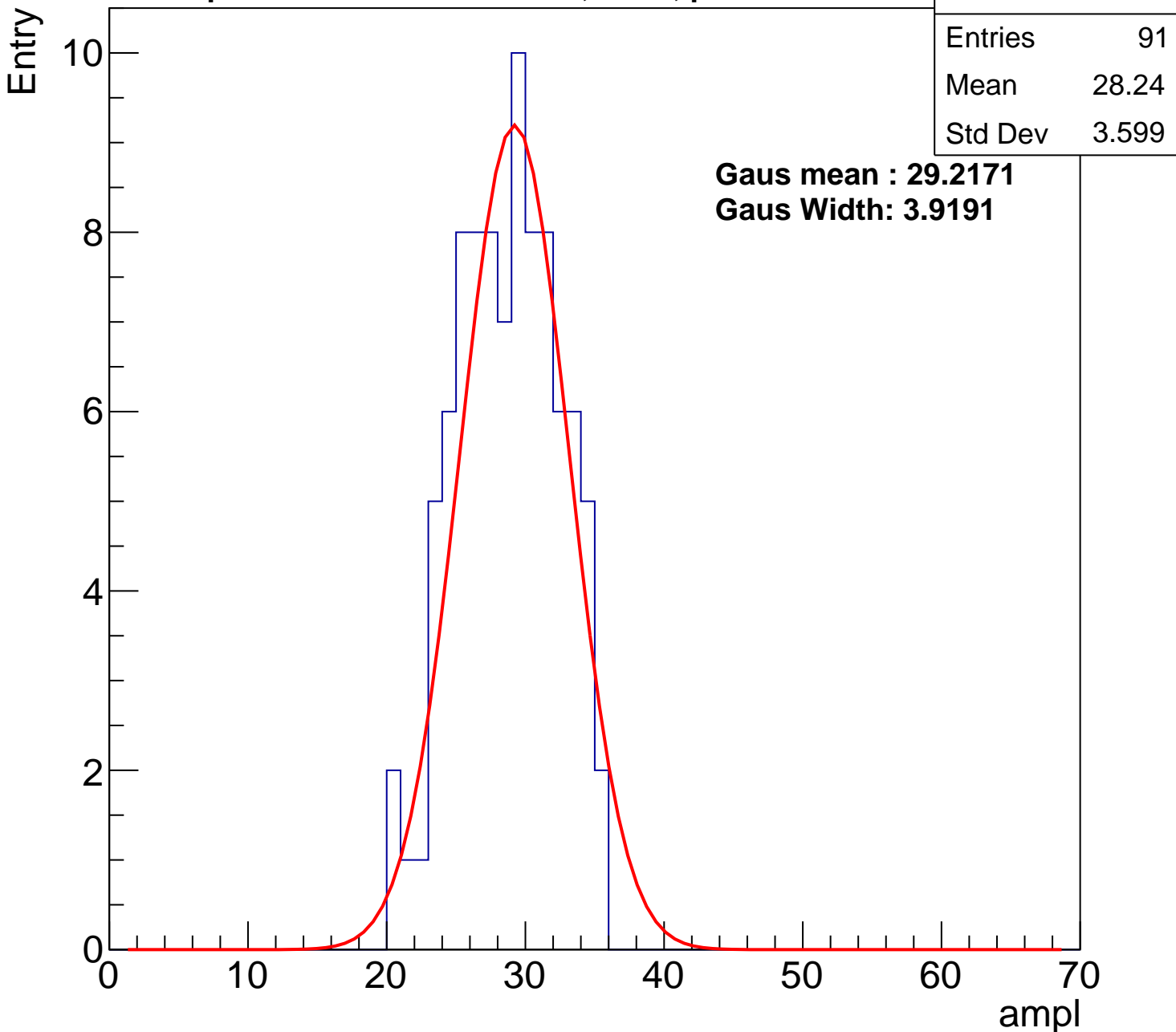
**Gaus Width: 3.9191**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch67, adc1

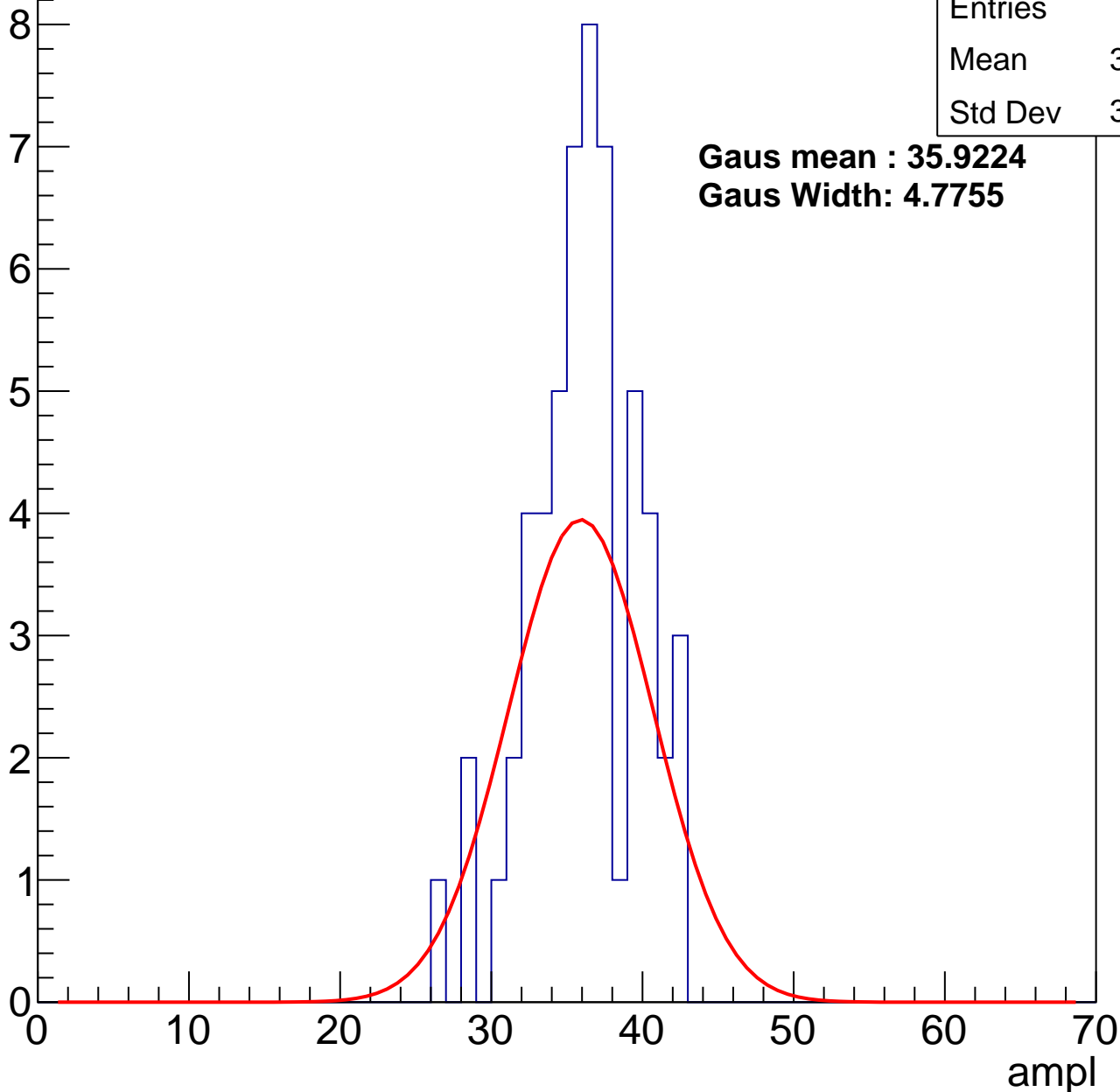
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	35.66
Std Dev	3.567

**Gaus mean : 35.9224**

**Gaus Width: 4.7755**



# B0L000S, U18-ch67, adc2

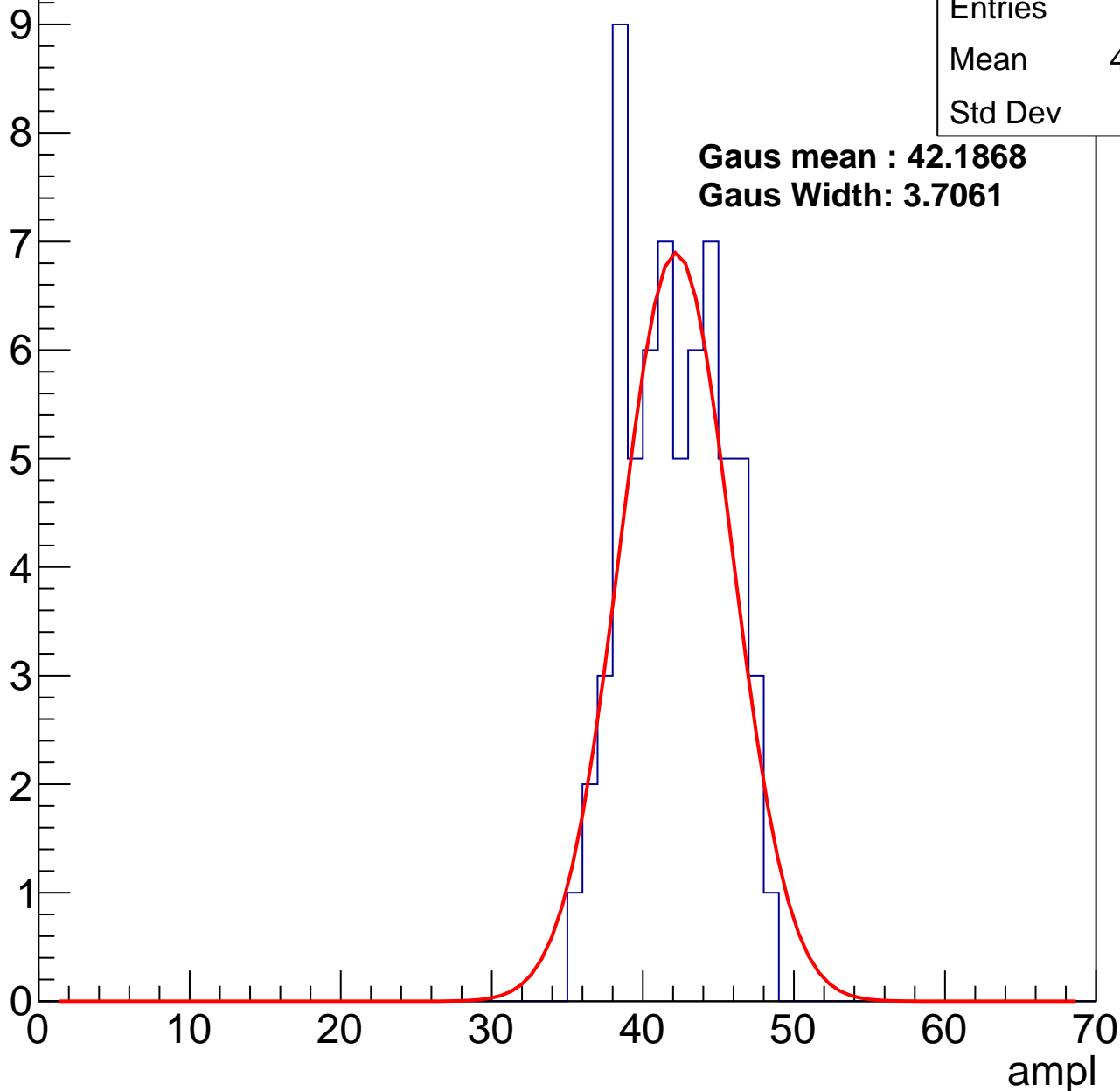
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.57
Std Dev	3.22

**Gaus mean : 42.1868**

**Gaus Width: 3.7061**

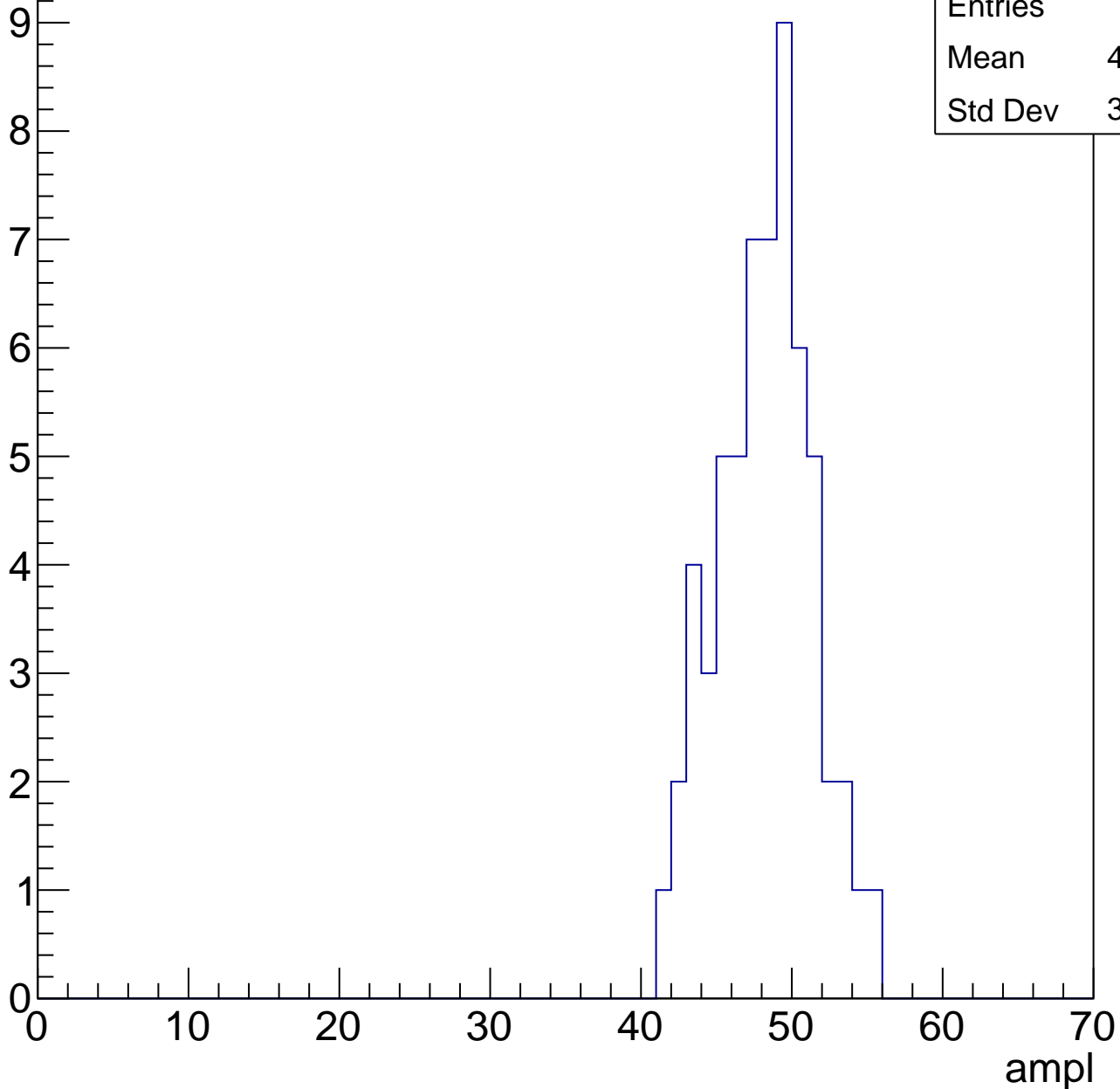


# B0L000S, U18-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	47.73
Std Dev	3.114

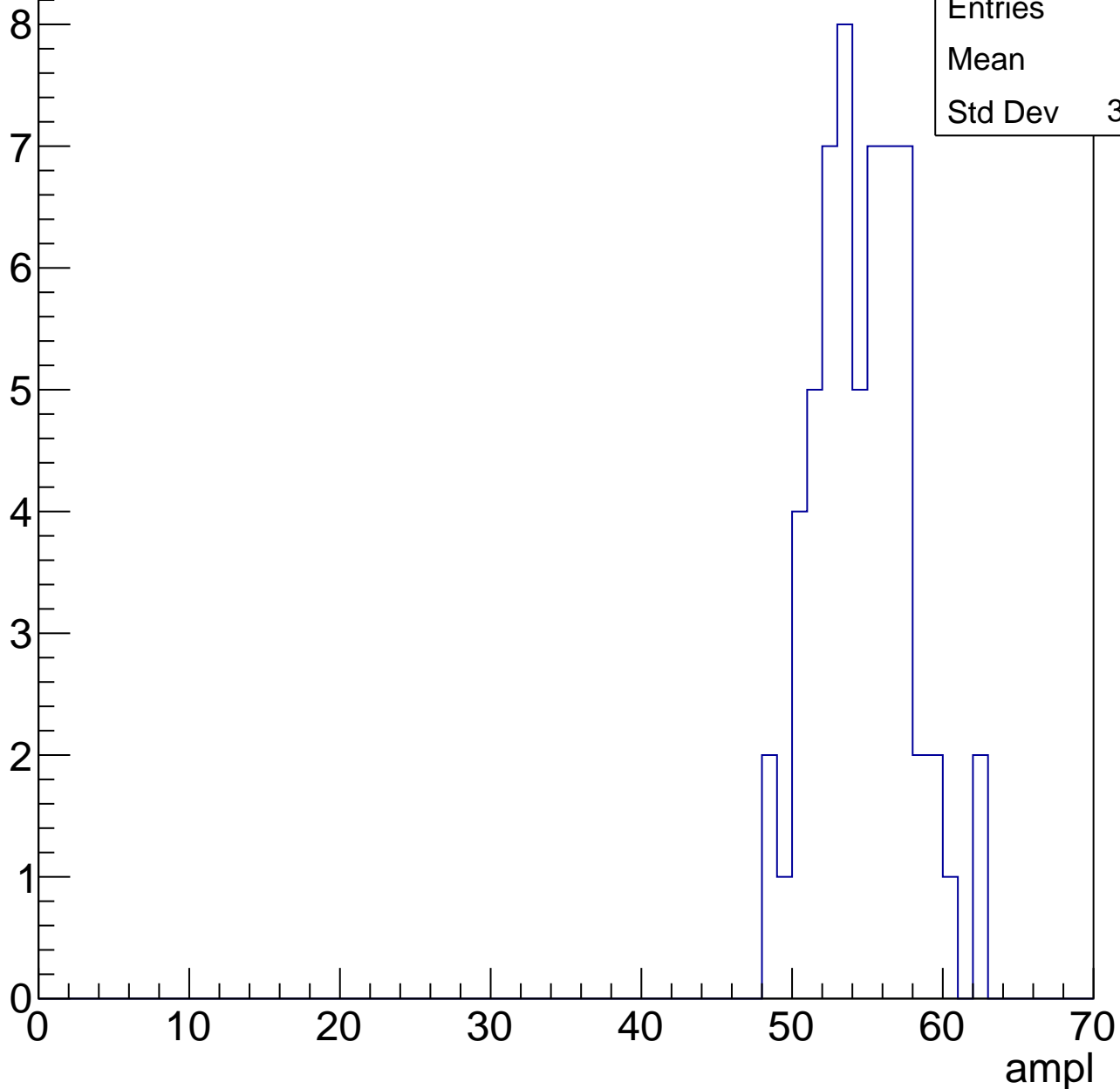


# B0L000S, U18-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

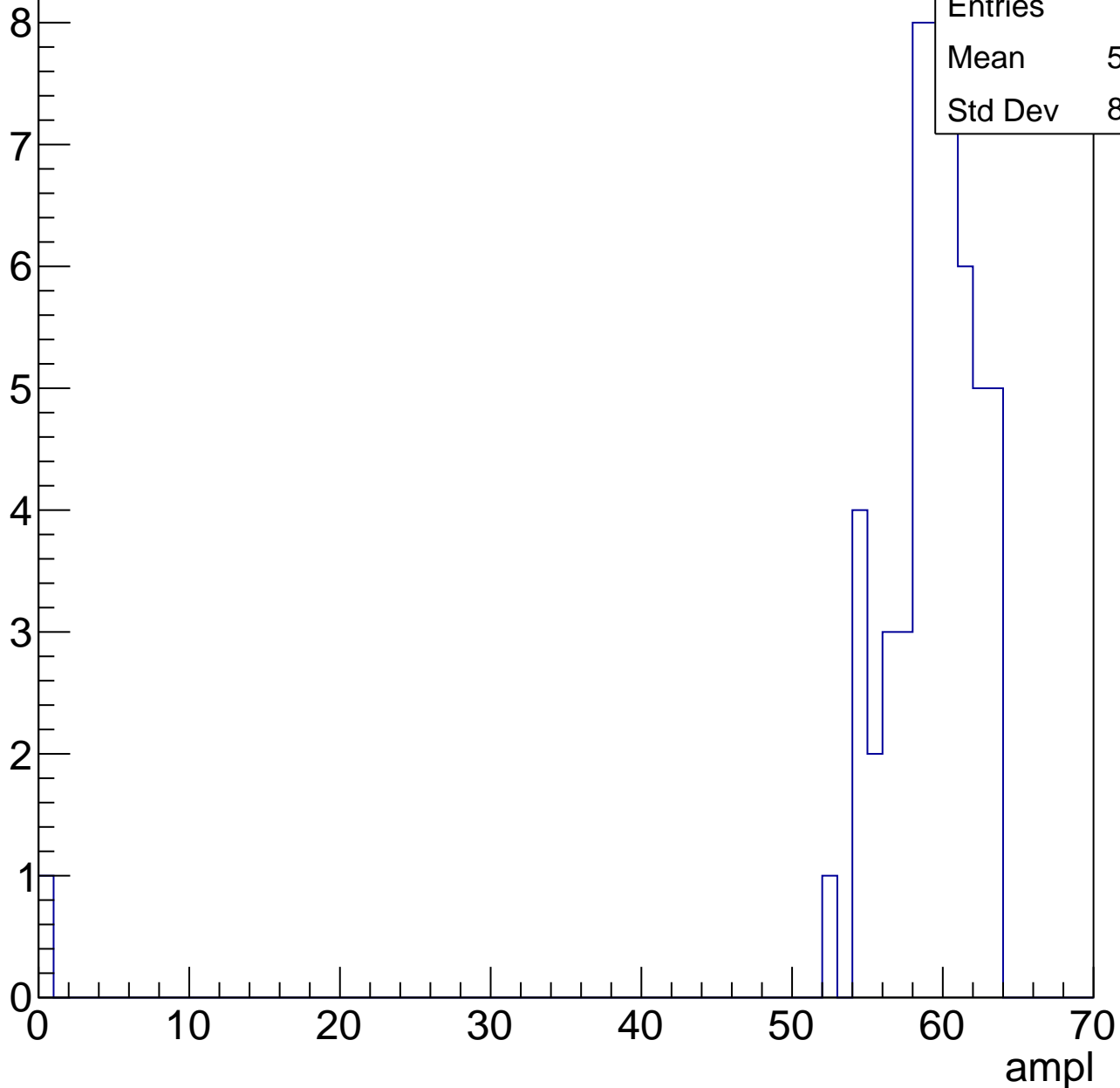
Entries	60
Mean	54.2
Std Dev	3.119



# B0L000S, U18-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

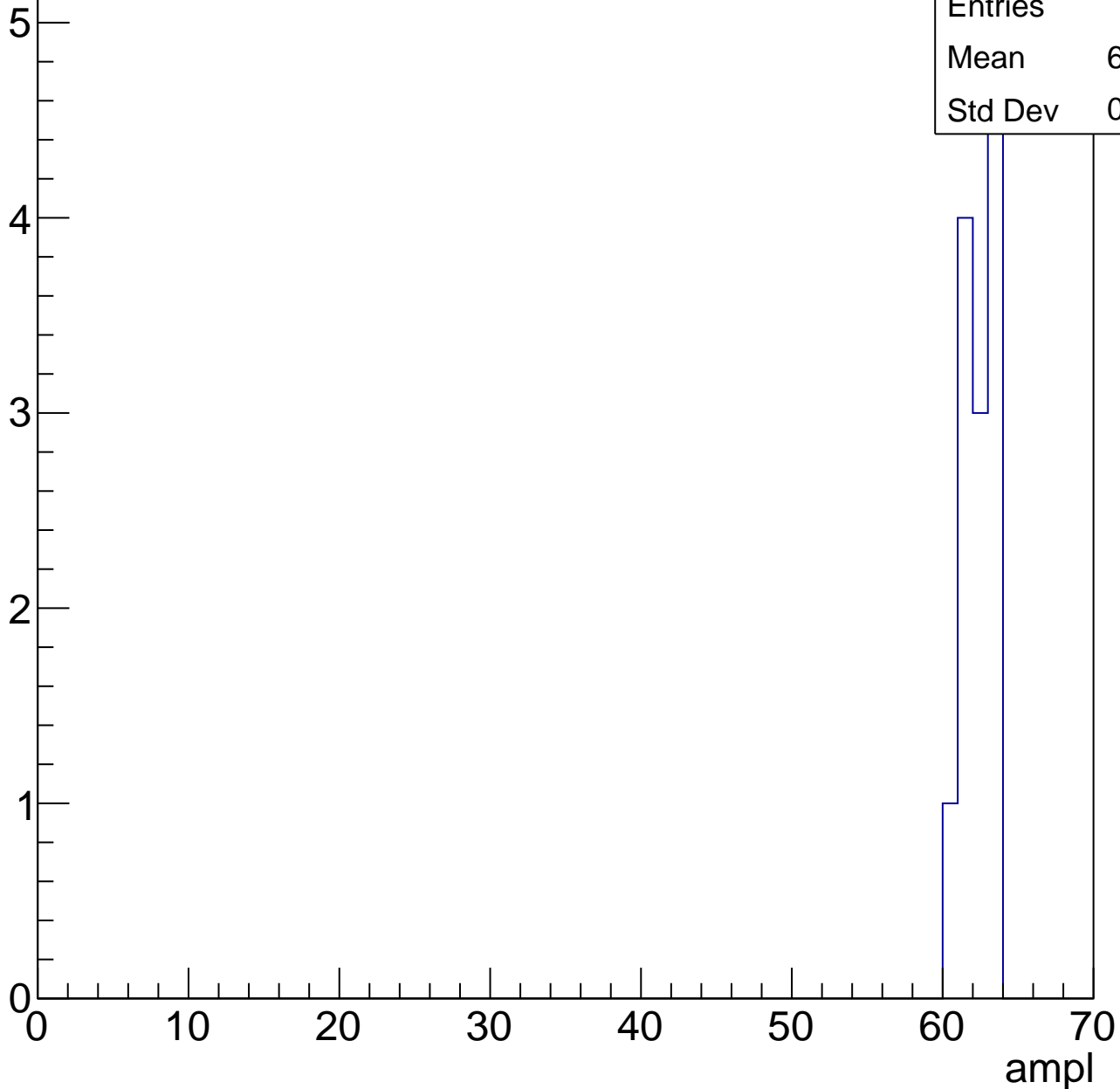


# B0L000S, U18-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.92
Std Dev	0.997





# B0L000S, U18-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch68, adc0

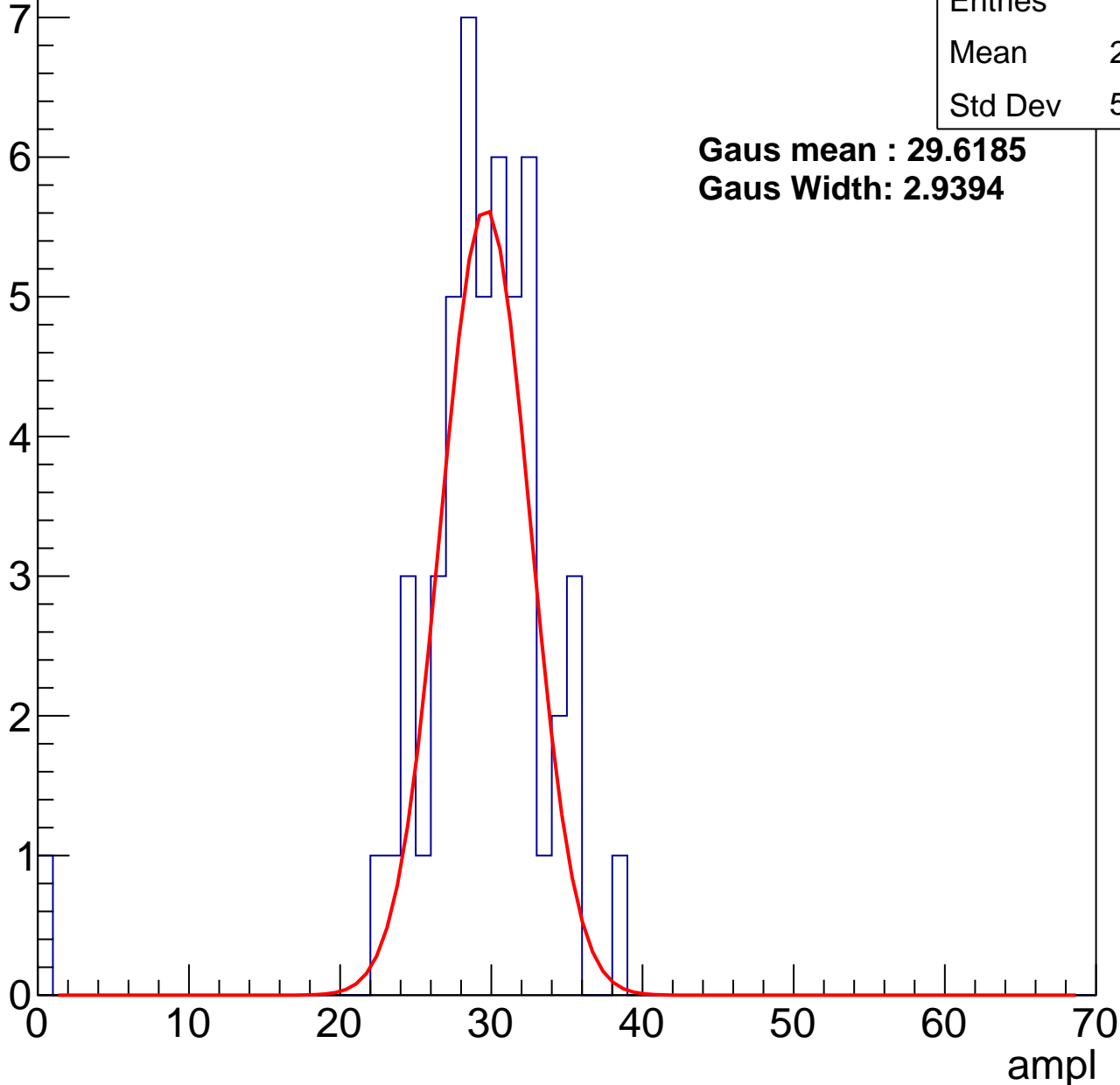
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	28.76
Std Dev	5.245

**Gaus mean : 29.6185**

**Gaus Width: 2.9394**



# B0L000S, U18-ch68, adc1

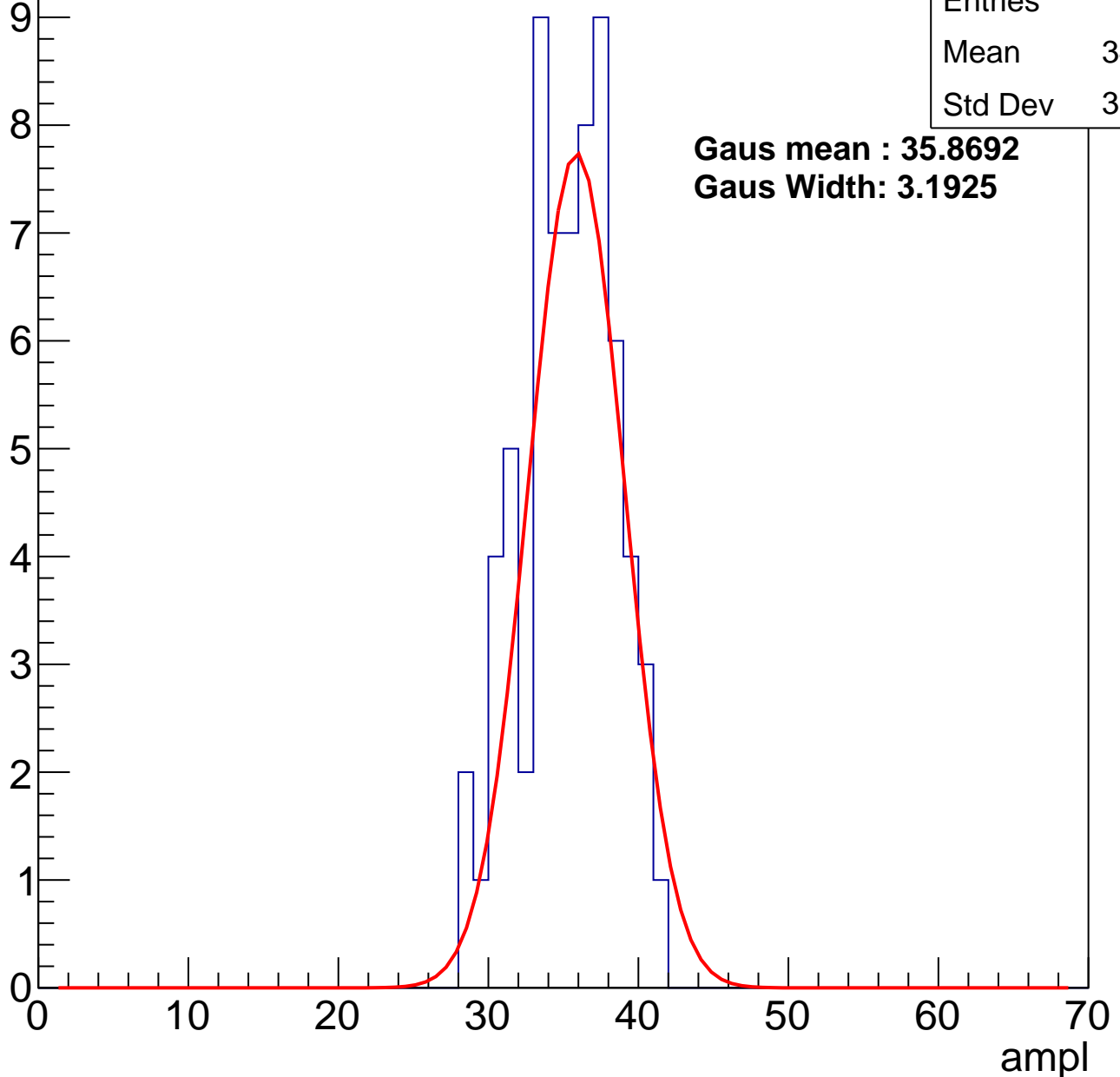
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	34.85
Std Dev	3.083

**Gaus mean : 35.8692**

**Gaus Width: 3.1925**



# B0L000S, U18-ch68, adc2

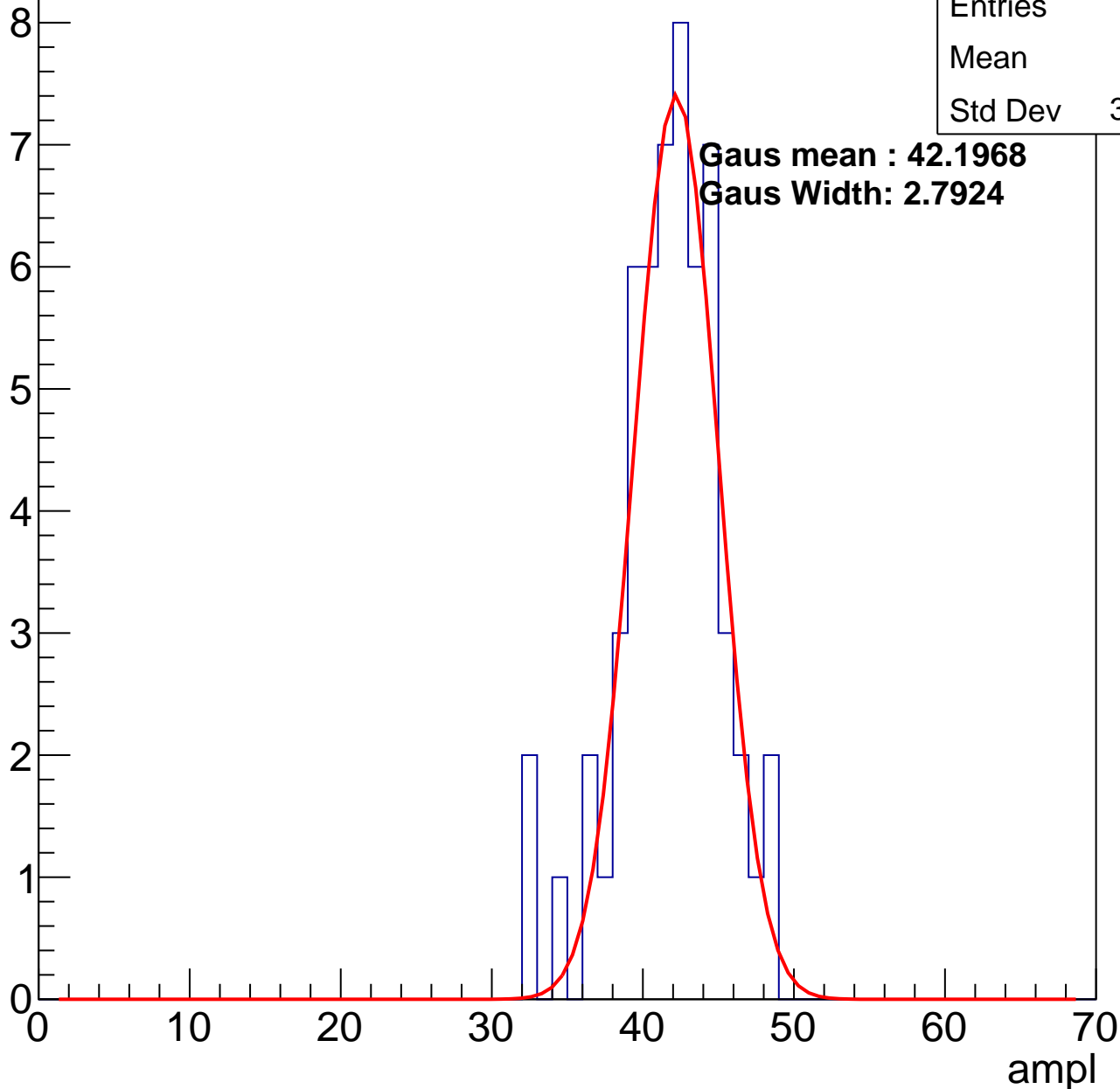
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	41.3
Std Dev	3.392

**Gaus mean : 42.1968**

**Gaus Width: 2.7924**

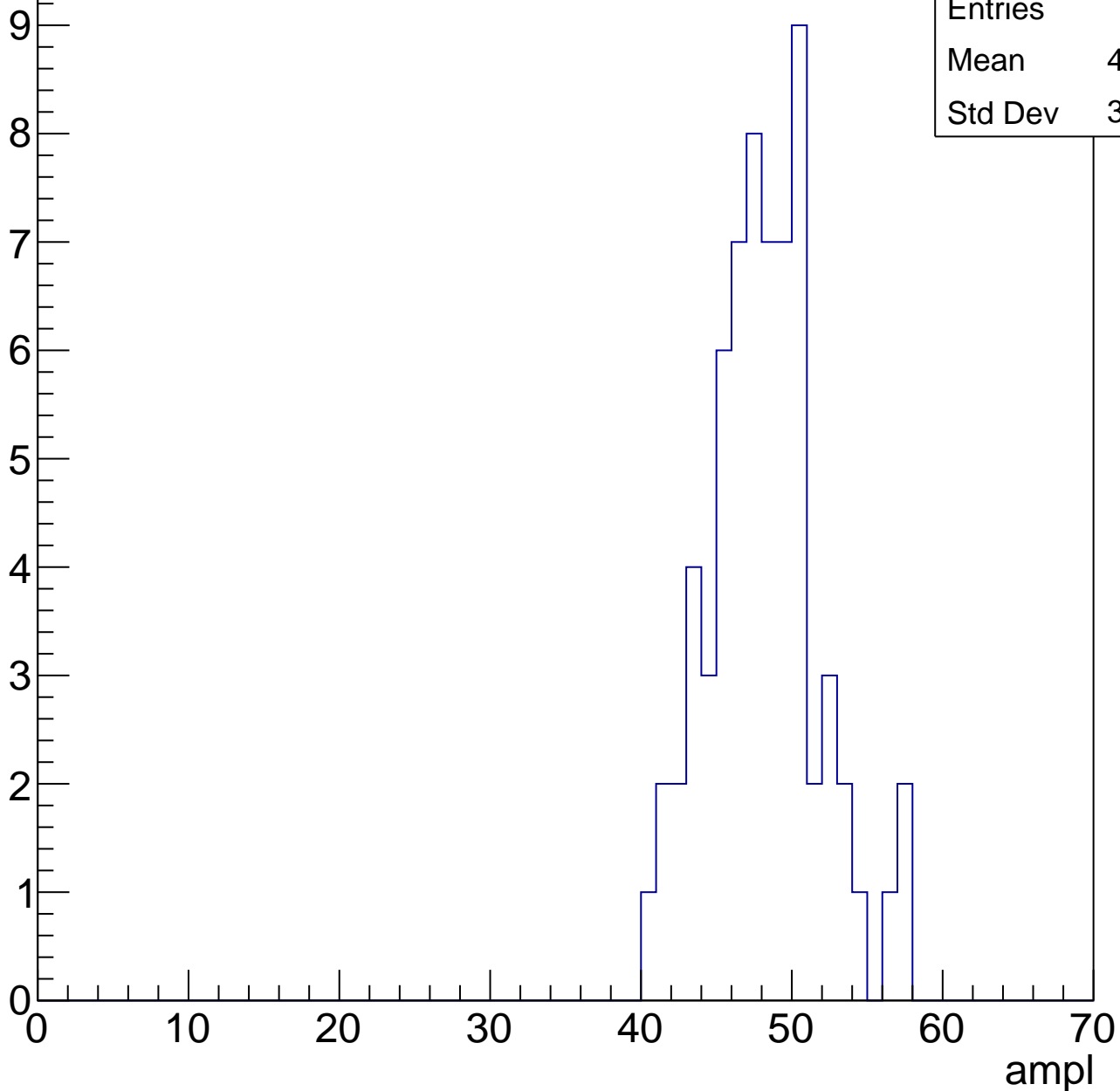


# B0L000S, U18-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	47.69
Std Dev	3.637

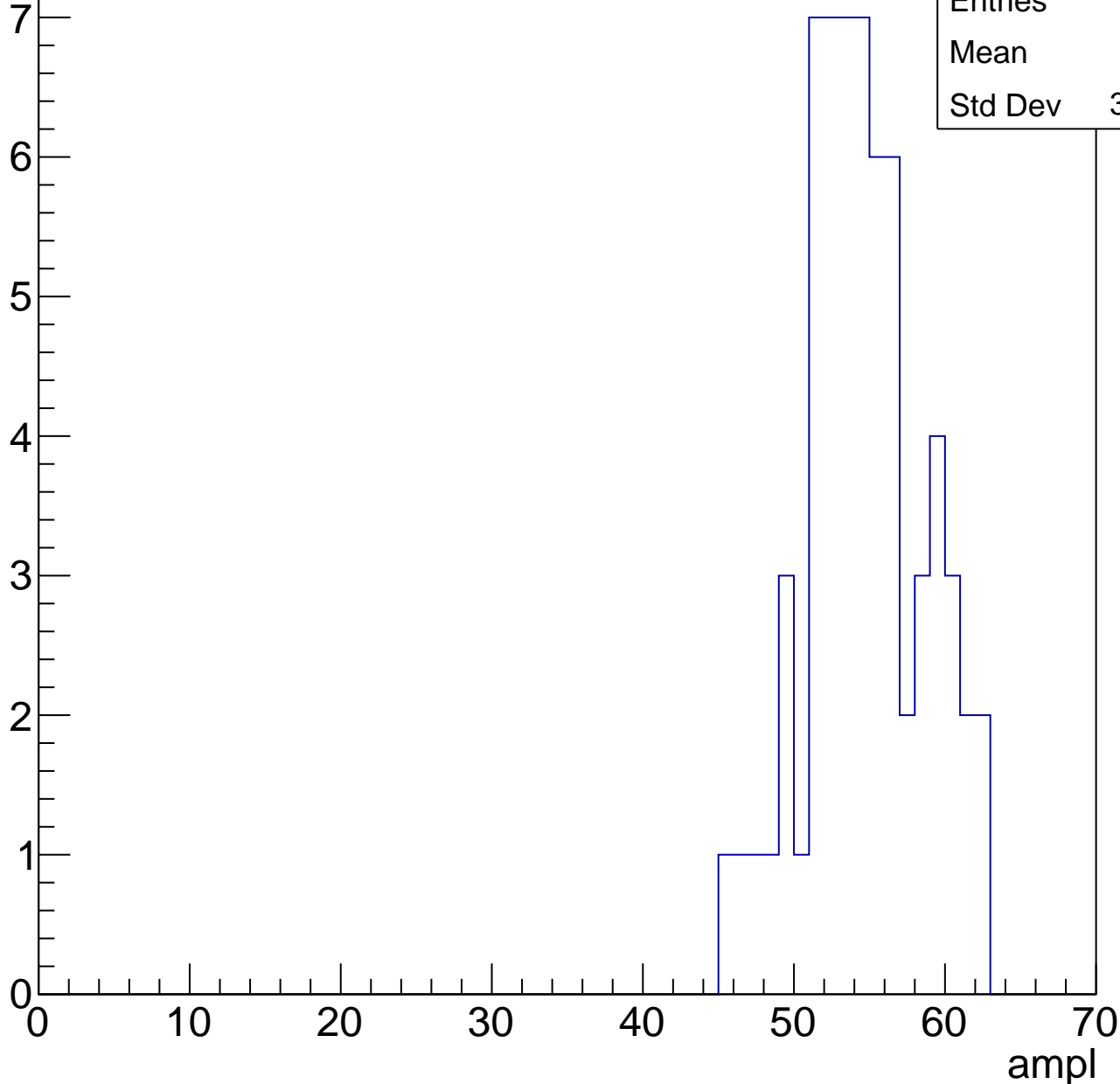


# B0L000S, U18-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	54.2
Std Dev	3.845

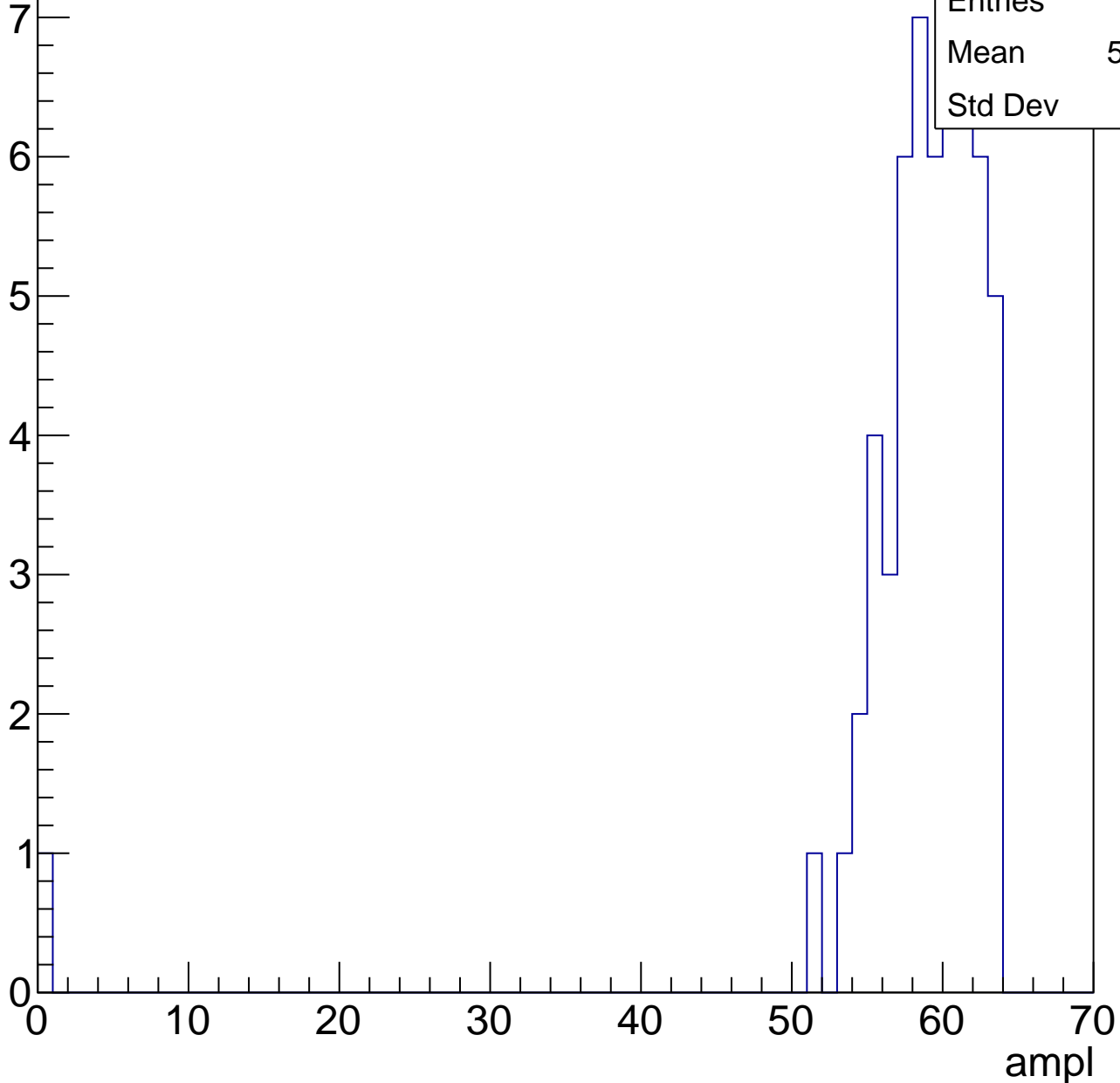


# B0L000S, U18-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	57.79
Std Dev	8.28

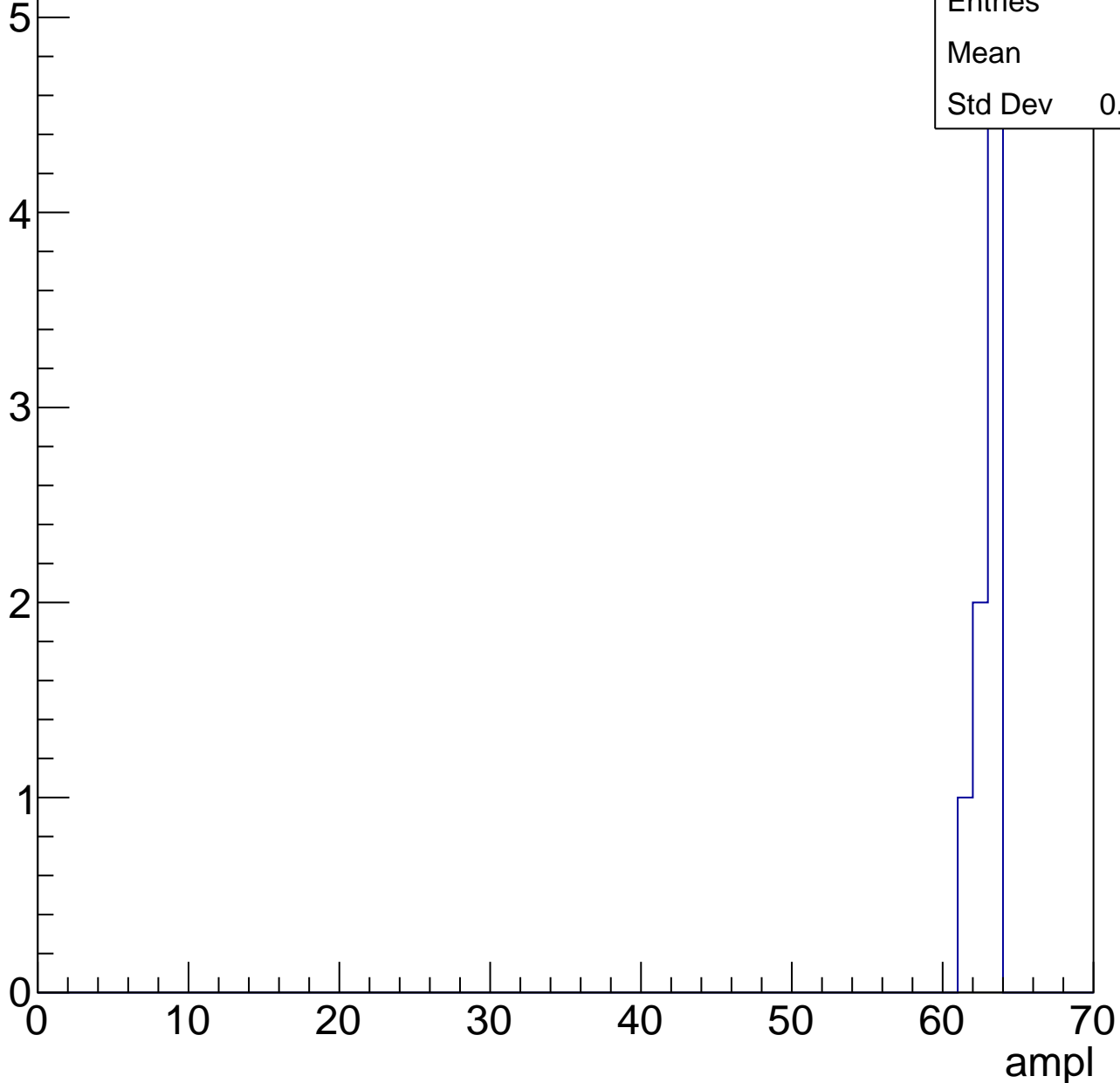


# B0L000S, U18-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071





# B0L000S, U18-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch69, adc0

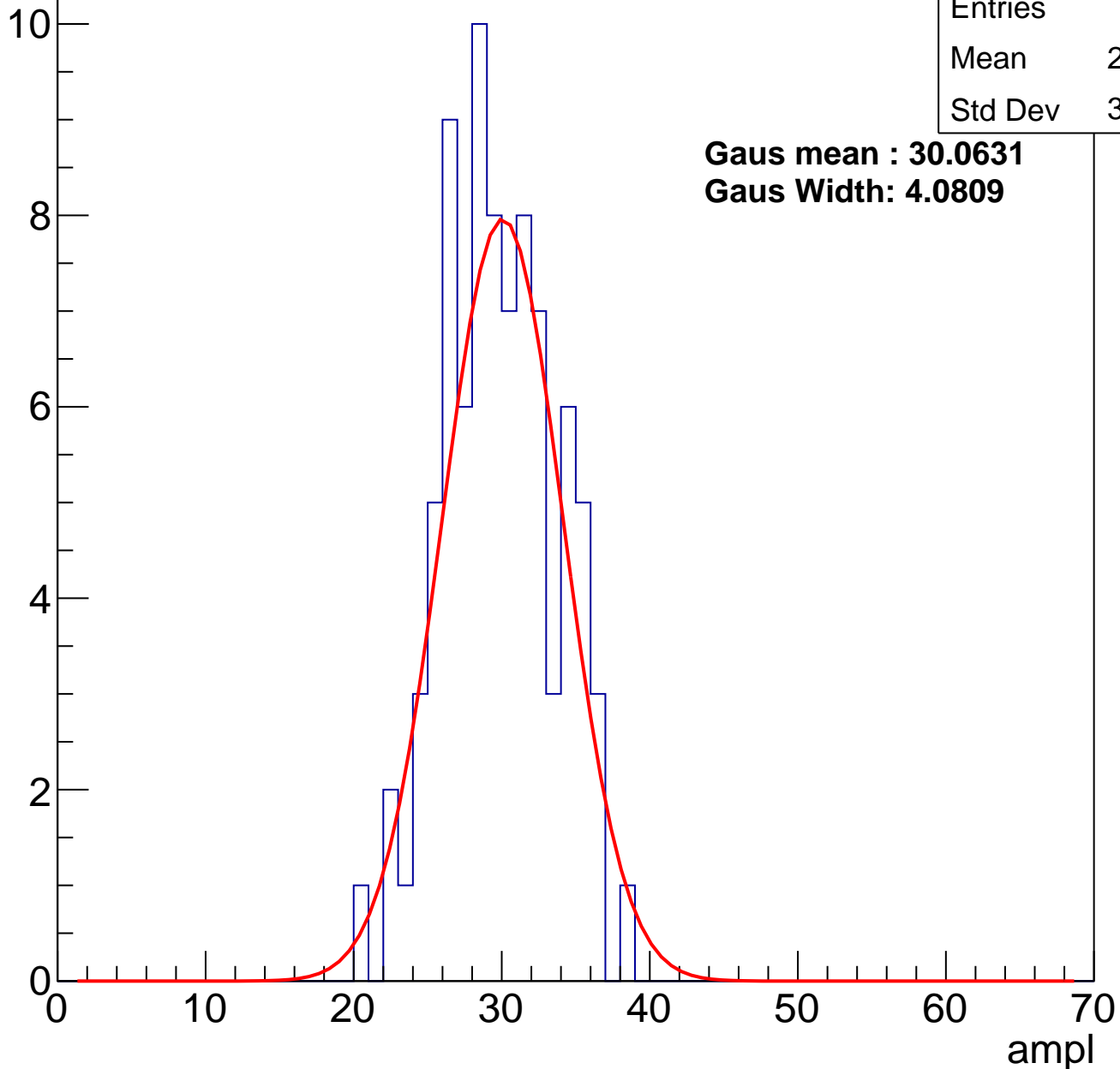
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	29.39
Std Dev	3.726

**Gaus mean : 30.0631**

**Gaus Width: 4.0809**

Entry



# B0L000S, U18-ch69, adc1

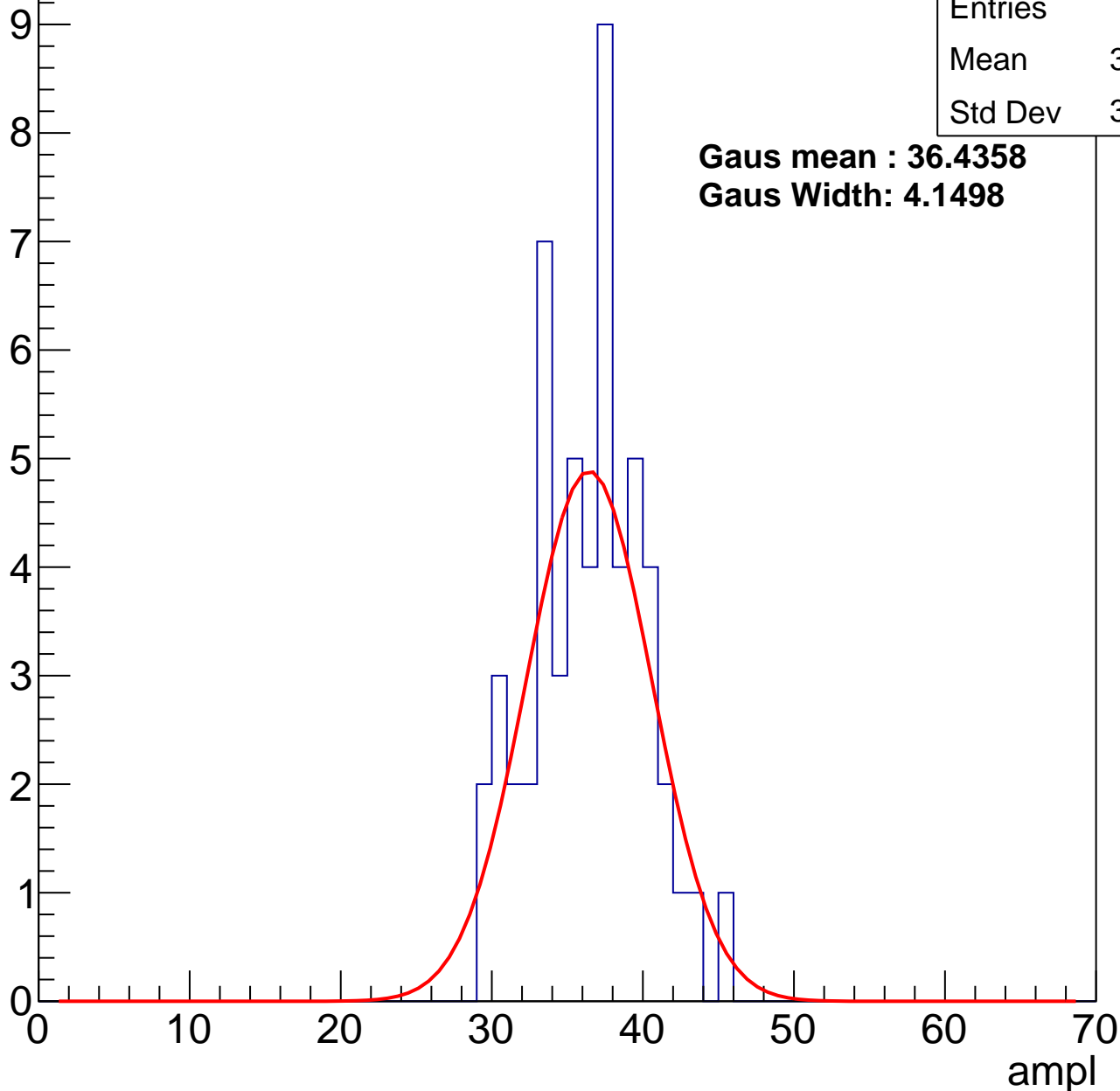
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.96
Std Dev	3.613

**Gaus mean : 36.4358**

**Gaus Width: 4.1498**



# B0L000S, U18-ch69, adc2

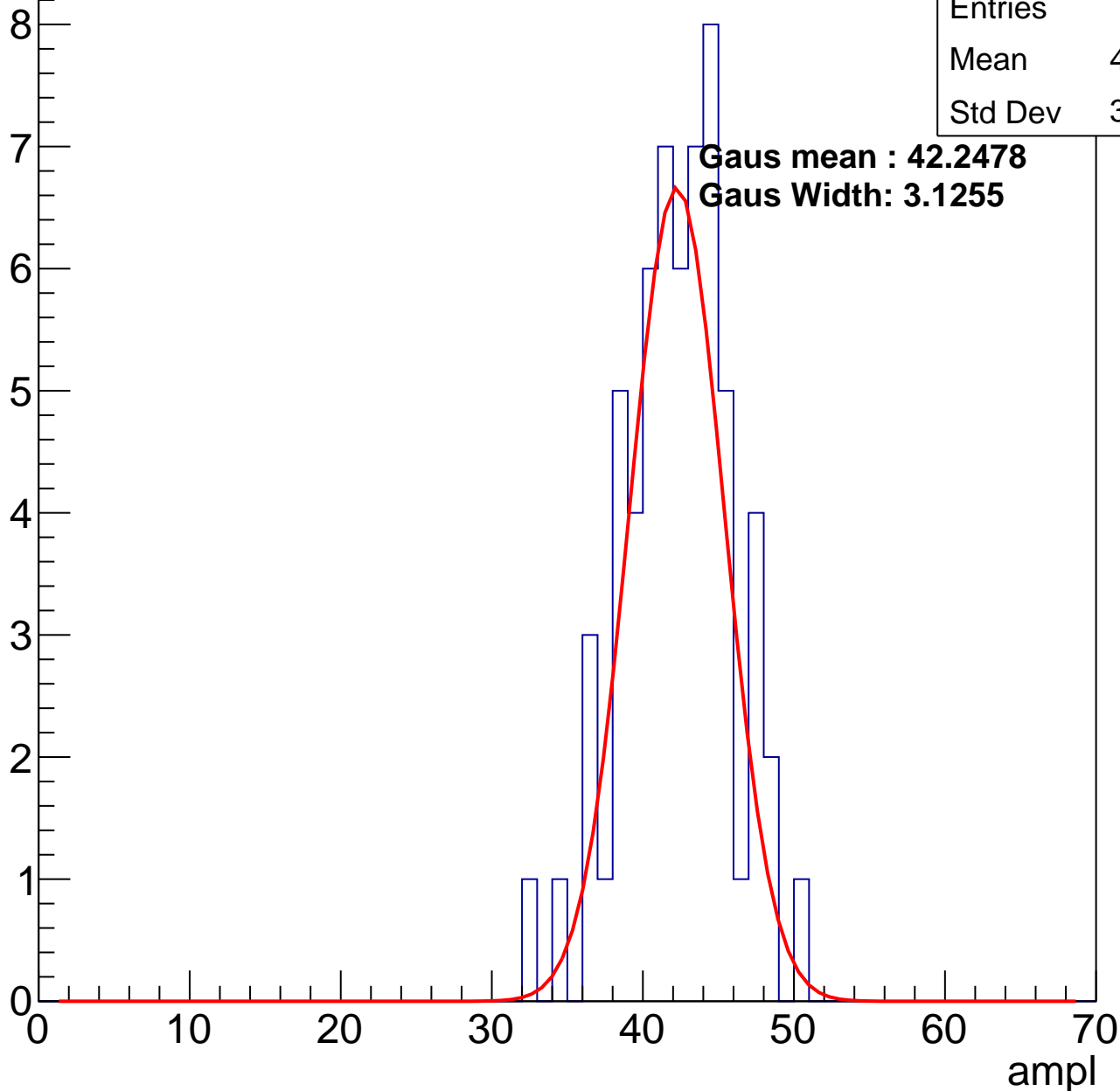
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	41.84
Std Dev	3.543

**Gaus mean : 42.2478**

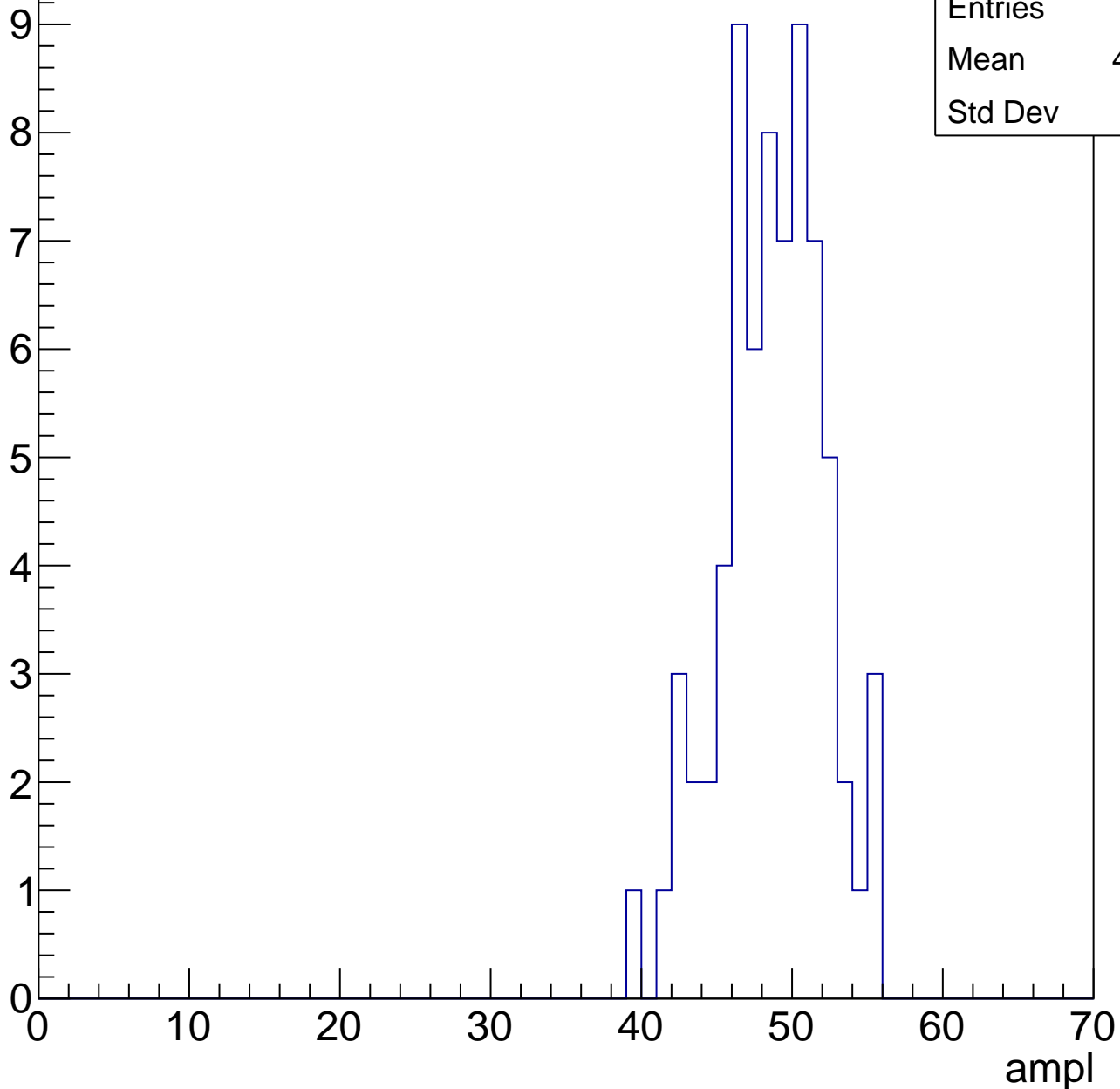
**Gaus Width: 3.1255**



# B0L000S, U18-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



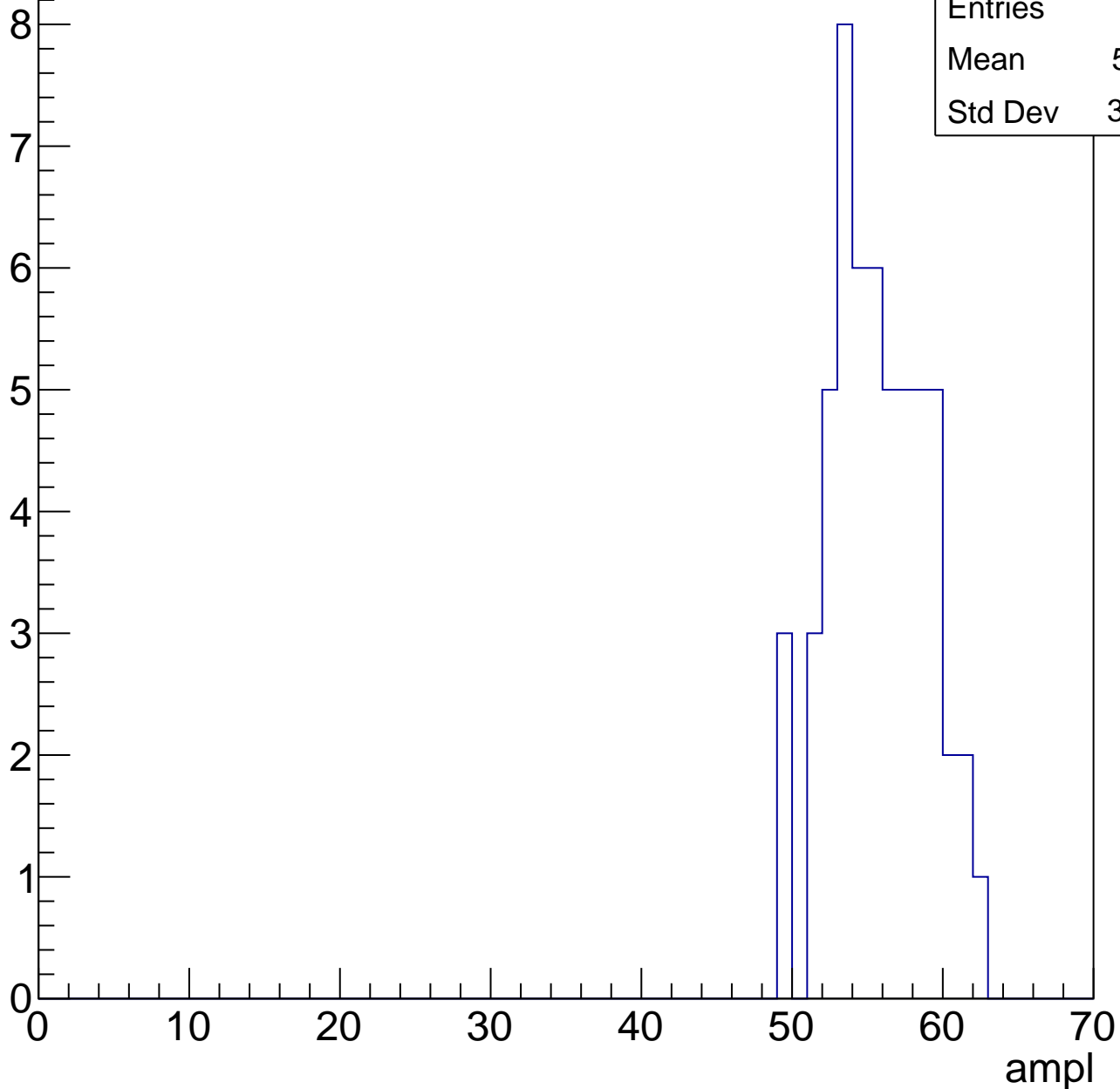
Entries	70
Mean	48.21
Std Dev	3.41

# B0L000S, U18-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.21
Std Dev	3.155



# B0L000S, U18-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	51
Mean	58.2
Std Dev	8.63

ampl

0

10

20

30

40

50

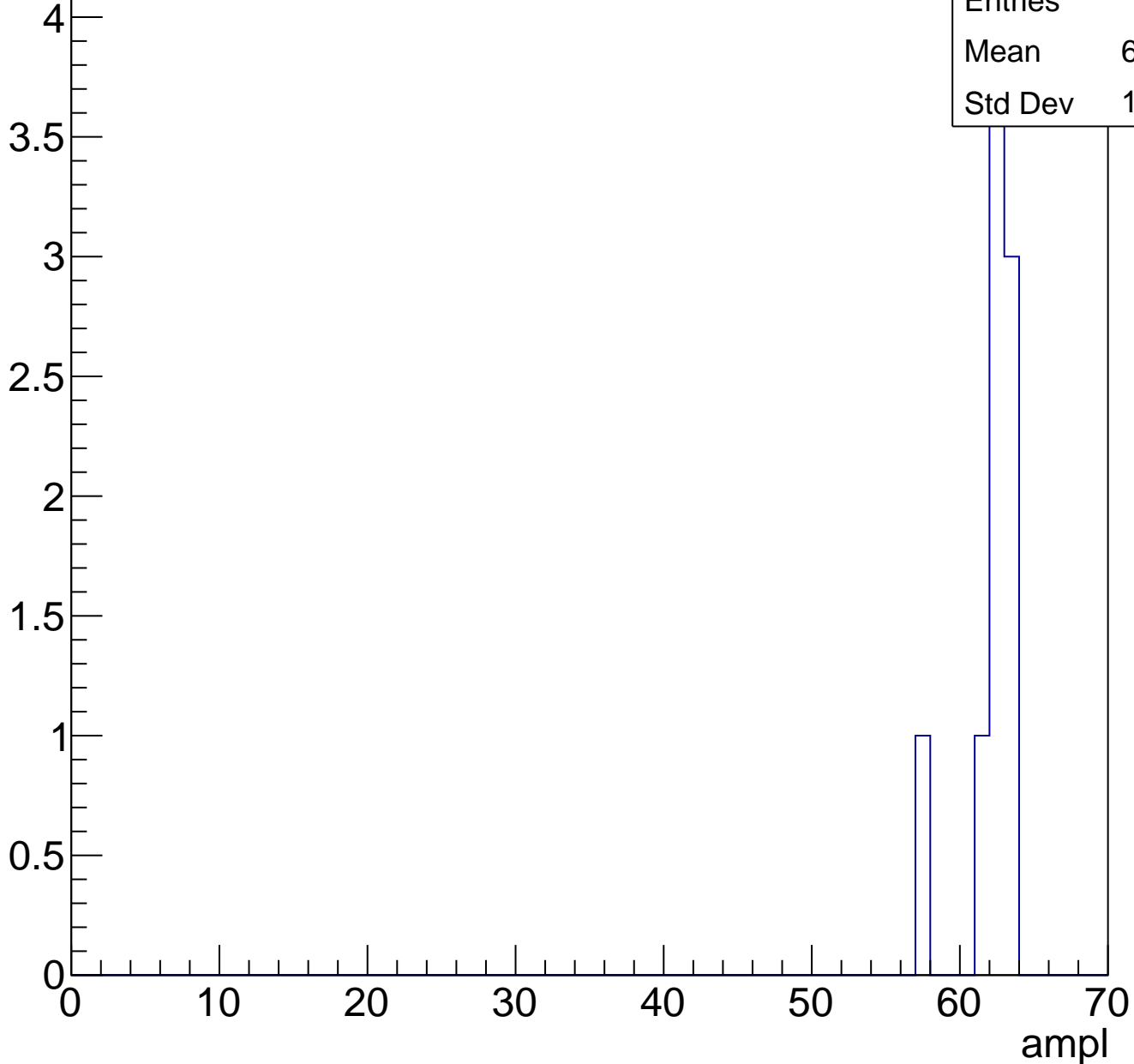
60

70

# B0L000S, U18-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U18-ch70, adc0

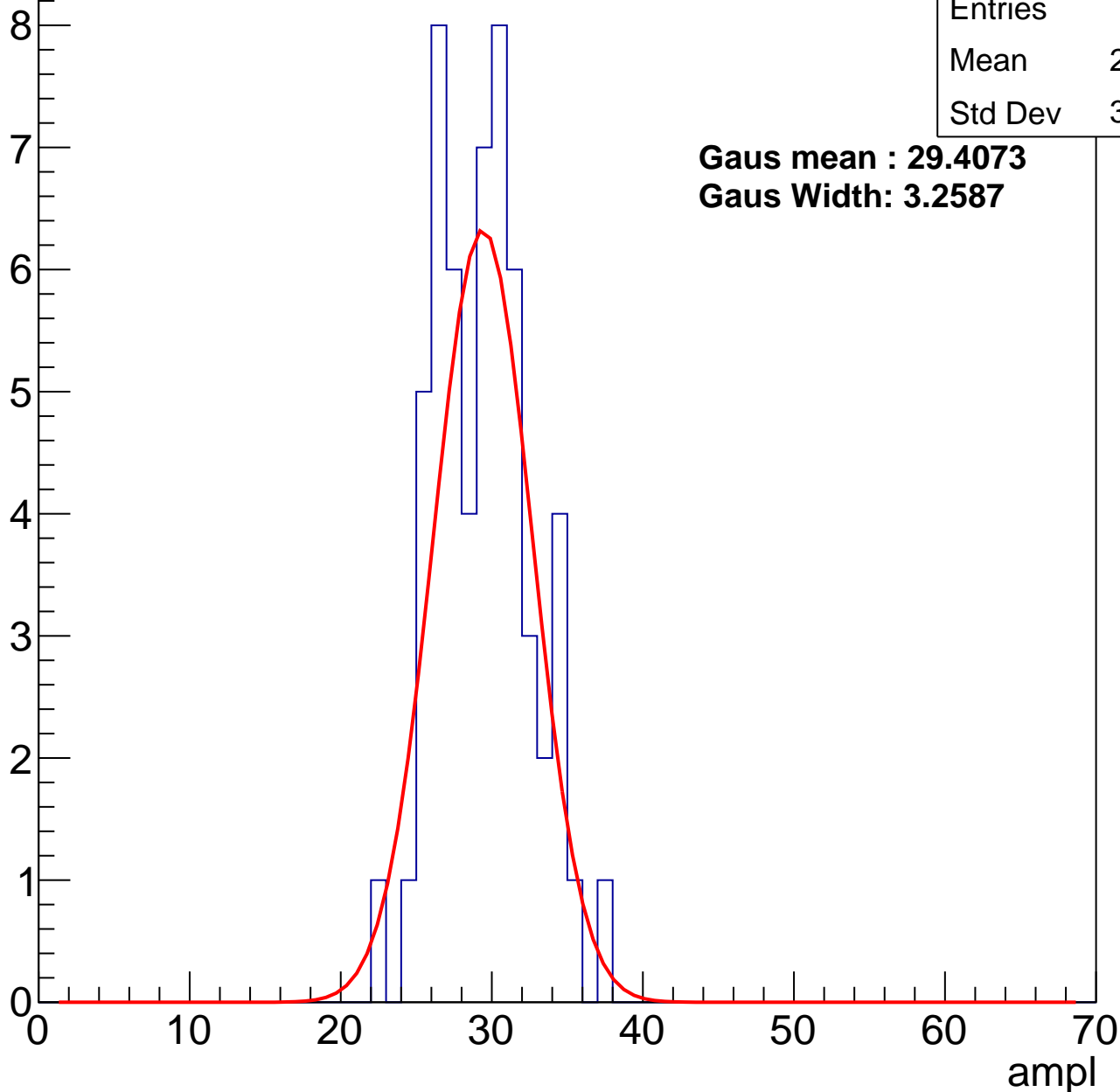
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	28.98
Std Dev	3.092

**Gaus mean : 29.4073**

**Gaus Width: 3.2587**



# B0L000S, U18-ch70, adc1

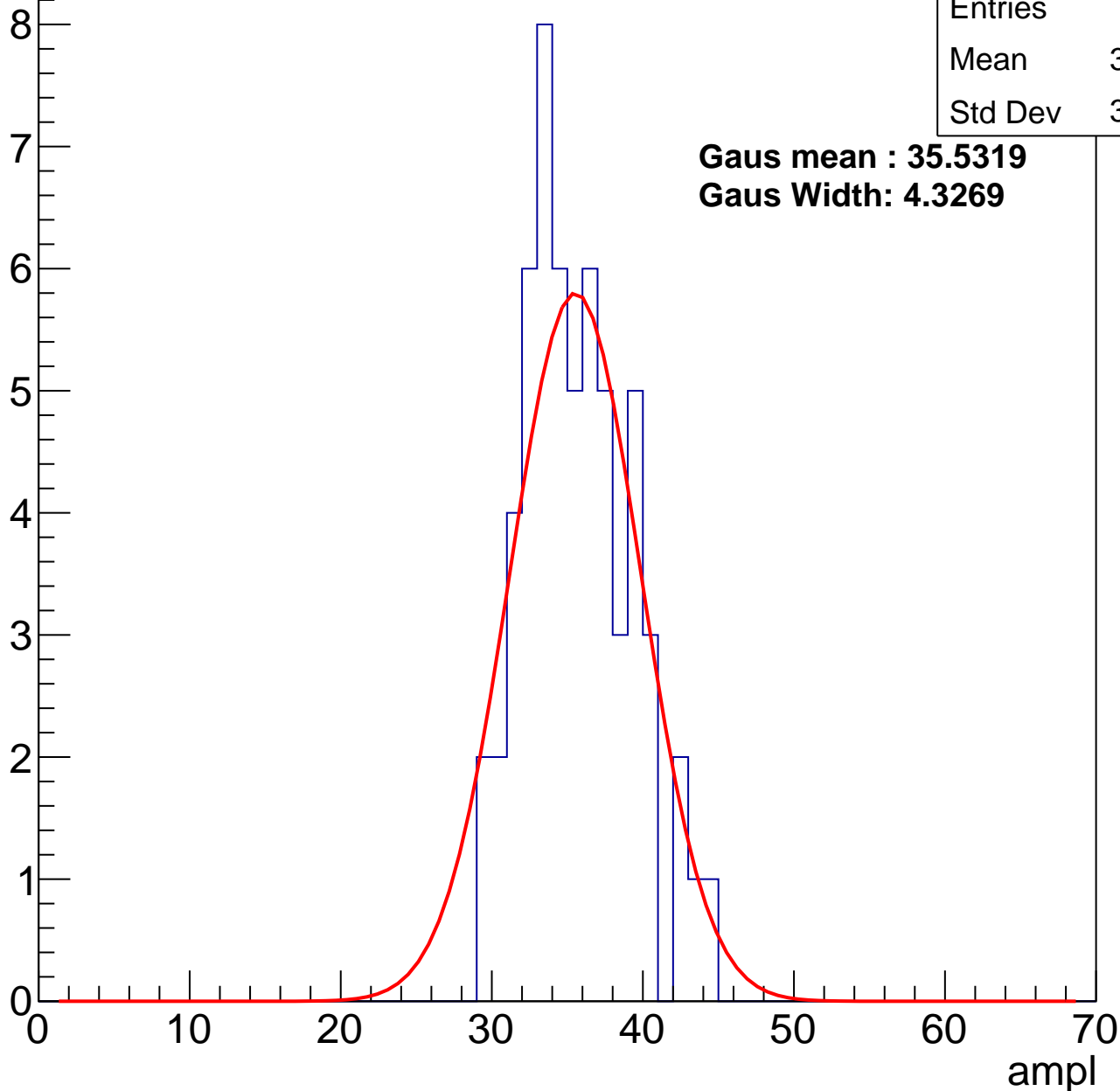
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.22
Std Dev	3.508

**Gaus mean : 35.5319**

**Gaus Width: 4.3269**



# B0L000S, U18-ch70, adc2

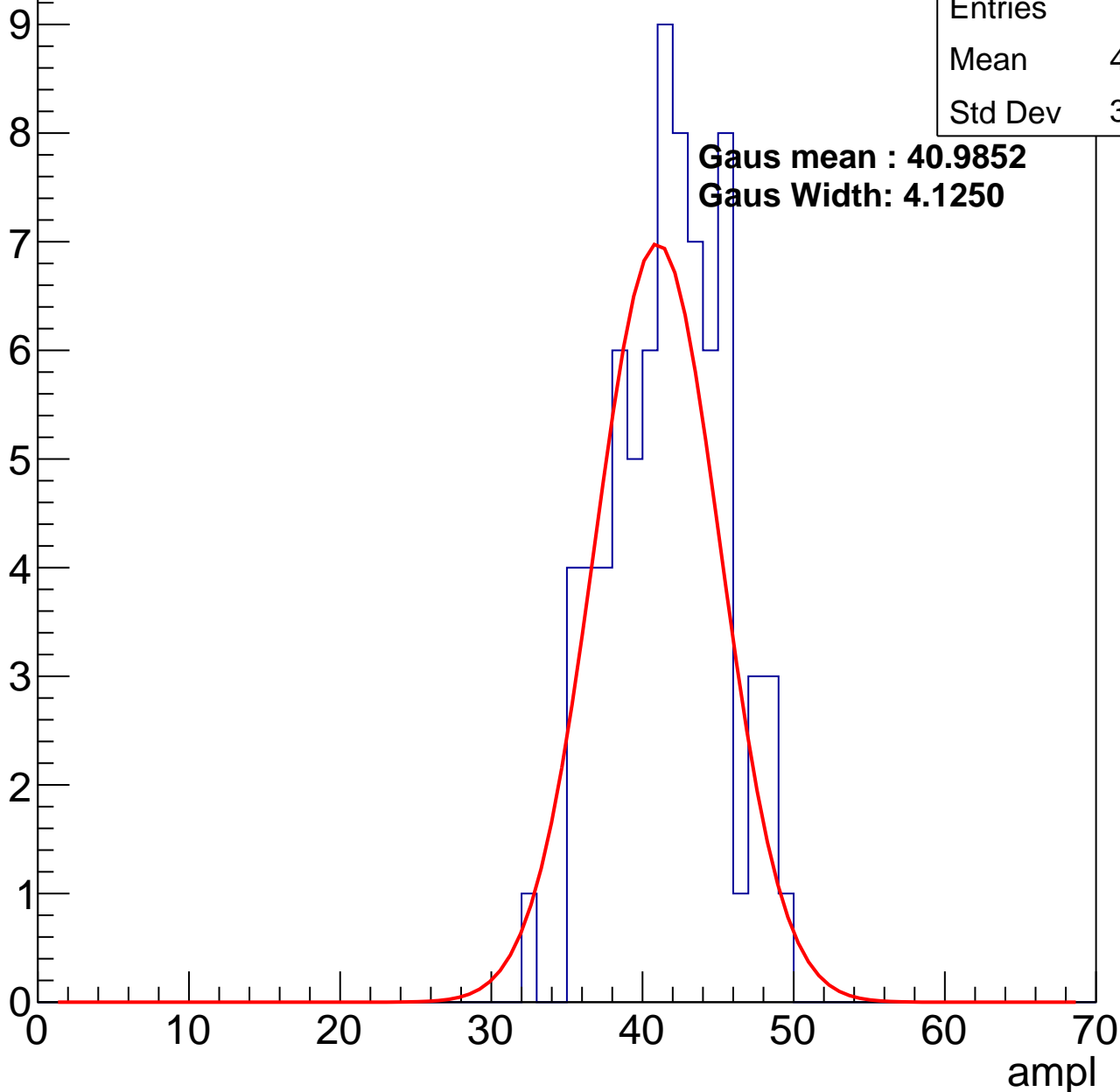
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	41.28
Std Dev	3.673

**Gaus mean : 40.9852**

**Gaus Width: 4.1250**

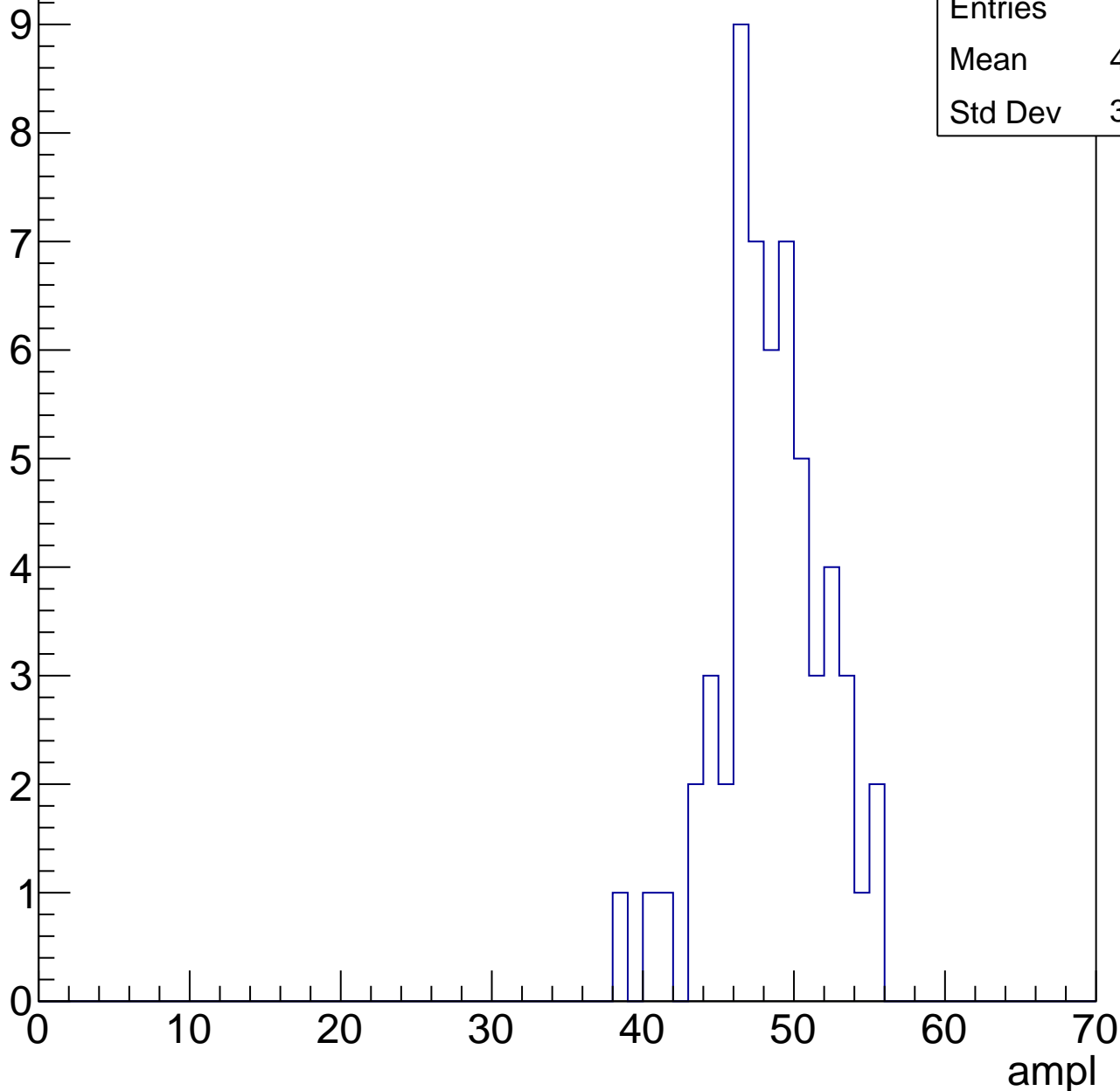


# B0L000S, U18-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	47.98
Std Dev	3.502

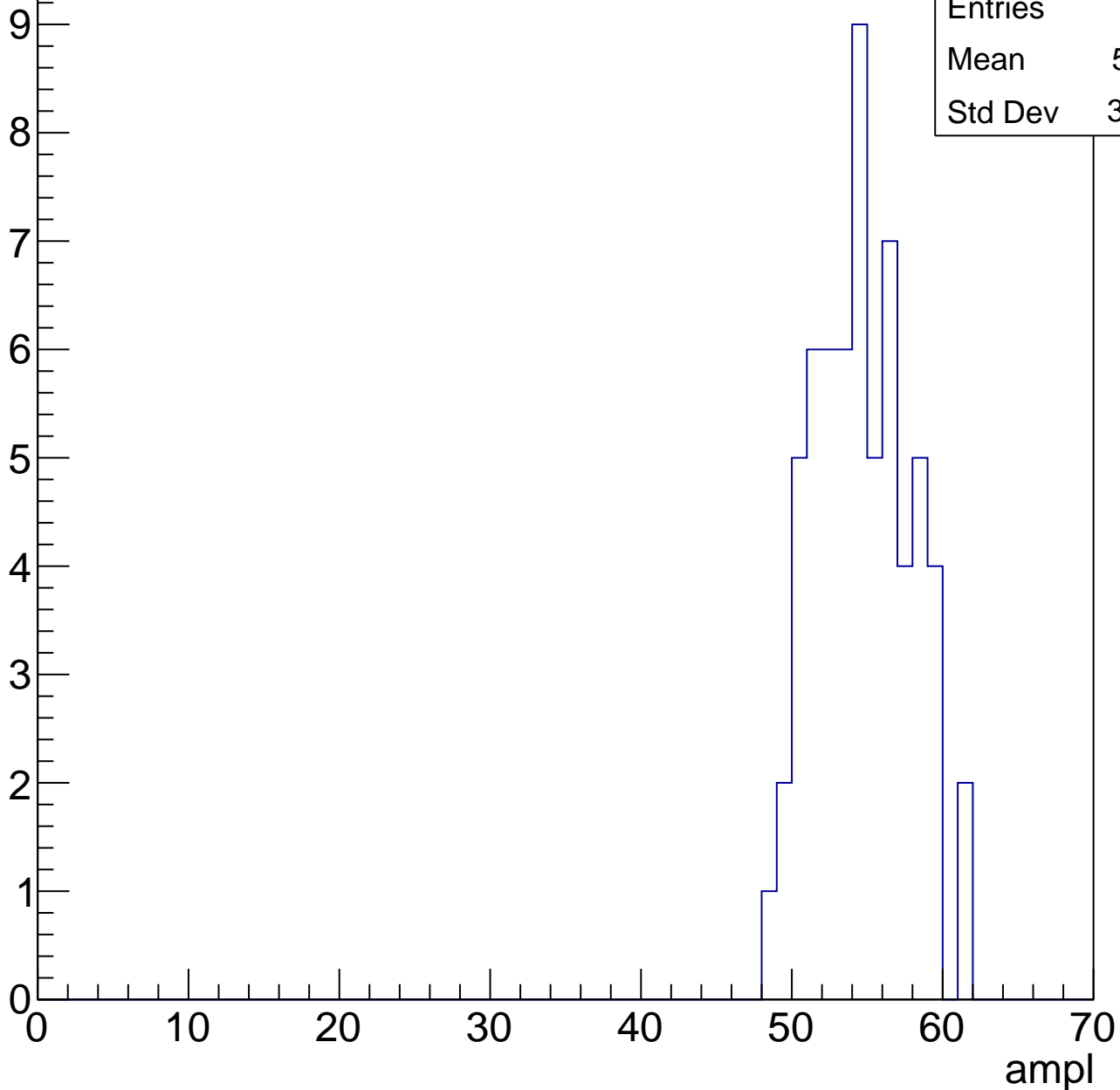


# B0L000S, U18-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	54.21
Std Dev	3.096

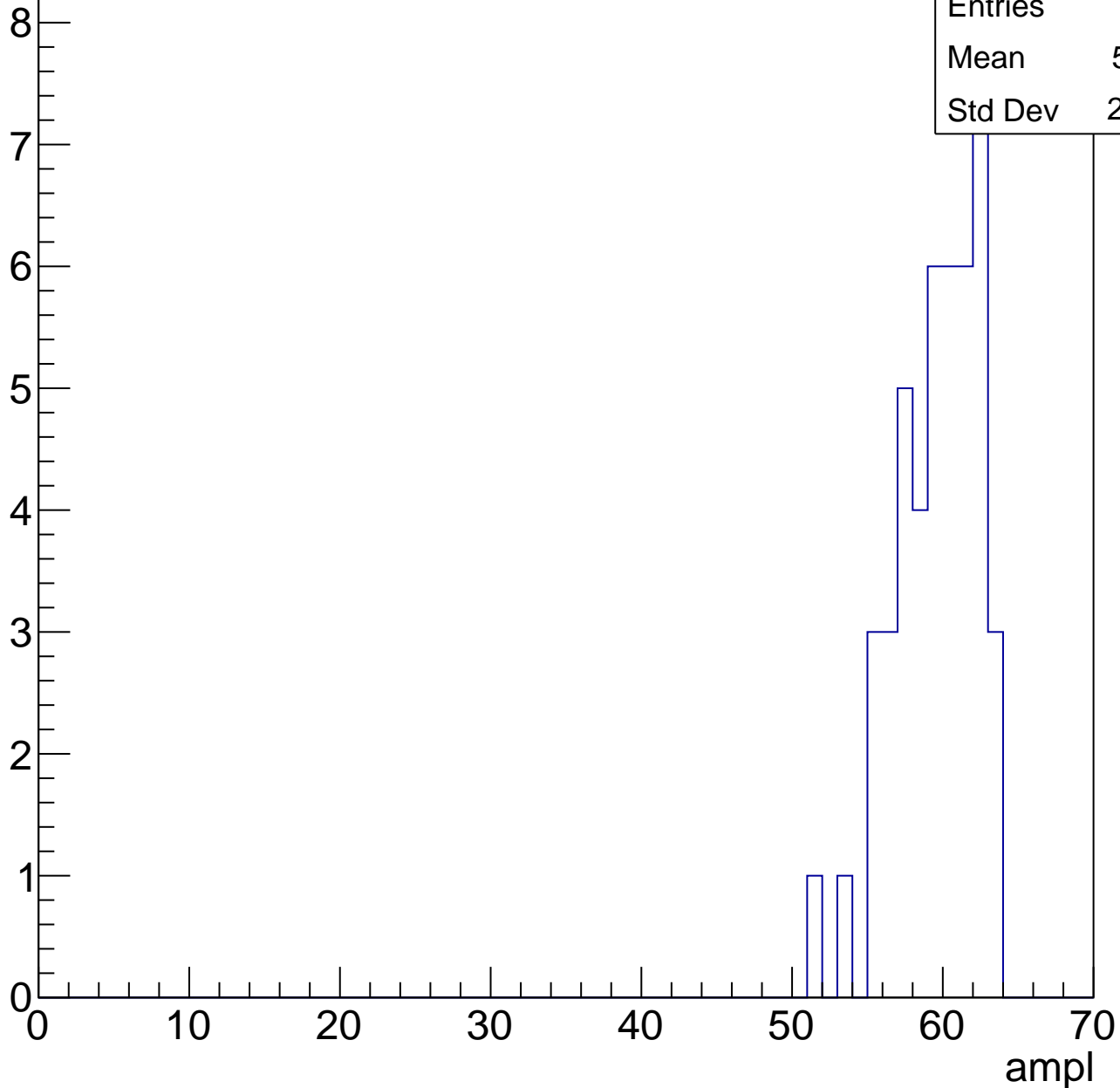


# B0L000S, U18-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	59.11
Std Dev	2.752

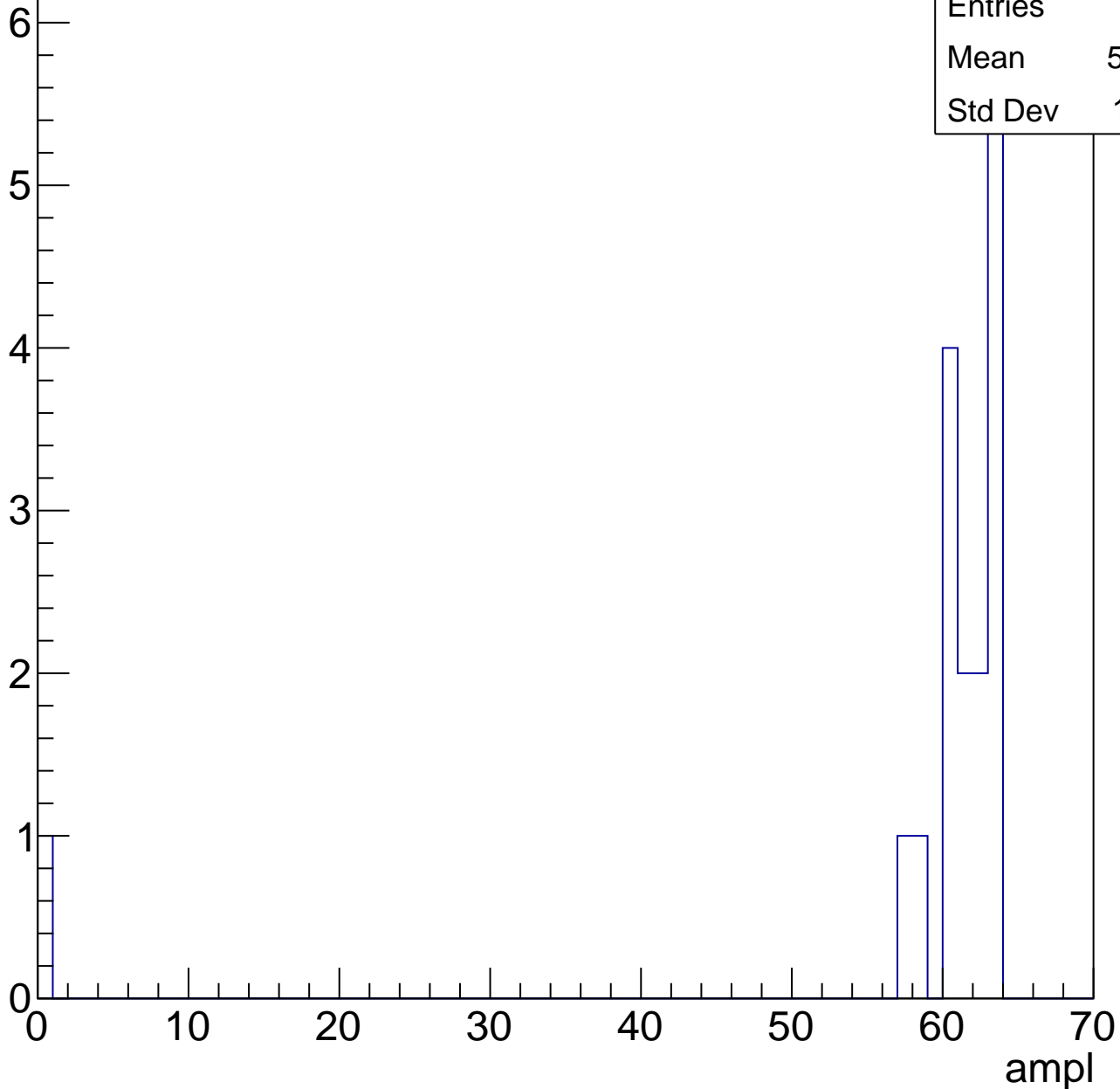


# B0L000S, U18-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	57.59
Std Dev	14.51





# B0L000S, U18-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch71, adc0

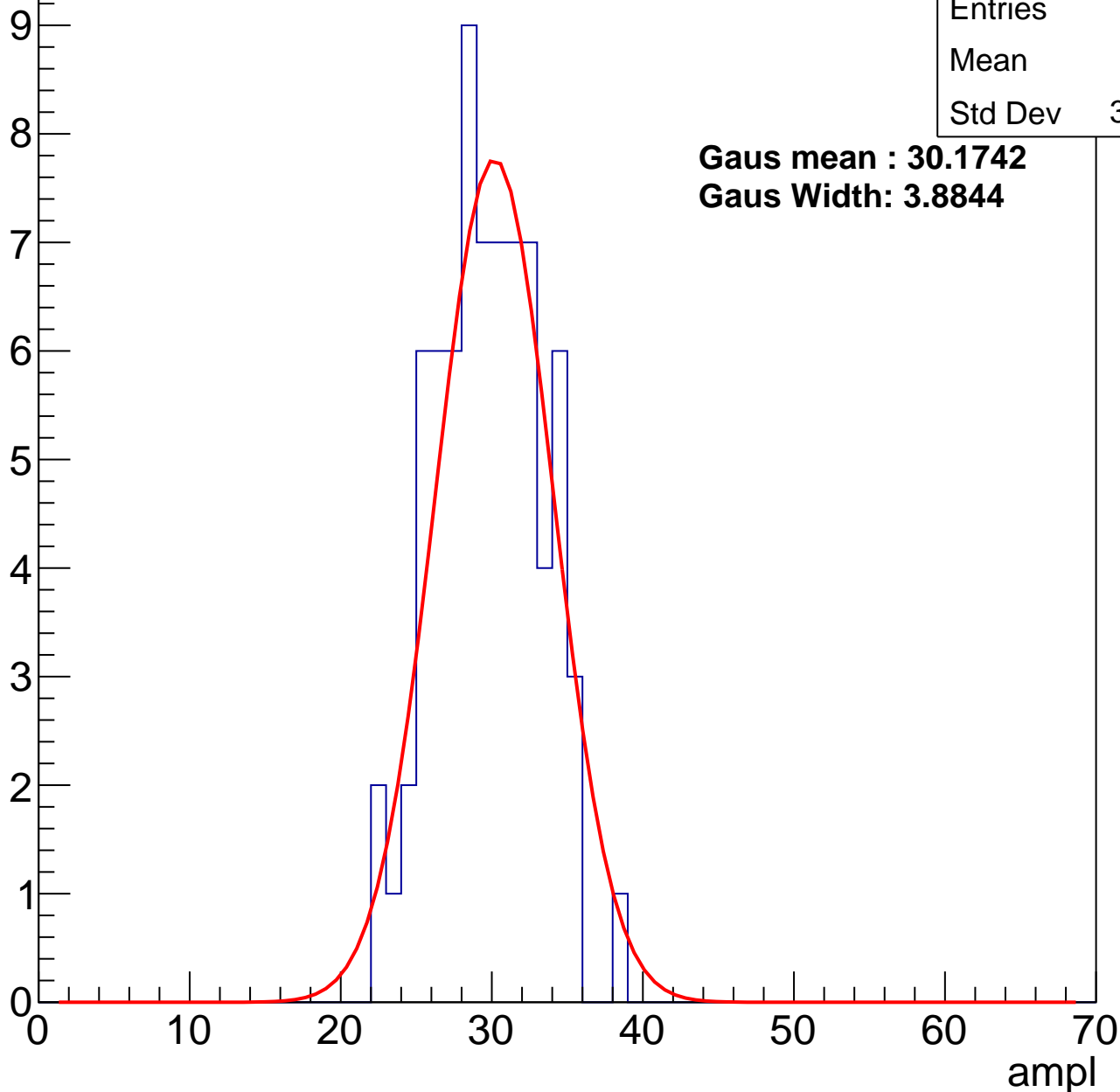
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.3
Std Dev	3.412

**Gaus mean : 30.1742**

**Gaus Width: 3.8844**



# B0L000S, U18-ch71, adc1

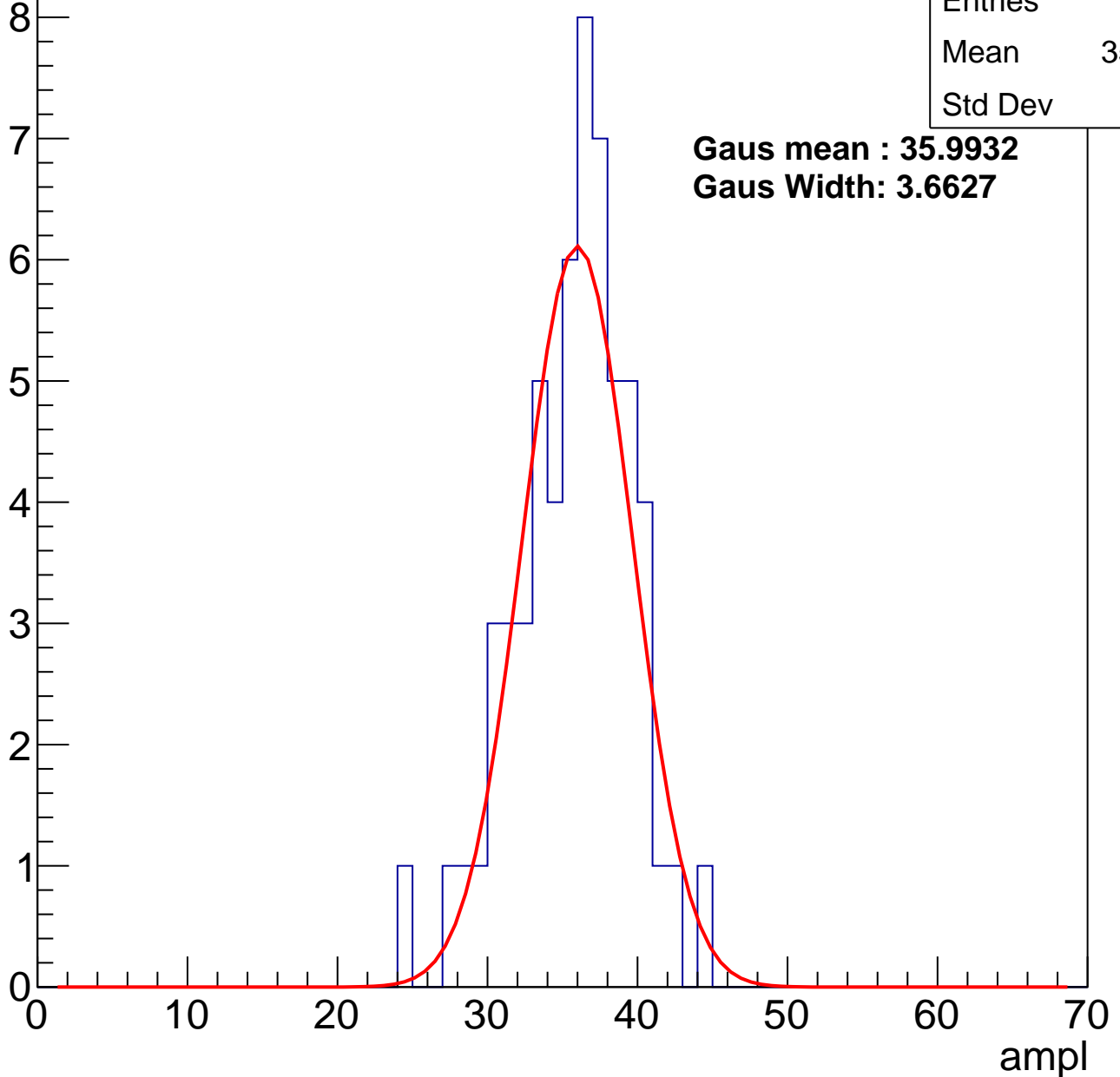
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	35.28
Std Dev	3.8

**Gaus mean : 35.9932**

**Gaus Width: 3.6627**



# B0L000S, U18-ch71, adc2

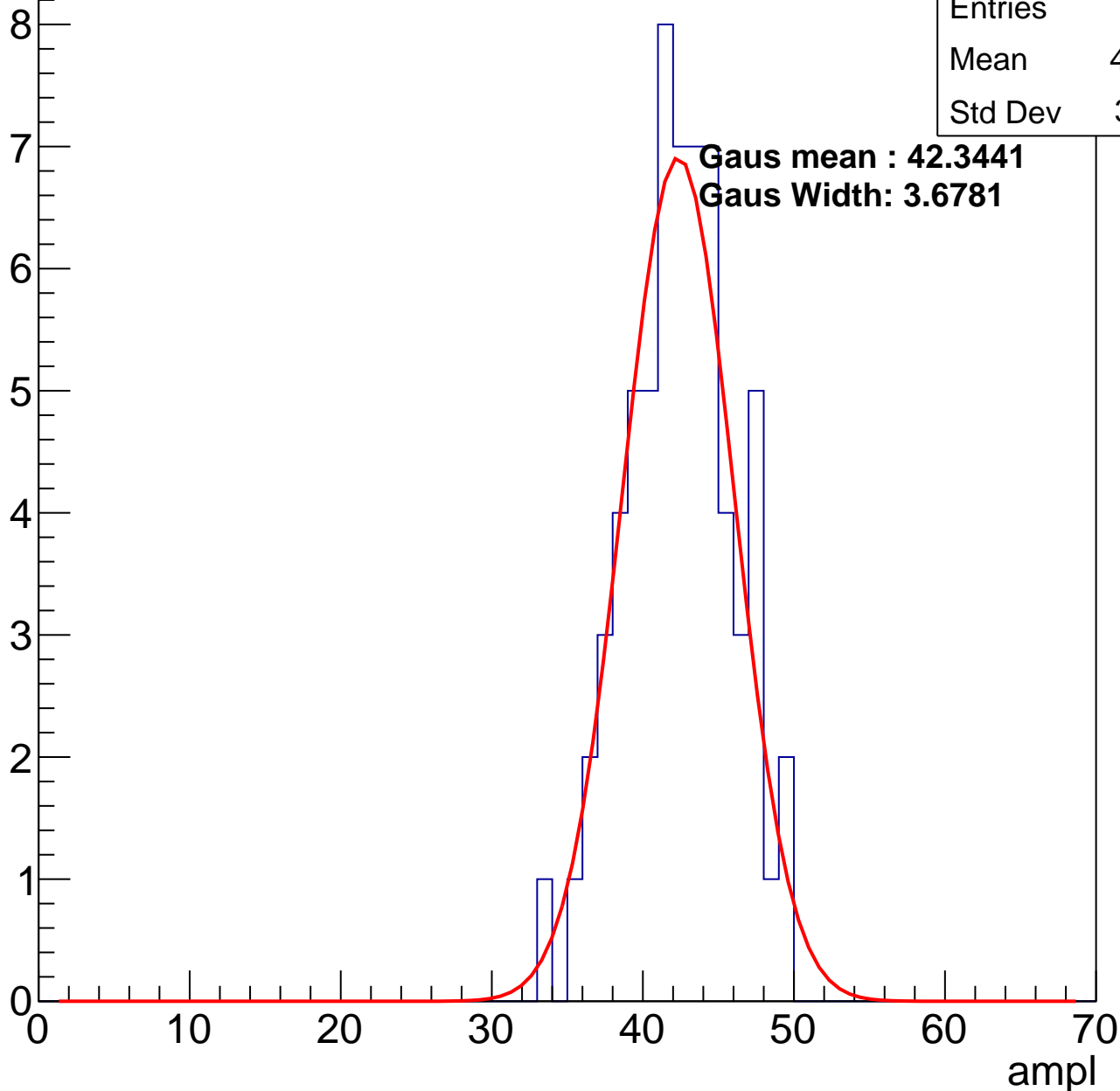
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	41.97
Std Dev	3.491

**Gaus mean : 42.3441**

**Gaus Width: 3.6781**

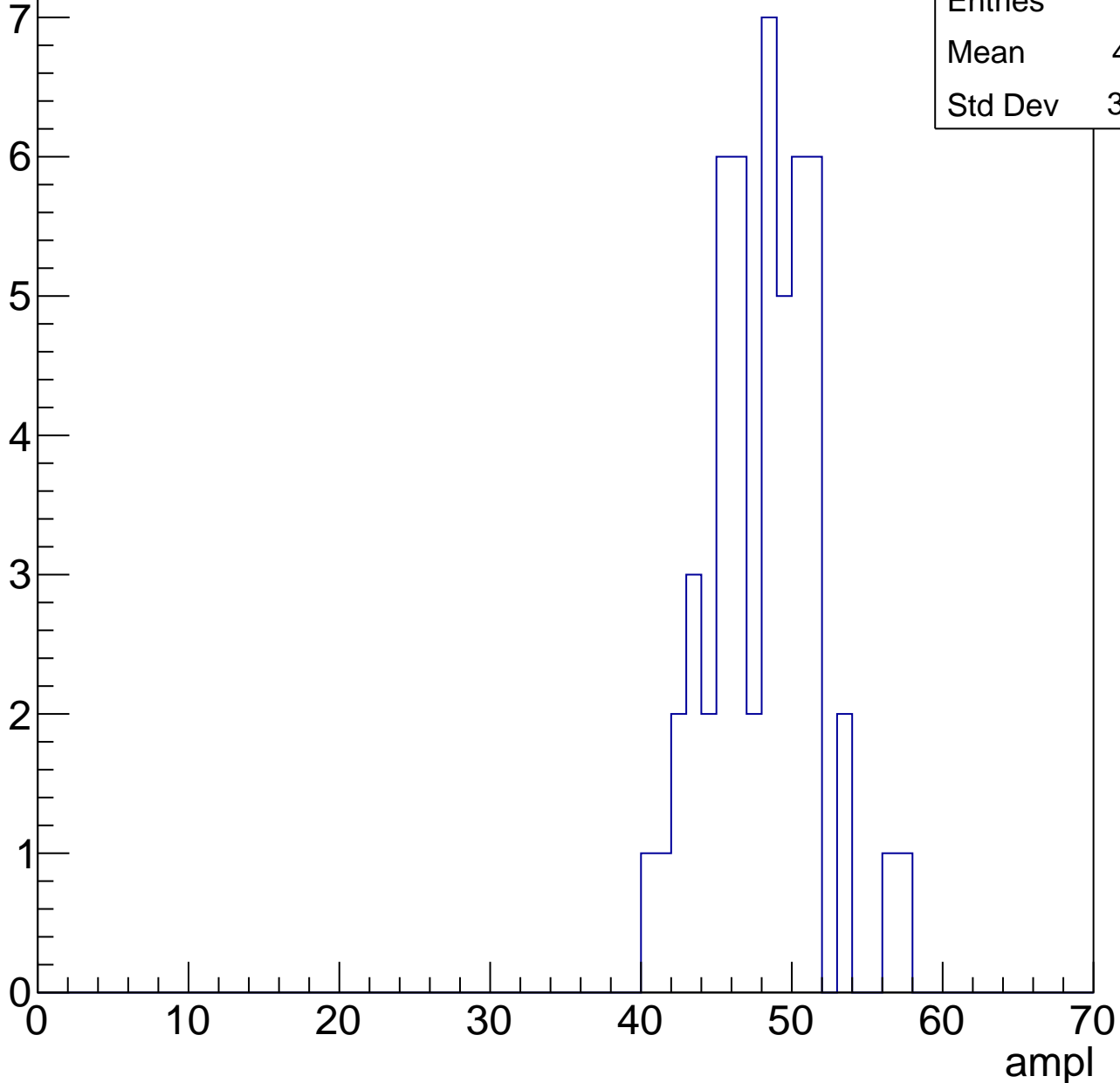


# B0L000S, U18-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	47.61
Std Dev	3.548



# B0L000S, U18-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	53.44
Std Dev	3.21

Entry

10

8

6

4

2

0

0

10

20

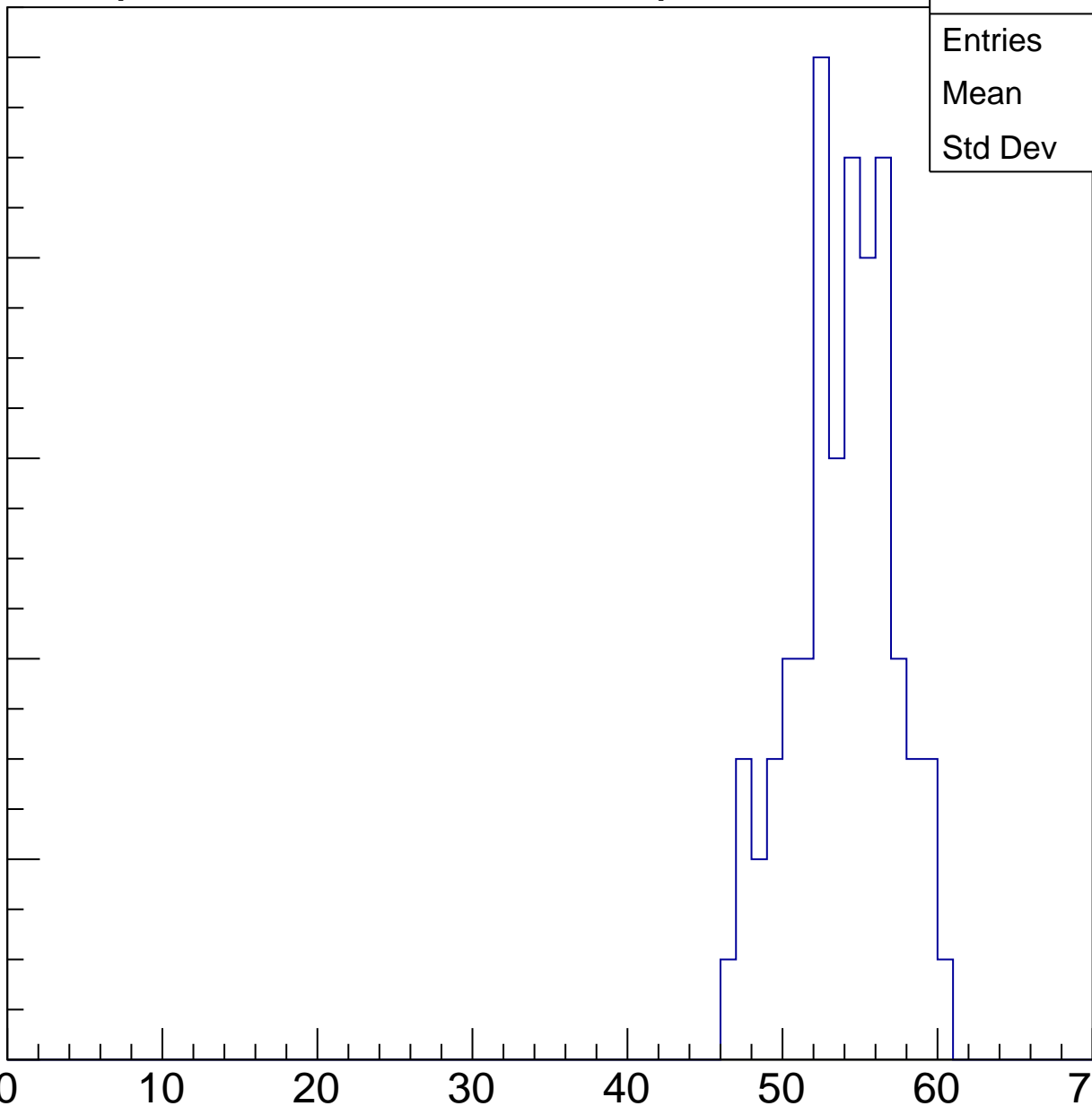
30

40

50

60

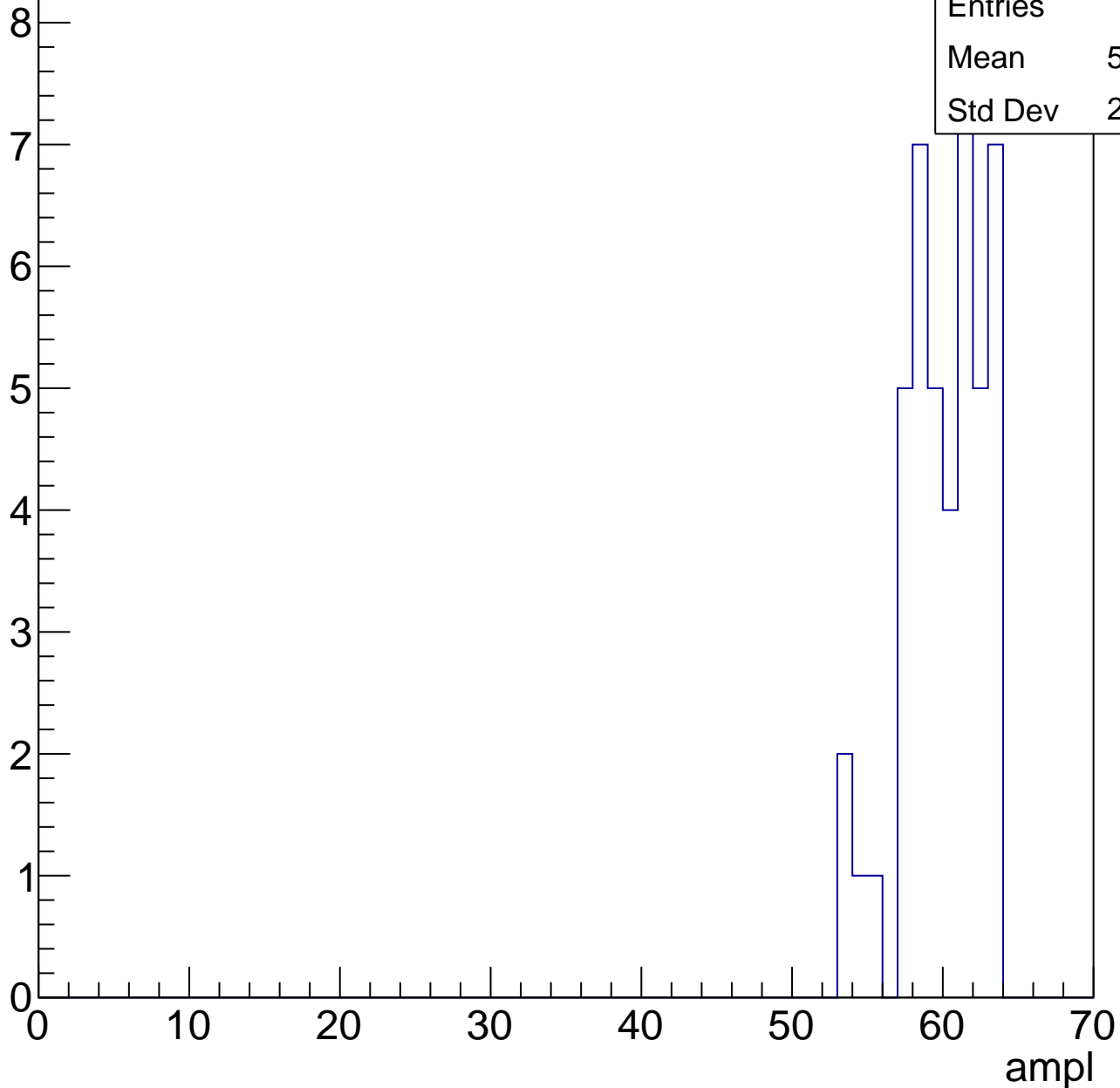
ampl



# B0L000S, U18-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

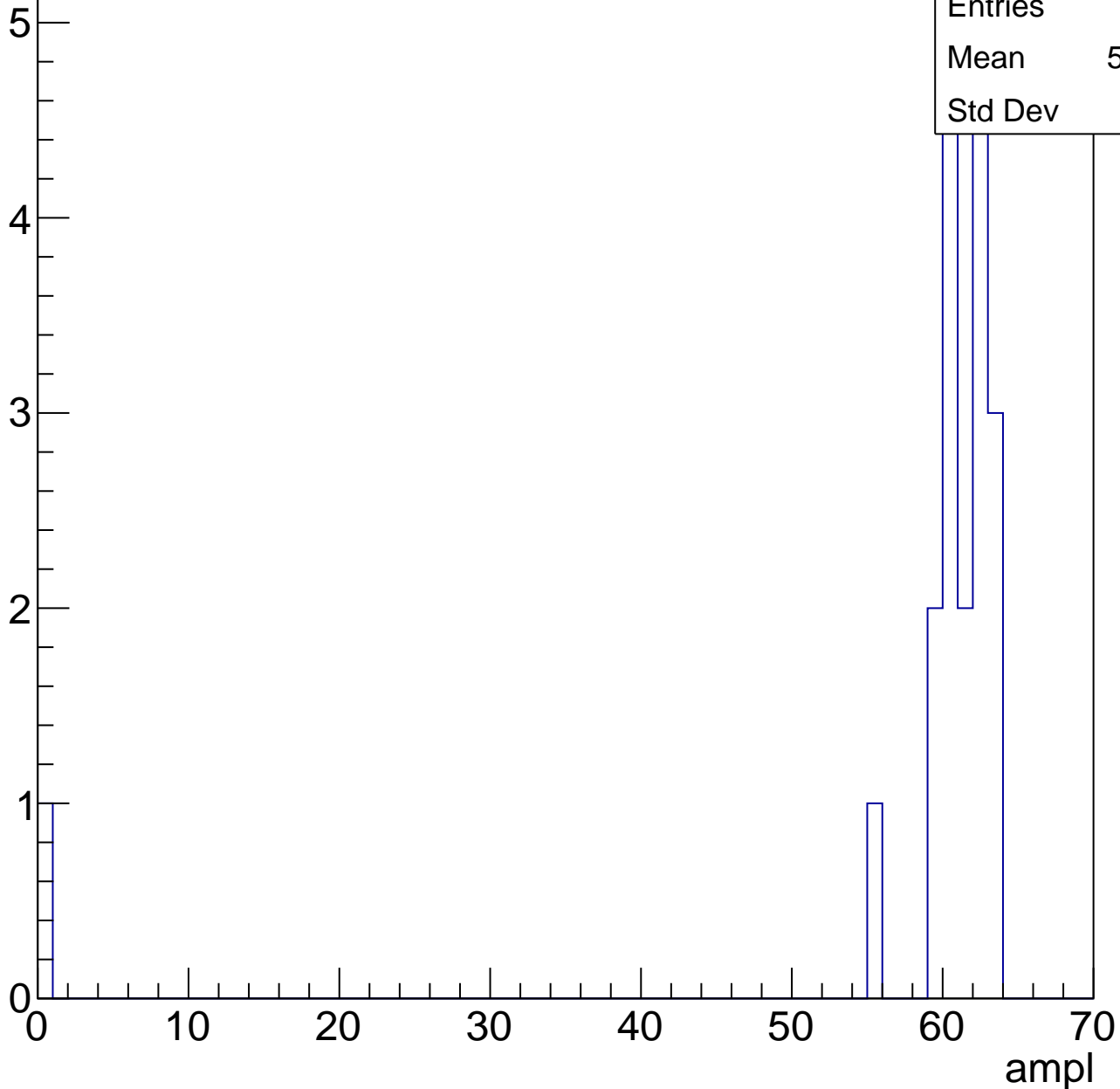


# B0L000S, U18-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	57.58
Std Dev	13.7





# B0L000S, U18-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch72, adc0

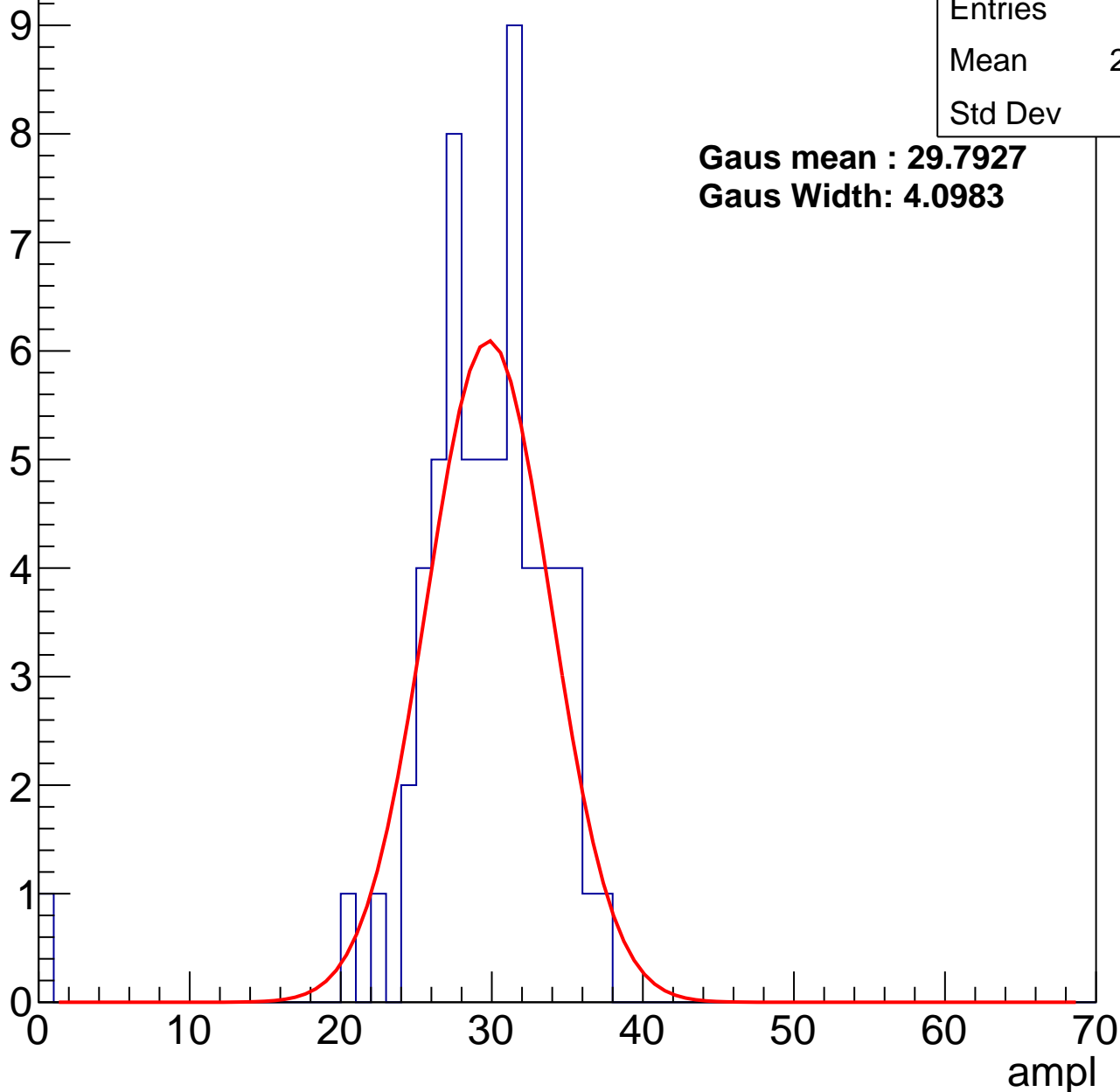
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.05
Std Dev	5.1

**Gaus mean : 29.7927**

**Gaus Width: 4.0983**



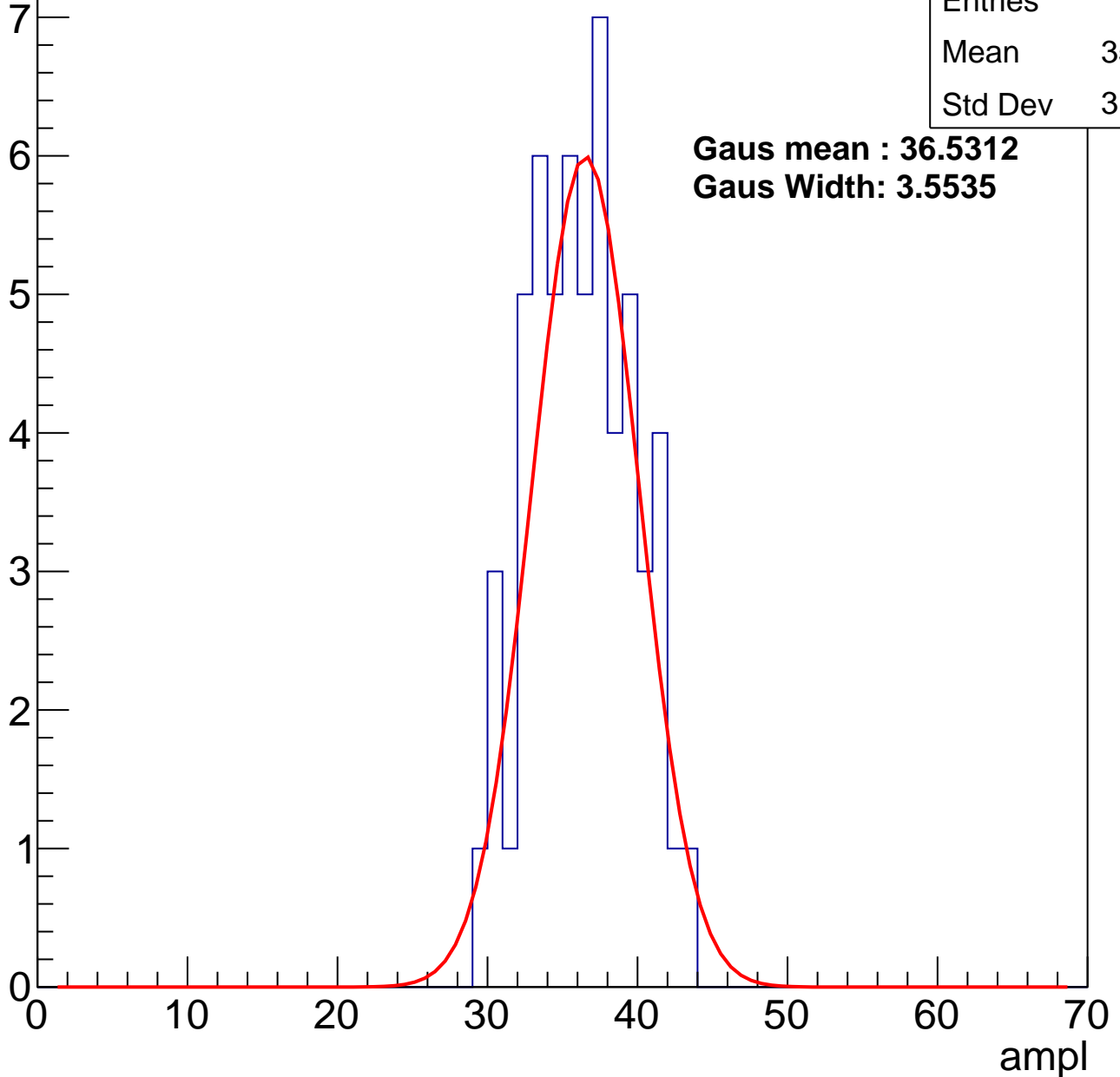
# B0L000S, U18-ch72, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.84
Std Dev	3.355

**Gaus mean : 36.5312**  
**Gaus Width: 3.5535**



# B0L000S, U18-ch72, adc2

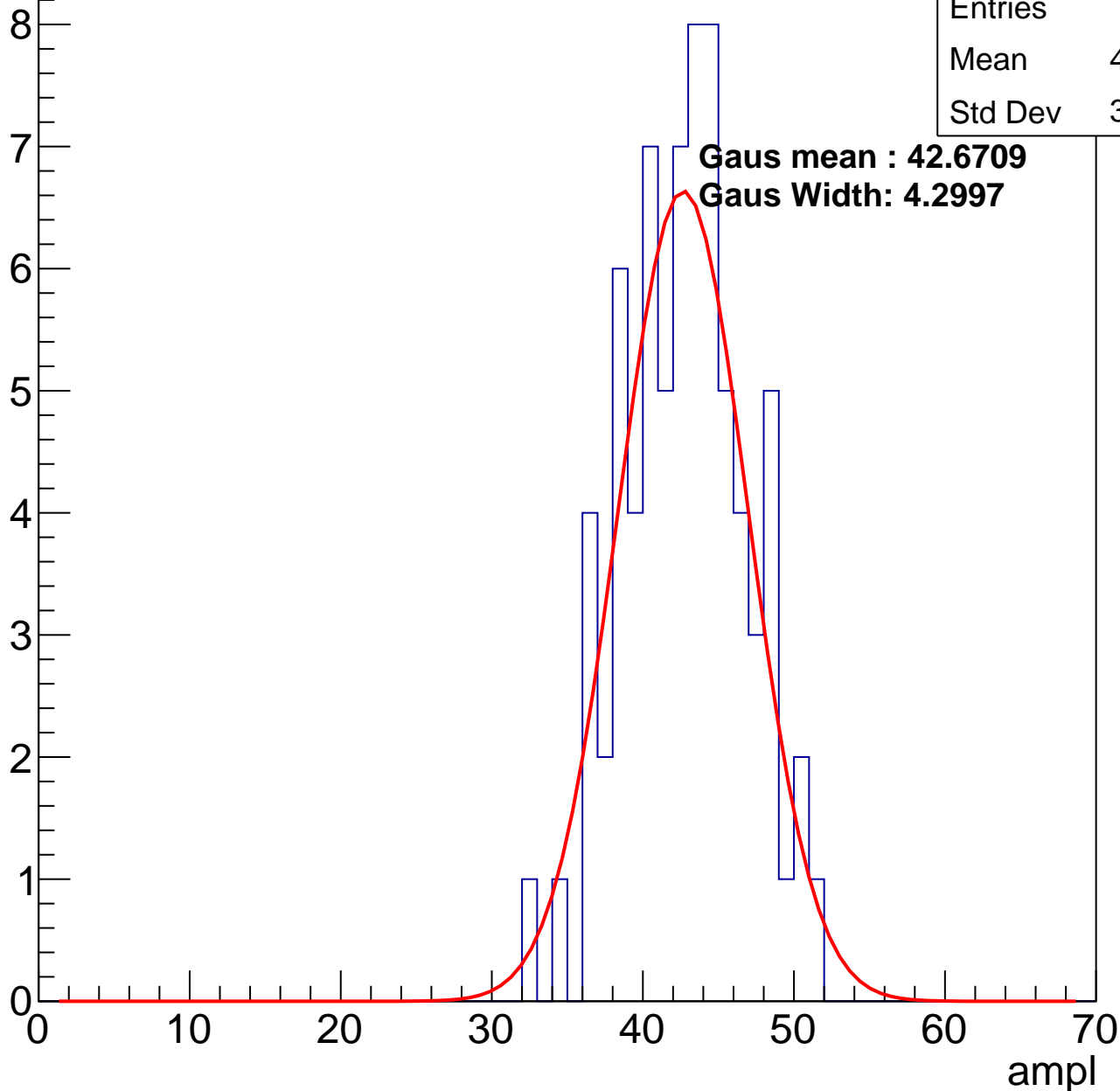
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.34
Std Dev	3.987

**Gaus mean : 42.6709**

**Gaus Width: 4.2997**

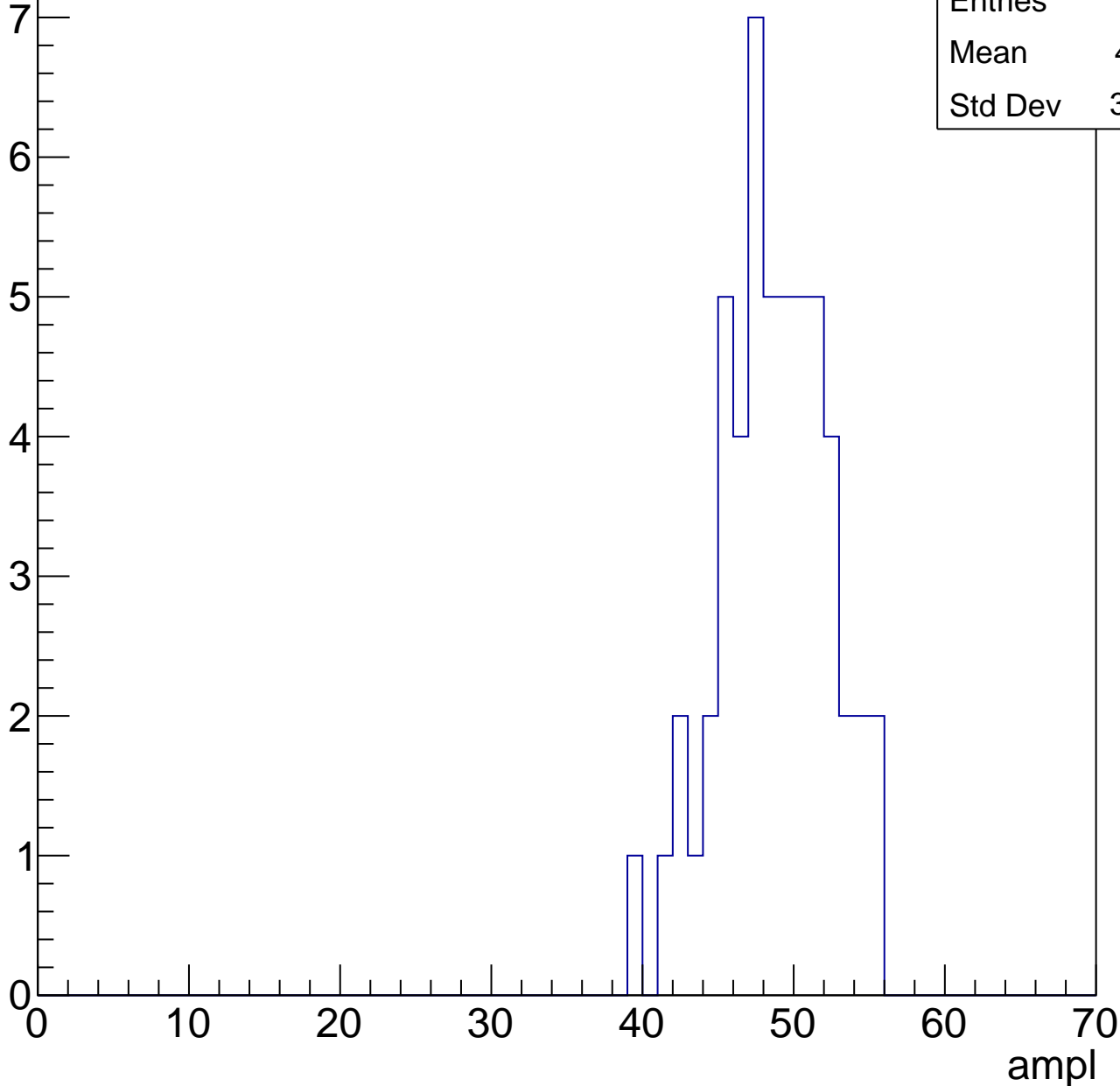


# B0L000S, U18-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	48.21
Std Dev	3.579

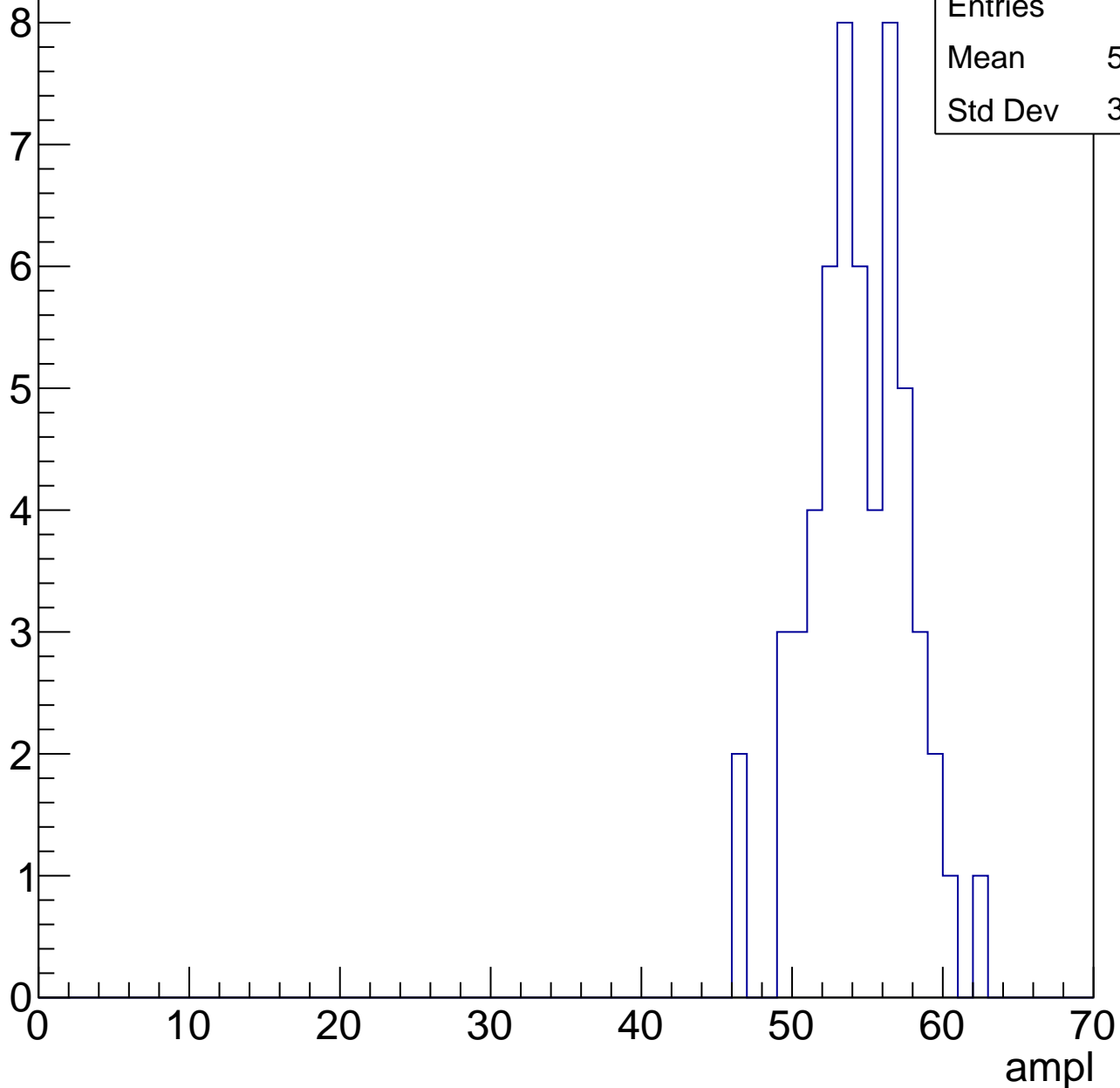


# B0L000S, U18-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	53.93
Std Dev	3.267

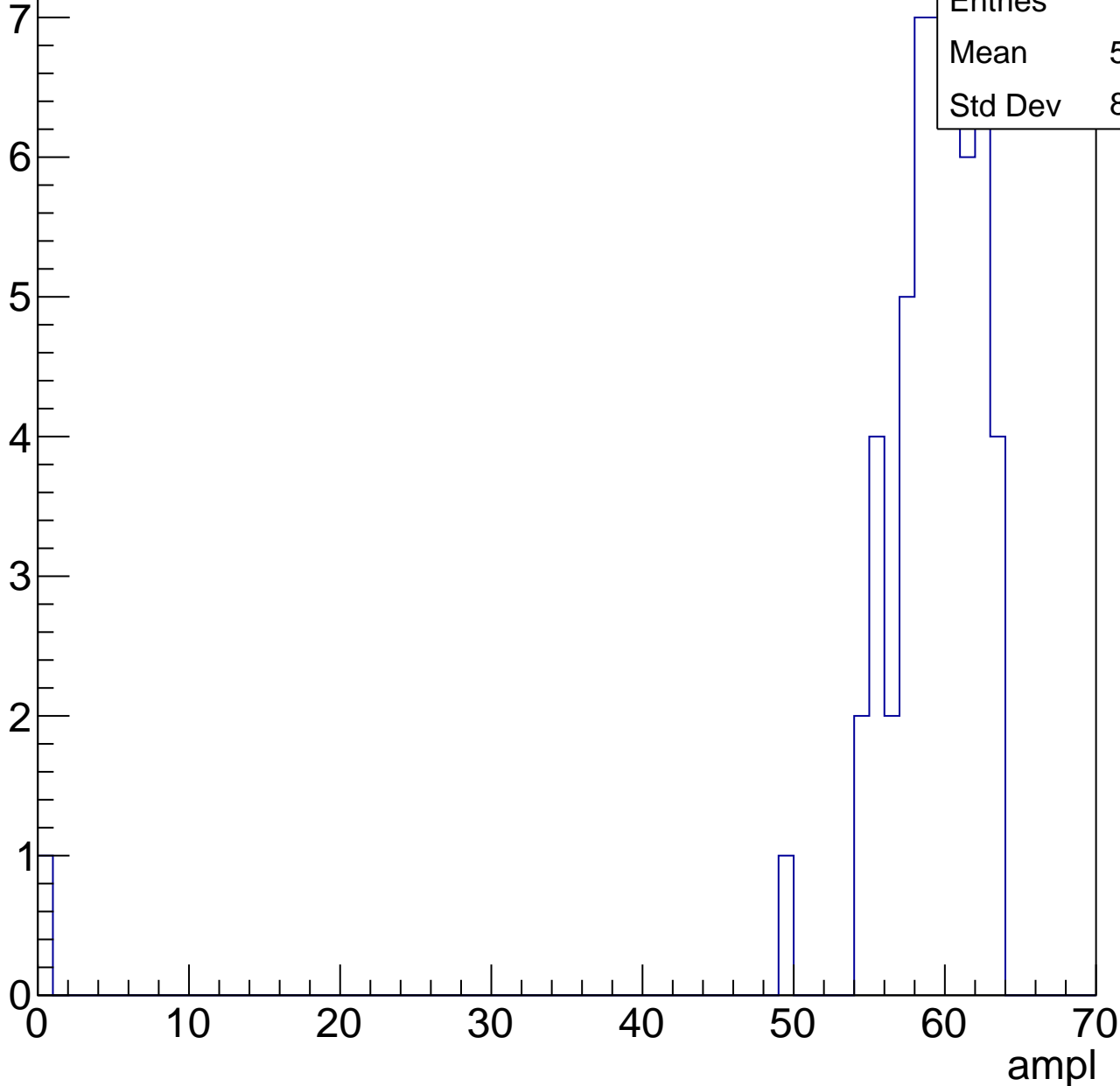


# B0L000S, U18-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

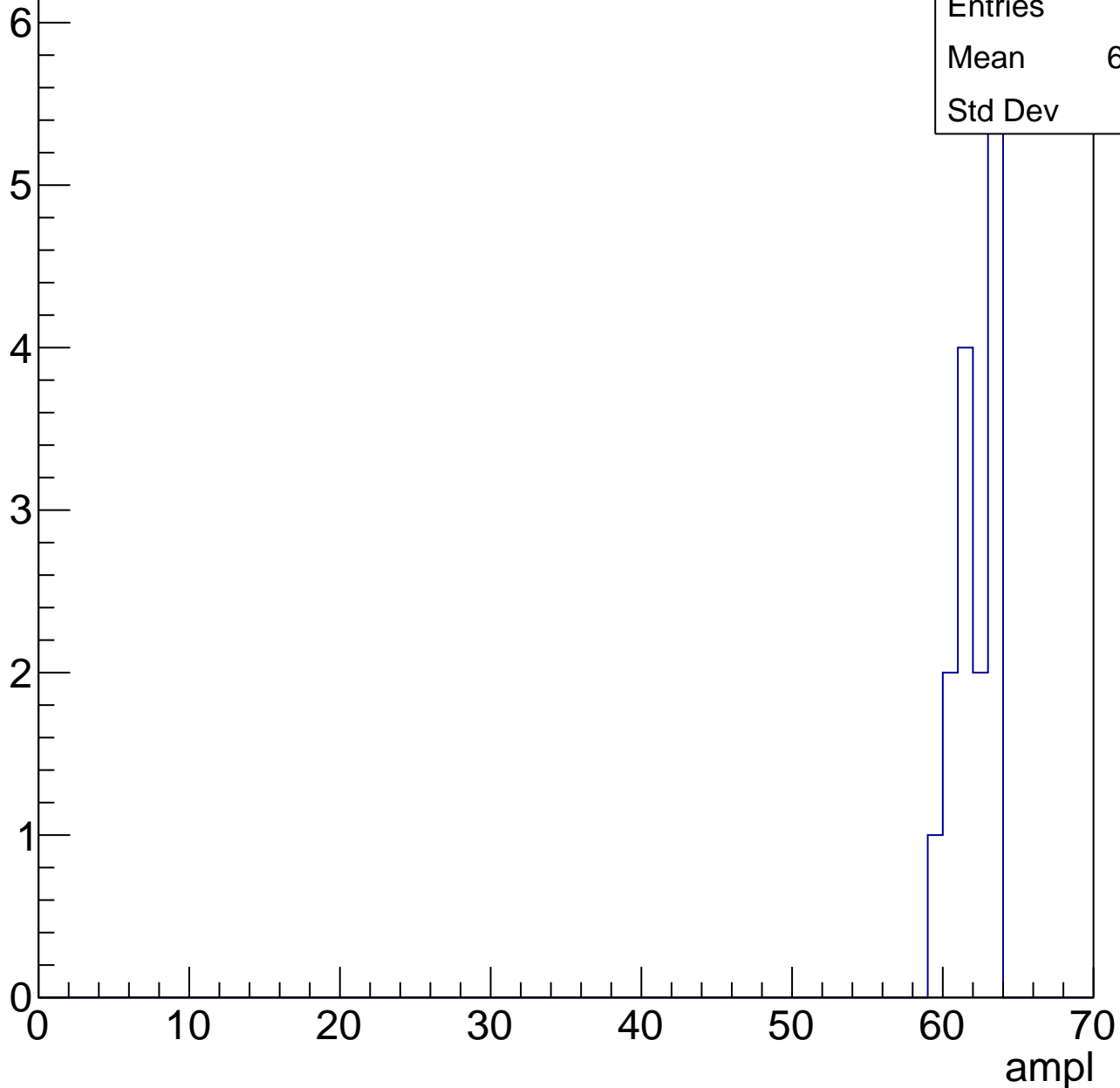
Entries	53
Mean	57.83
Std Dev	8.496



# B0L000S, U18-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	15
Mean	61.67
Std Dev	1.3



# B0L000S, U18-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch73, adc0

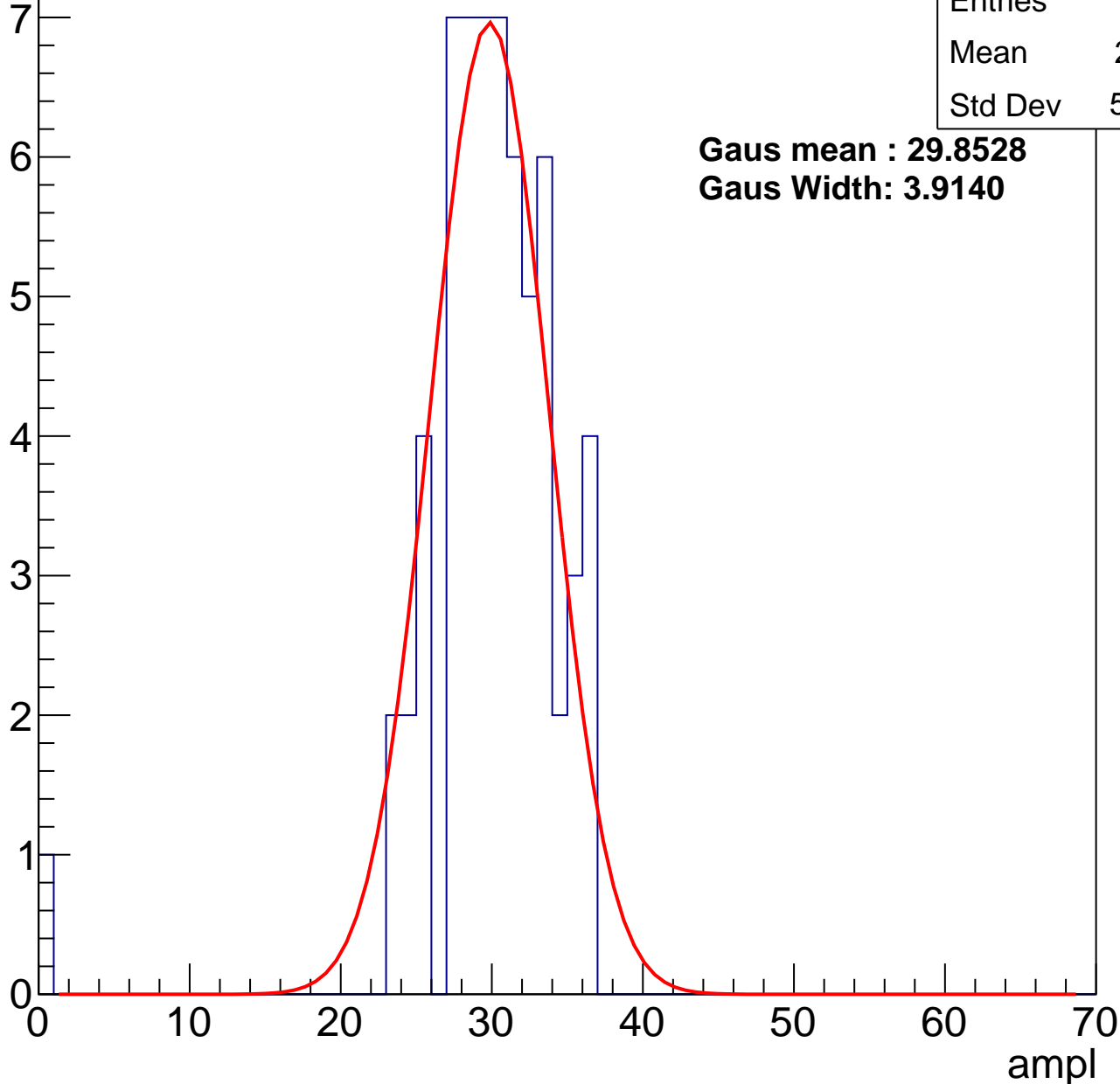
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.41
Std Dev	5.004

**Gaus mean : 29.8528**

**Gaus Width: 3.9140**



# B0L000S, U18-ch73, adc1

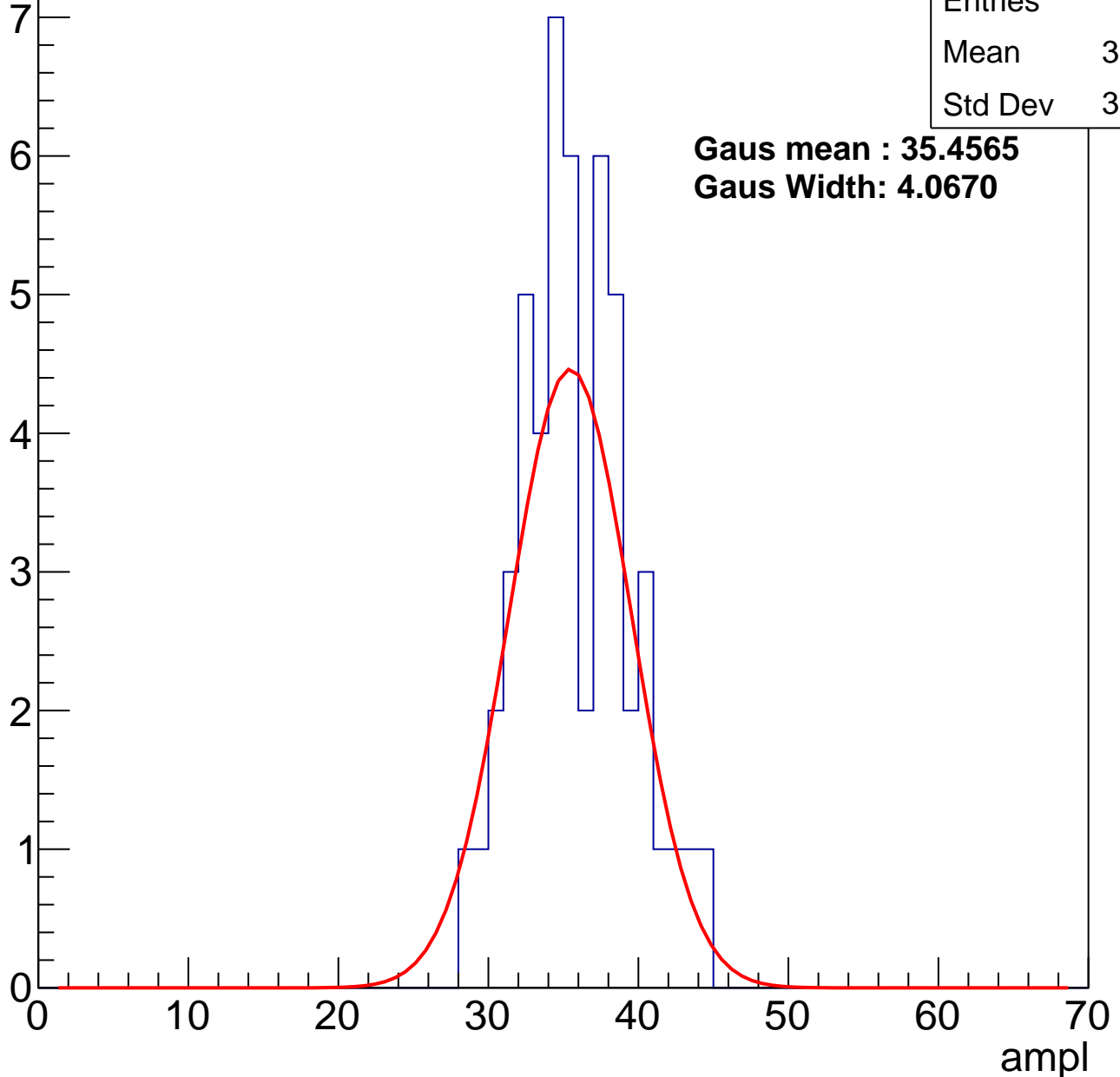
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	35.33
Std Dev	3.579

**Gaus mean : 35.4565**

**Gaus Width: 4.0670**



# B0L000S, U18-ch73, adc2

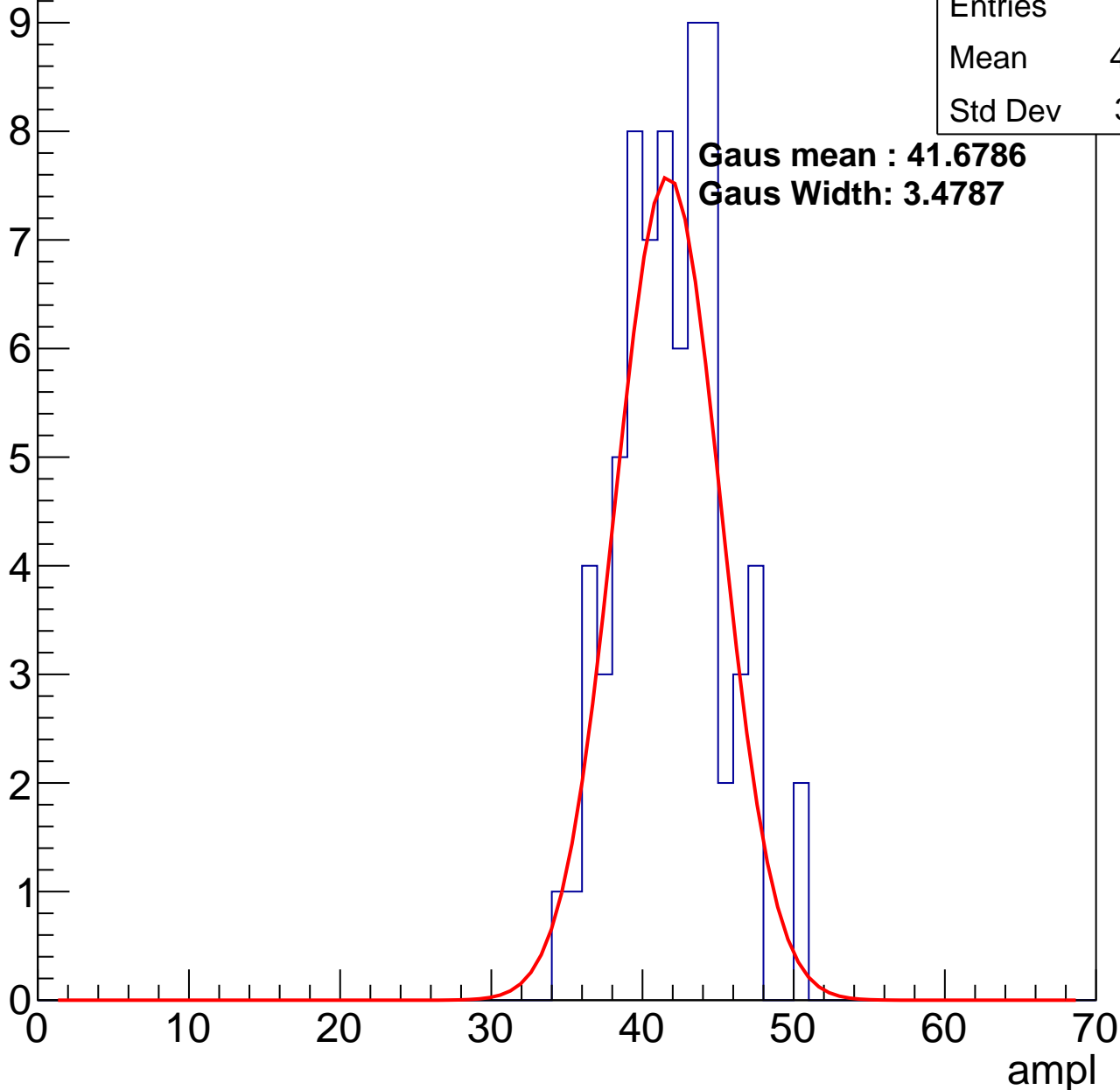
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	41.46
Std Dev	3.411

**Gaus mean : 41.6786**

**Gaus Width: 3.4787**

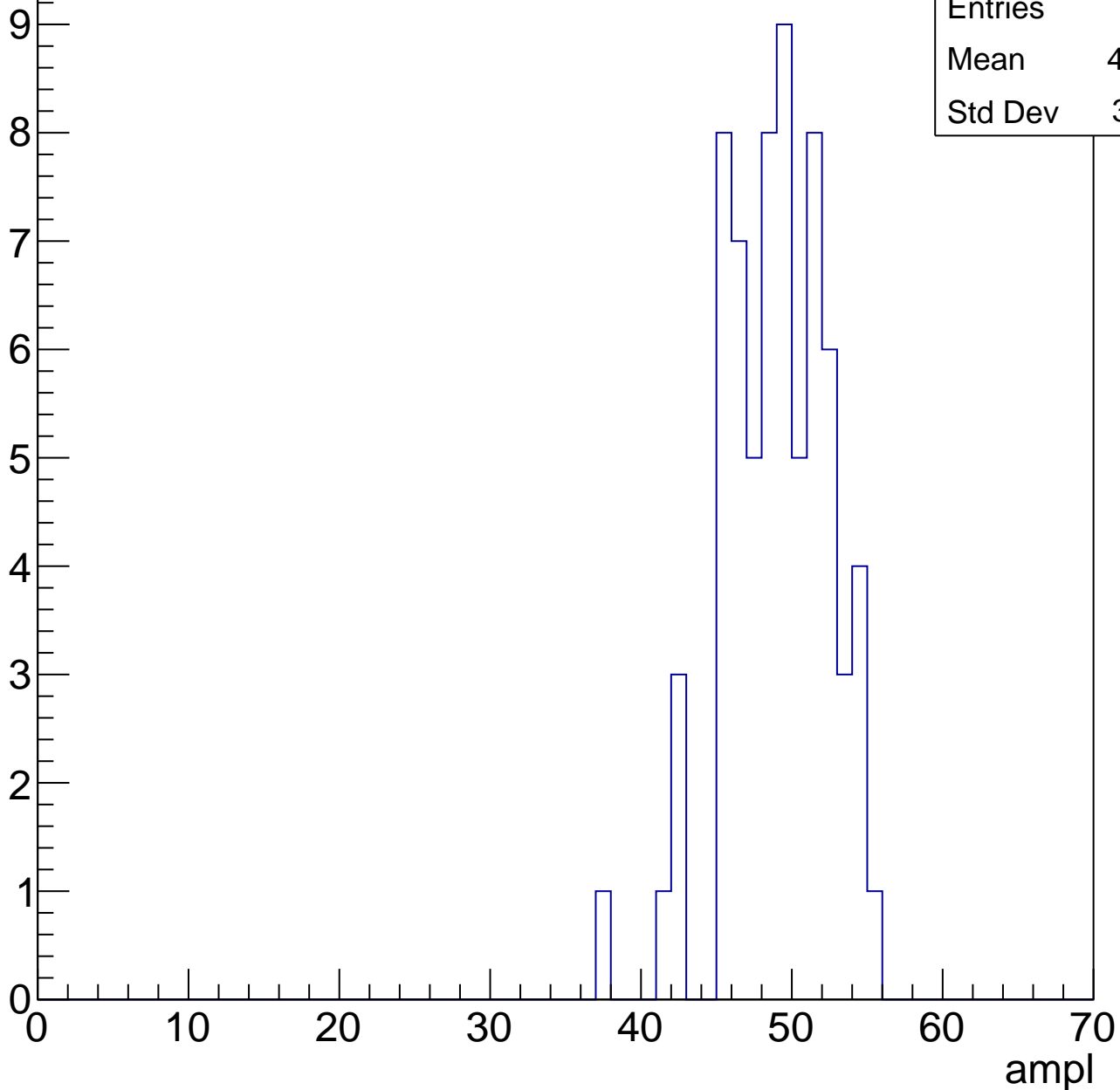


# B0L000S, U18-ch73, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	48.49
Std Dev	3.471

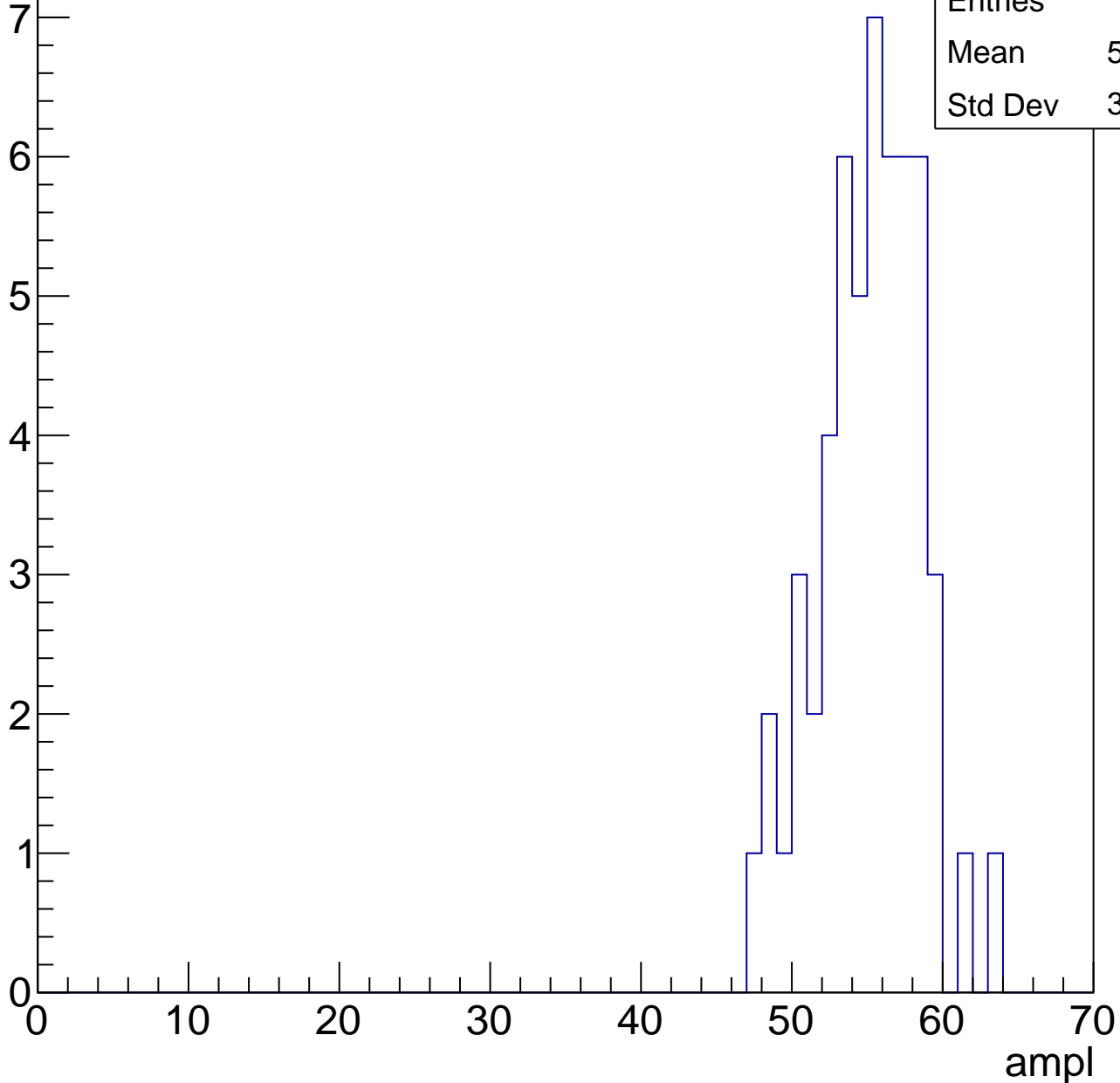


# B0L000S, U18-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	54.67
Std Dev	3.322



# B0L000S, U18-ch73, adc5

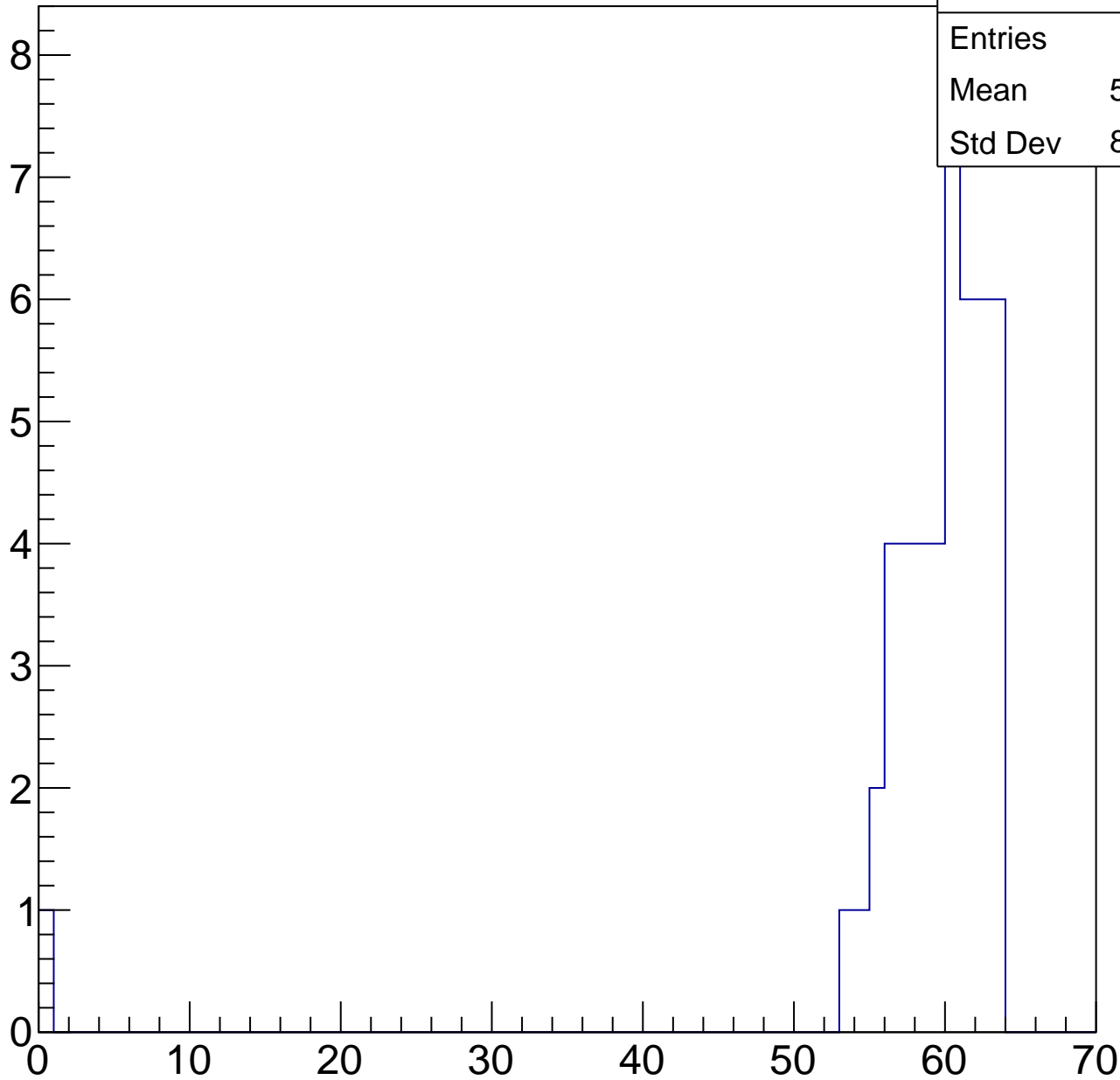
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	58.15
Std Dev	8.966

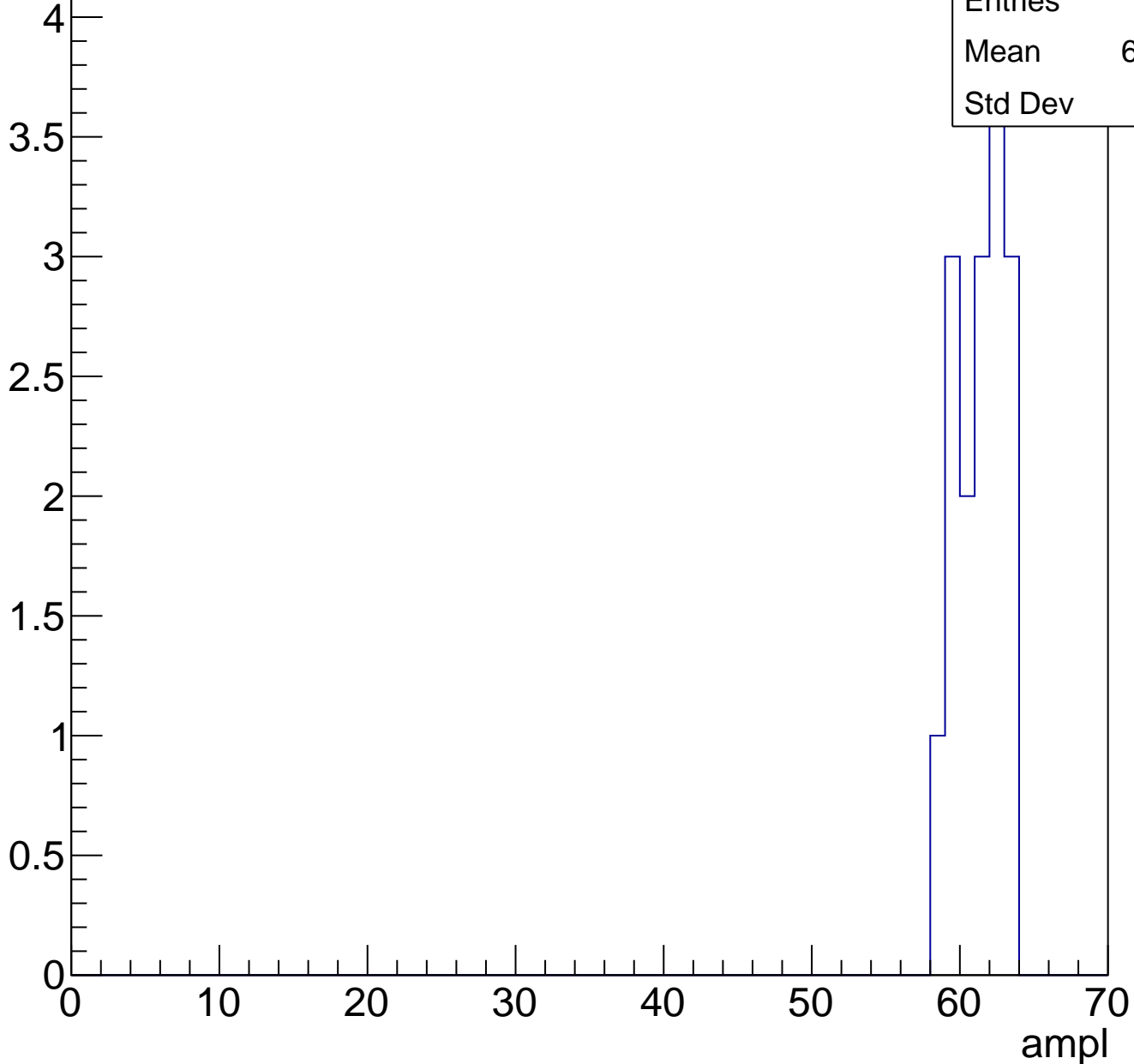
ampl



# B0L000S, U18-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch74, adc0

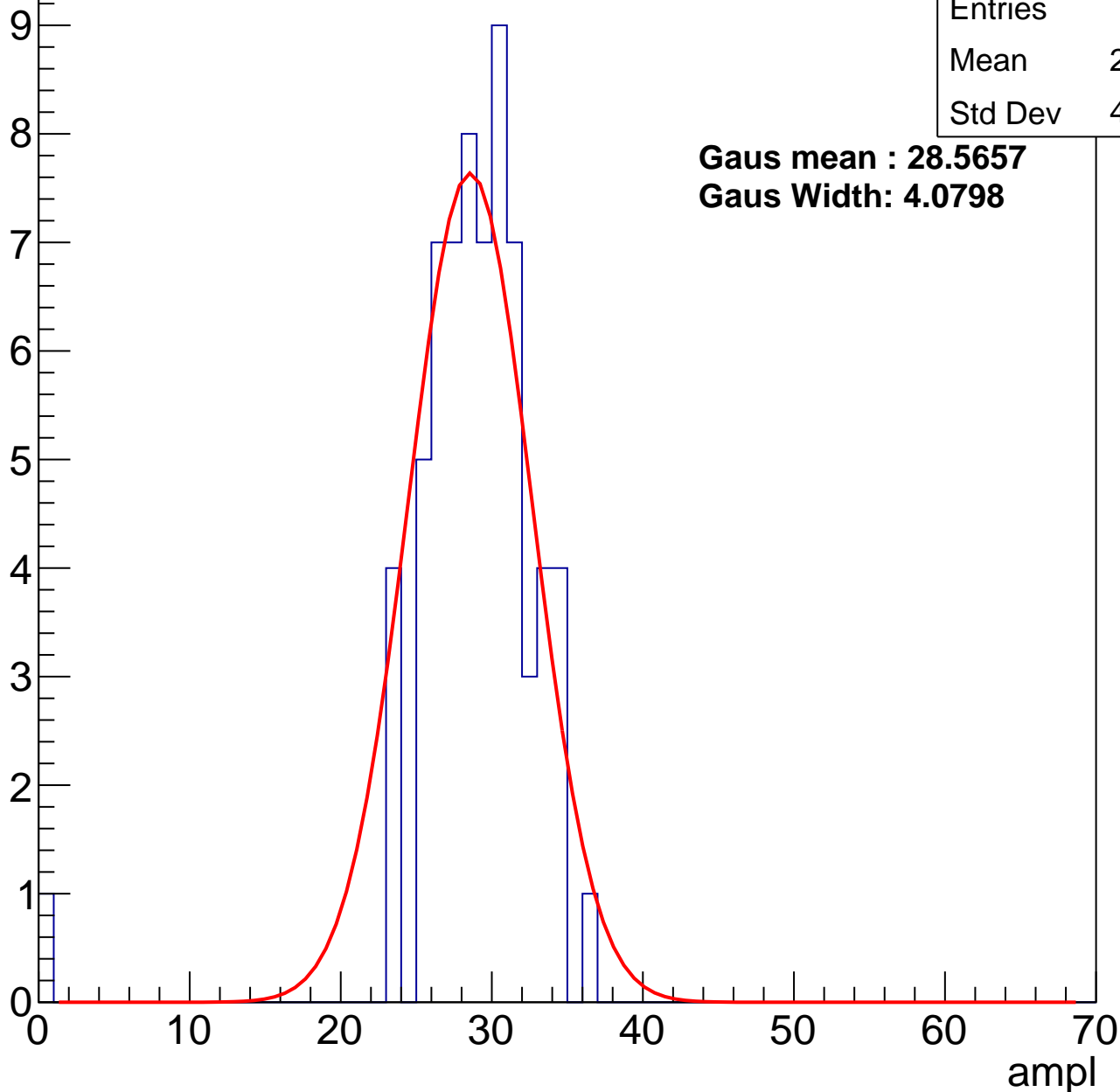
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.39
Std Dev	4.597

**Gaus mean : 28.5657**

**Gaus Width: 4.0798**



# B0L000S, U18-ch74, adc1

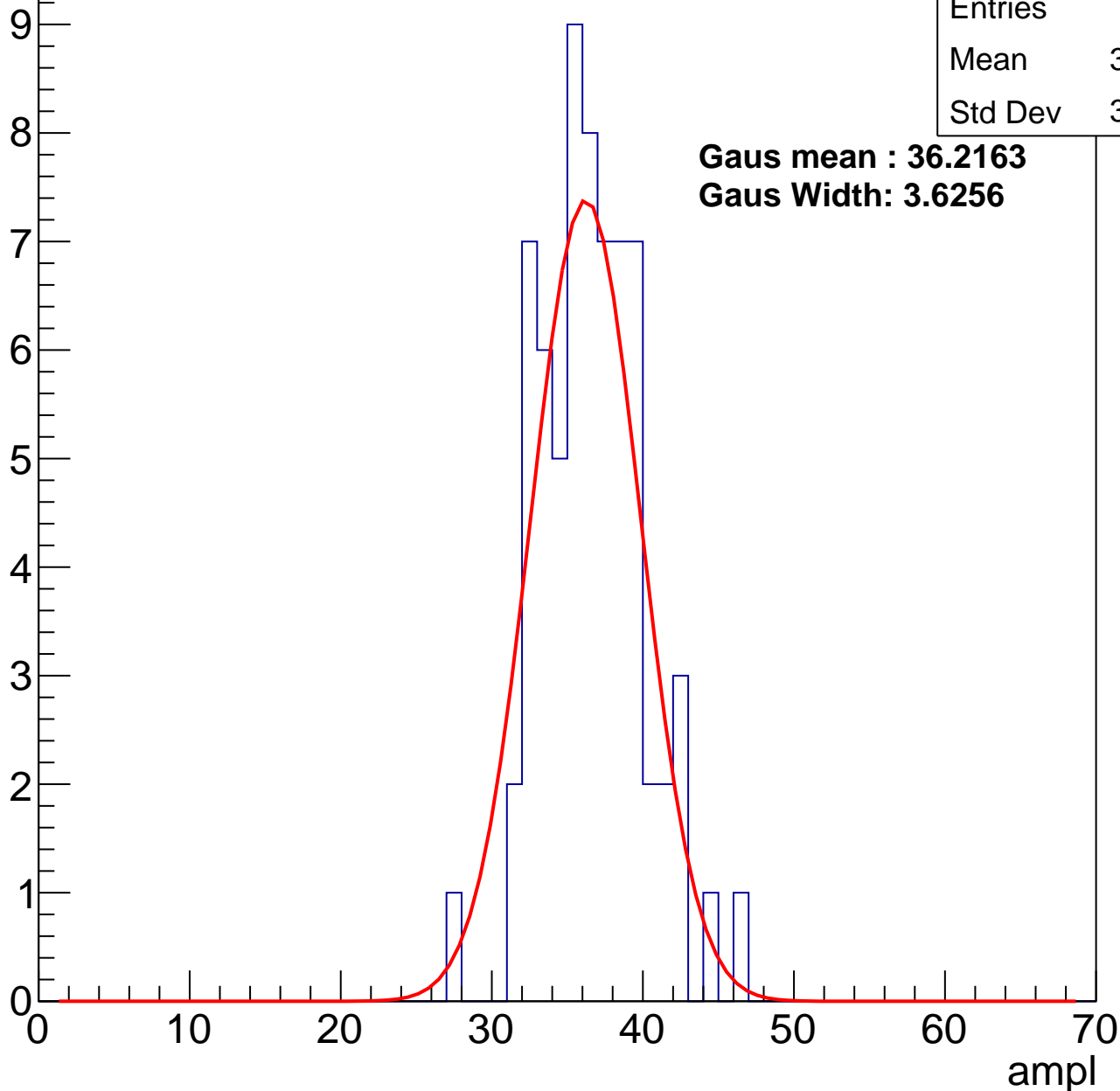
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.18
Std Dev	3.378

**Gaus mean : 36.2163**

**Gaus Width: 3.6256**



# B0L000S, U18-ch74, adc2

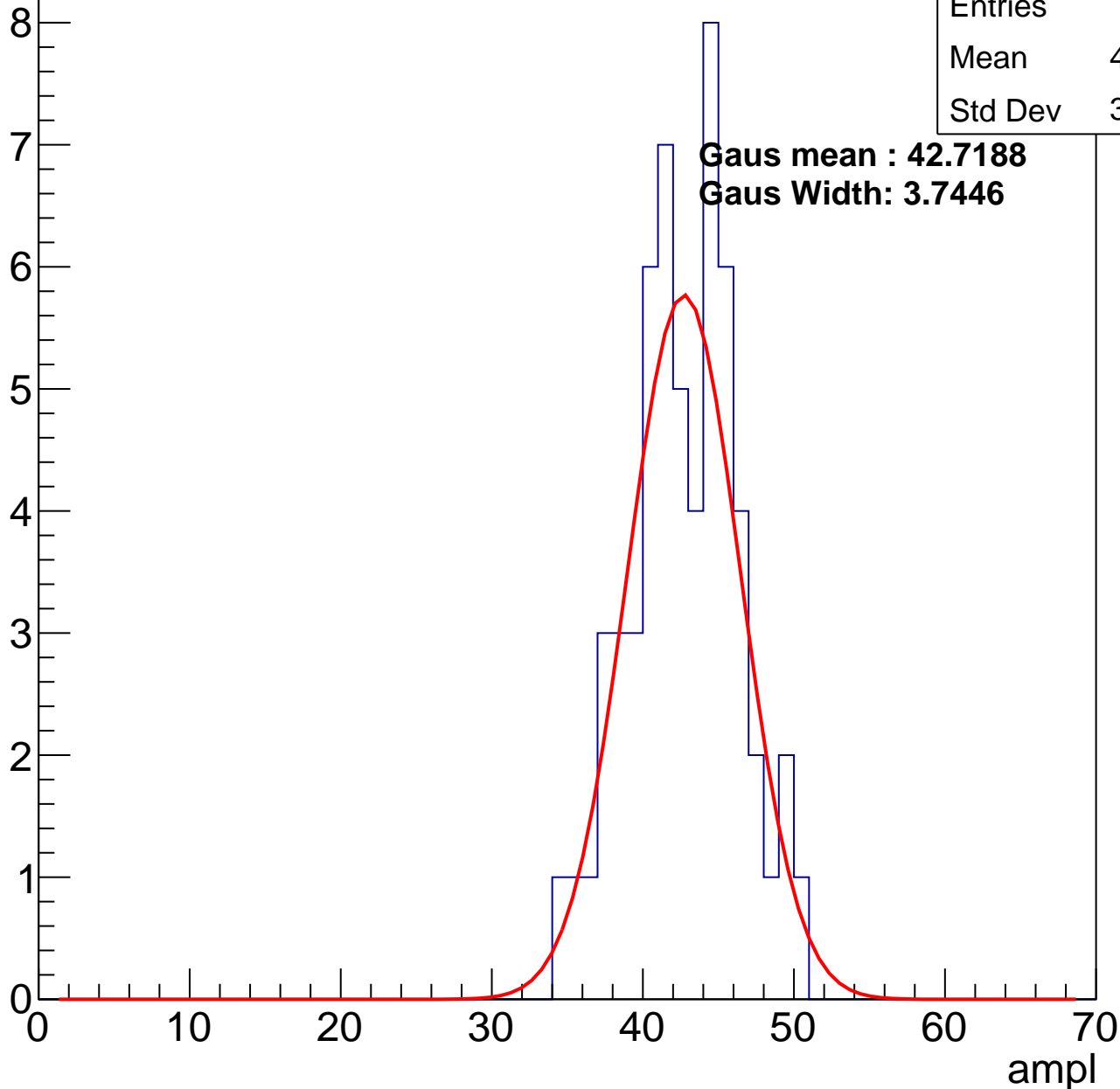
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	42.28
Std Dev	3.542

**Gaus mean : 42.7188**

**Gaus Width: 3.7446**

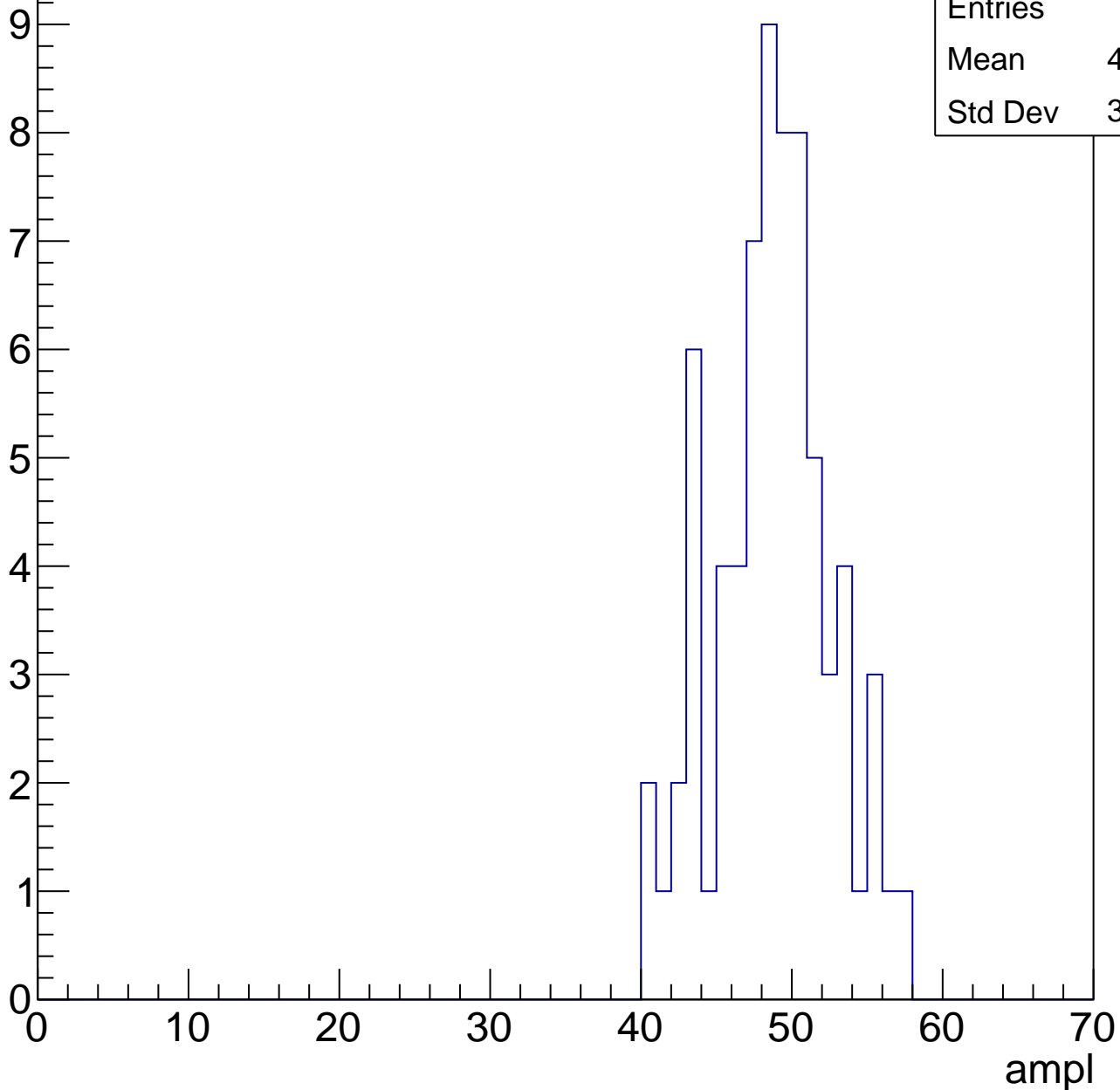


# B0L000S, U18-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.27
Std Dev	3.836

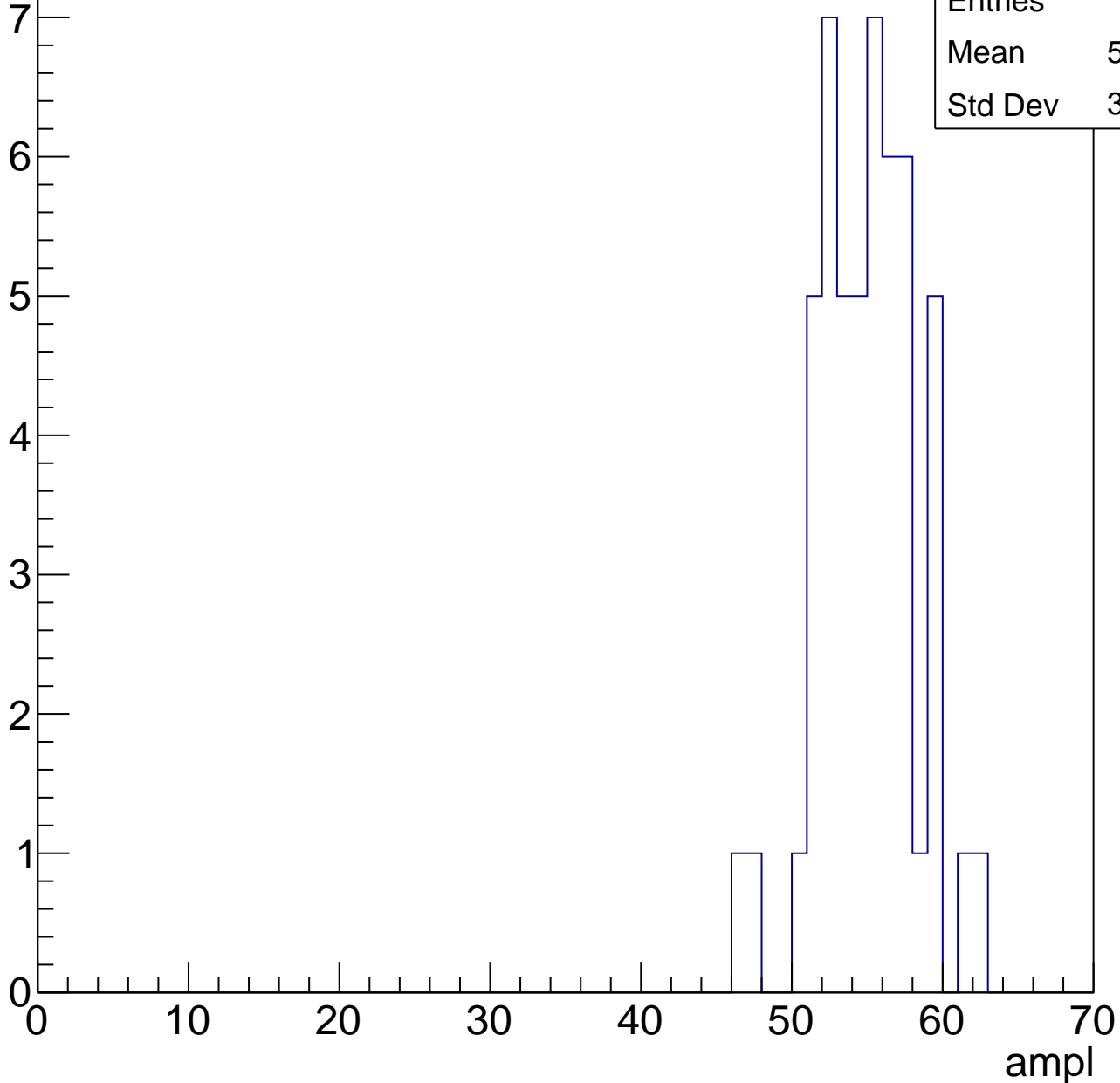


# B0L000S, U18-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

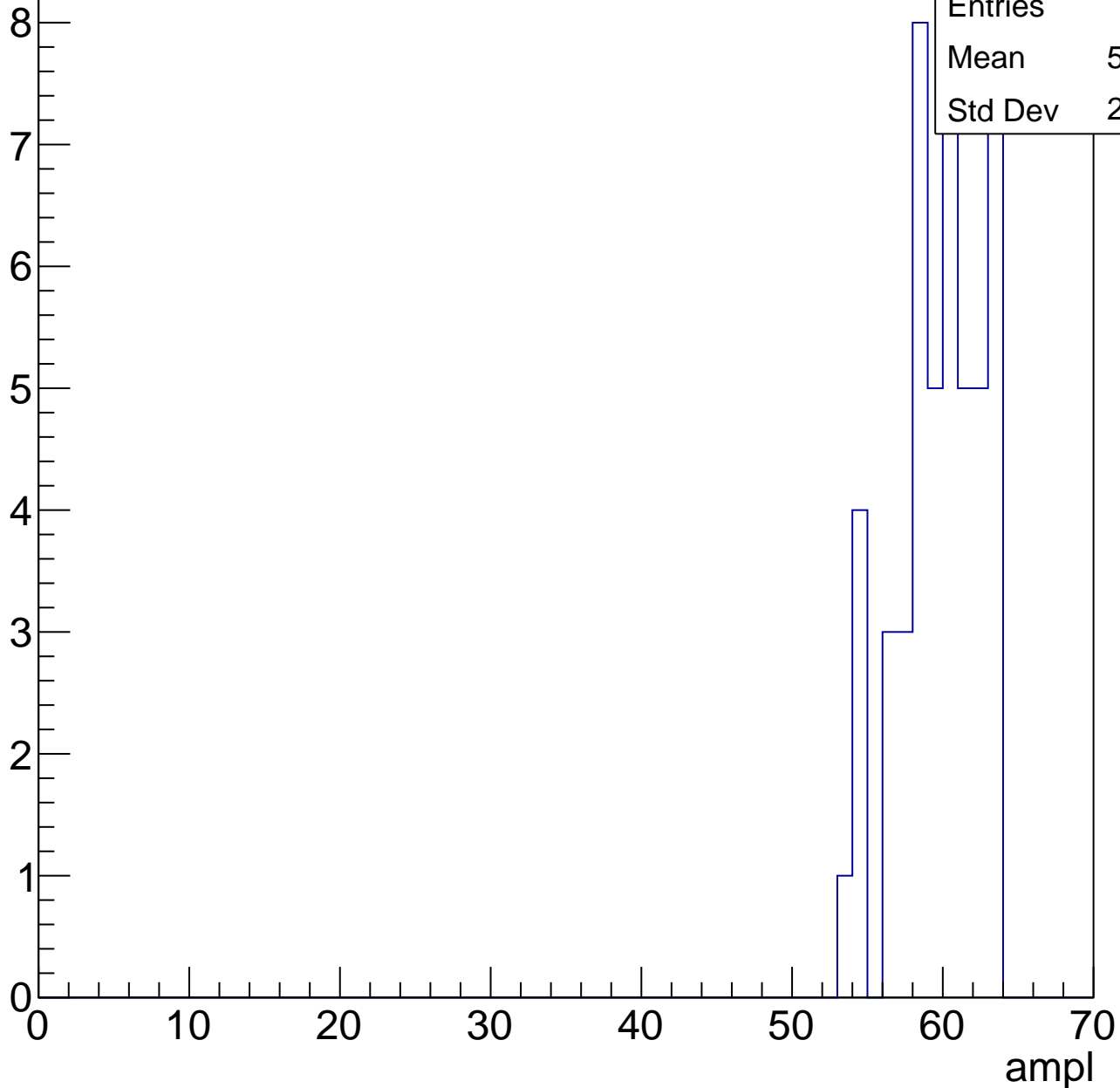
Entries	52
Mean	54.54
Std Dev	3.195



# B0L000S, U18-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

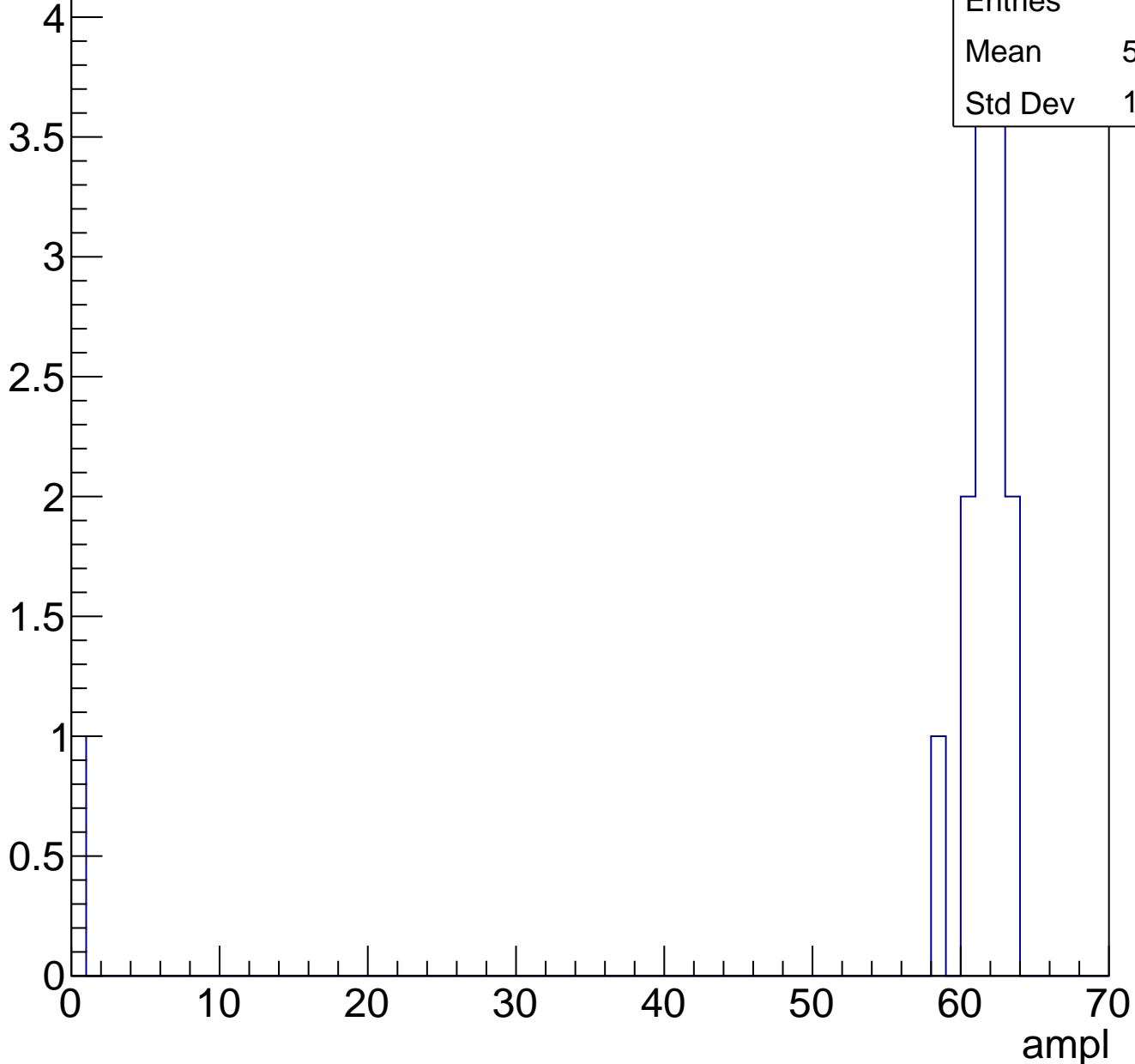
Entry



# B0L000S, U18-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch75, adc0

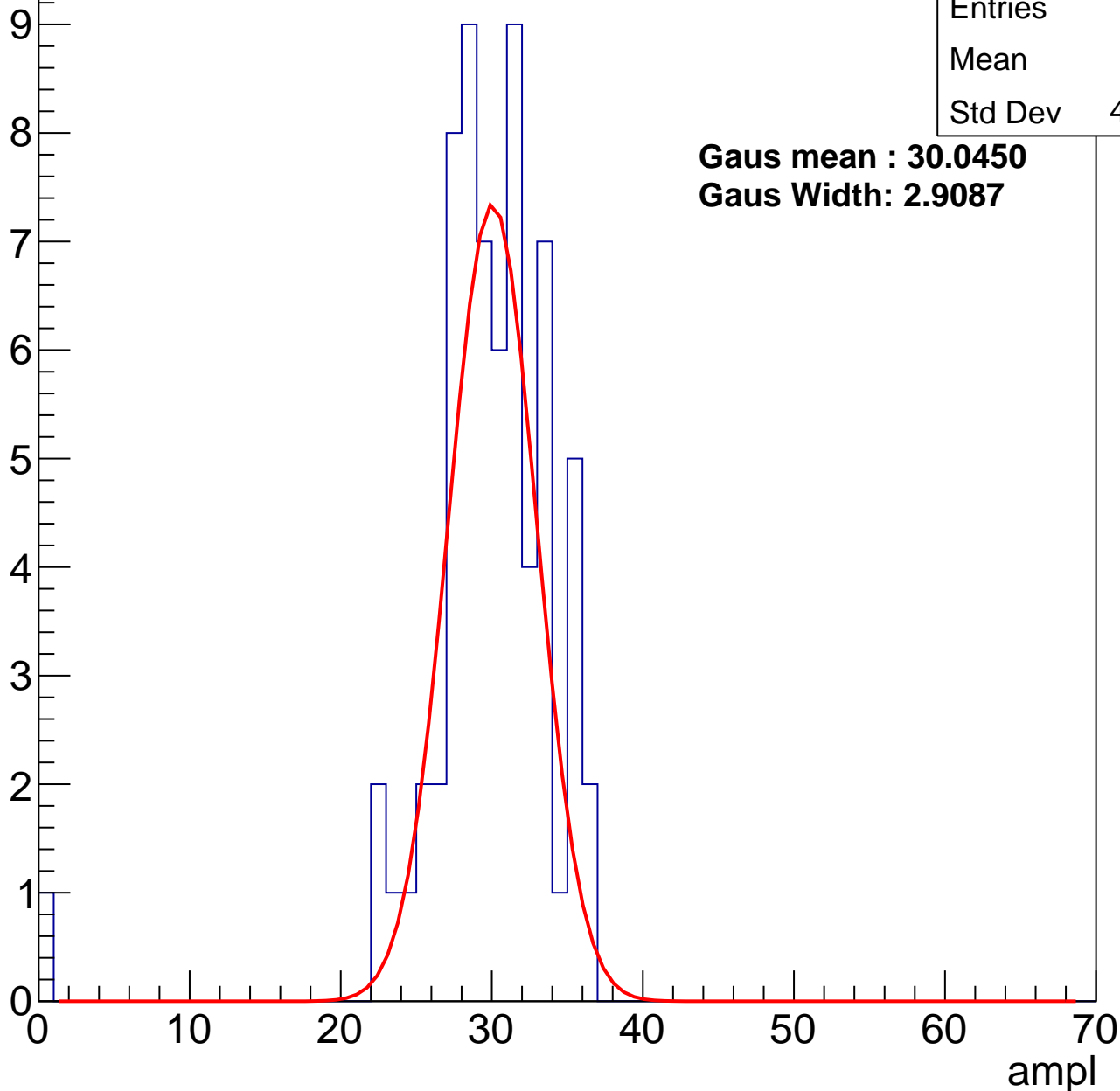
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.3
Std Dev	4.853

**Gaus mean : 30.0450**

**Gaus Width: 2.9087**



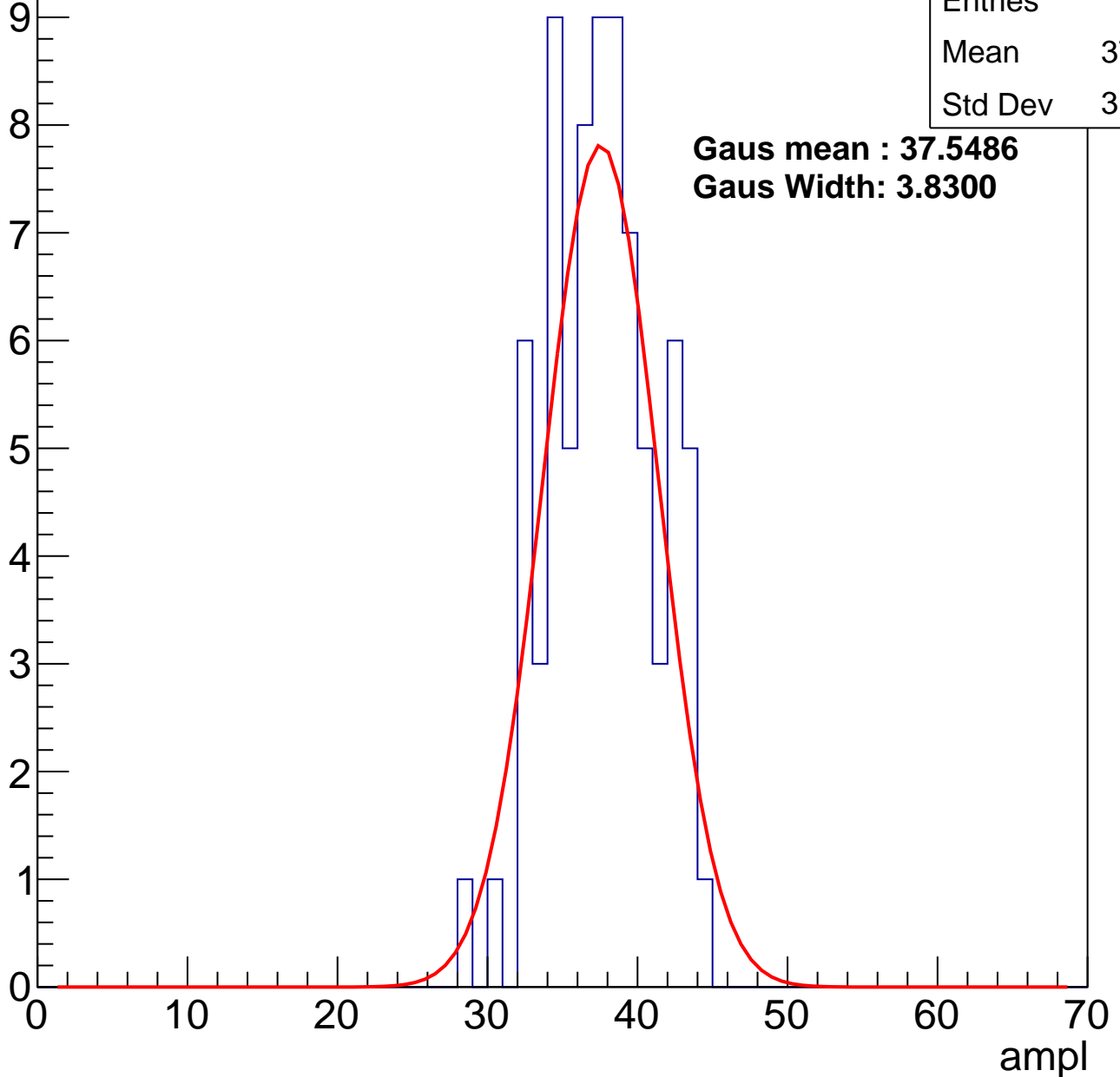
# B0L000S, U18-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	37.18
Std Dev	3.478

**Gaus mean : 37.5486**  
**Gaus Width: 3.8300**



# B0L000S, U18-ch75, adc2

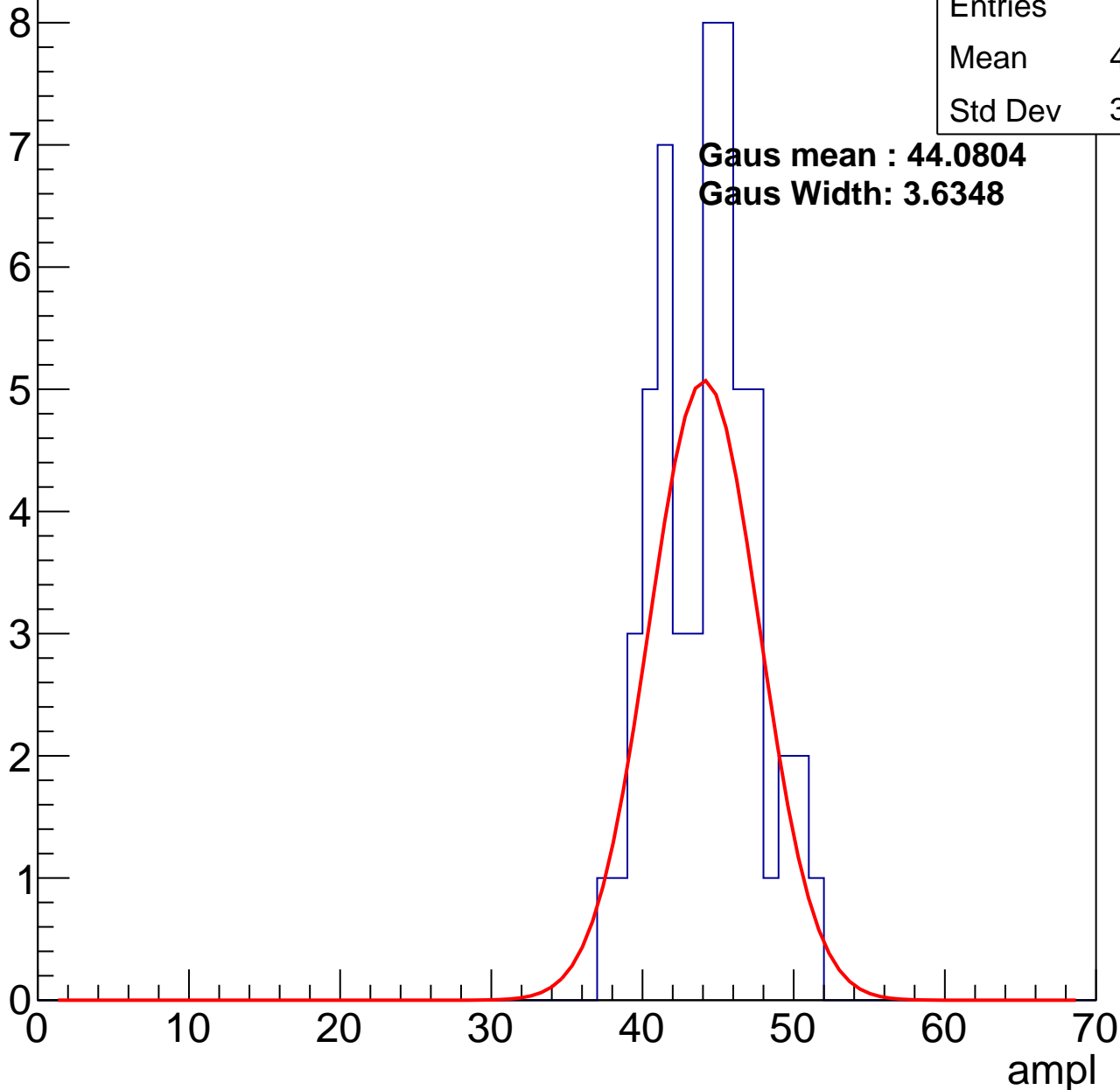
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	43.78
Std Dev	3.234

**Gaus mean : 44.0804**

**Gaus Width: 3.6348**

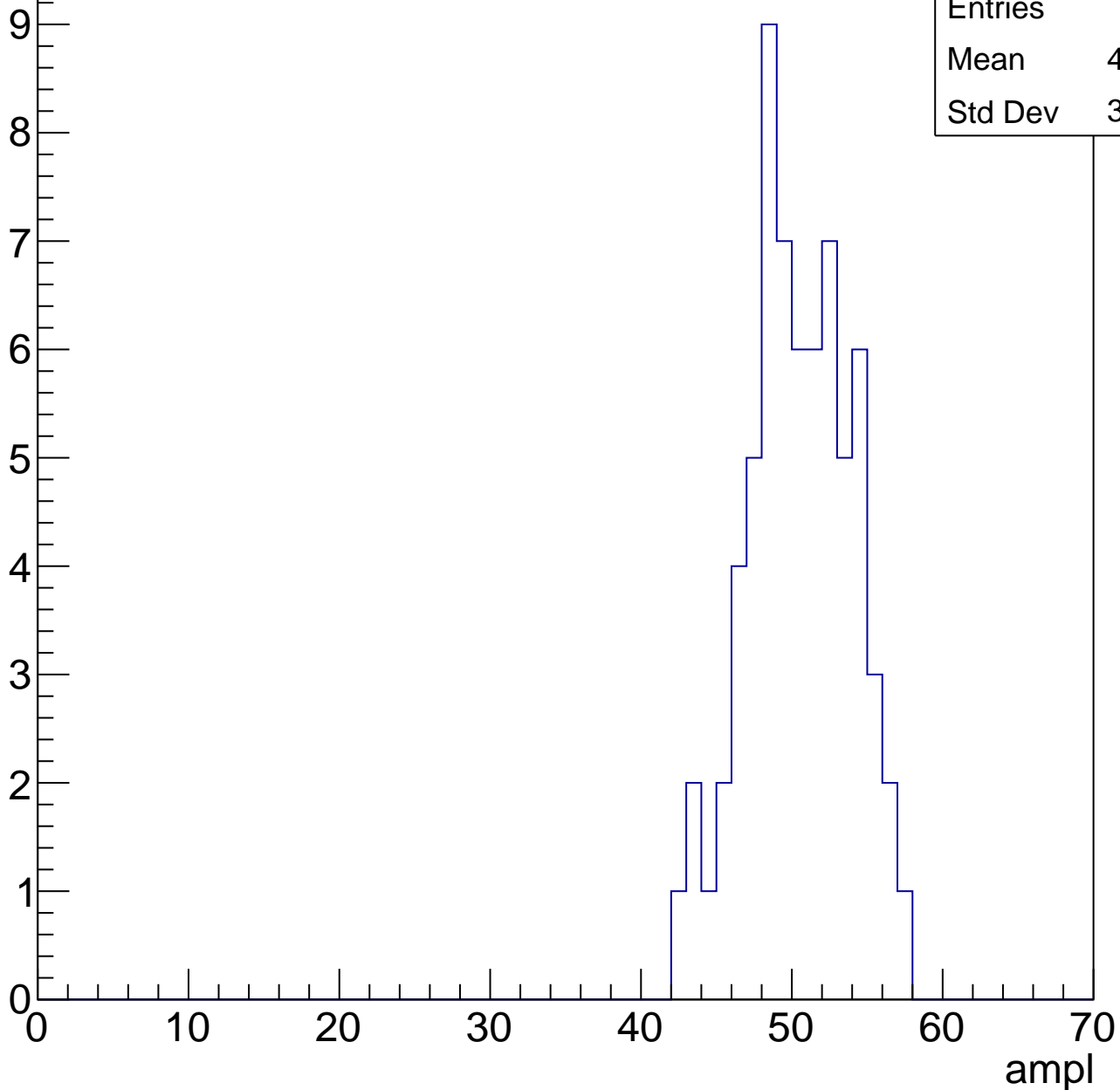


# B0L000S, U18-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.99
Std Dev	3.392

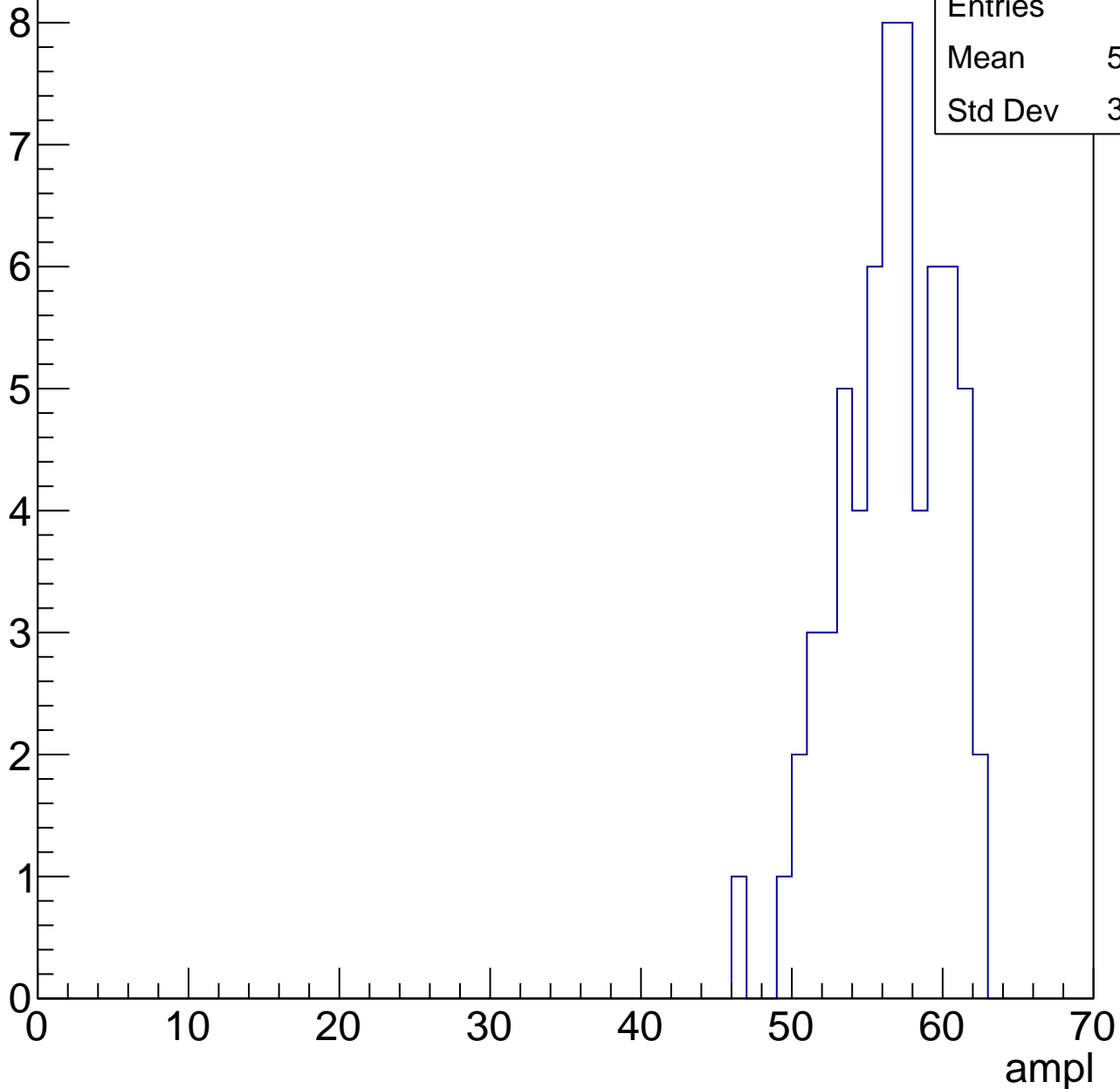


# B0L000S, U18-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	56.16
Std Dev	3.492

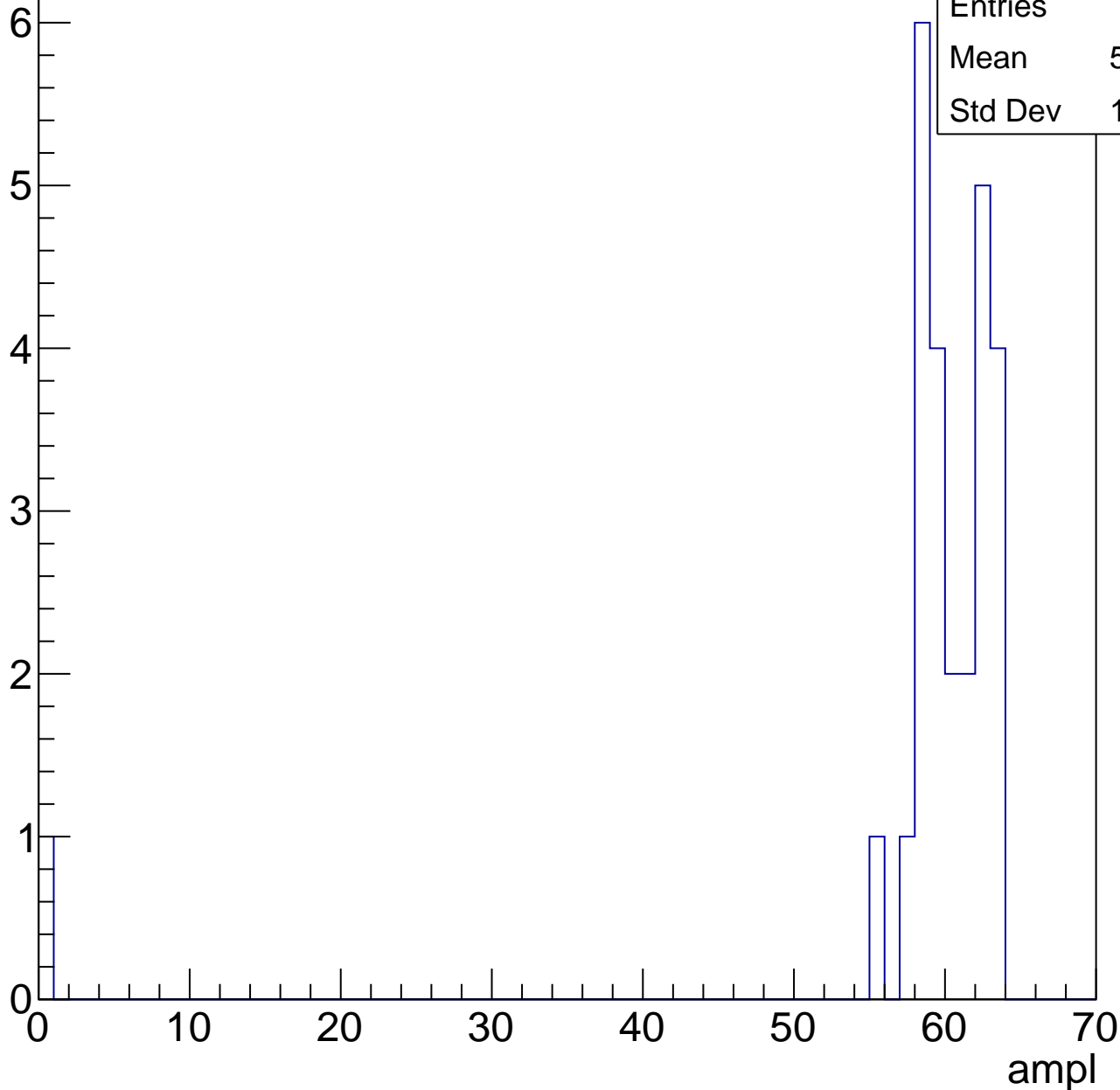


# B0L000S, U18-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	26
Mean	57.69
Std Dev	11.74

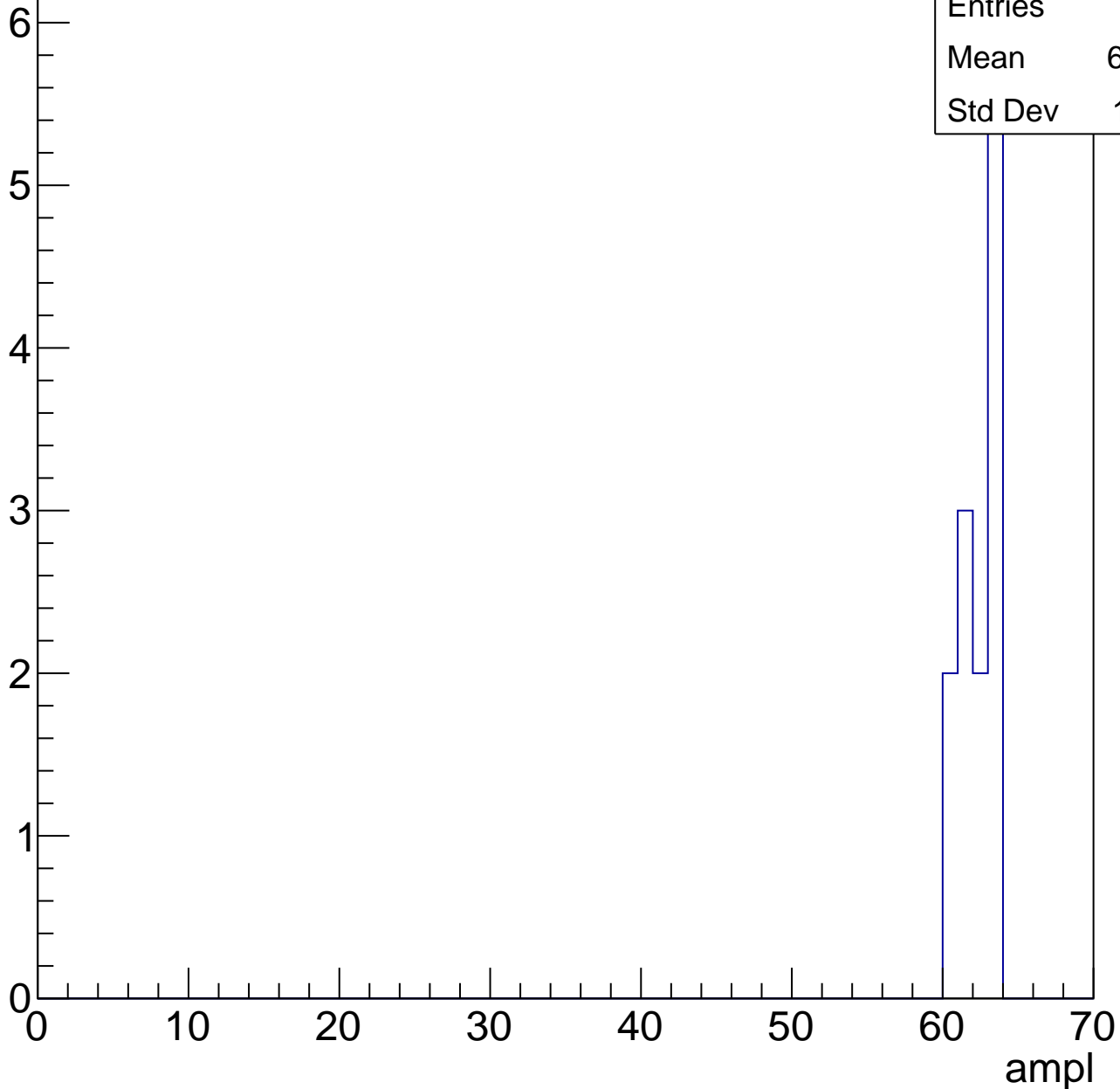


# B0L000S, U18-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.92
Std Dev	1.141





# B0L000S, U18-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch76, adc0

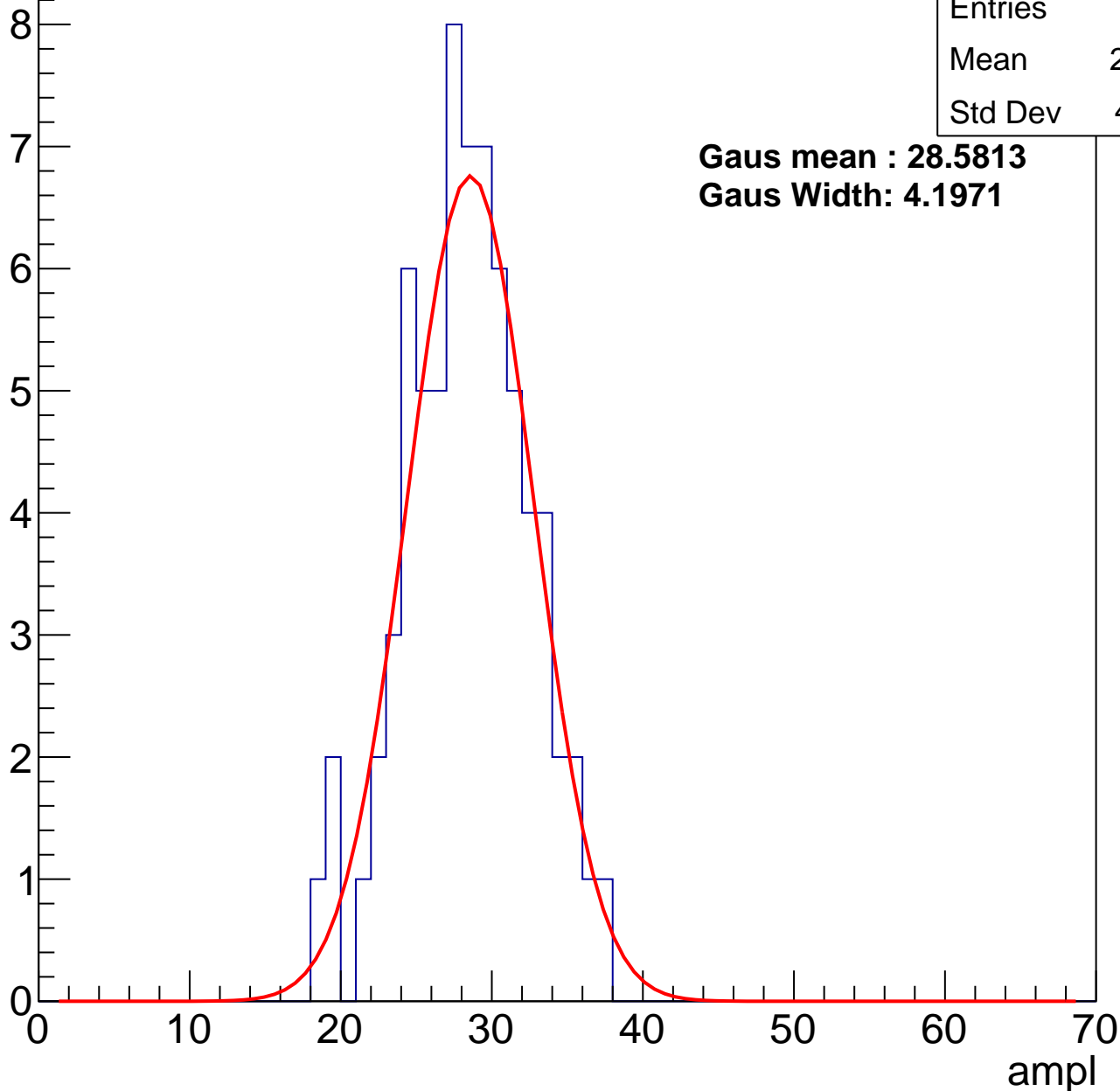
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	27.92
Std Dev	4.051

**Gaus mean : 28.5813**

**Gaus Width: 4.1971**



# B0L000S, U18-ch76, adc1

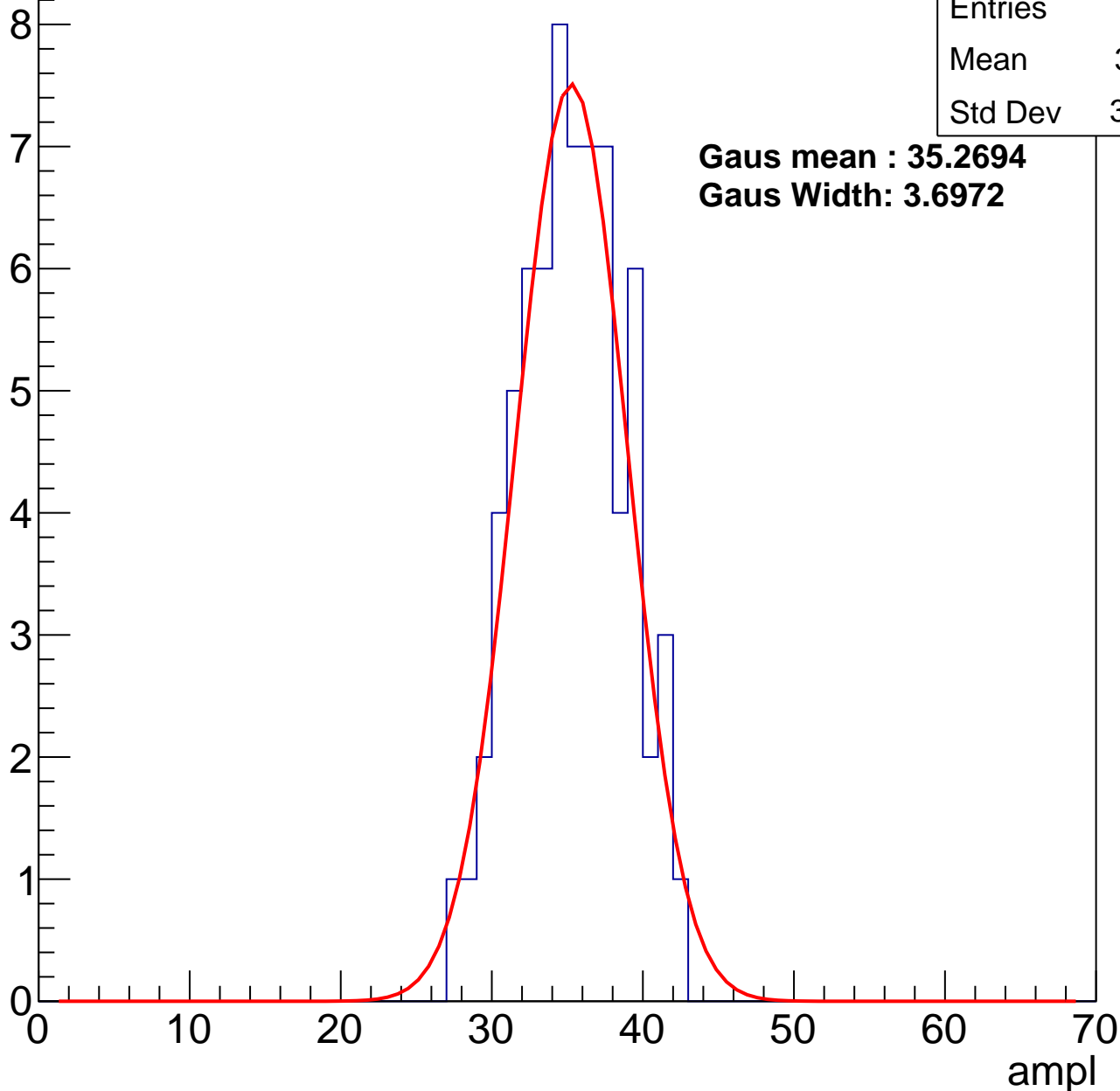
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	34.81
Std Dev	3.428

**Gaus mean : 35.2694**

**Gaus Width: 3.6972**



# B0L000S, U18-ch76, adc2

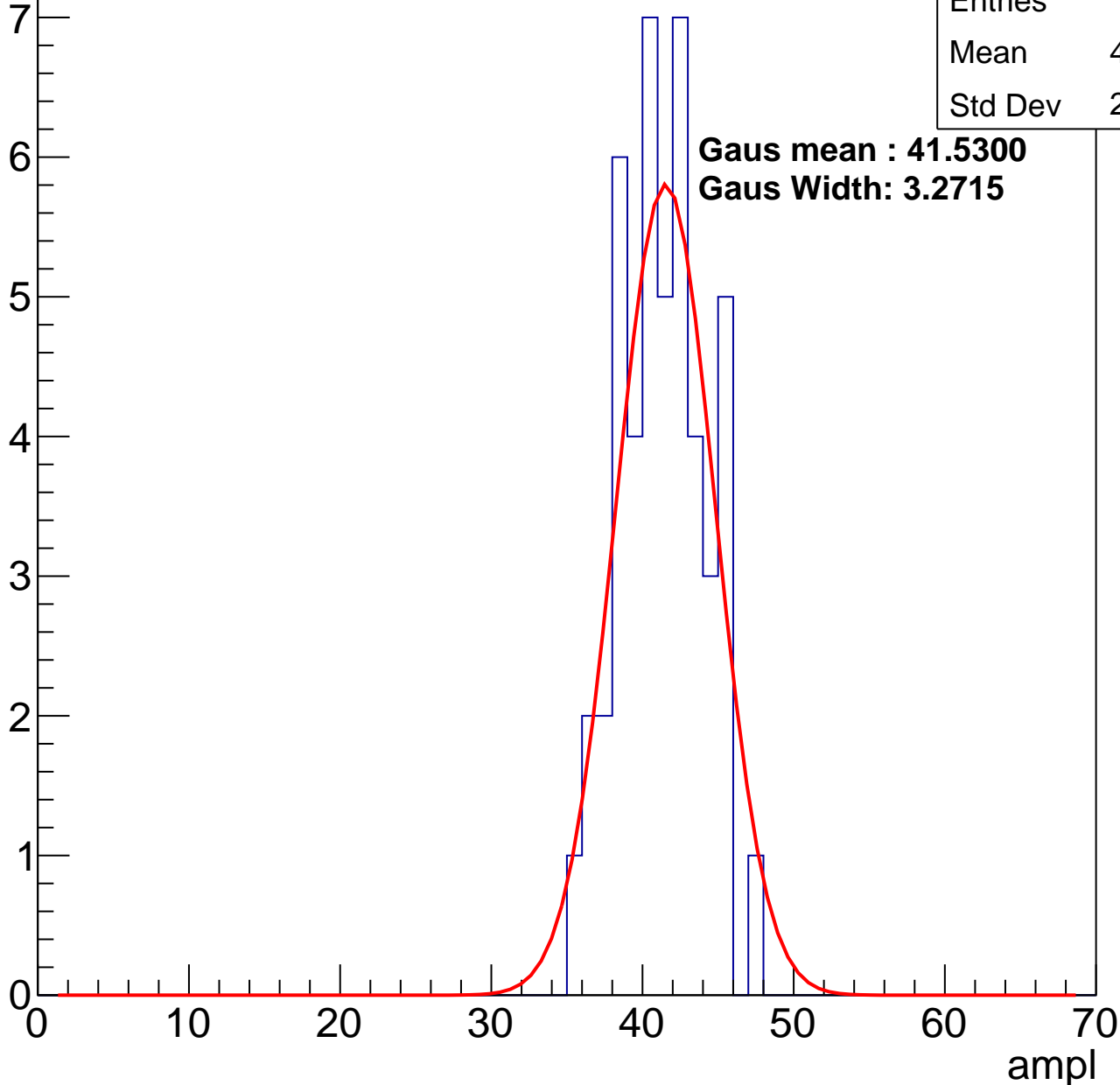
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	40.85
Std Dev	2.767

**Gaus mean : 41.5300**

**Gaus Width: 3.2715**

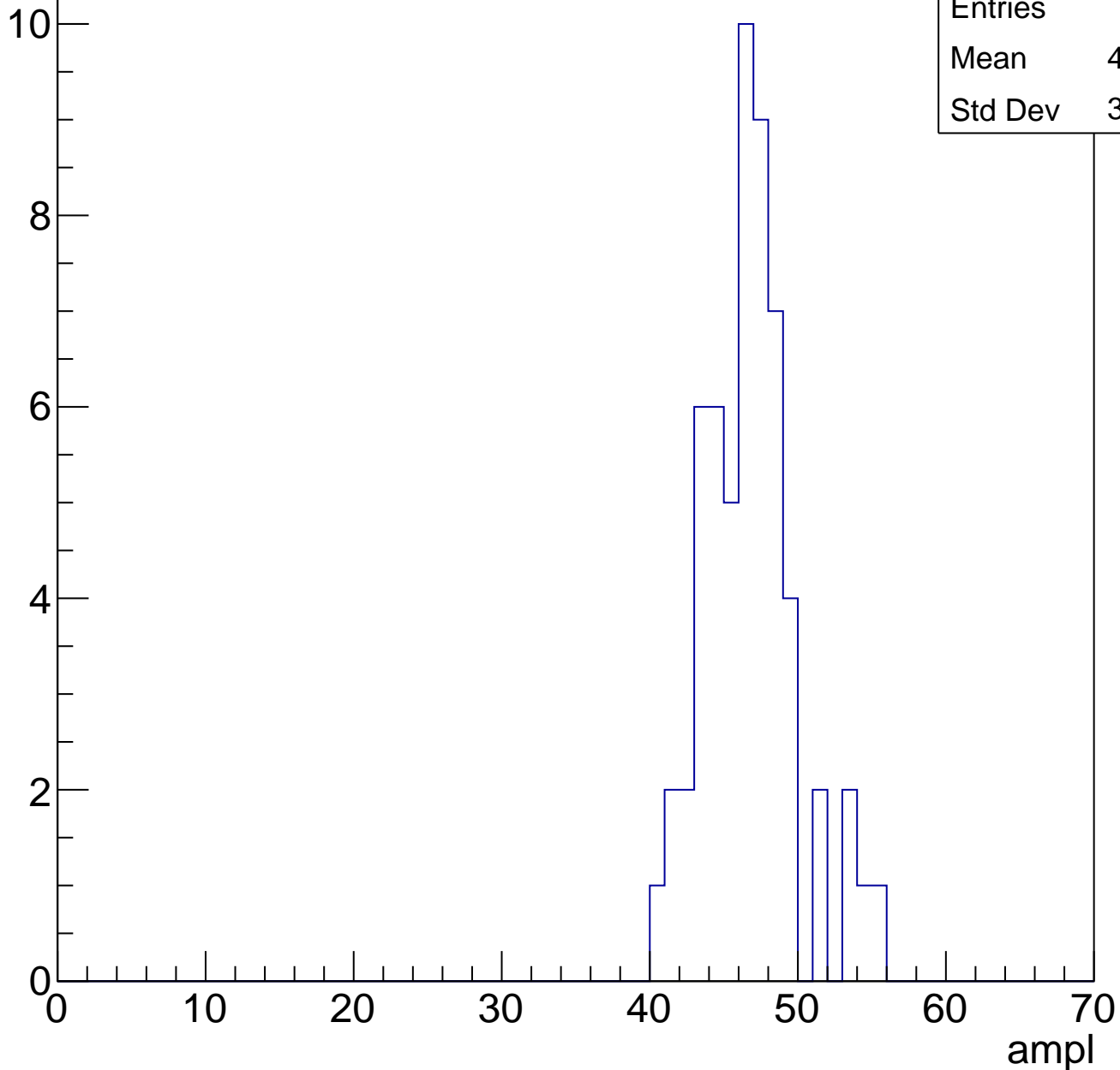


# B0L000S, U18-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	58
Mean	46.29
Std Dev	3.113

Entry



# B0L000S, U18-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

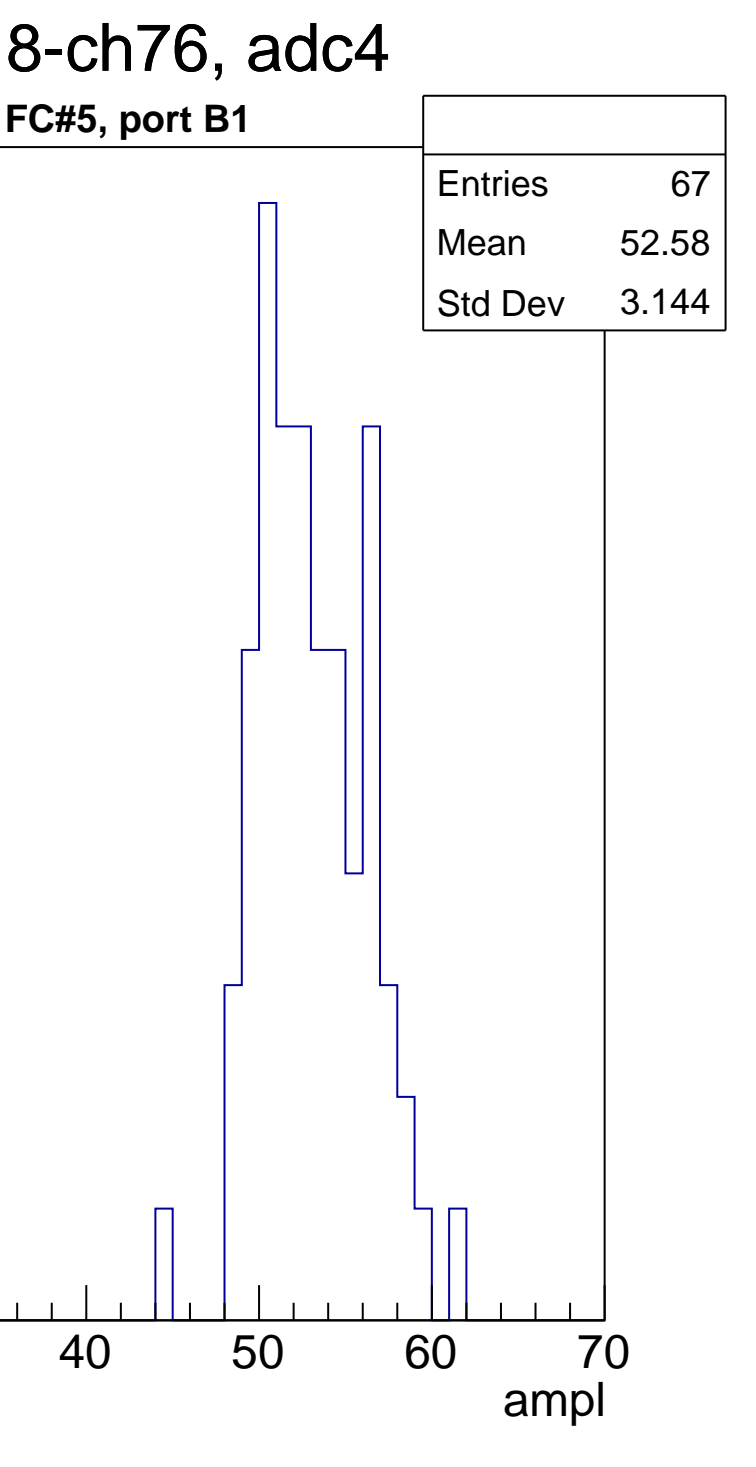
Entries	67
Mean	52.58
Std Dev	3.144

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

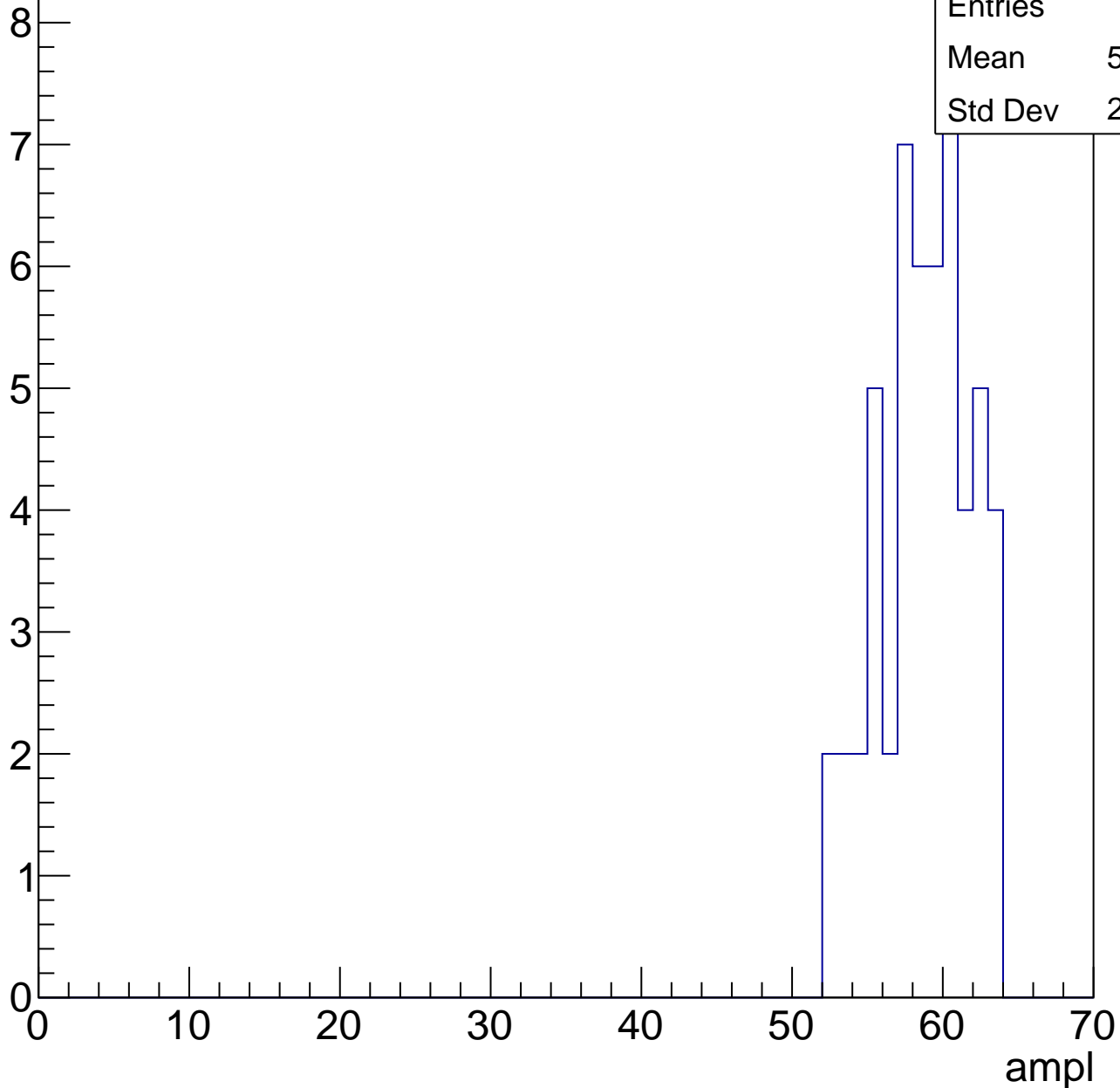
ampl



# B0L000S, U18-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

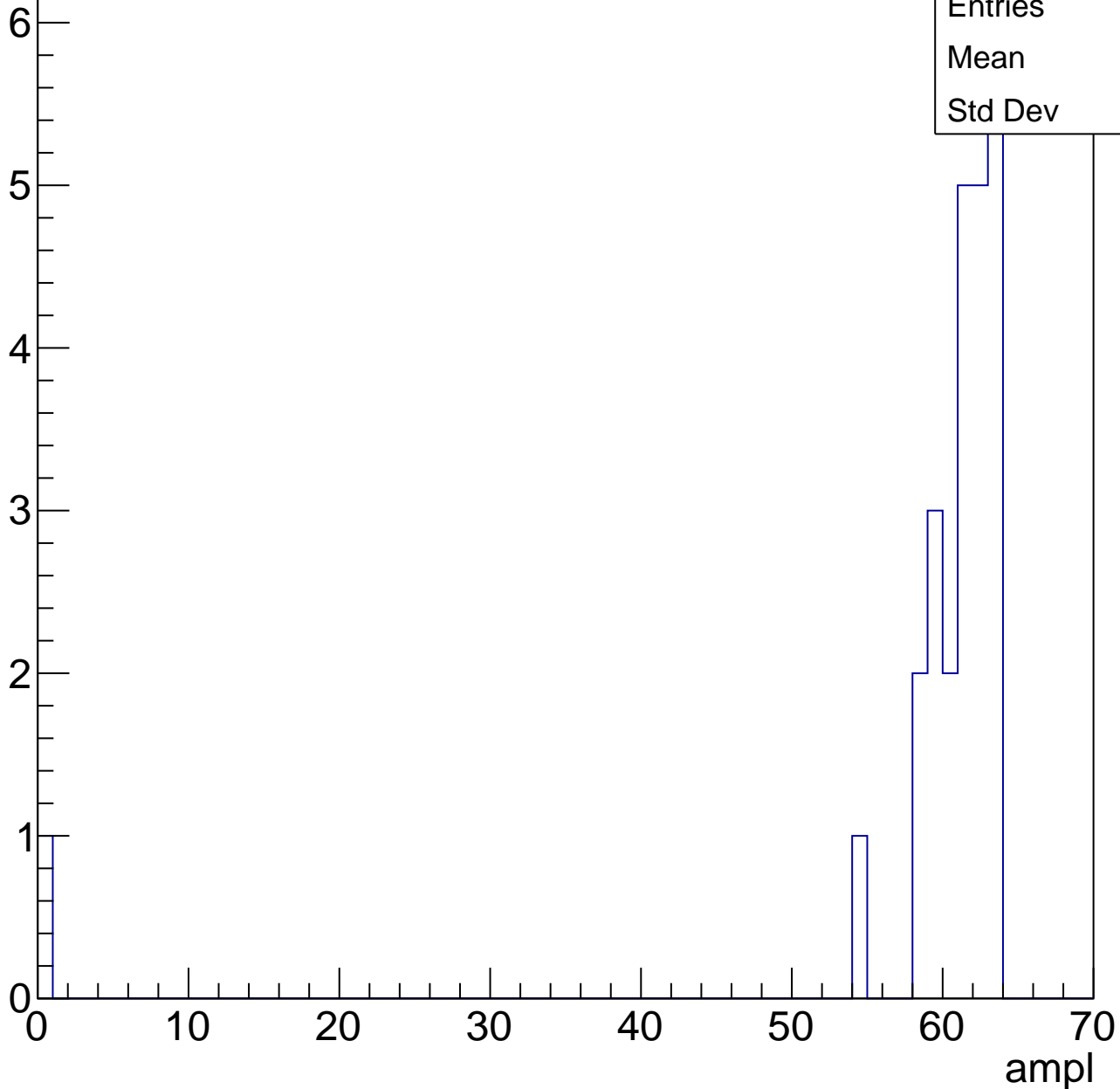


# B0L000S, U18-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	25
Mean	58.4
Std Dev	12.1





# B0L000S, U18-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch77, adc0

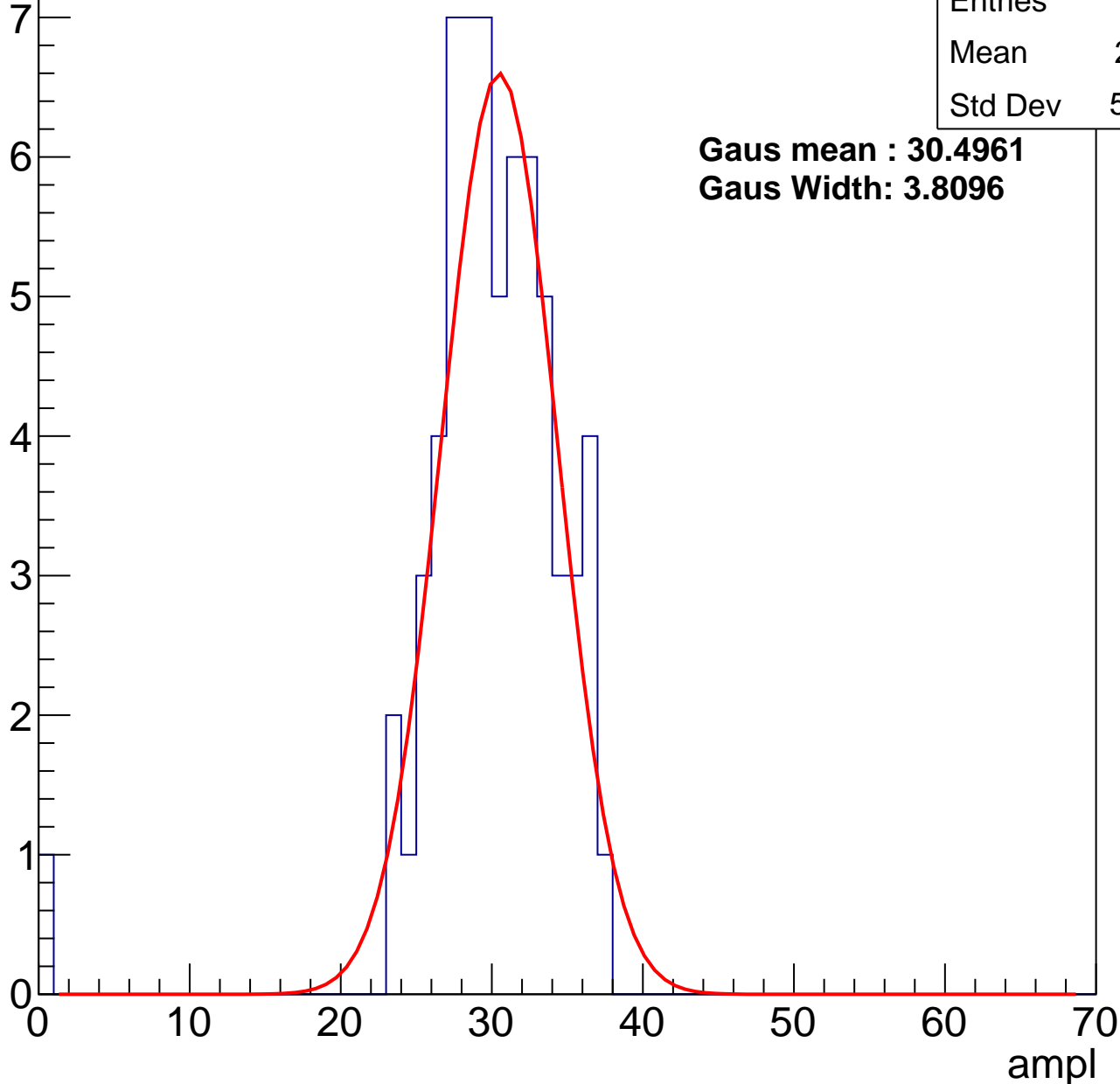
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.51
Std Dev	5.033

**Gaus mean : 30.4961**

**Gaus Width: 3.8096**



# B0L000S, U18-ch77, adc1

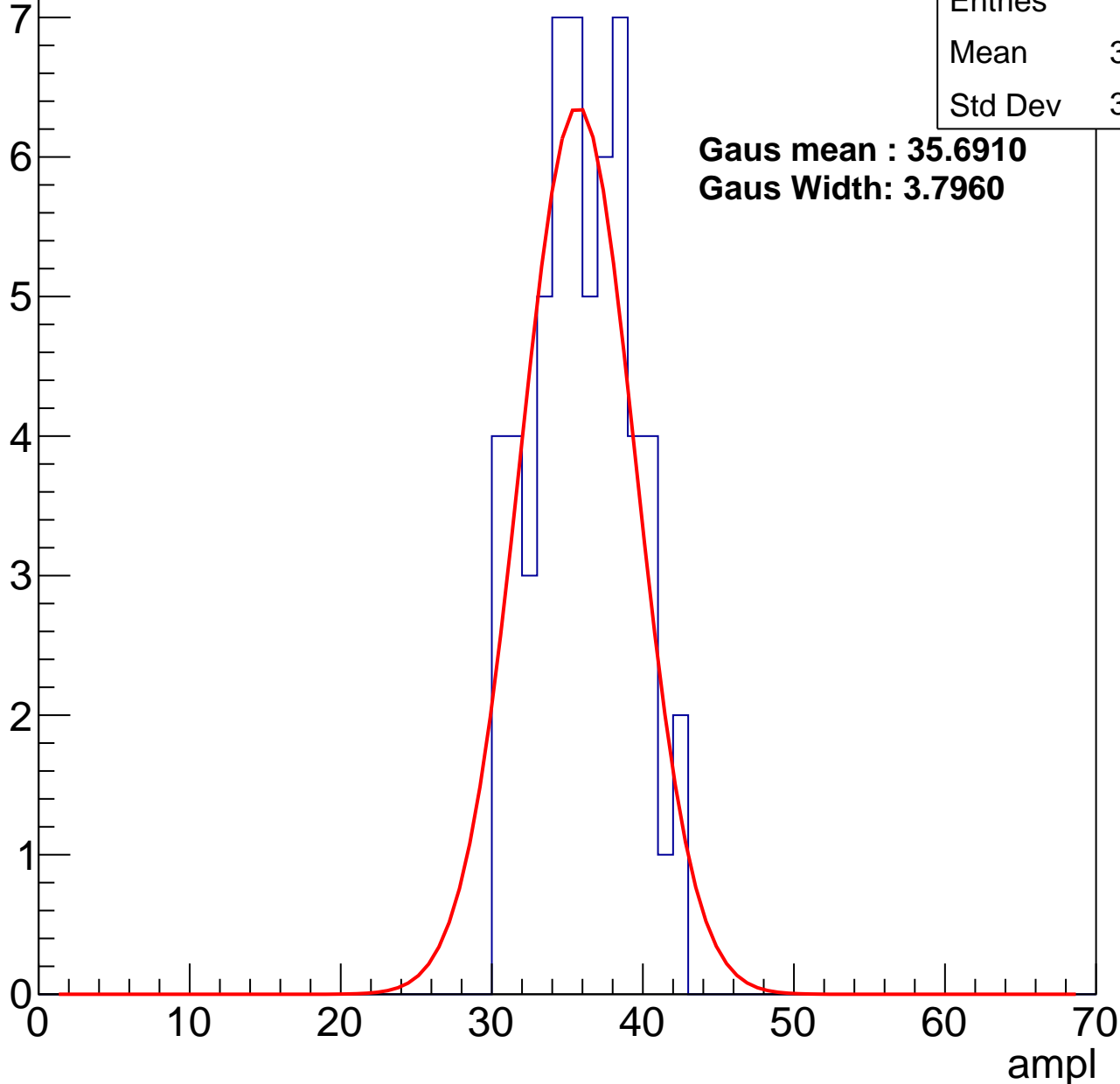
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.54
Std Dev	3.164

**Gaus mean : 35.6910**

**Gaus Width: 3.7960**



# B0L000S, U18-ch77, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.59
Std Dev	3.16

**Gaus mean : 42.9727**

**Gaus Width: 3.4318**

10

8

6

4

2

0

ampl

0

10

20

30

40

50

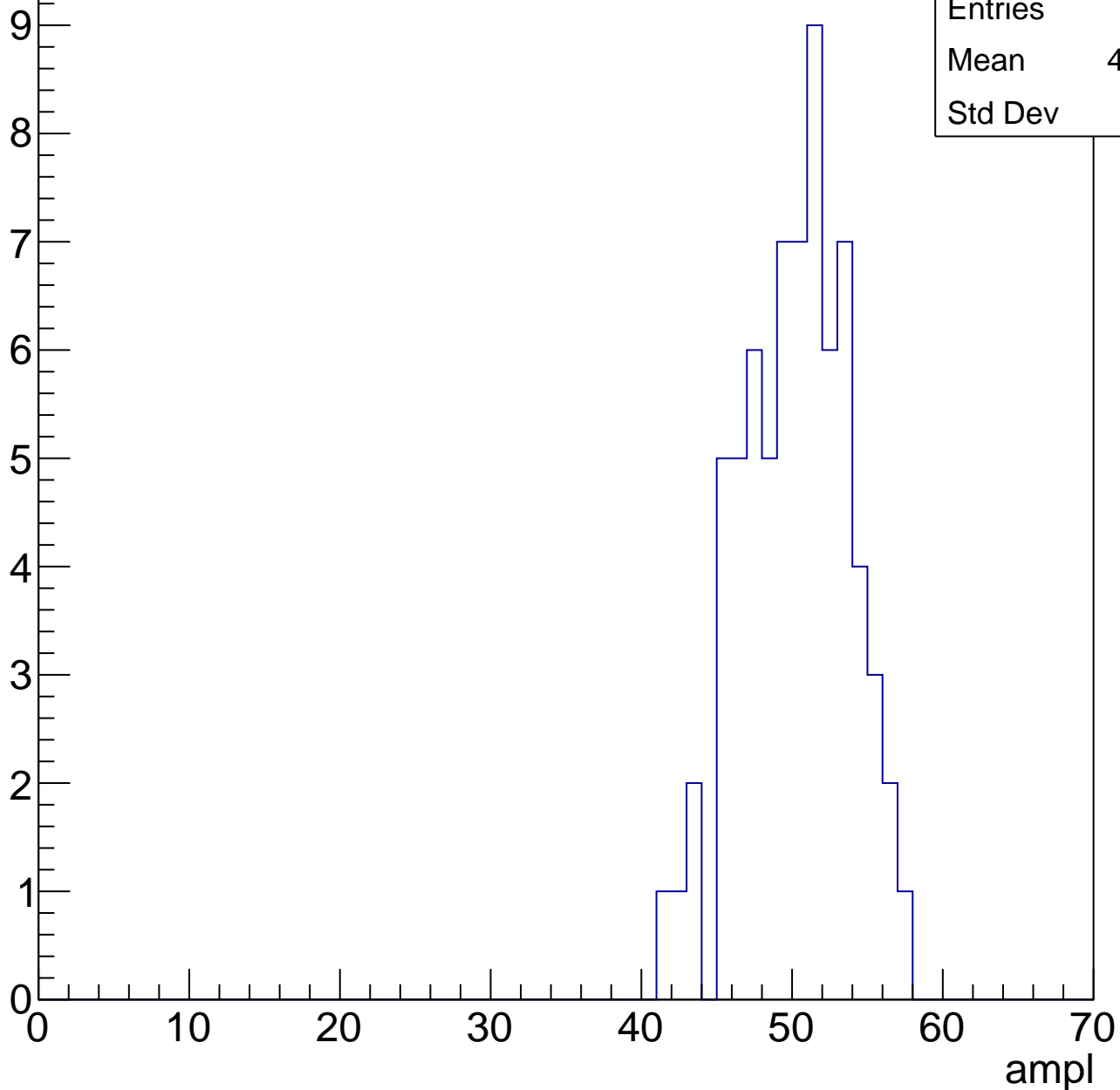
60

70

# B0L000S, U18-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

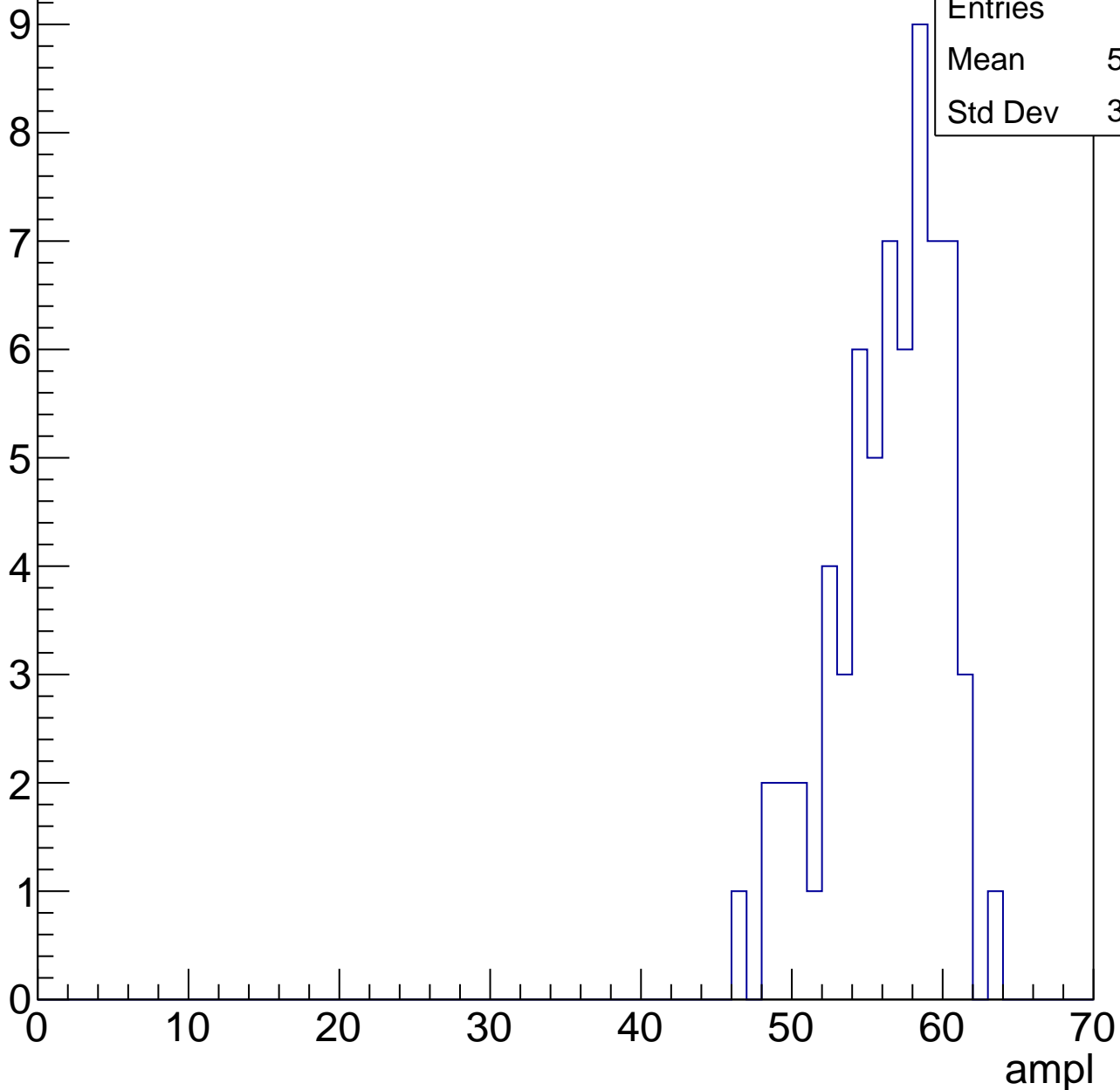


# B0L000S, U18-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	55.94
Std Dev	3.659

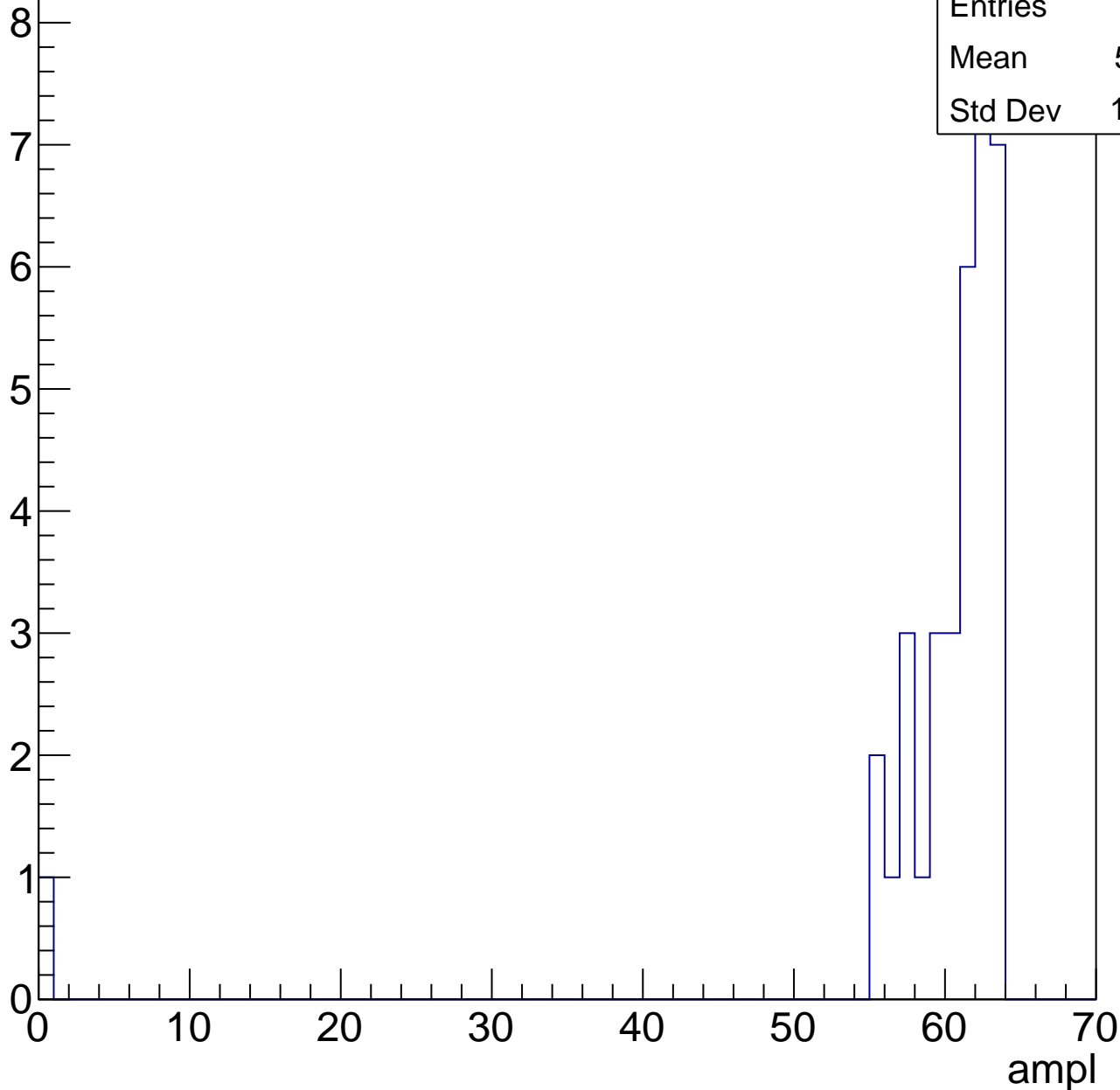


# B0L000S, U18-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.71
Std Dev	10.34



# B0L000S, U18-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U18-ch78, adc0

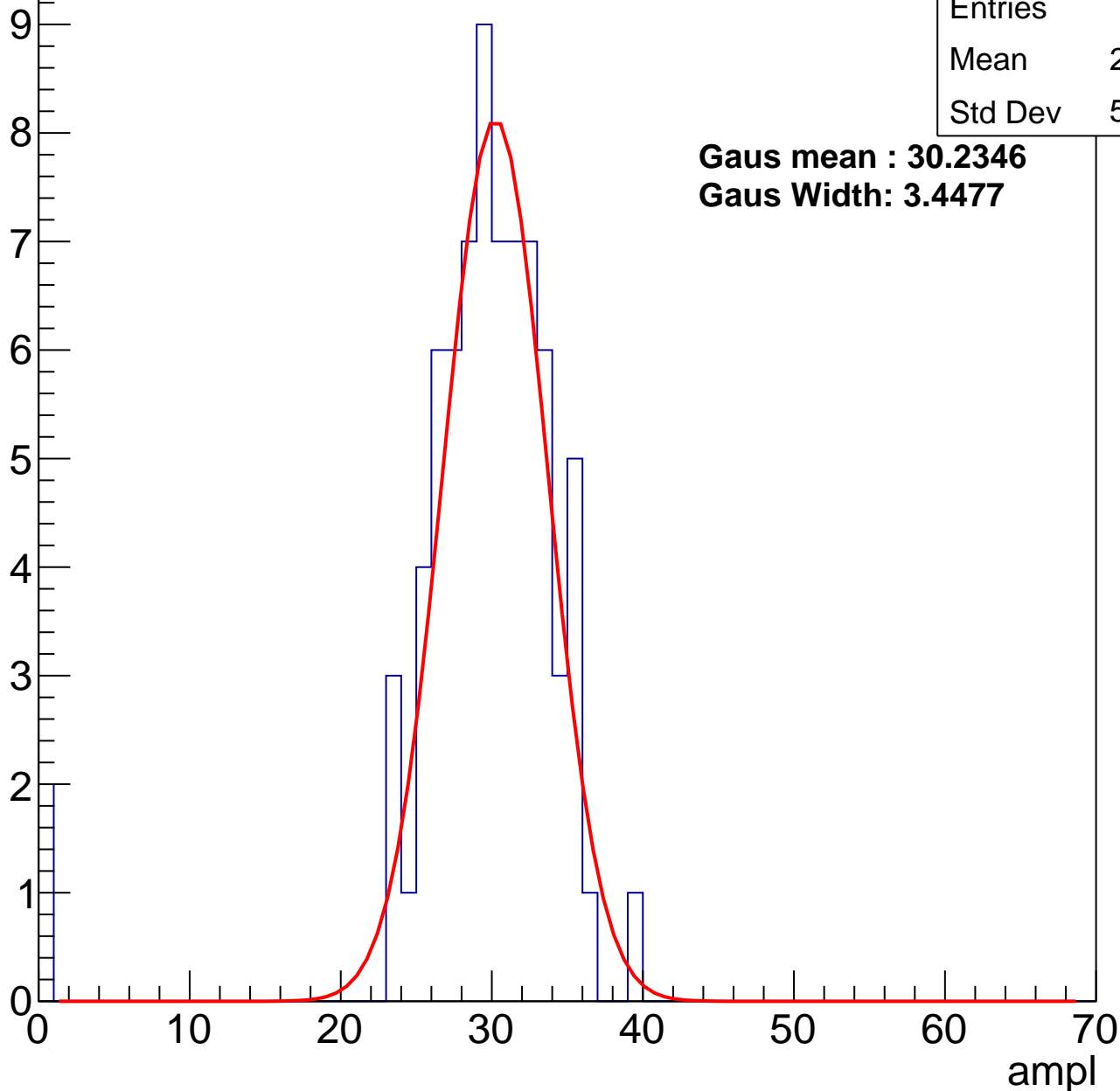
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	28.92
Std Dev	5.844

**Gaus mean : 30.2346**

**Gaus Width: 3.4477**



# B0L000S, U18-ch78, adc1

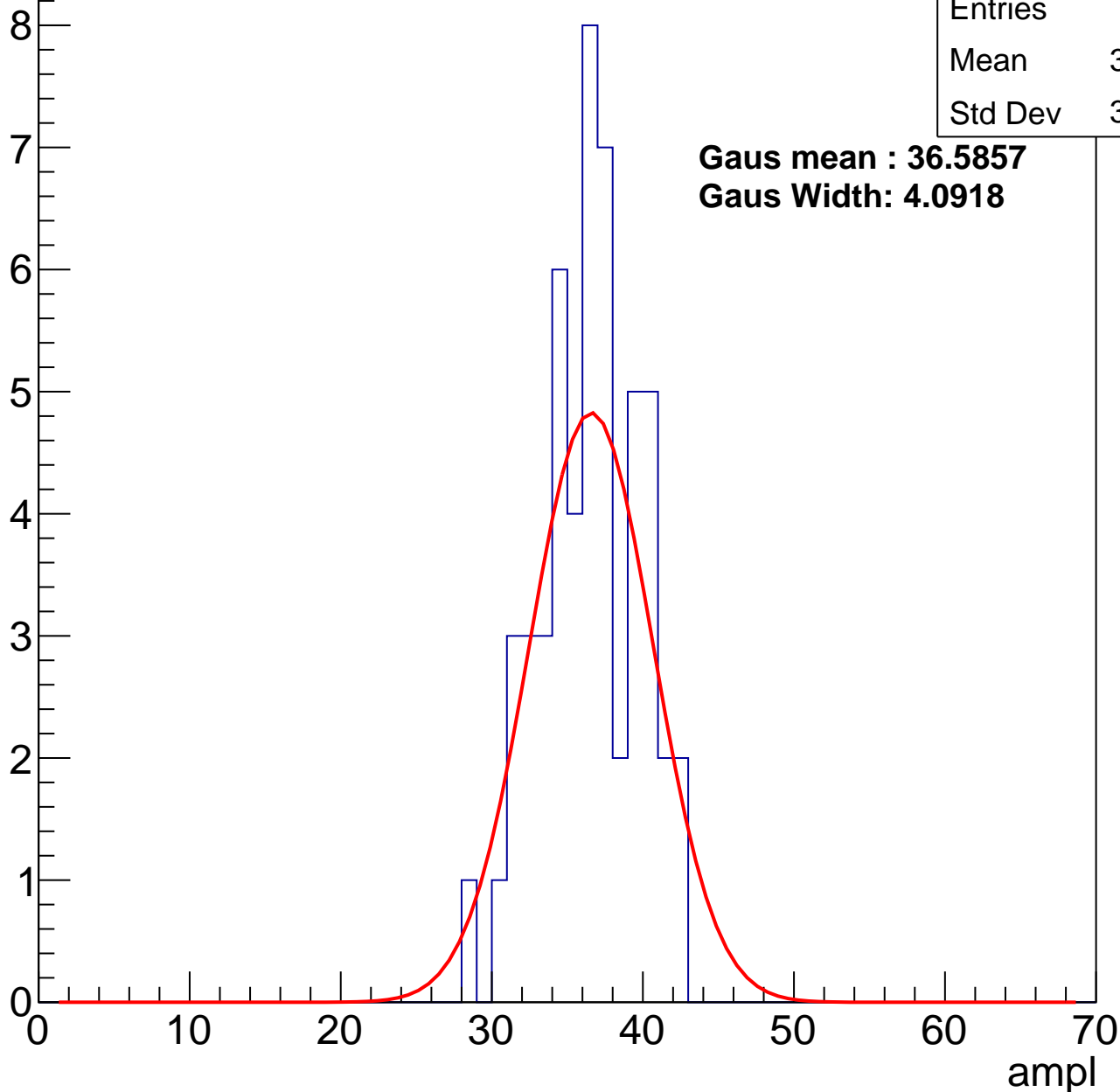
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	36.04
Std Dev	3.222

**Gaus mean : 36.5857**

**Gaus Width: 4.0918**



# B0L000S, U18-ch78, adc2

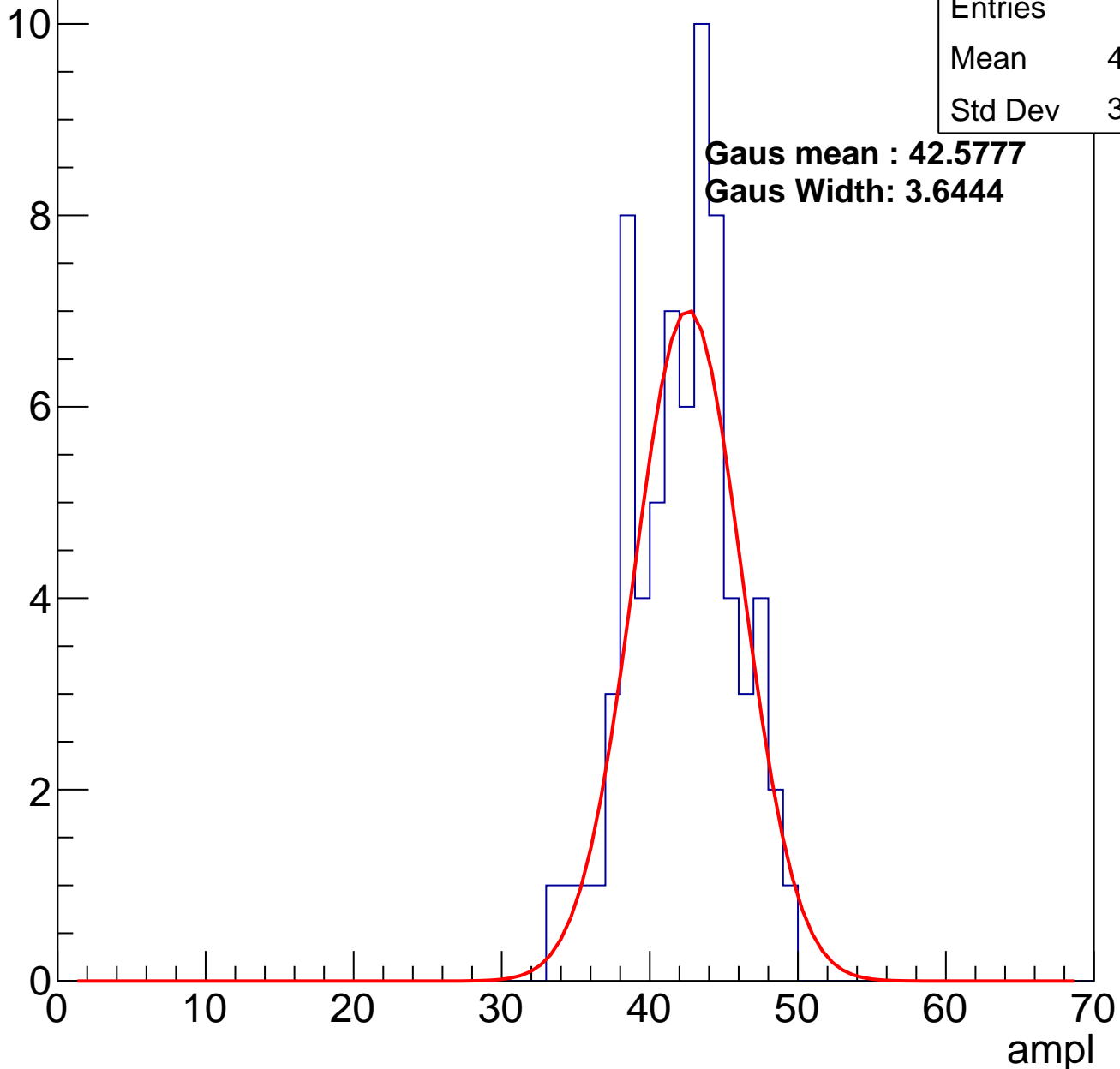
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	41.75
Std Dev	3.487

**Gaus mean : 42.5777**

**Gaus Width: 3.6444**

Entry

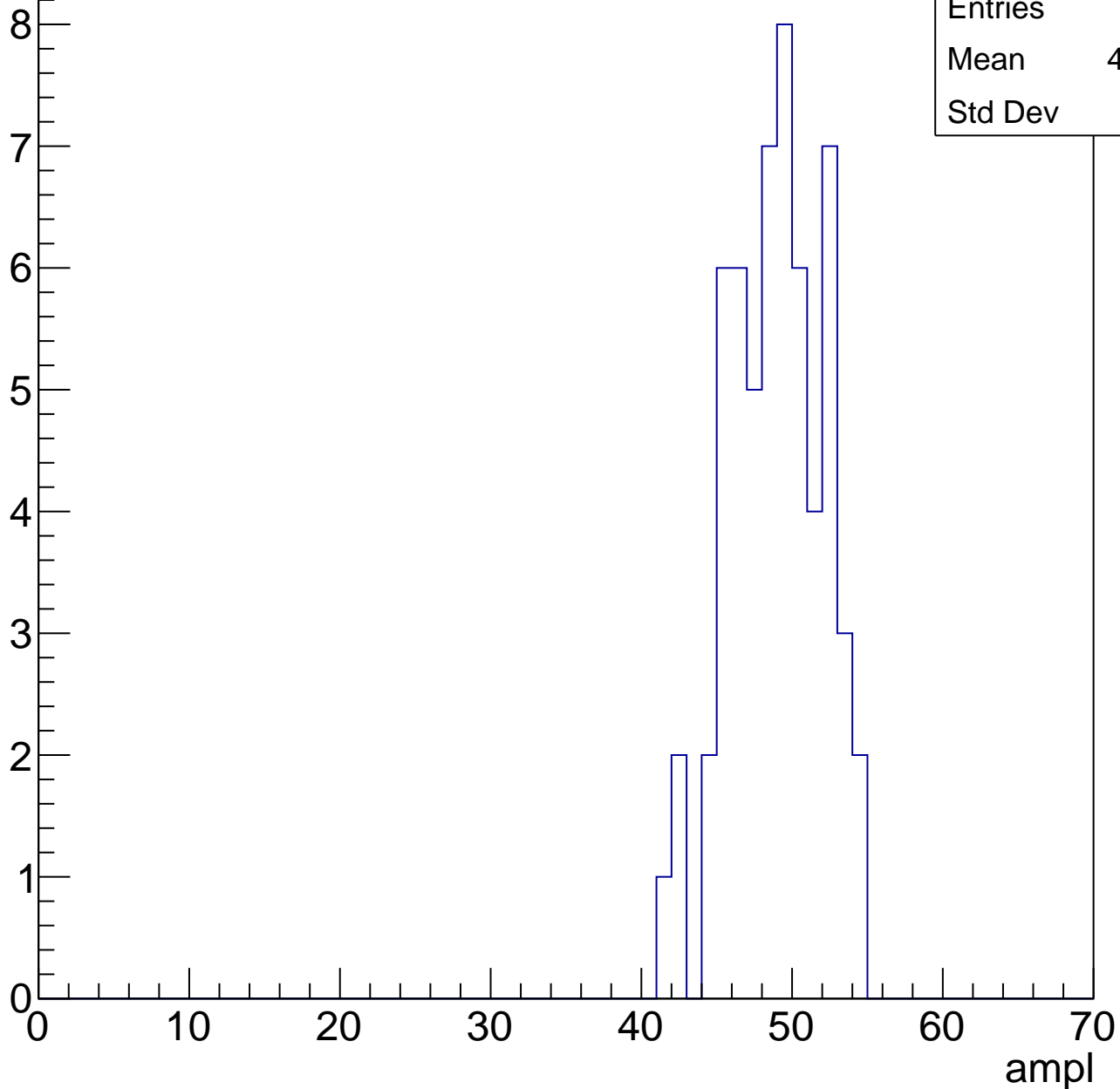


# B0L000S, U18-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	48.42
Std Dev	3.06

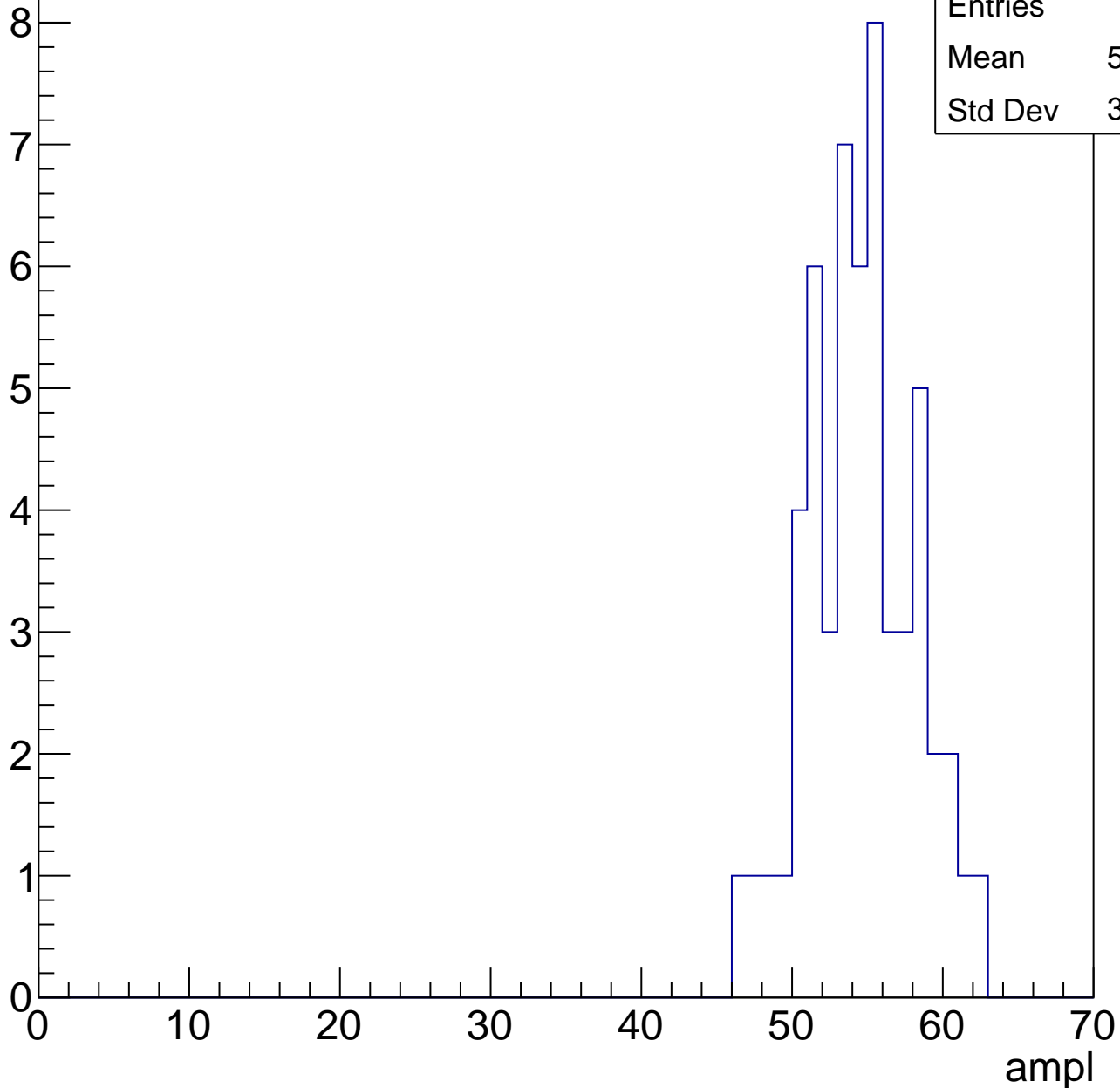


# B0L000S, U18-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

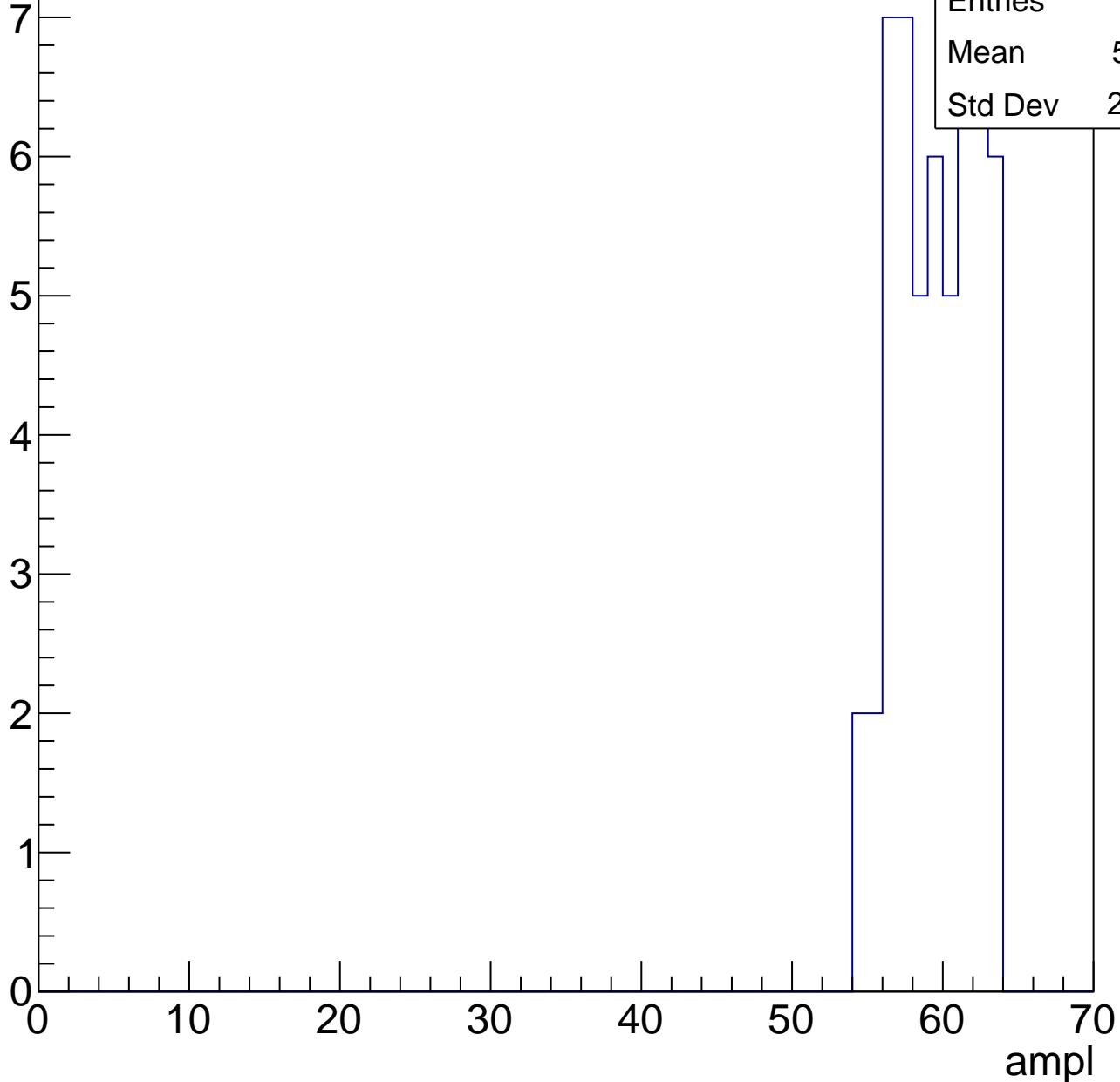
Entries	55
Mean	54.13
Std Dev	3.496



# B0L000S, U18-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

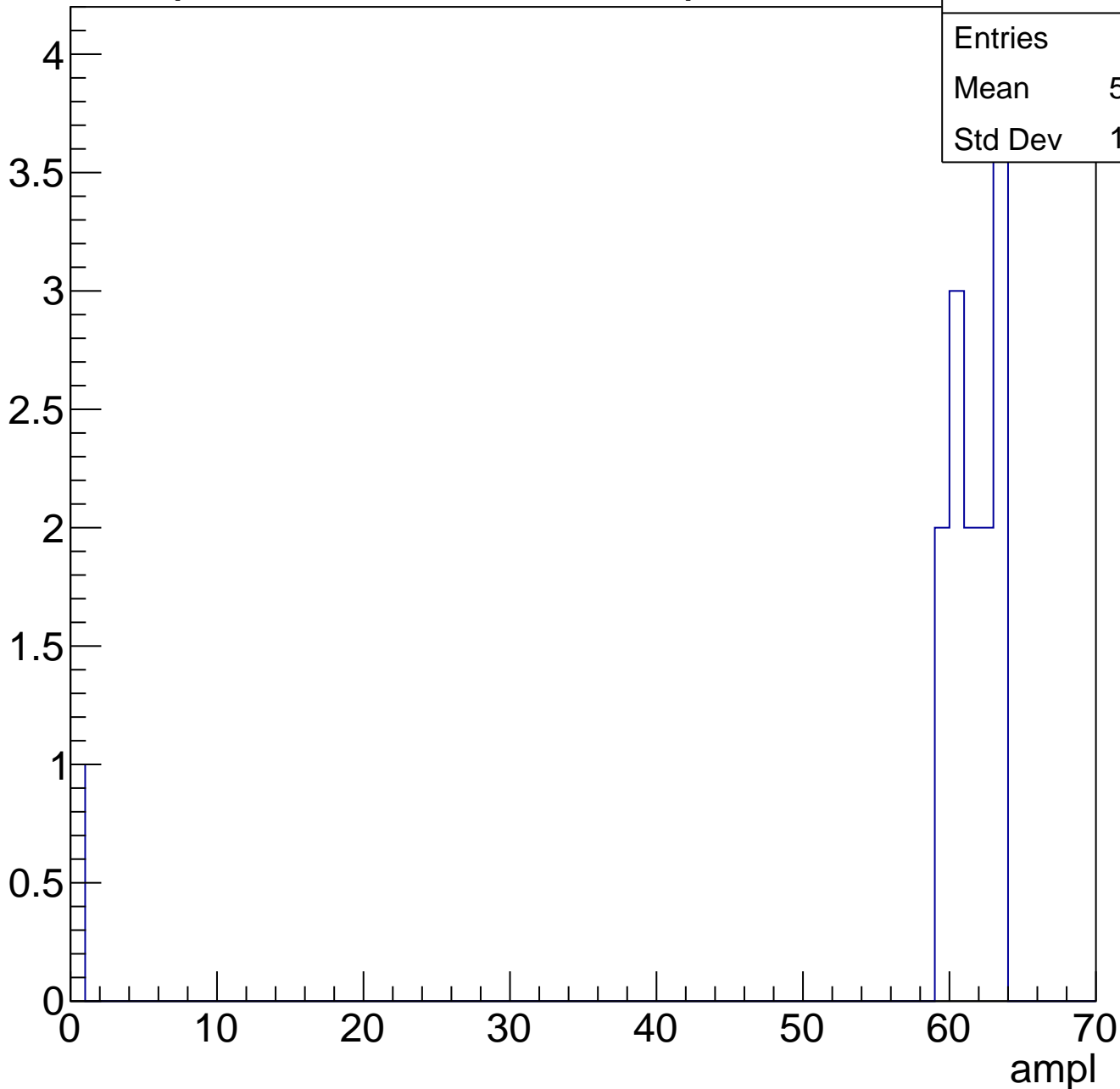


Entries	54
Mean	59.11
Std Dev	2.615

# B0L000S, U18-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch79, adc0

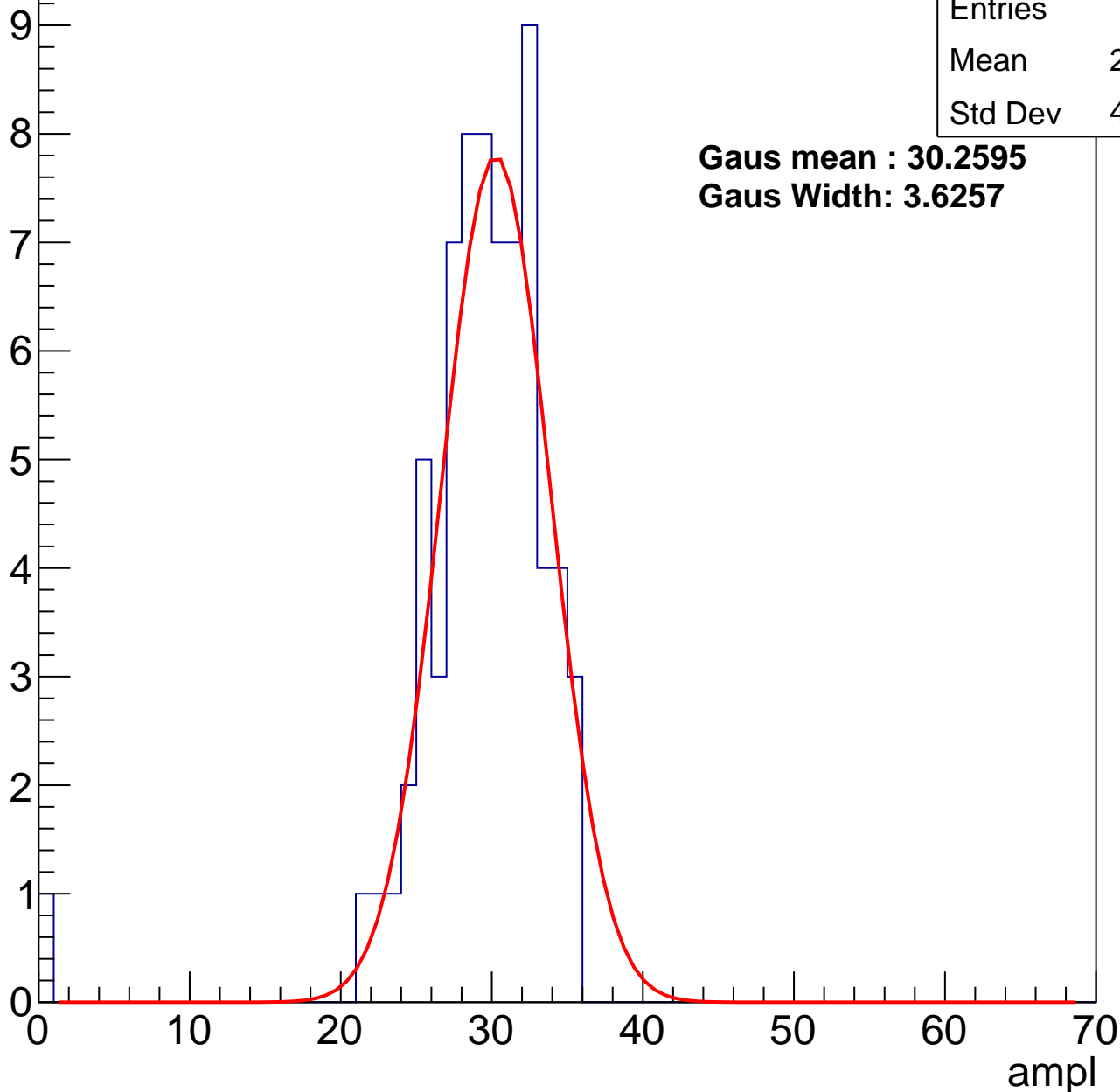
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.87
Std Dev	4.702

**Gaus mean : 30.2595**

**Gaus Width: 3.6257**



# B0L000S, U18-ch79, adc1

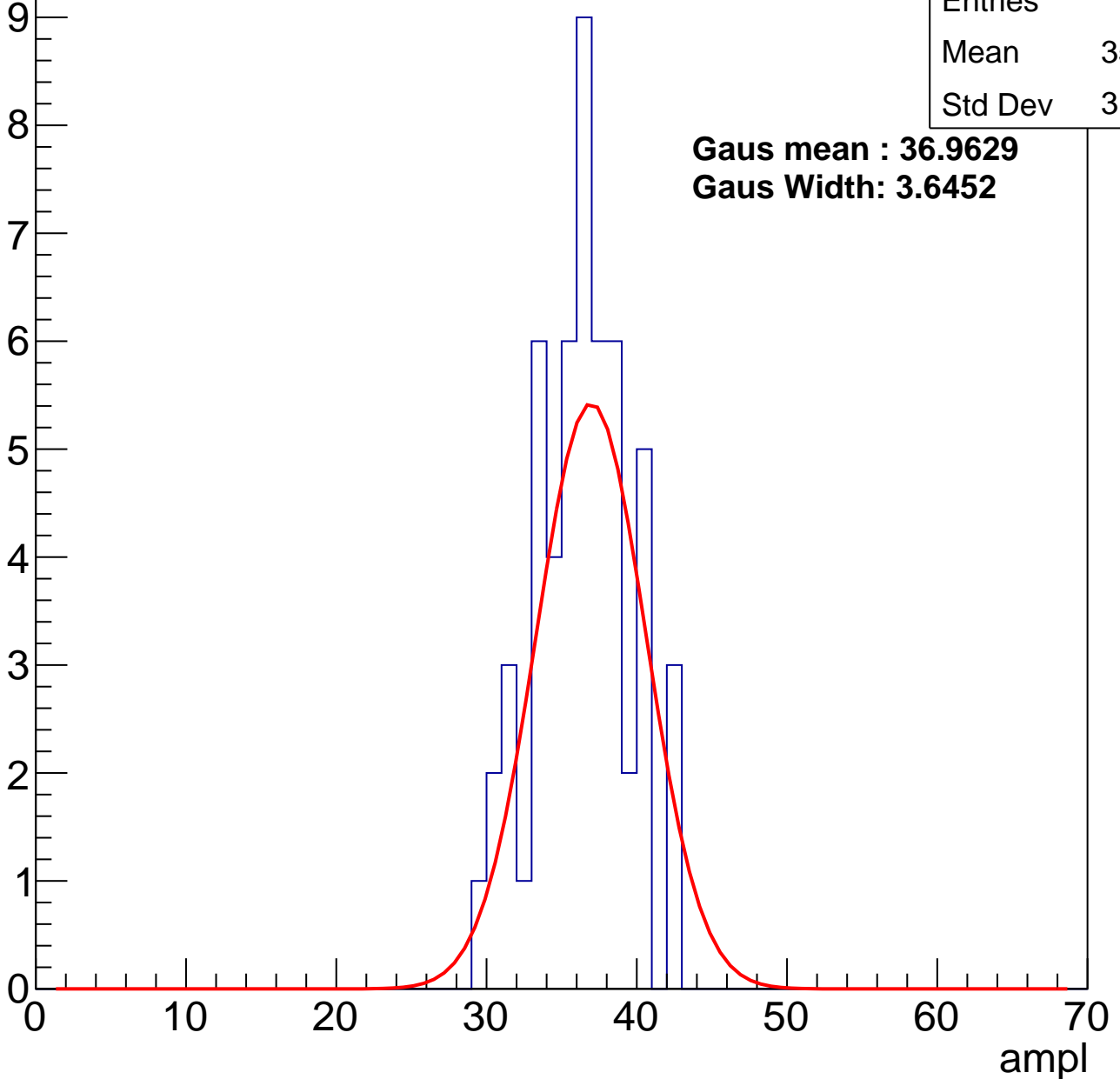
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	35.85
Std Dev	3.112

**Gaus mean : 36.9629**

**Gaus Width: 3.6452**



# B0L000S, U18-ch79, adc2

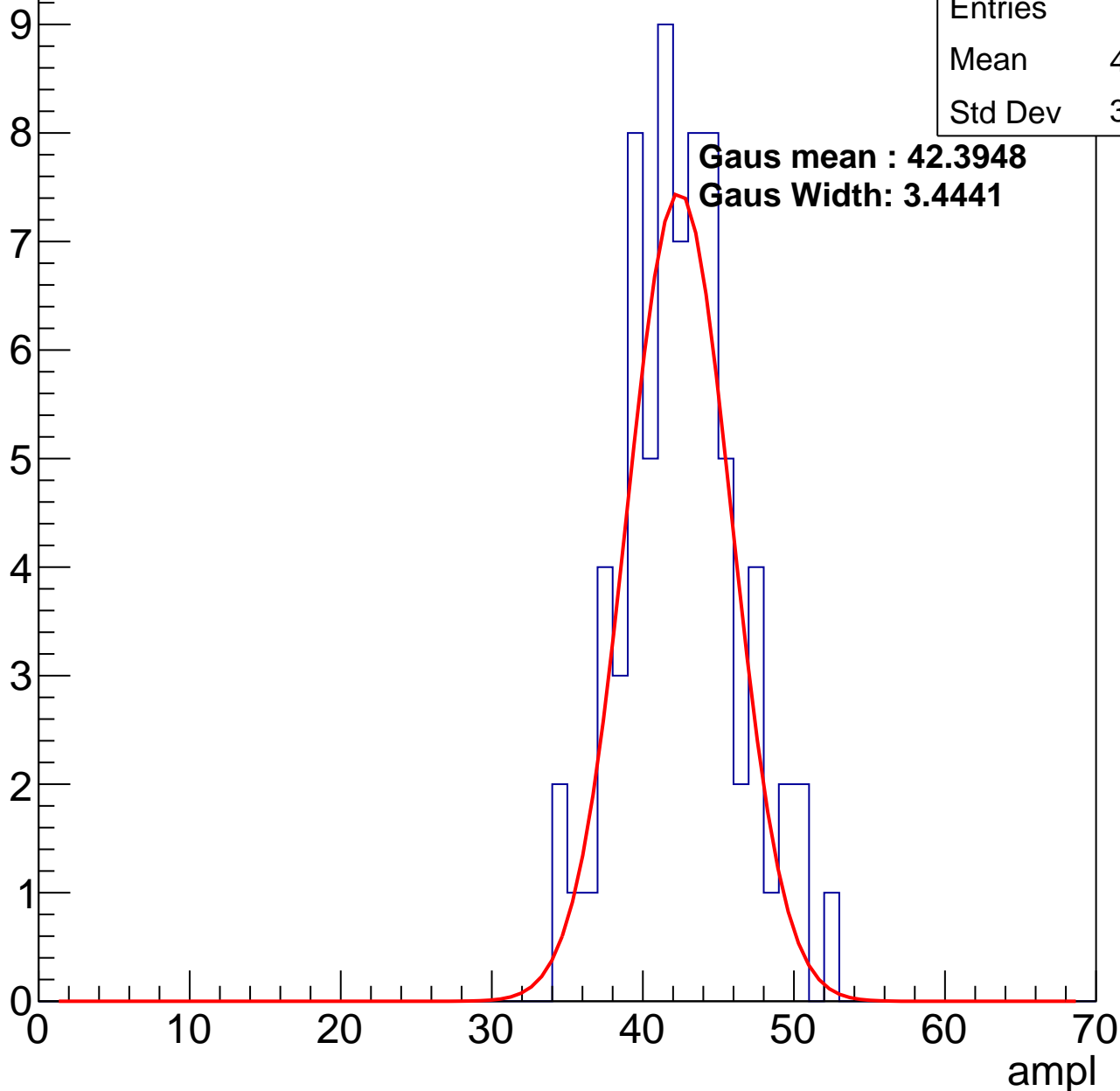
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	42.12
Std Dev	3.774

**Gaus mean : 42.3948**

**Gaus Width: 3.4441**

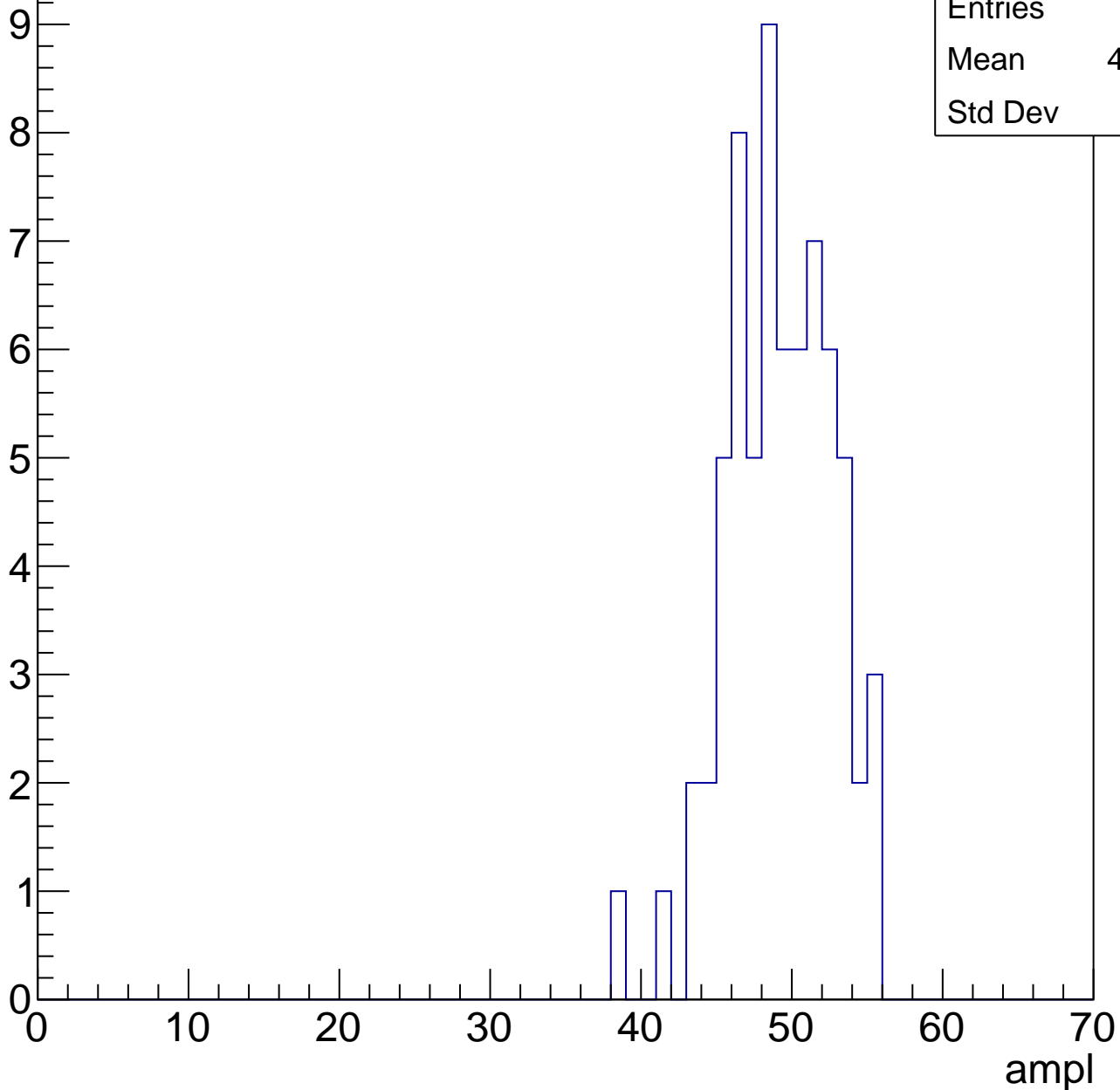


# B0L000S, U18-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	48.74
Std Dev	3.45

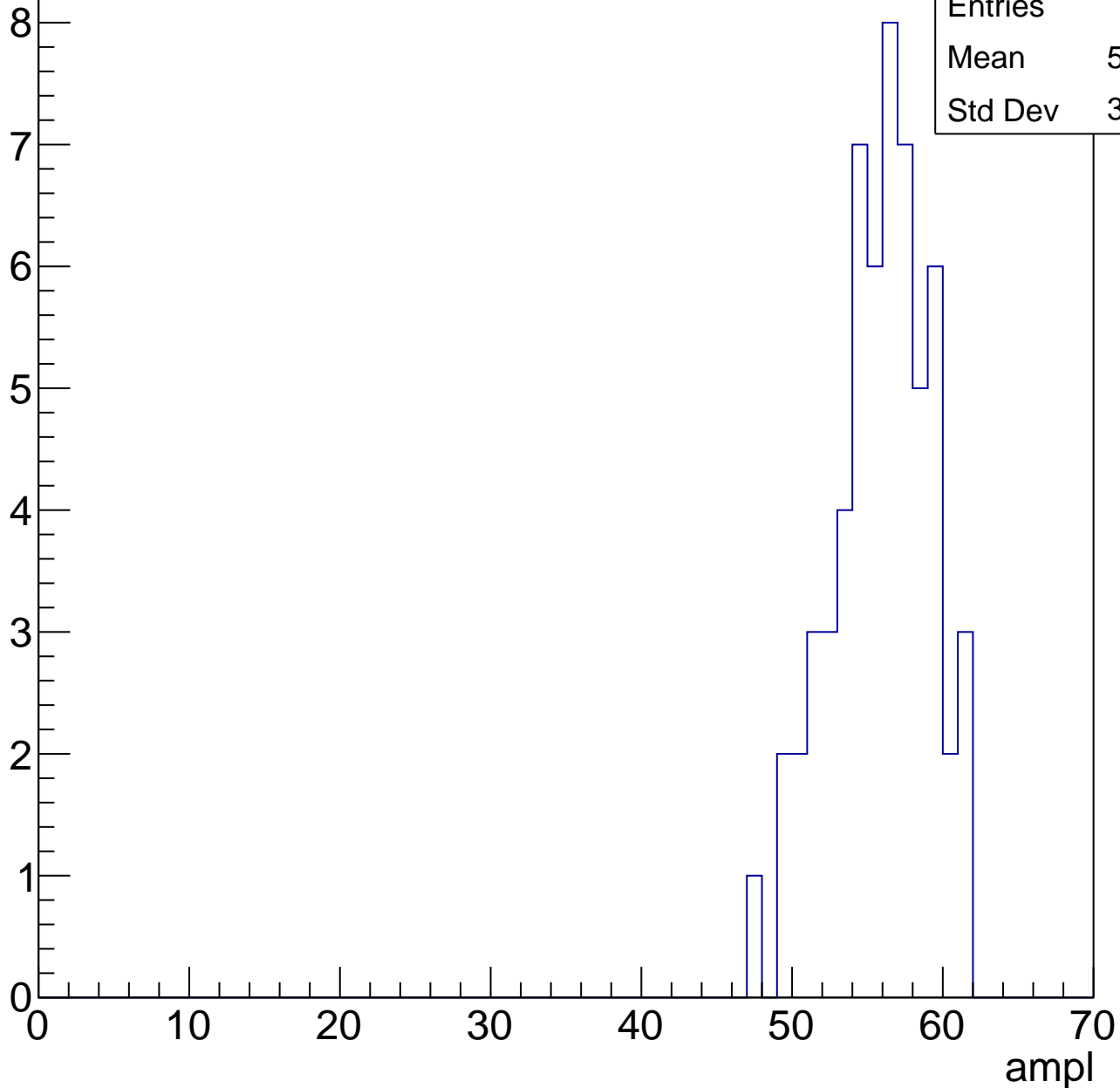


# B0L000S, U18-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

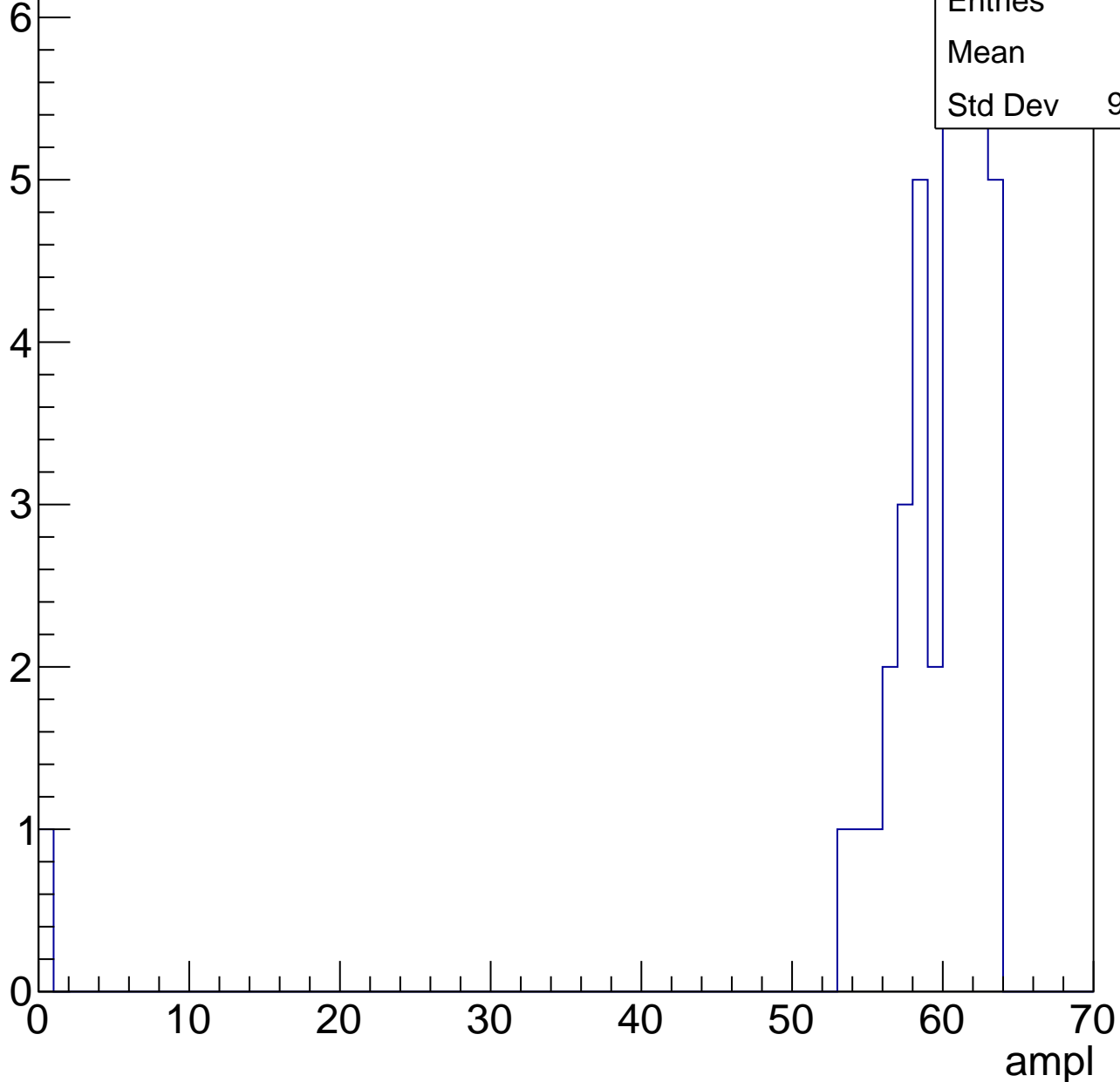
Entries	59
Mean	55.39
Std Dev	3.216



# B0L000S, U18-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

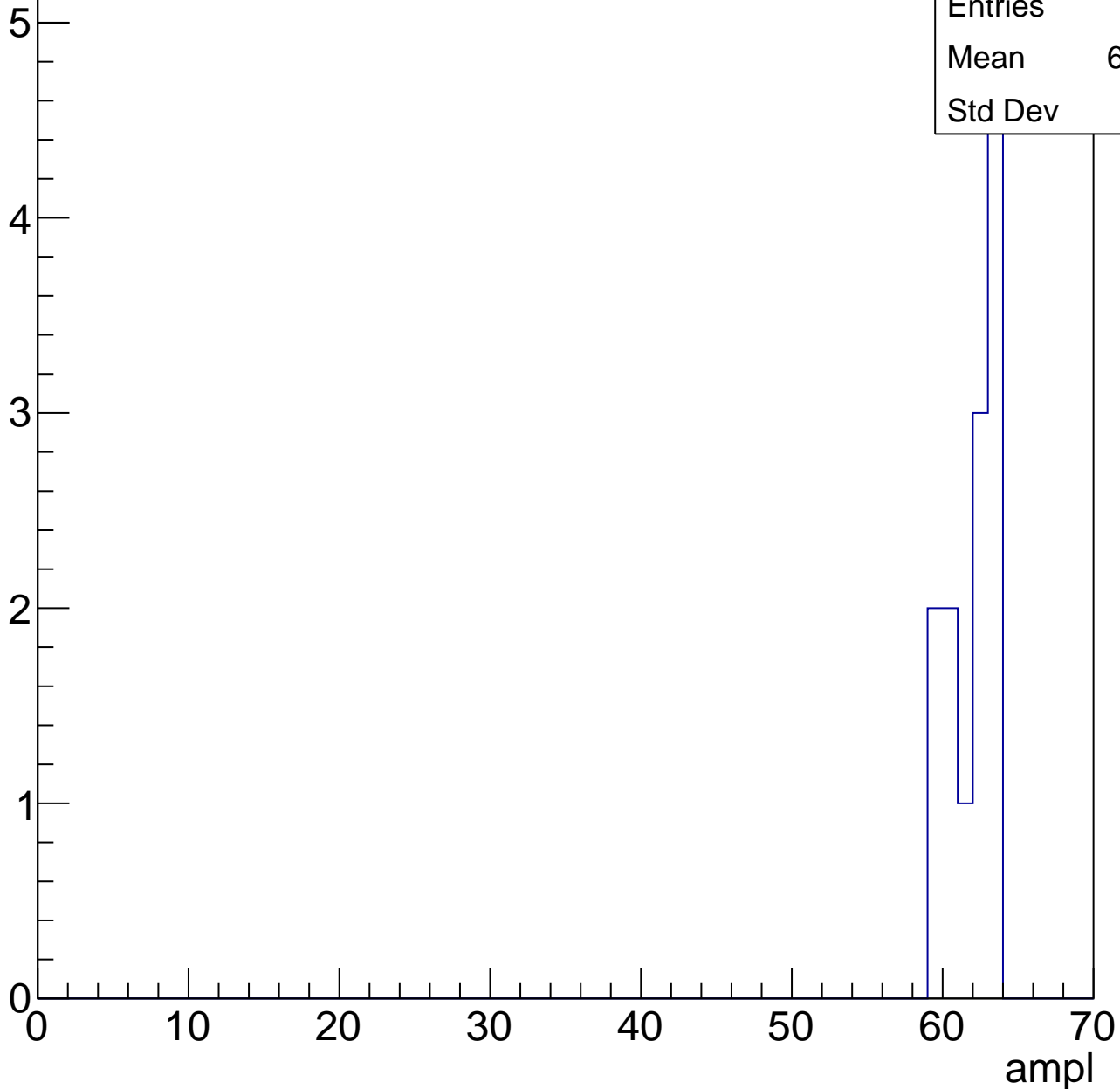


# B0L000S, U18-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	61.54
Std Dev	1.5

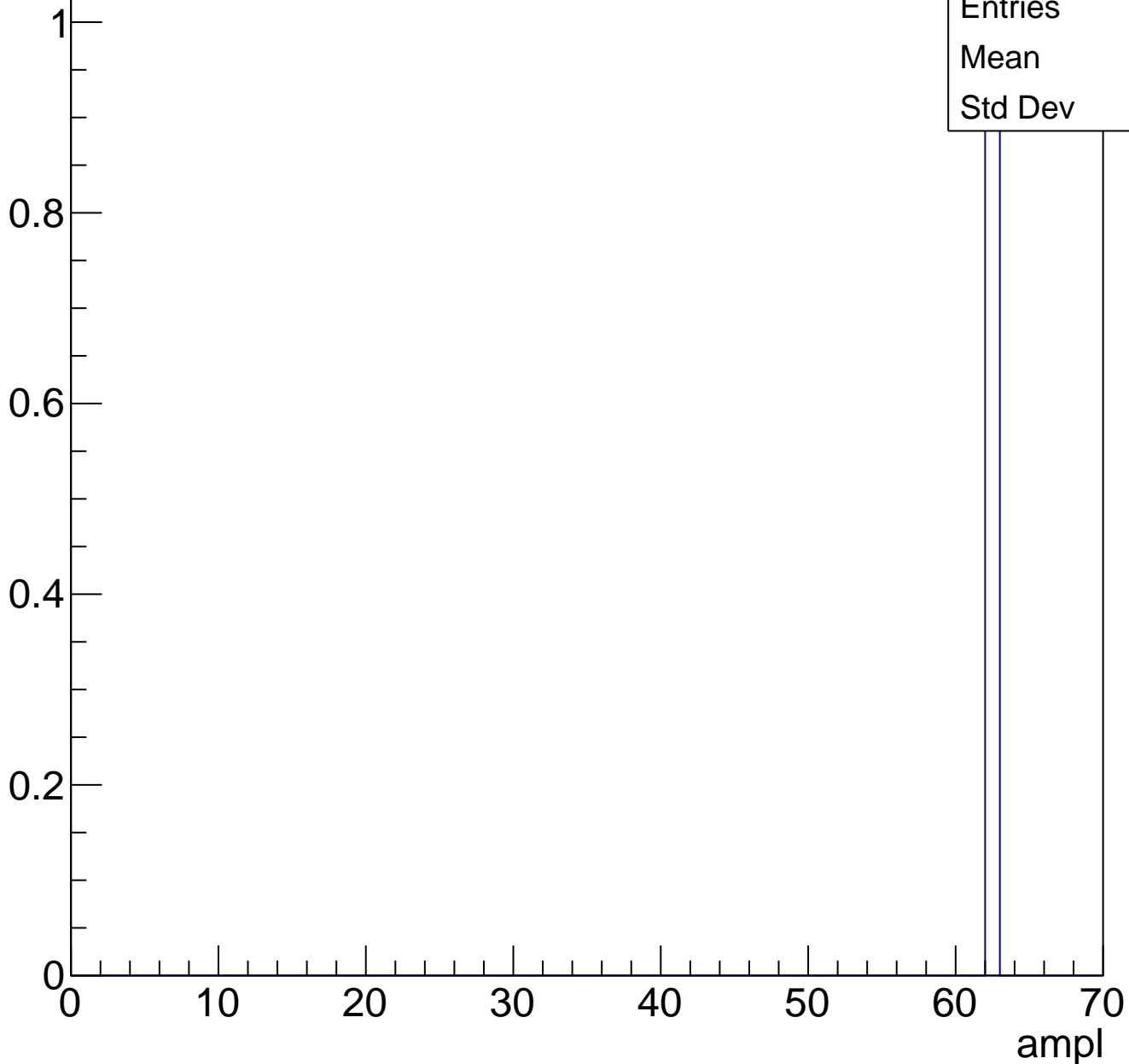




# B0L000S, U18-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	62
Std Dev	0

# B0L000S, U18-ch80, adc0

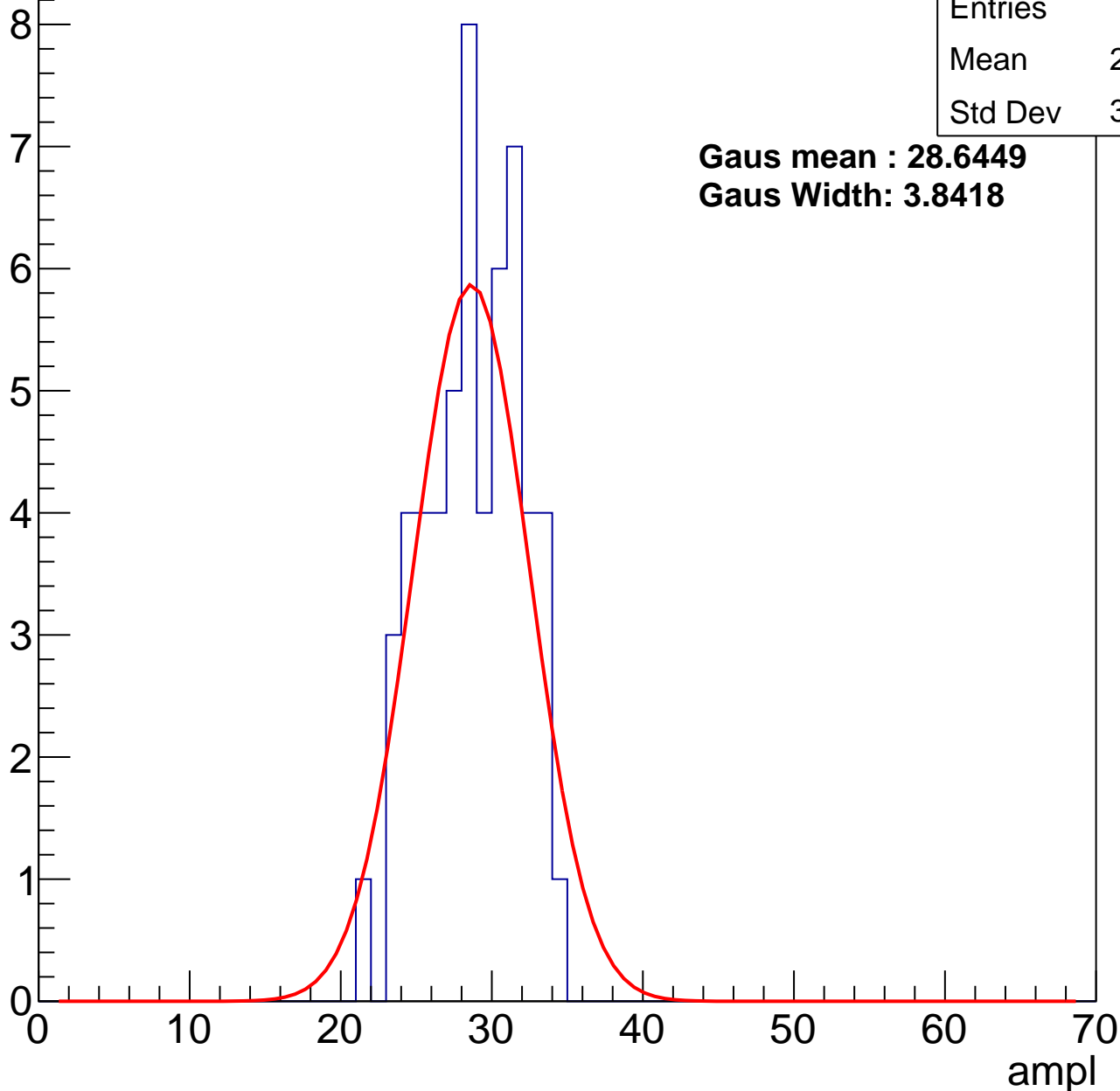
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	28.29
Std Dev	3.108

**Gaus mean : 28.6449**

**Gaus Width: 3.8418**



# B0L000S, U18-ch80, adc1

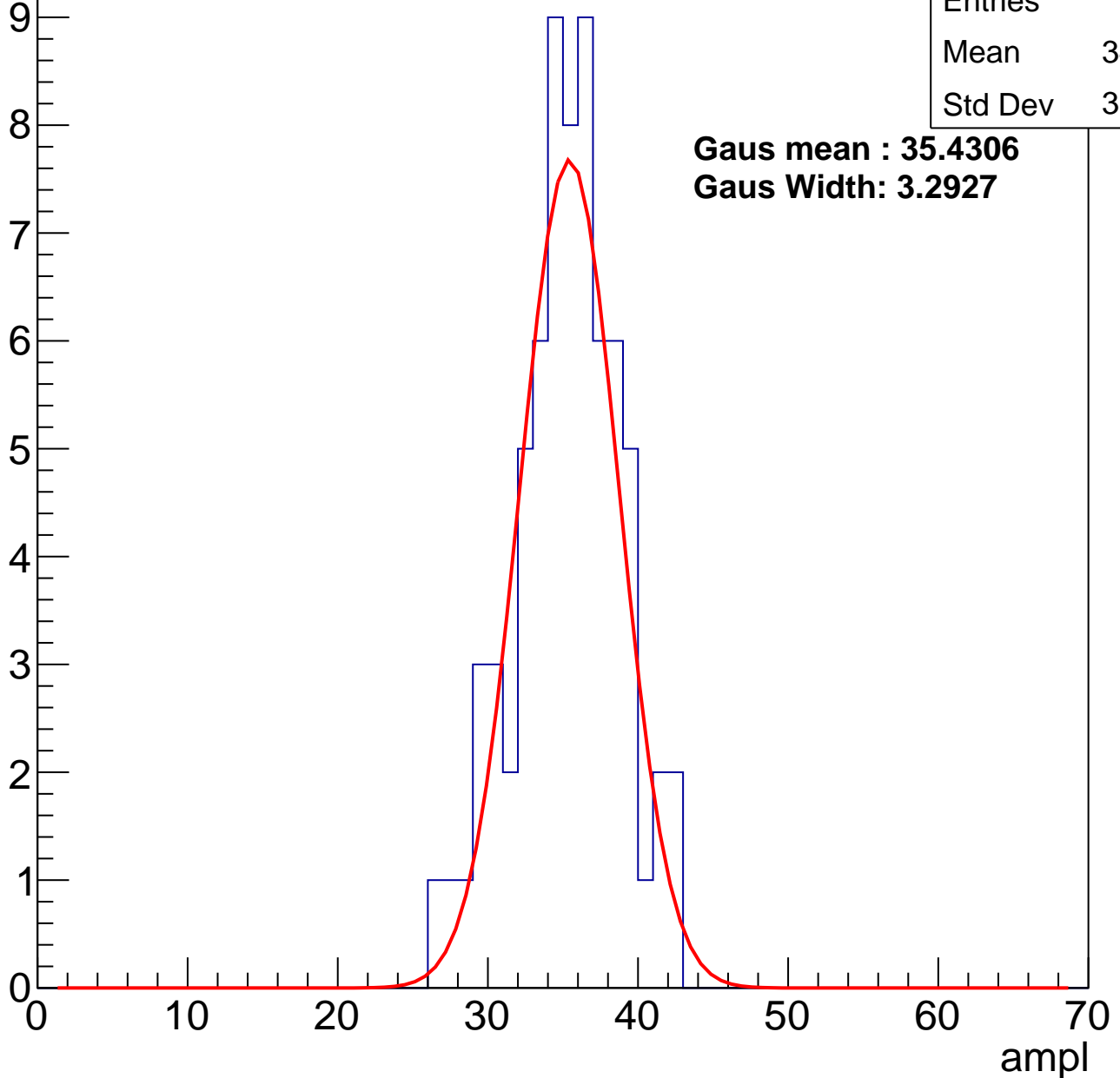
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	34.84
Std Dev	3.479

**Gaus mean : 35.4306**

**Gaus Width: 3.2927**



# B0L000S, U18-ch80, adc2

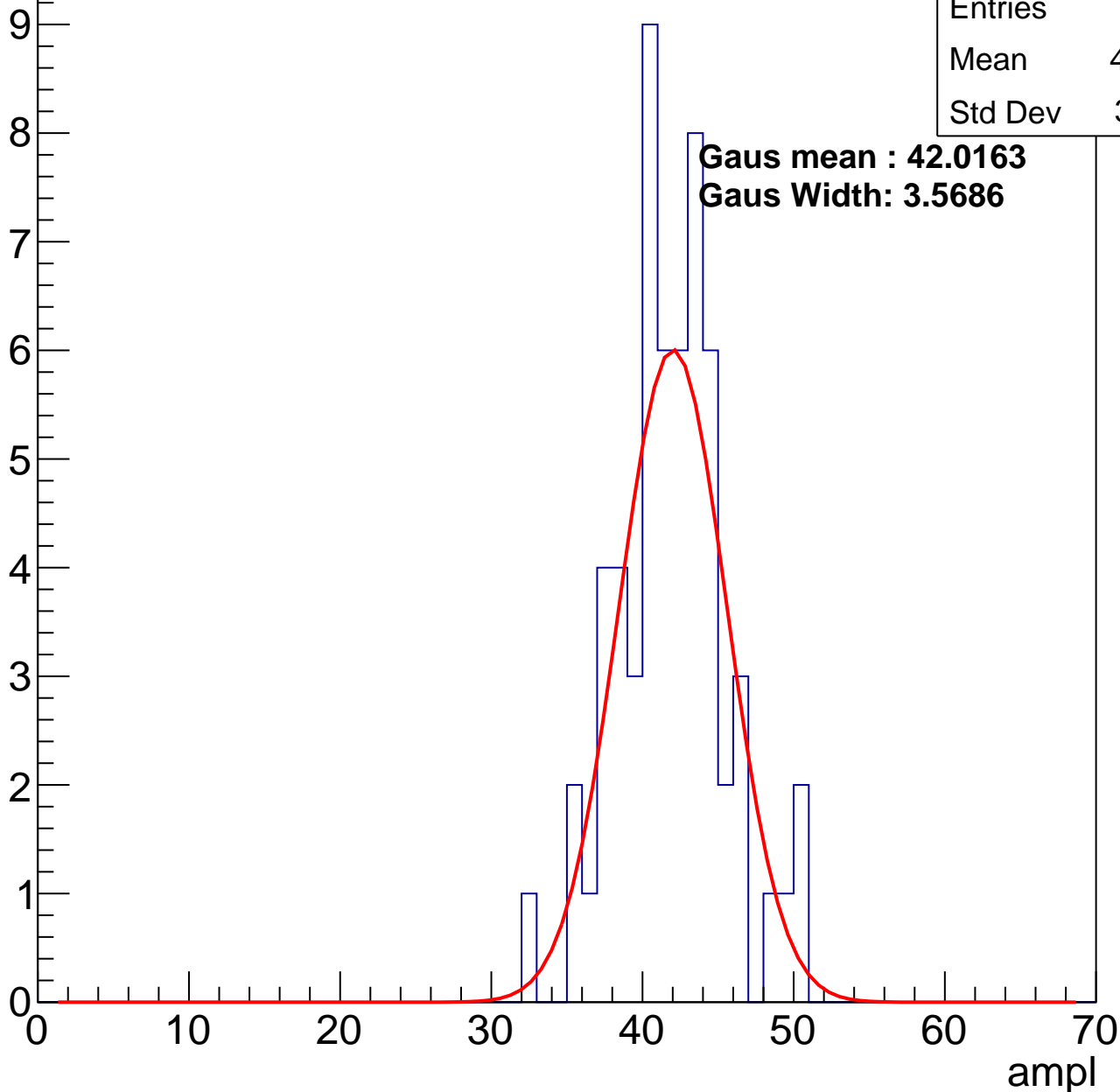
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	41.46
Std Dev	3.591

**Gaus mean : 42.0163**

**Gaus Width: 3.5686**

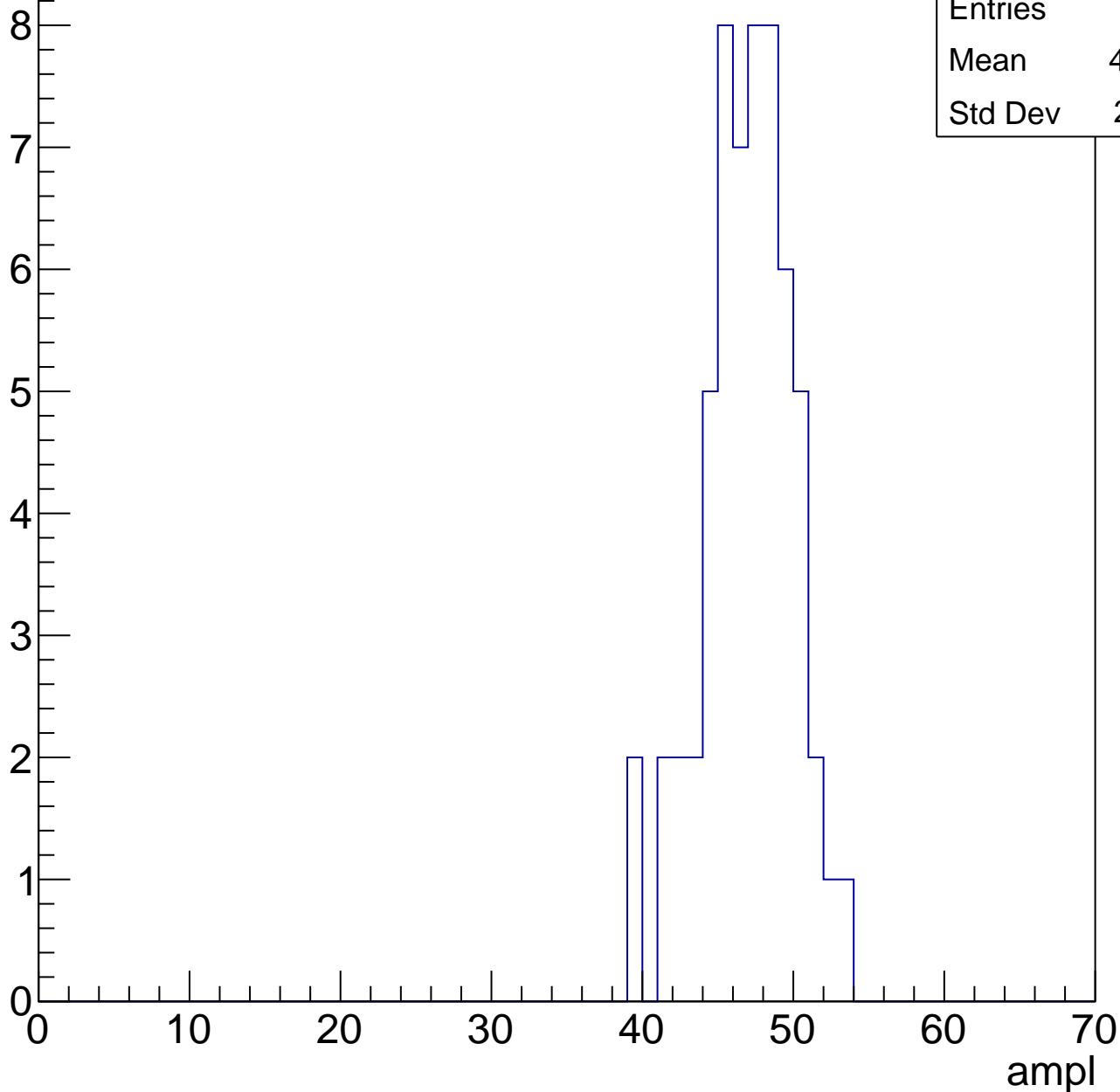


# B0L000S, U18-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

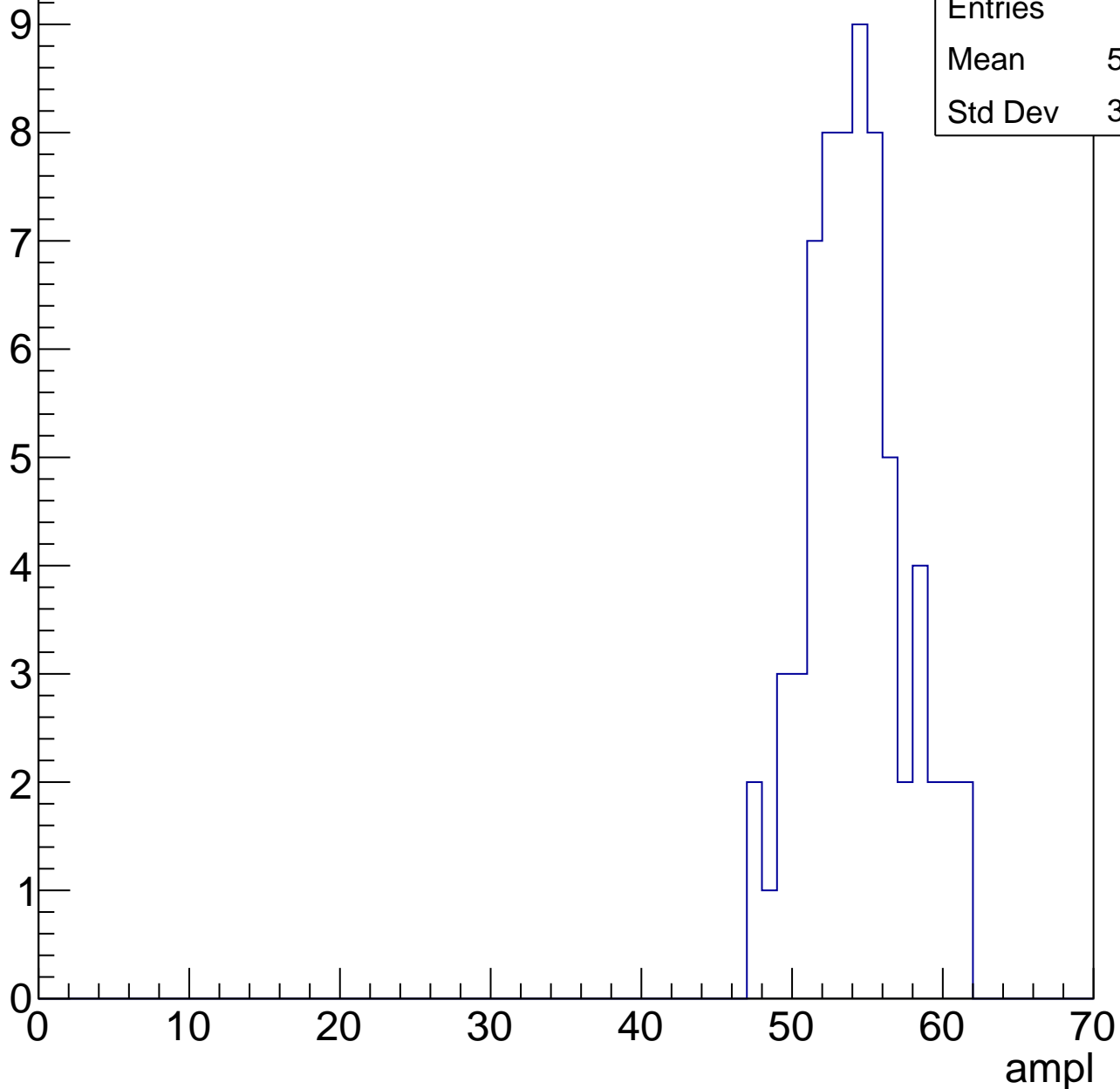
Entries	59
Mean	46.49
Std Dev	2.971



# B0L000S, U18-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	66
Mean	53.76
Std Dev	3.224

# B0L000S, U18-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	58.43
Std Dev	2.819

ampl

0

10

20

30

40

50

60

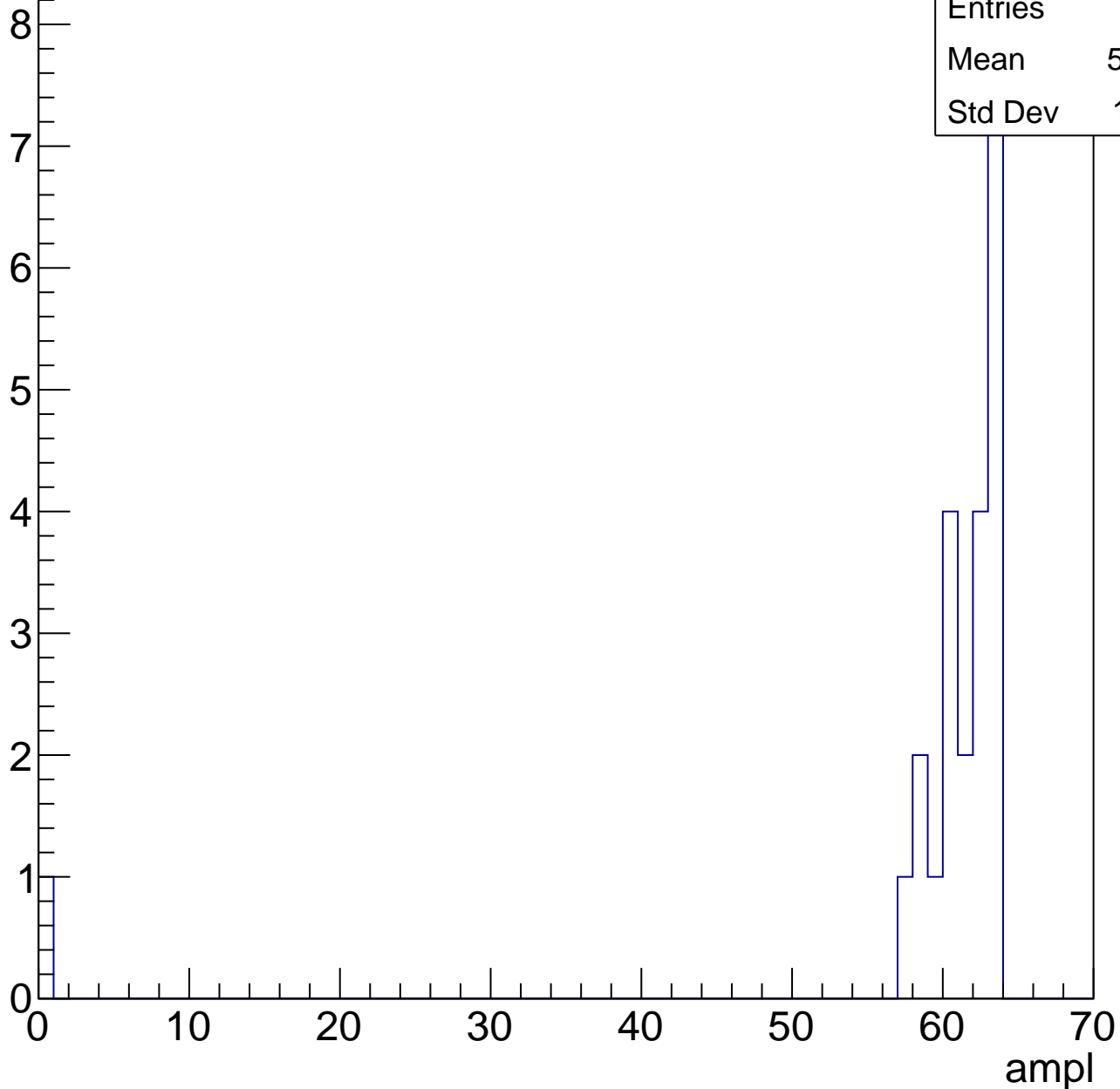
70

# B0L000S, U18-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.52
Std Dev	12.61

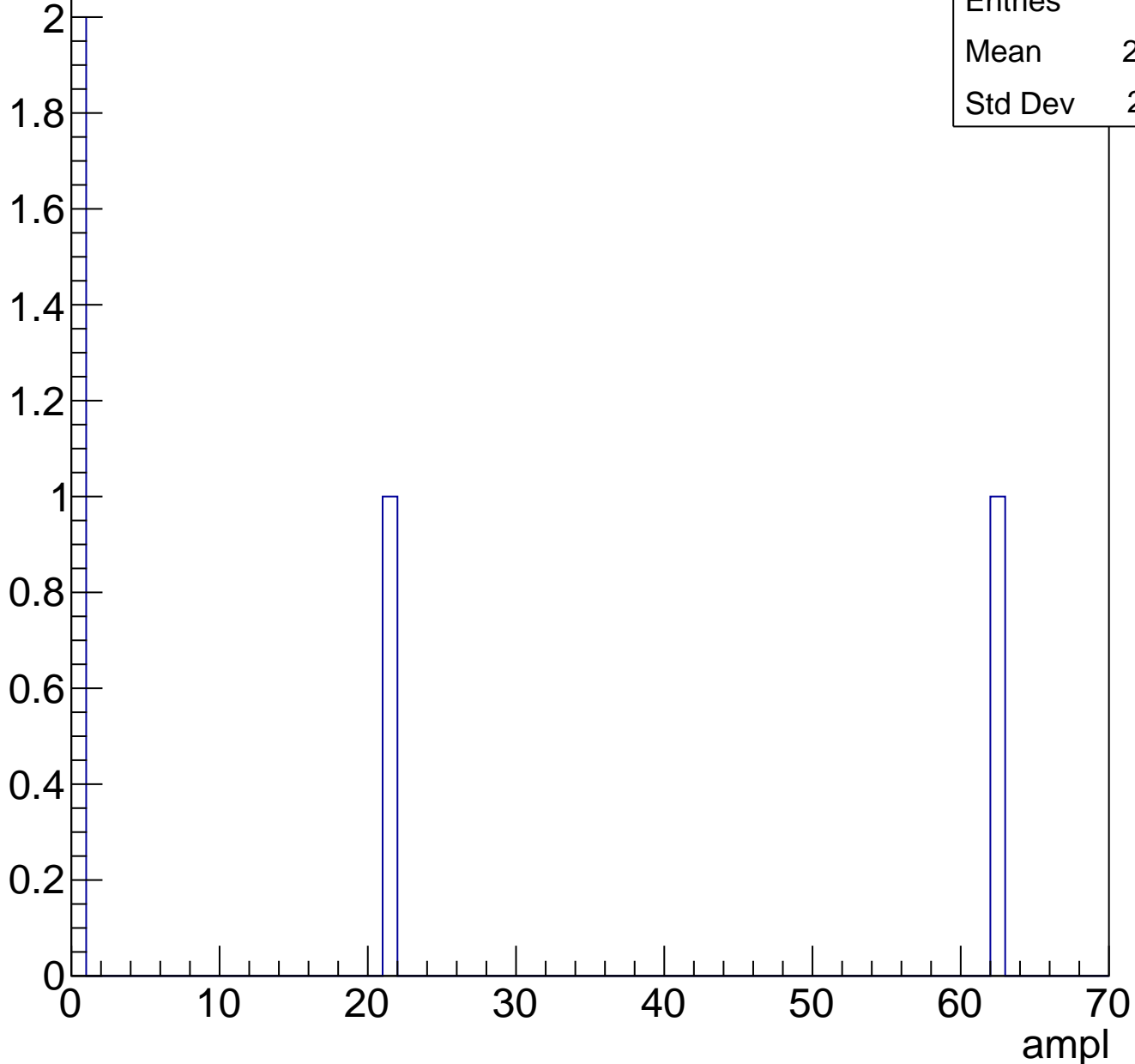




# B0L000S, U18-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	20.75
Std Dev	25.31

# B0L000S, U18-ch81, adc0

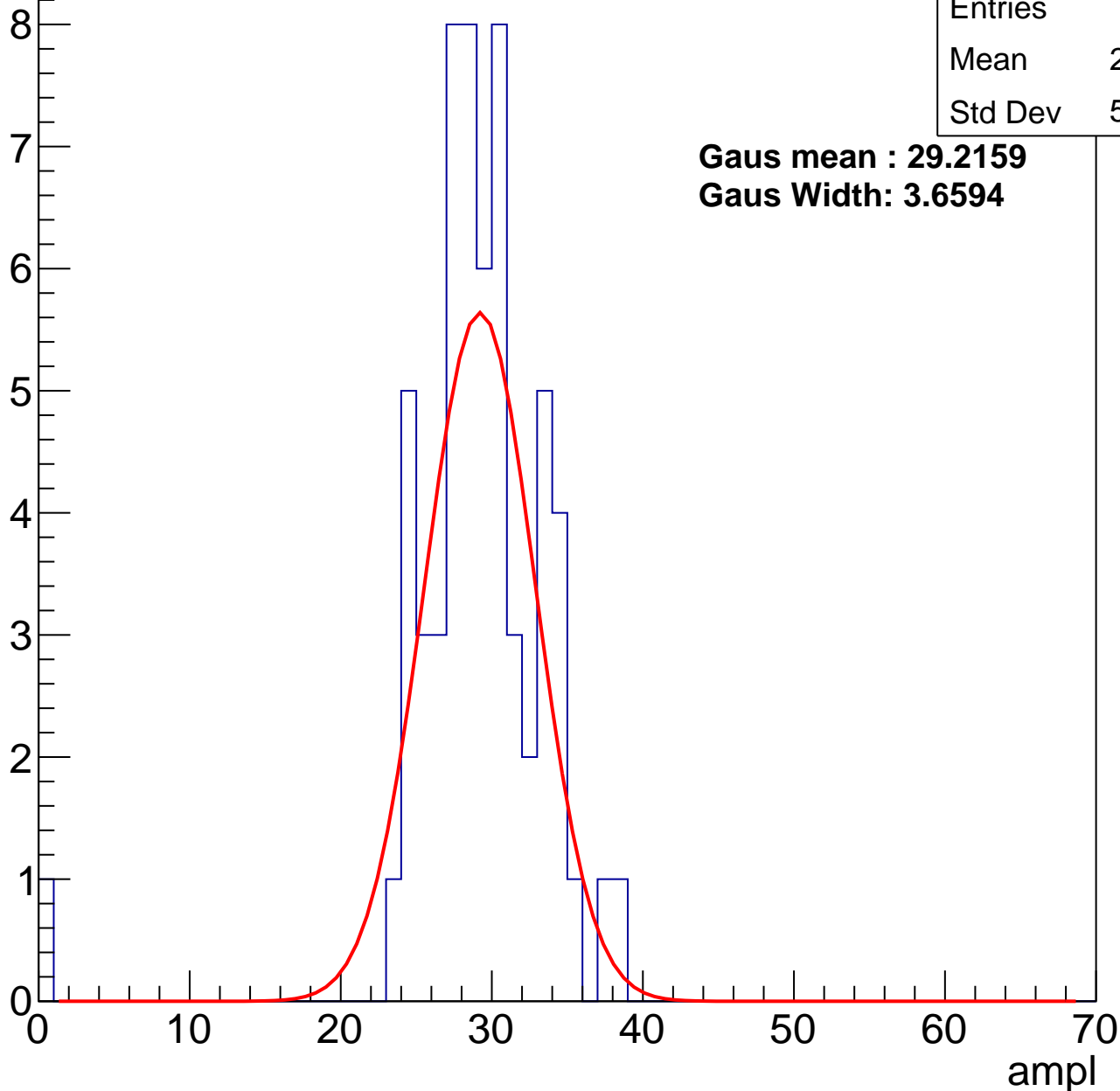
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	28.63
Std Dev	5.013

**Gaus mean : 29.2159**

**Gaus Width: 3.6594**



# B0L000S, U18-ch81, adc1

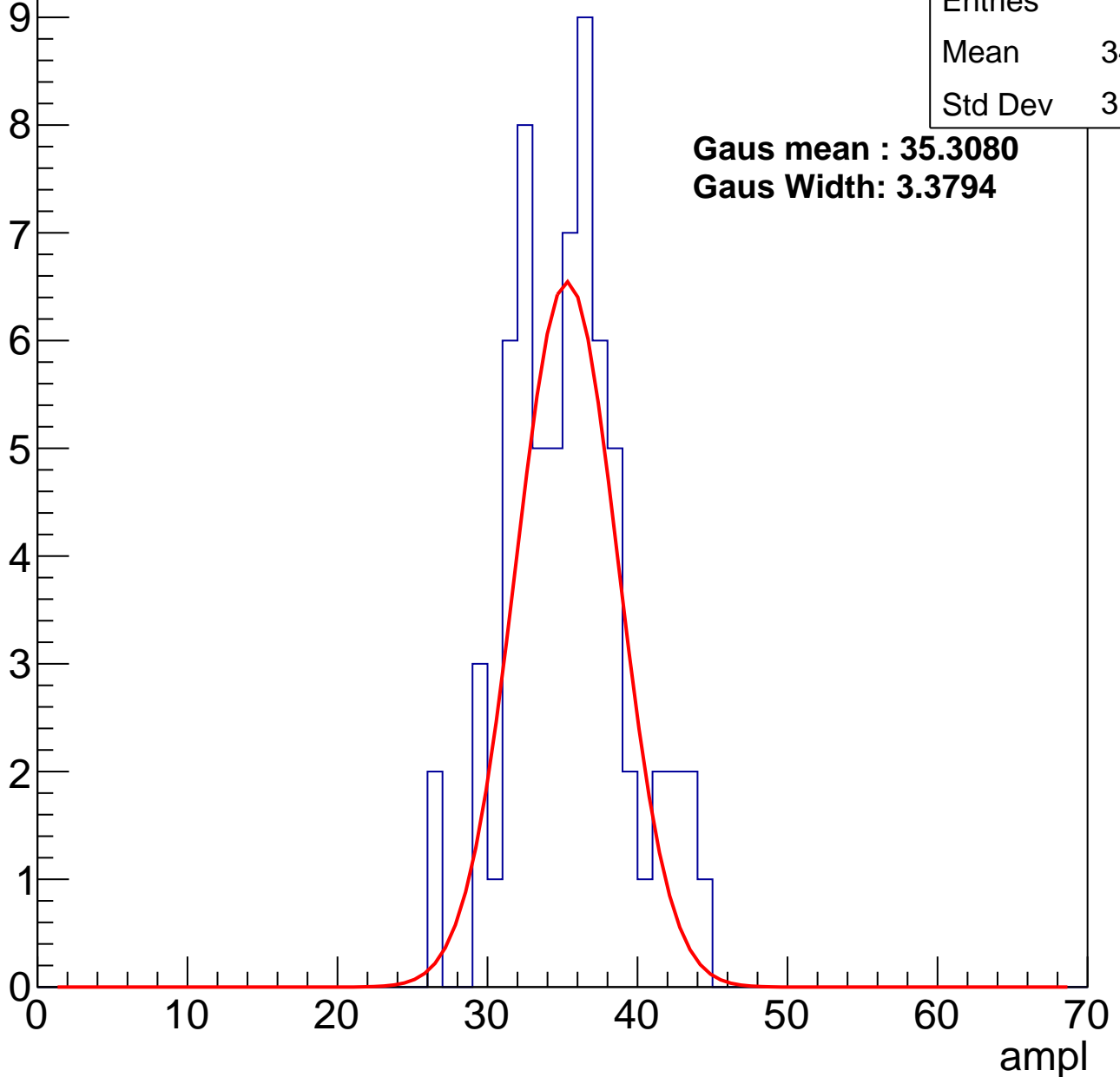
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	34.94
Std Dev	3.867

**Gaus mean : 35.3080**

**Gaus Width: 3.3794**



# B0L000S, U18-ch81, adc2

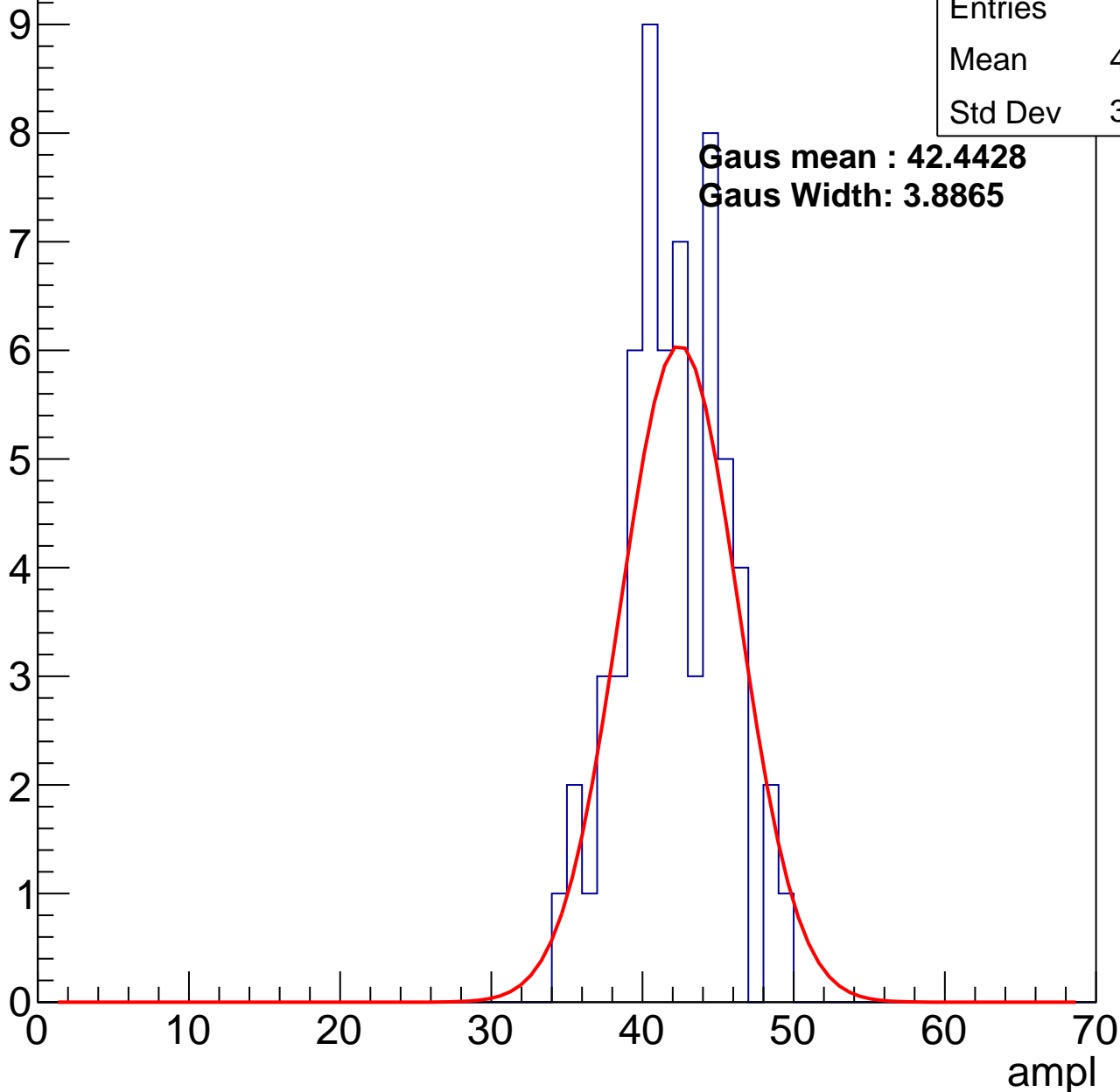
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	41.54
Std Dev	3.312

**Gaus mean : 42.4428**

**Gaus Width: 3.8865**

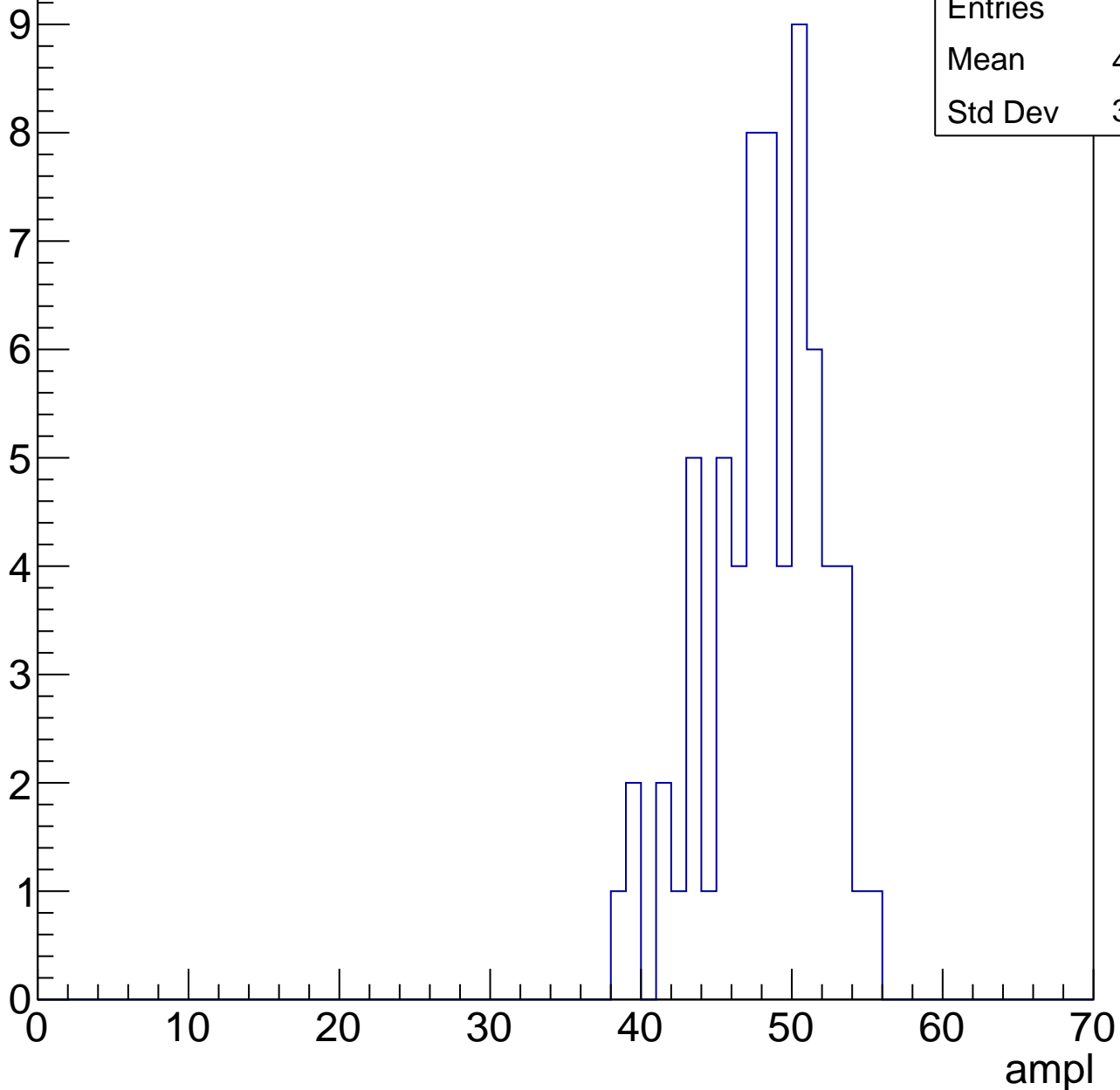


# B0L000S, U18-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

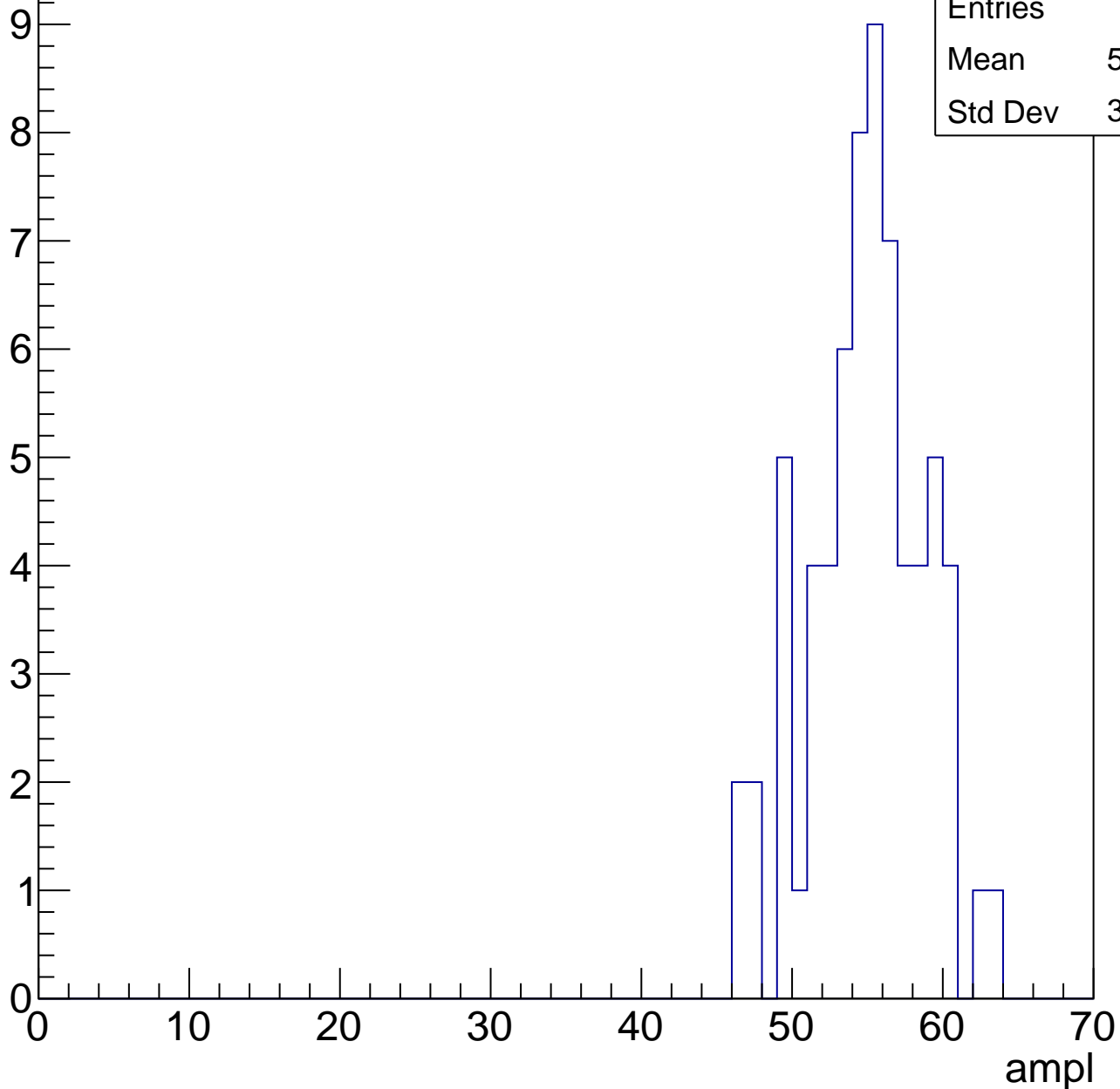
Entries	66
Mean	47.71
Std Dev	3.781



# B0L000S, U18-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

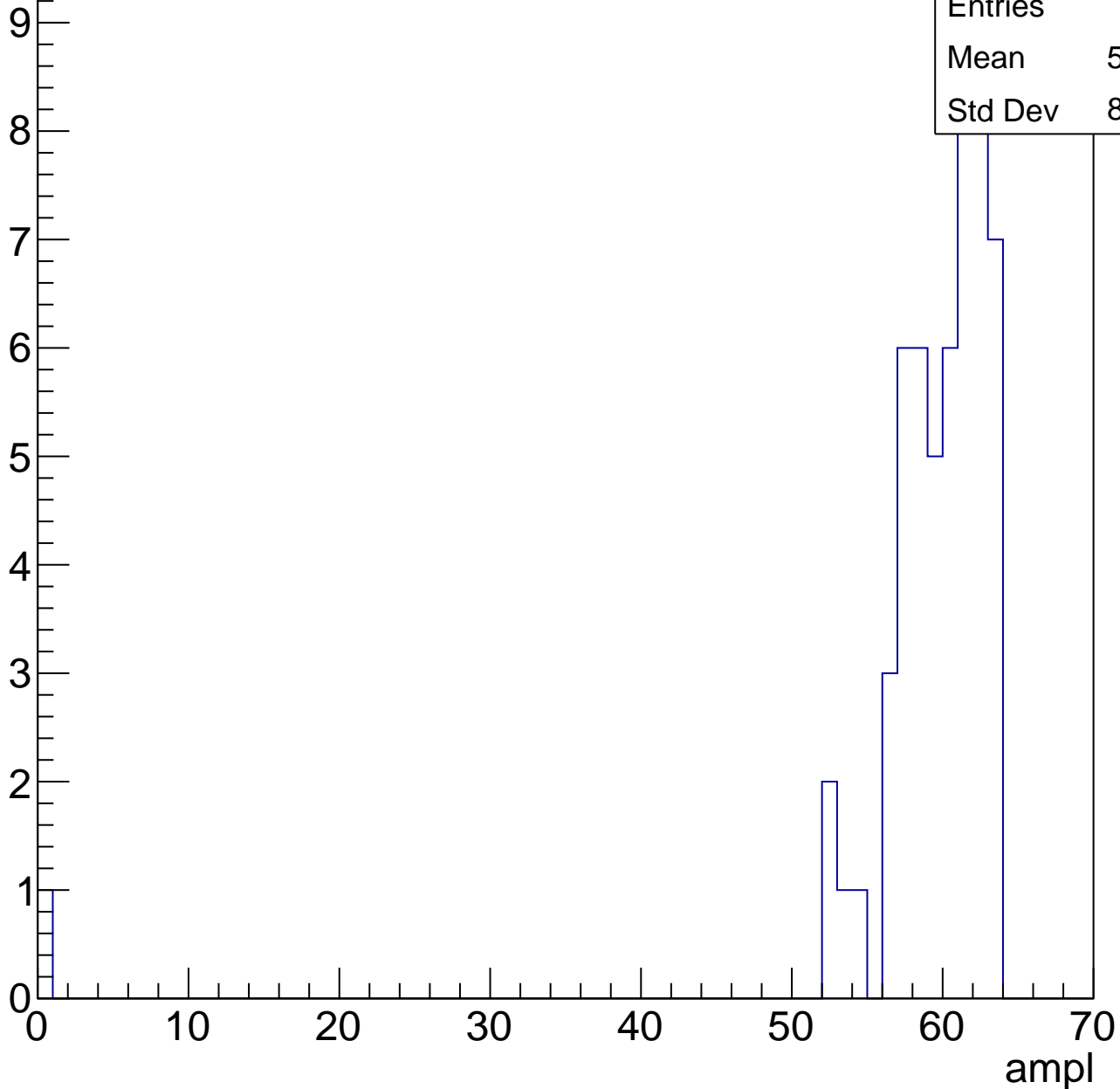


# B0L000S, U18-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.36
Std Dev	8.422



# B0L000S, U18-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

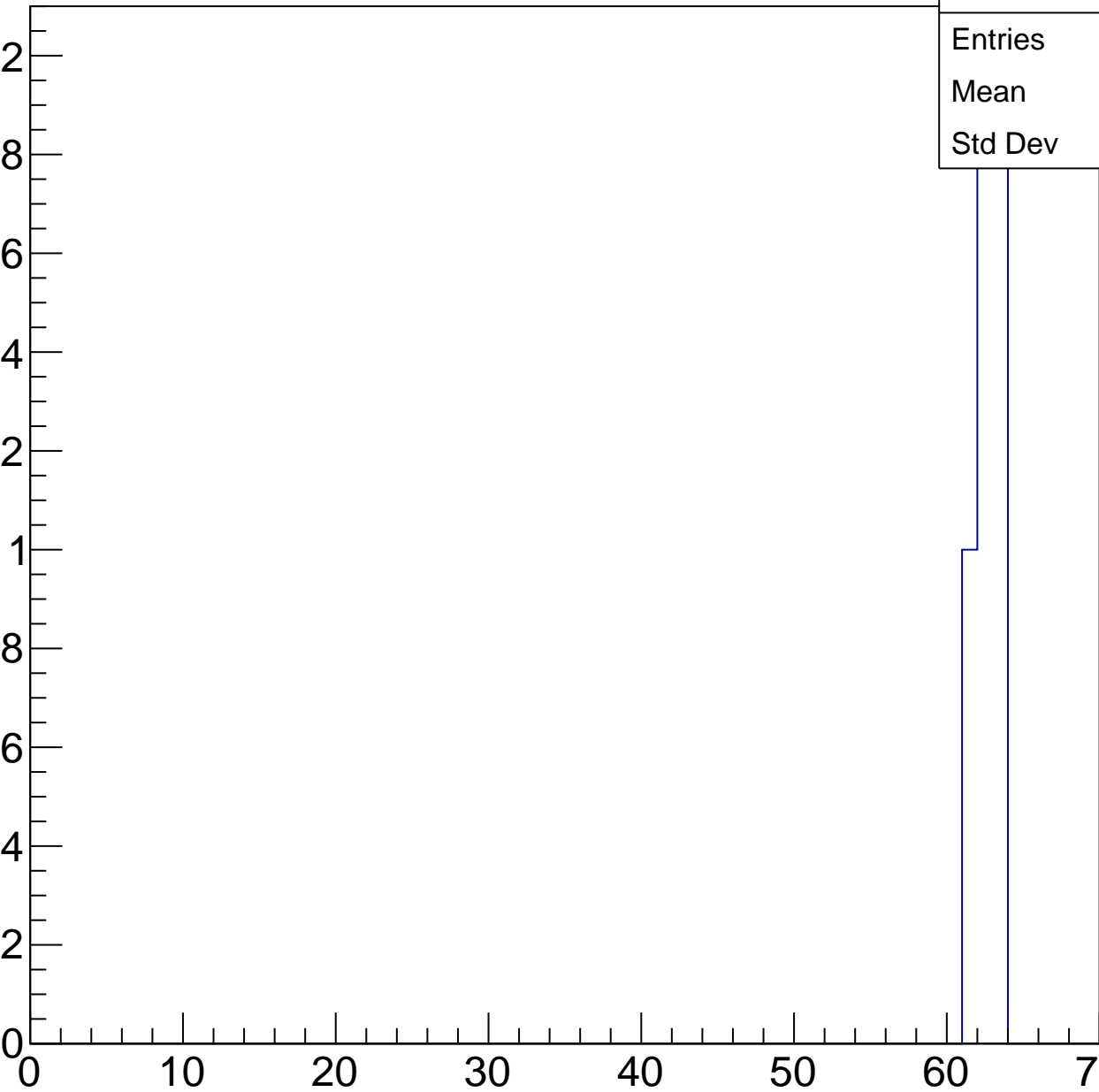
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl





# B0L000S, U18-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

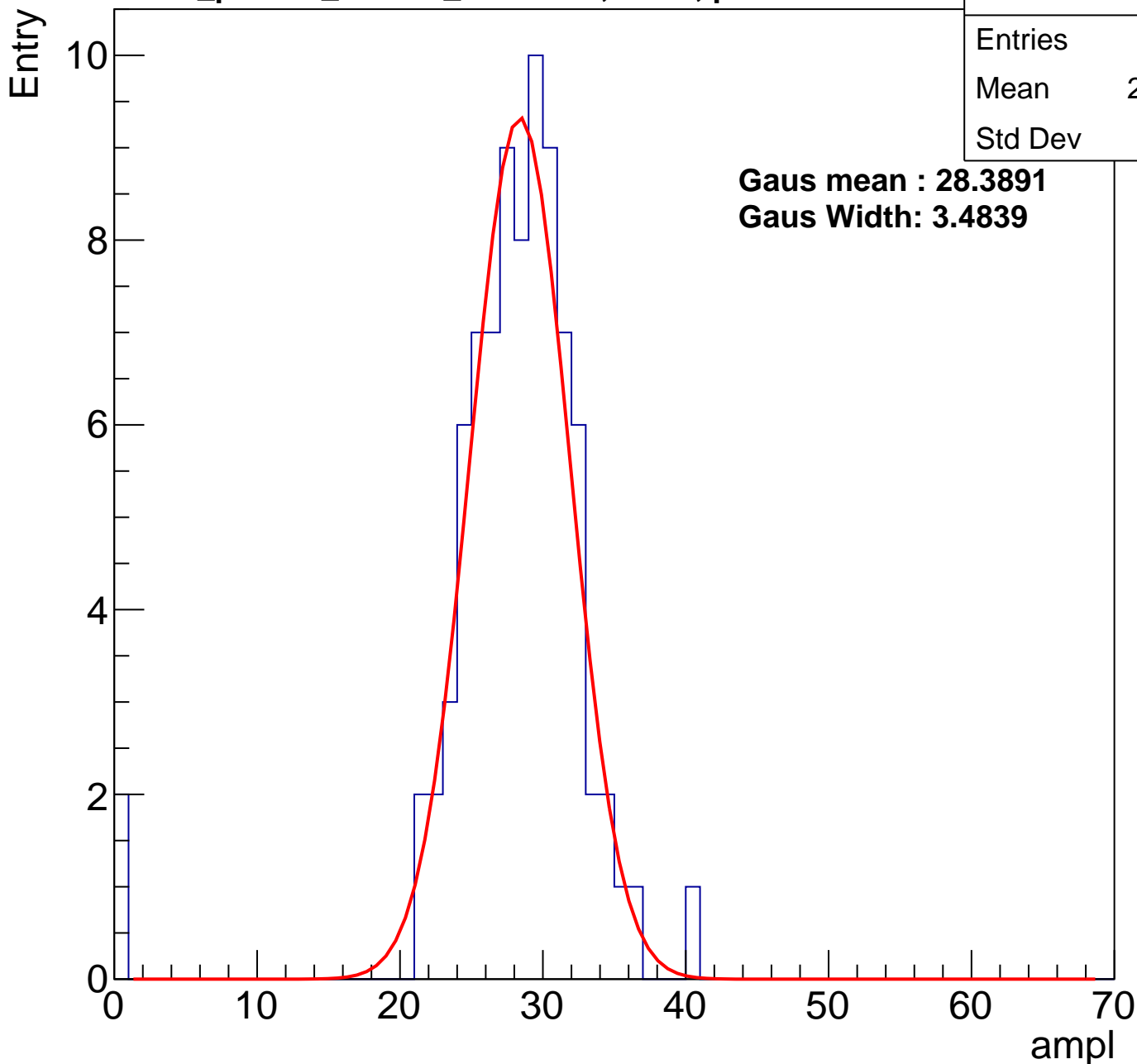
# B0L000S, U18-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	27.49
Std Dev	5.5

**Gaus mean : 28.3891**

**Gaus Width: 3.4839**



# B0L000S, U18-ch82, adc1

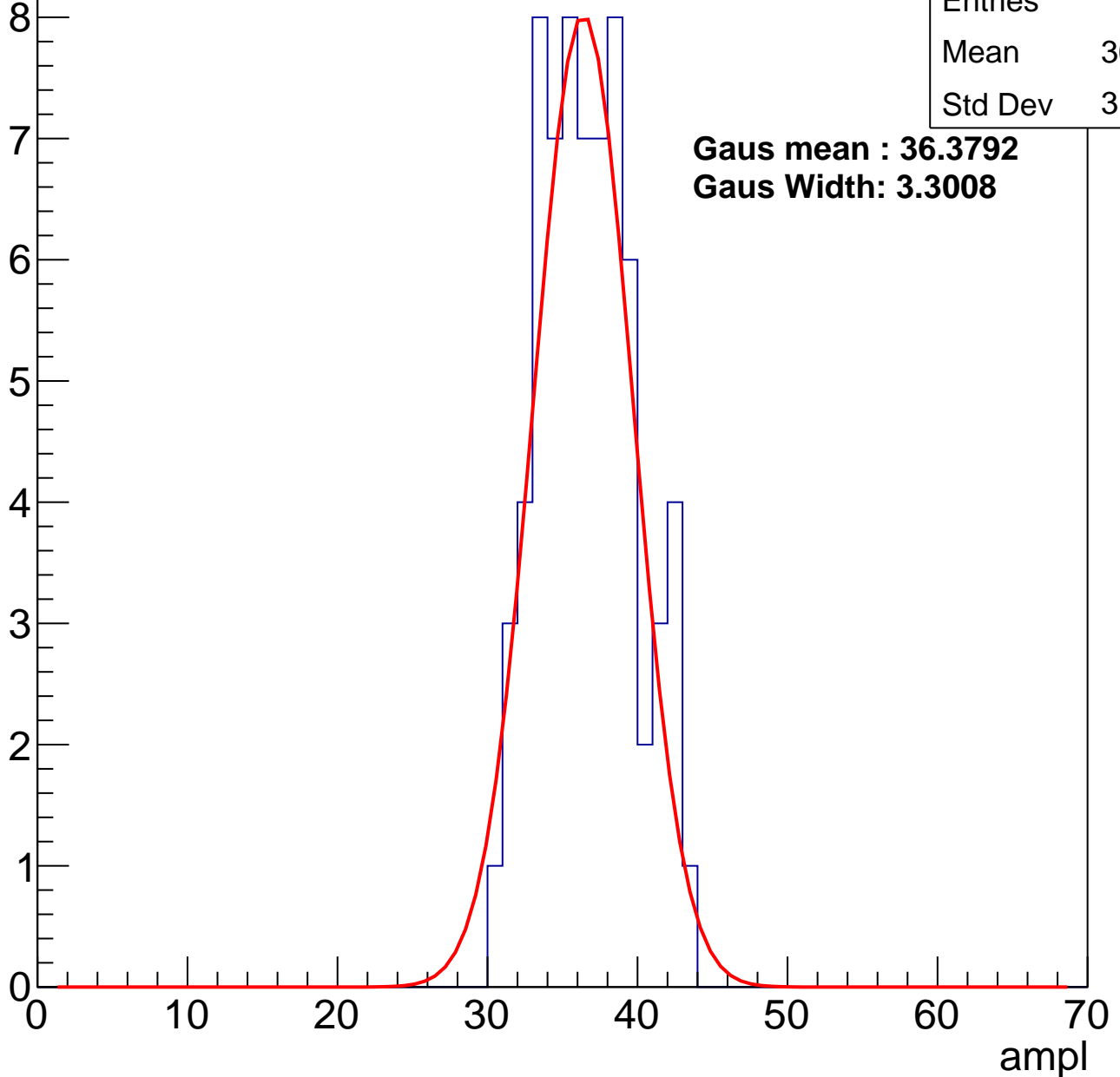
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.17
Std Dev	3.125

**Gaus mean : 36.3792**

**Gaus Width: 3.3008**



# B0L000S, U18-ch82, adc2

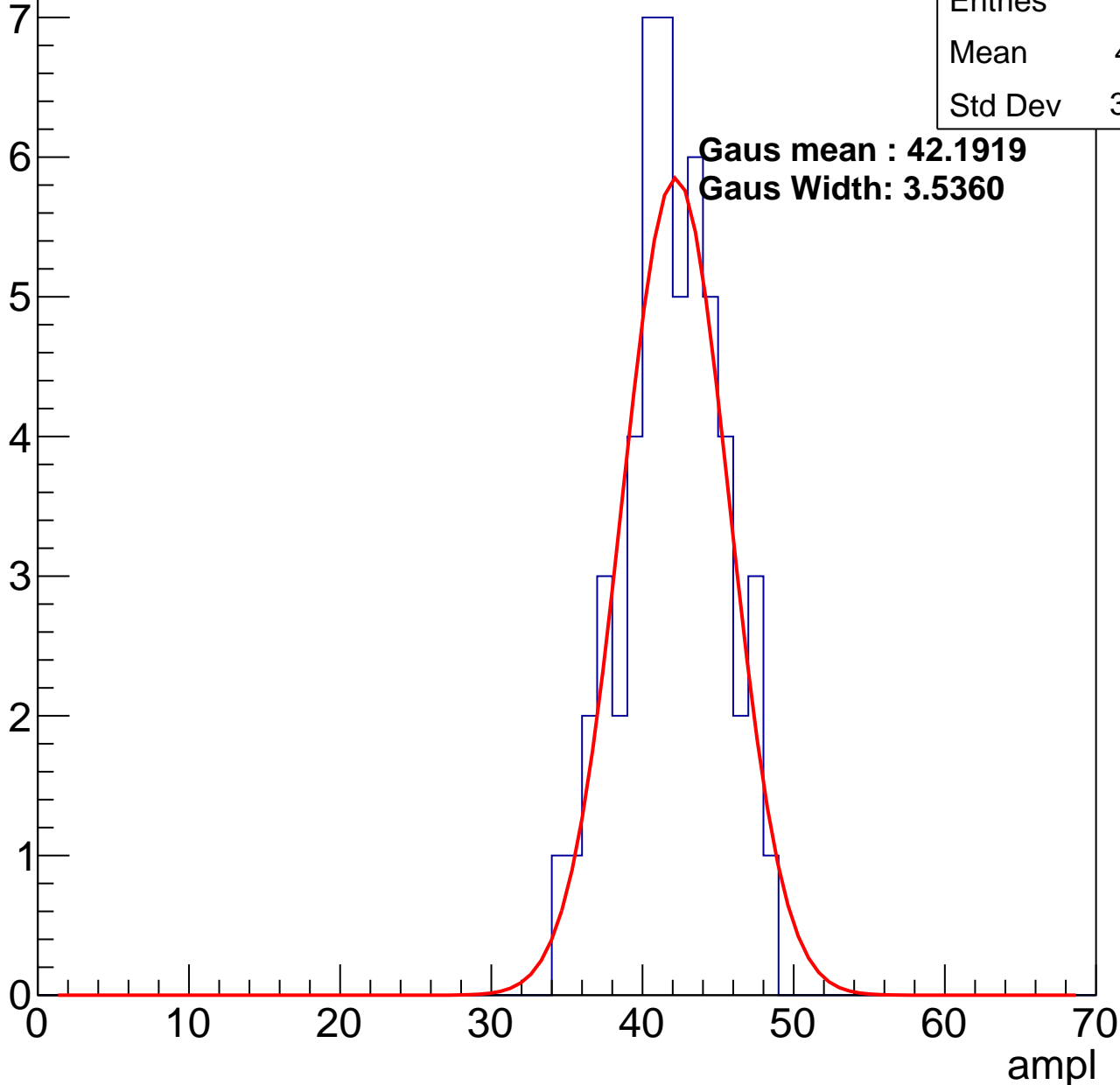
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	41.51
Std Dev	3.237

**Gaus mean : 42.1919**

**Gaus Width: 3.5360**



# B0L000S, U18-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

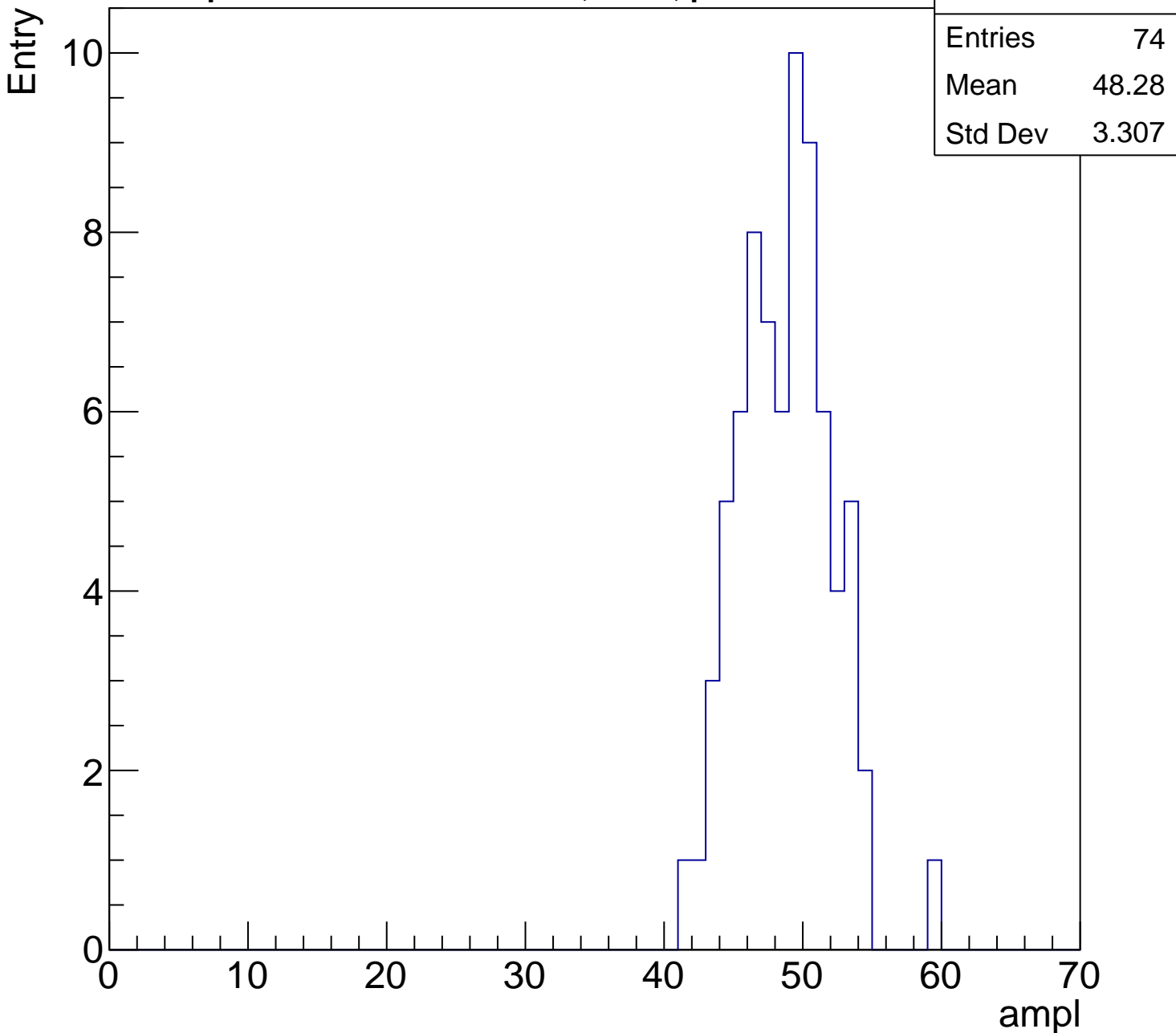
Entries	74
Mean	48.28
Std Dev	3.307

Entry

10  
8  
6  
4  
2  
0

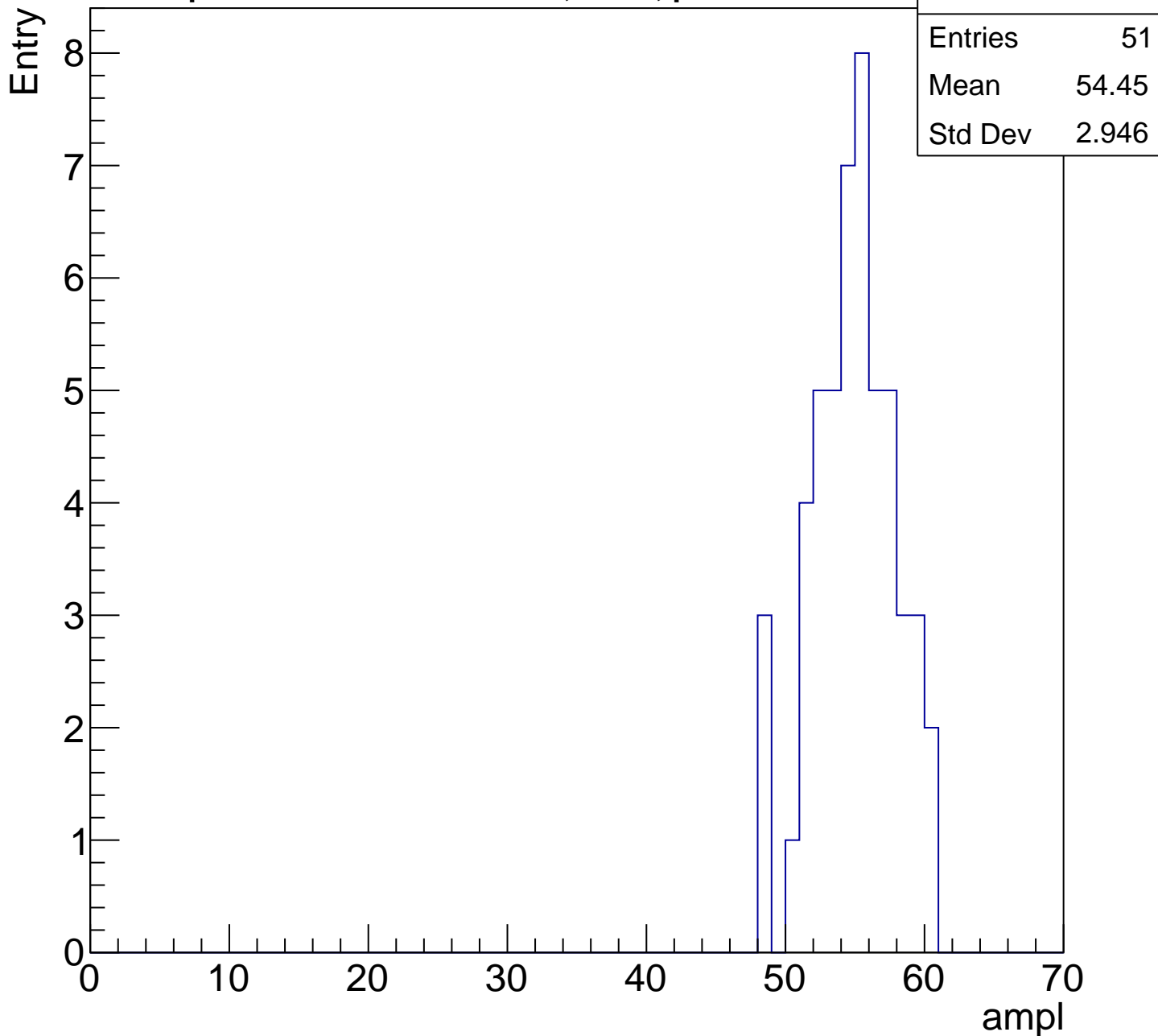
0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch82, adc4

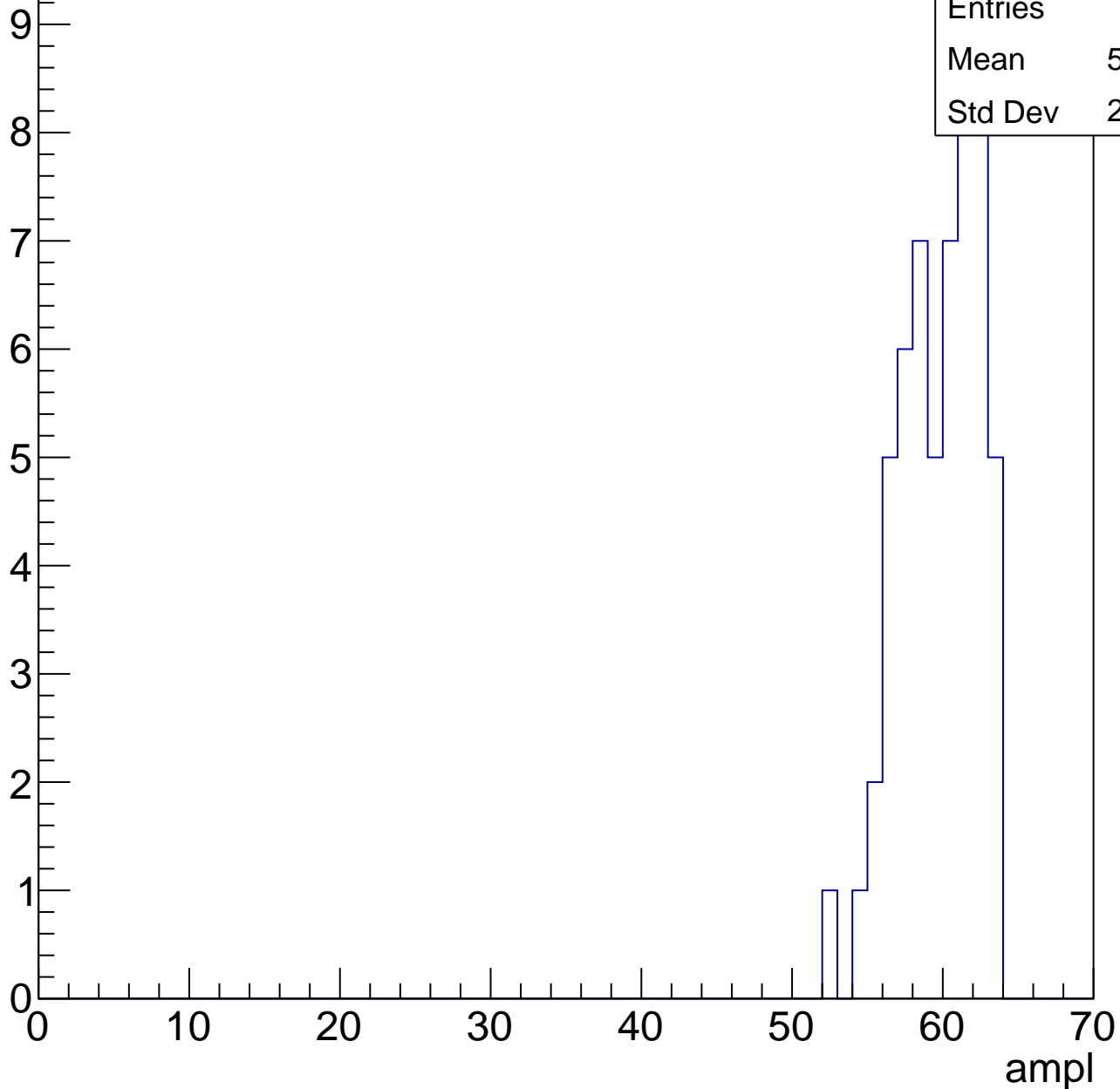
calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U18-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

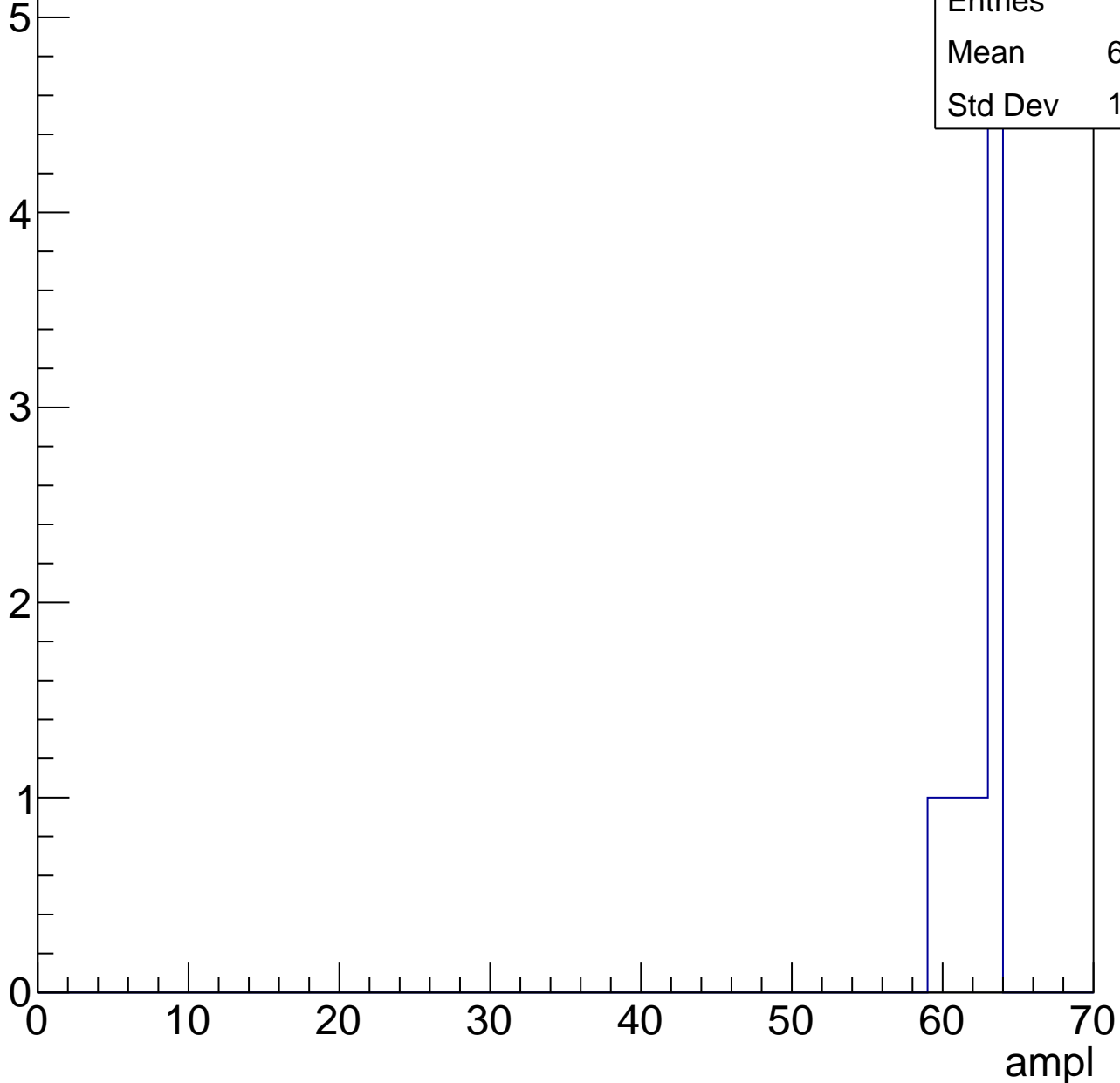


# B0L000S, U18-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	61.89
Std Dev	1.449



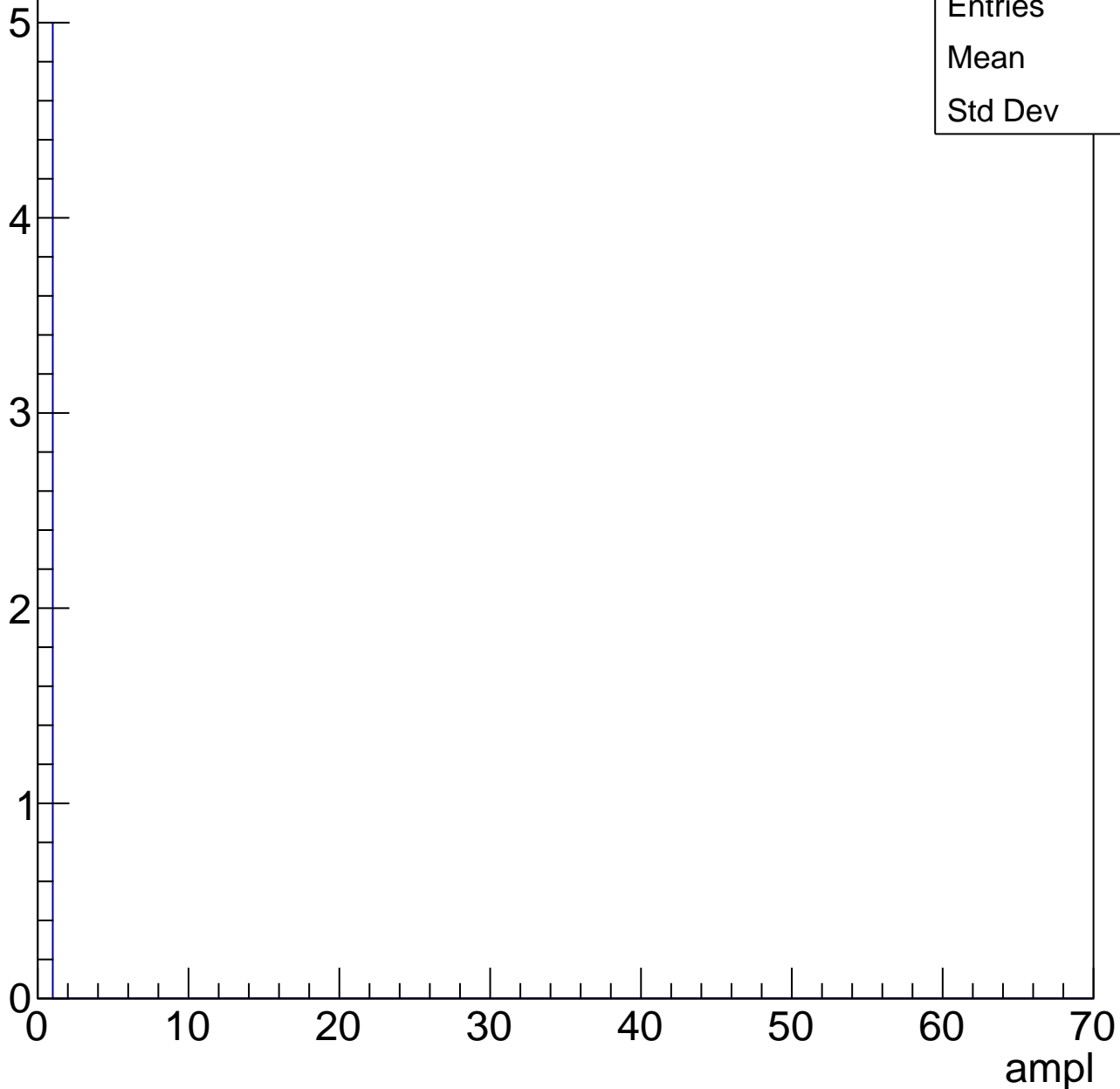


# B0L000S, U18-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	5
Mean	0
Std Dev	0



# B0L000S, U18-ch83, adc0

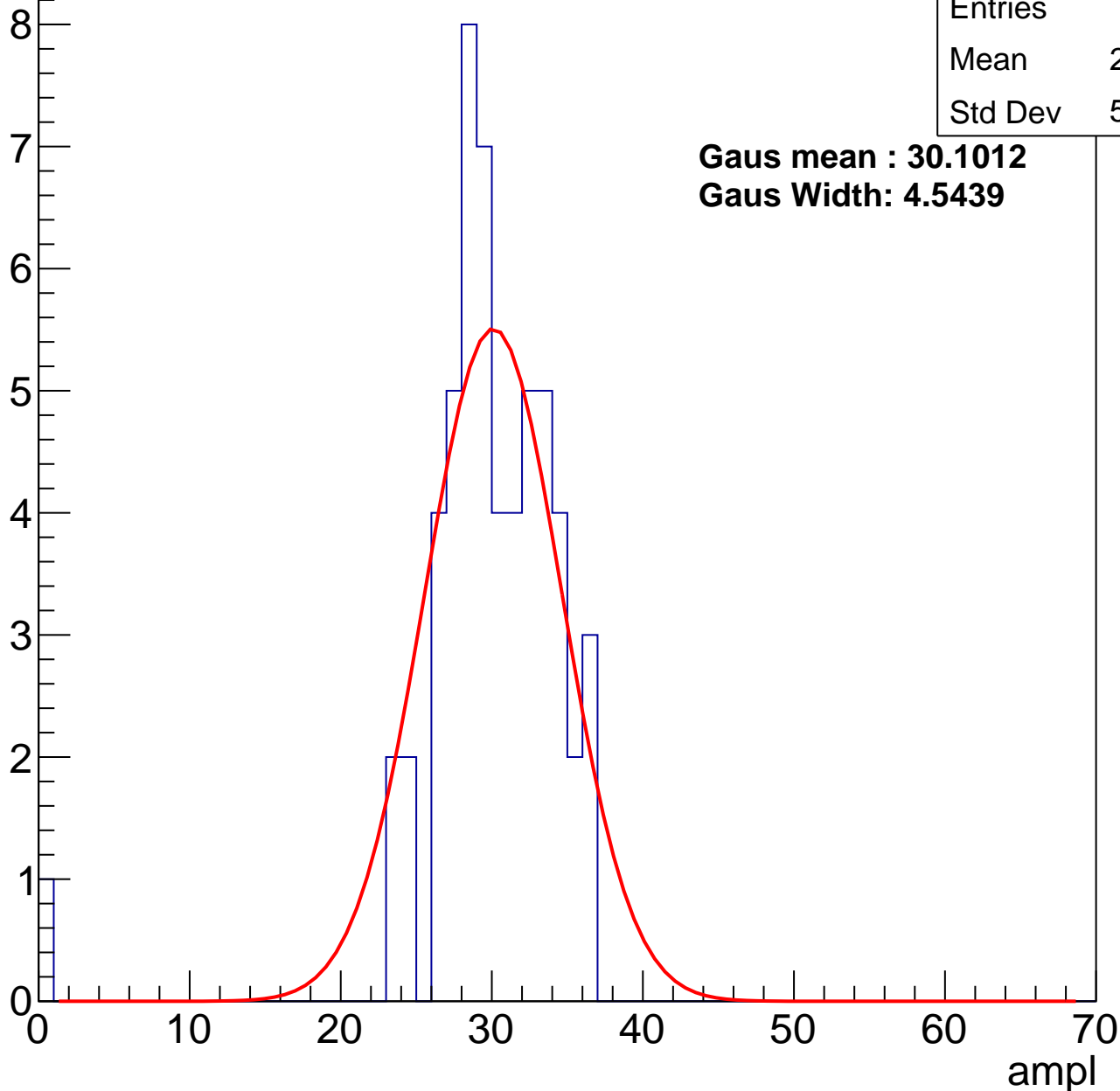
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.34
Std Dev	5.149

**Gaus mean : 30.1012**

**Gaus Width: 4.5439**



# B0L000S, U18-ch83, adc1

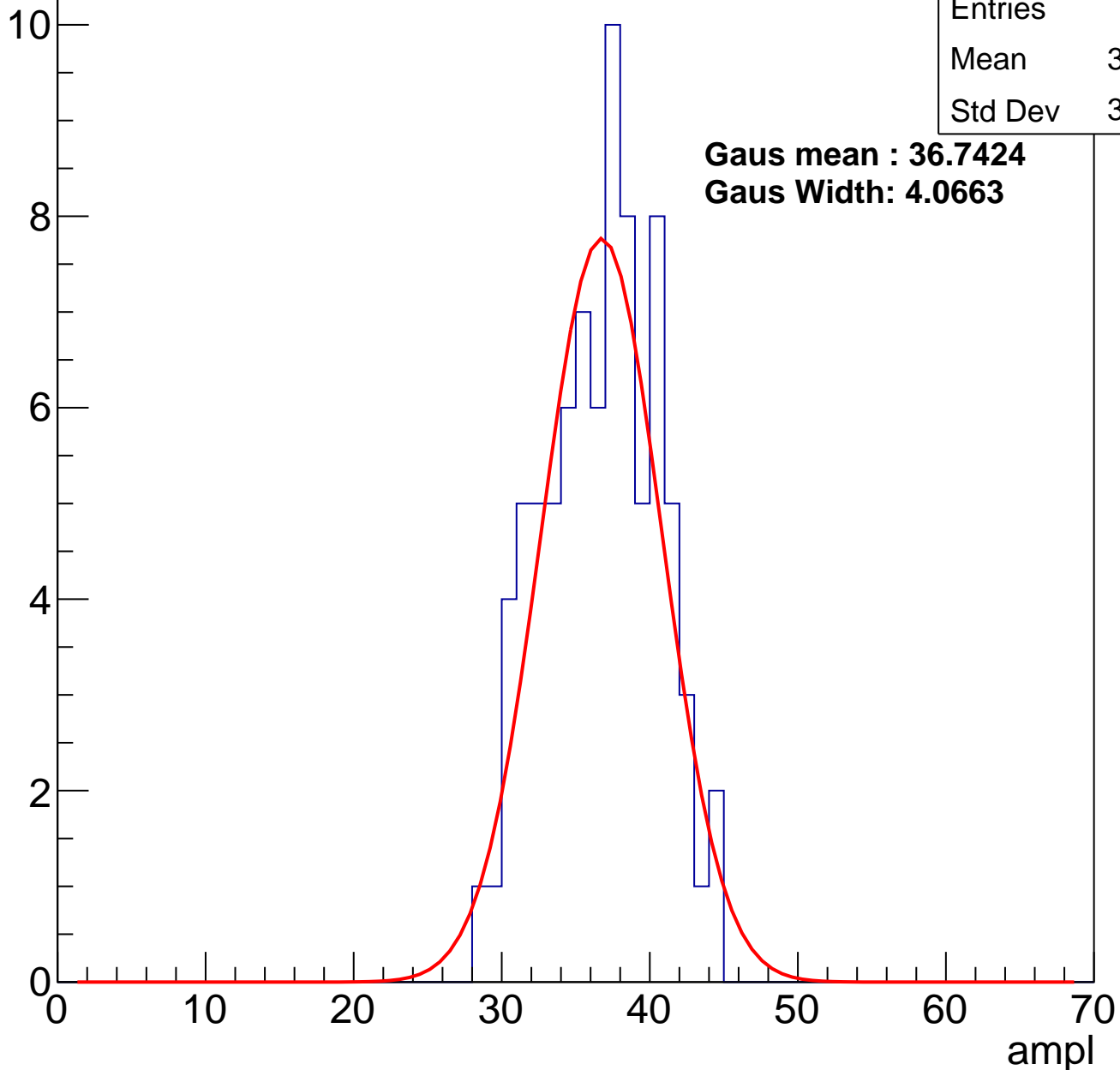
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	36.26
Std Dev	3.757

**Gaus mean : 36.7424**

**Gaus Width: 4.0663**

Entry



# B0L000S, U18-ch83, adc2

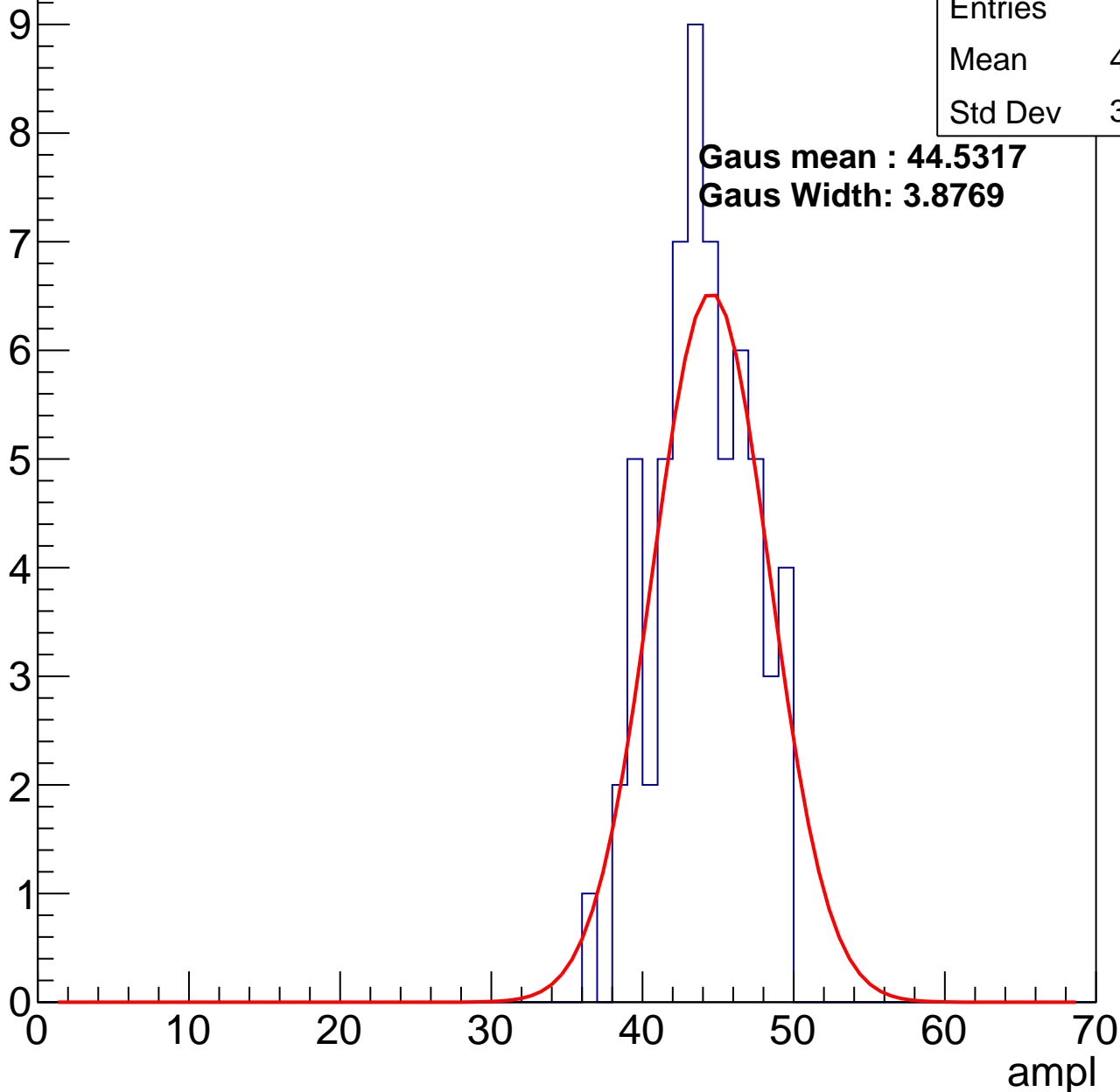
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.56
Std Dev	3.097

**Gaus mean : 44.5317**

**Gaus Width: 3.8769**

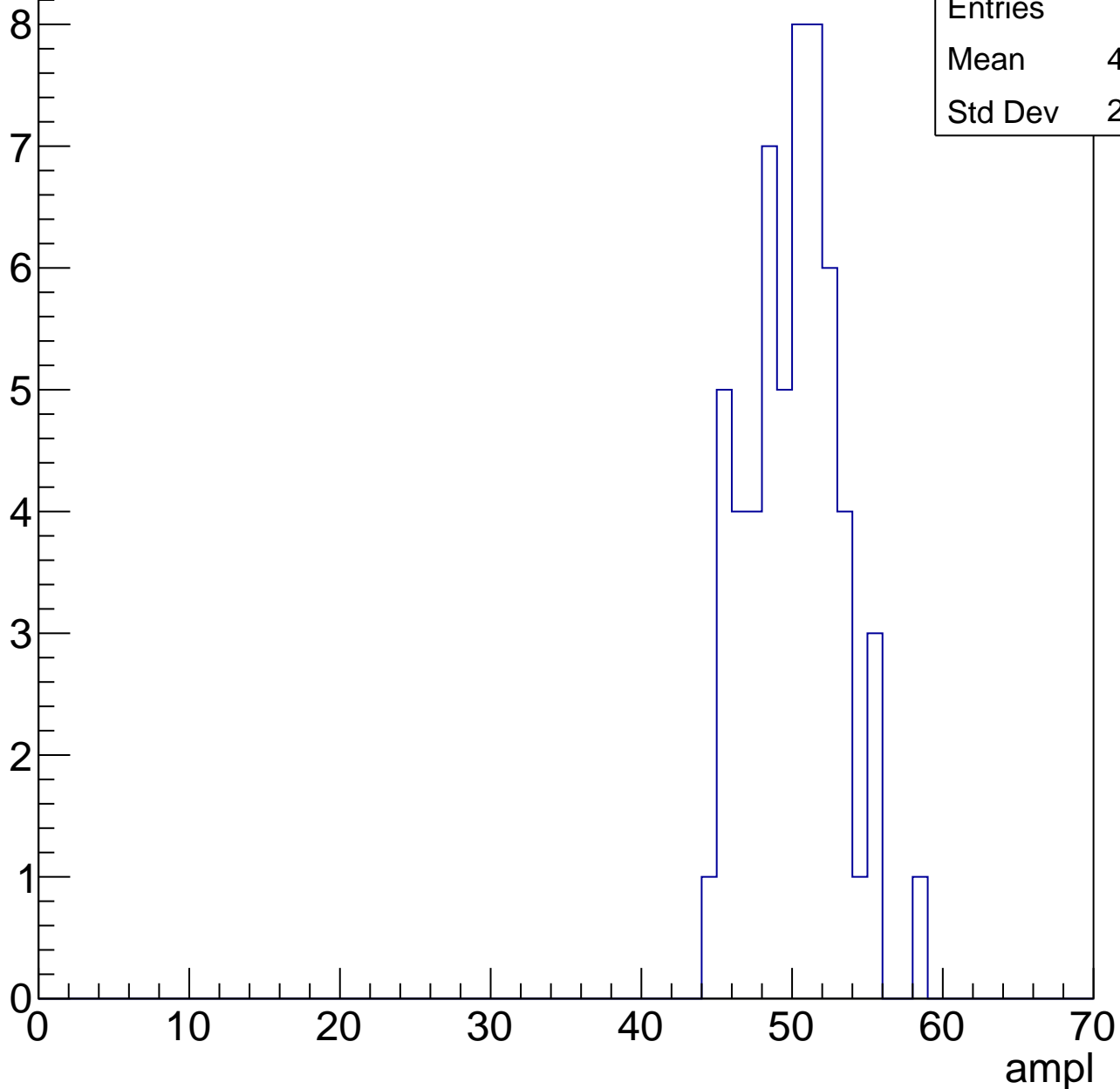


# B0L000S, U18-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

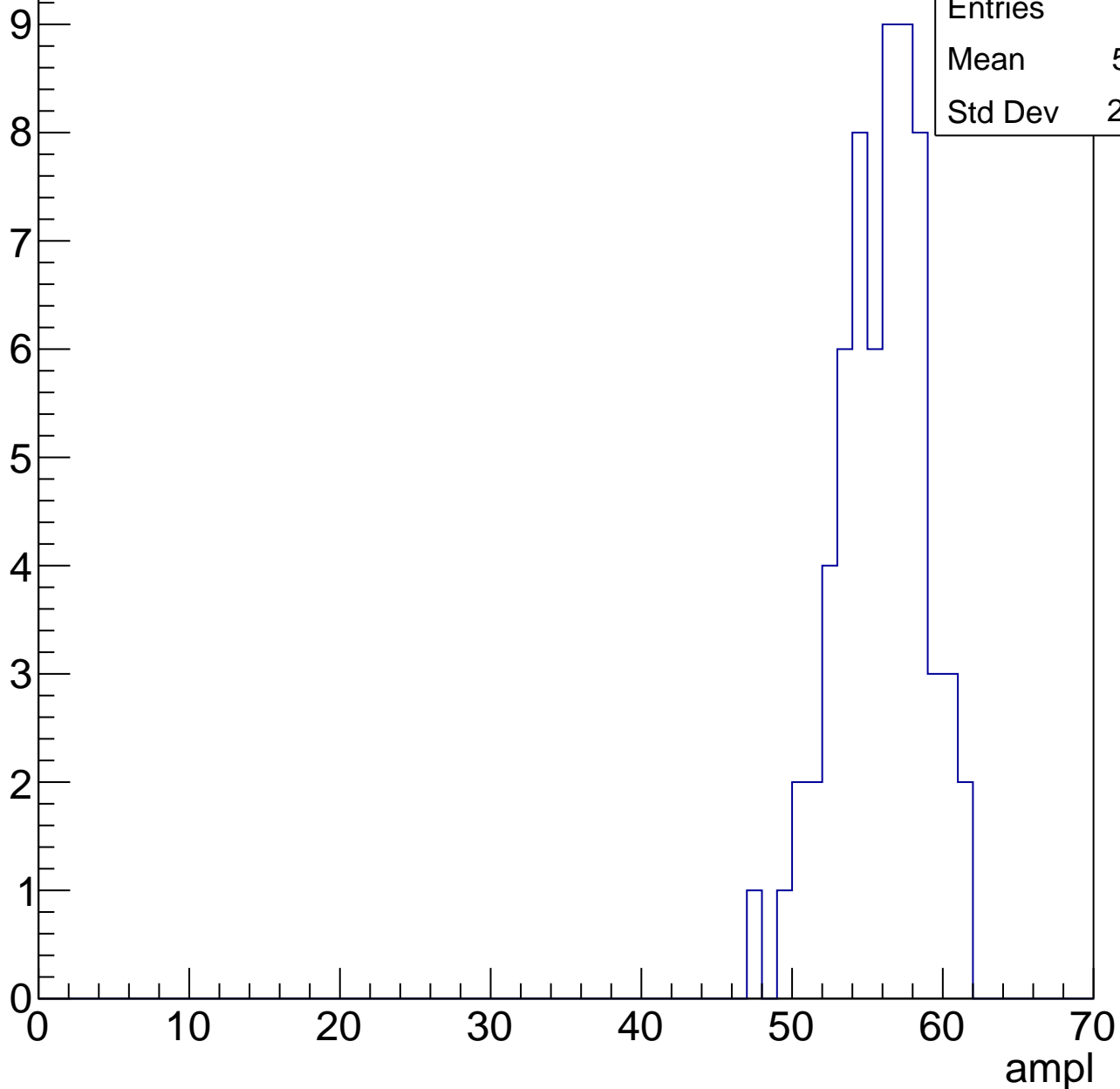
Entries	57
Mean	49.67
Std Dev	2.993



# B0L000S, U18-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 36

Mean 58.61

Std Dev 10.16

ampl

0

10

20

30

40

50

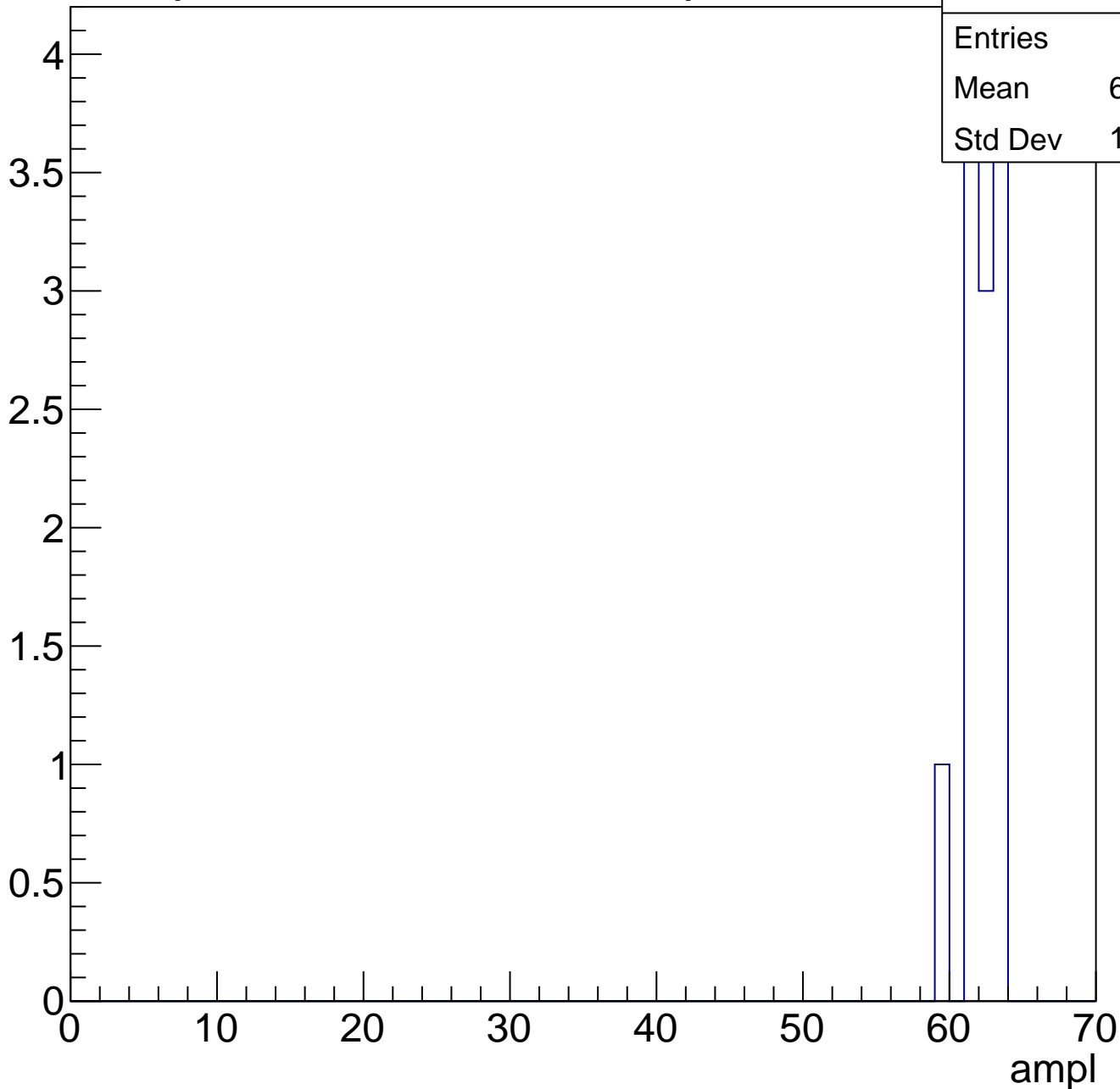
60

70

# B0L000S, U18-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch84, adc0

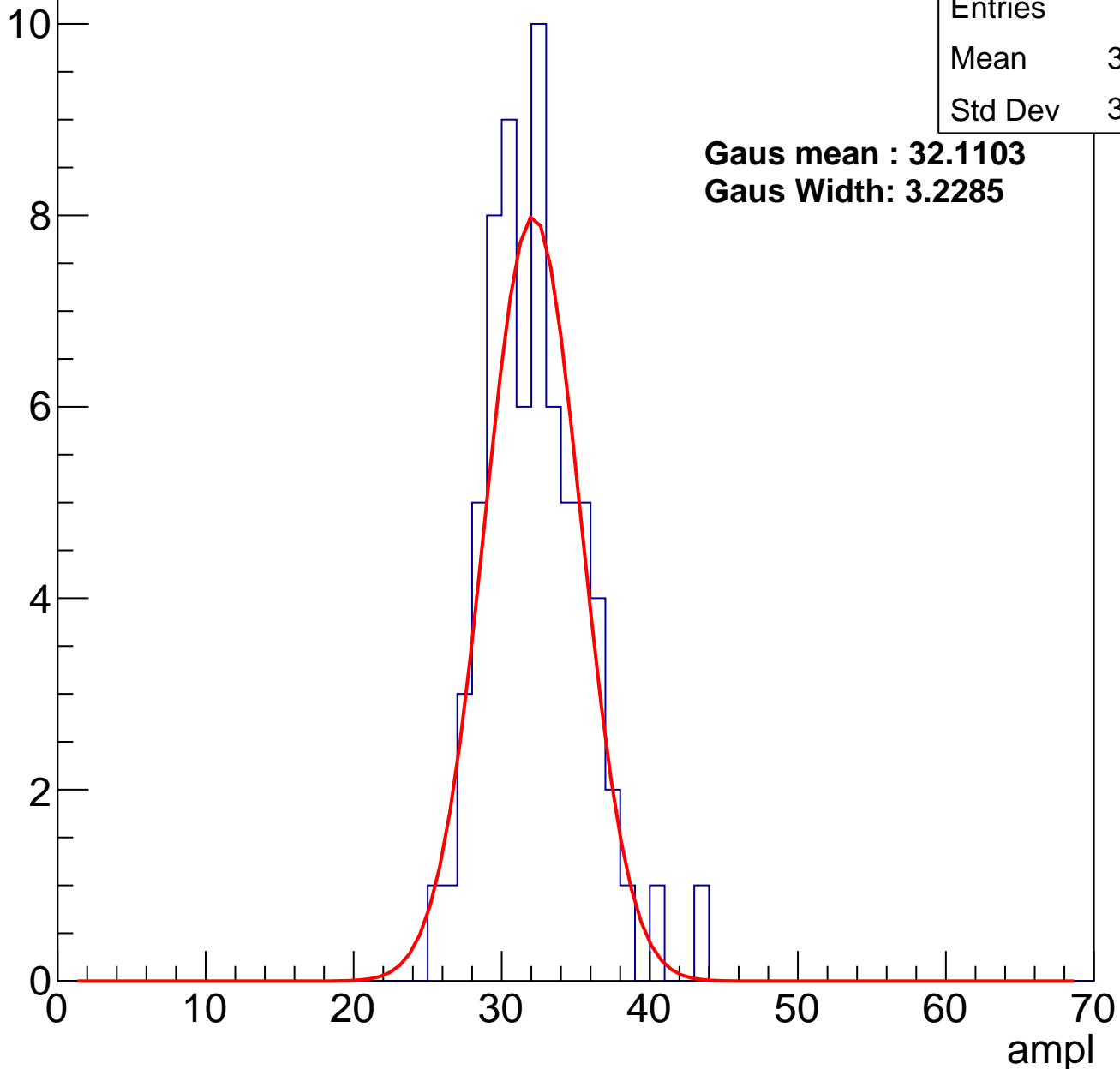
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	31.79
Std Dev	3.337

**Gaus mean : 32.1103**

**Gaus Width: 3.2285**

Entry



# B0L000S, U18-ch84, adc1

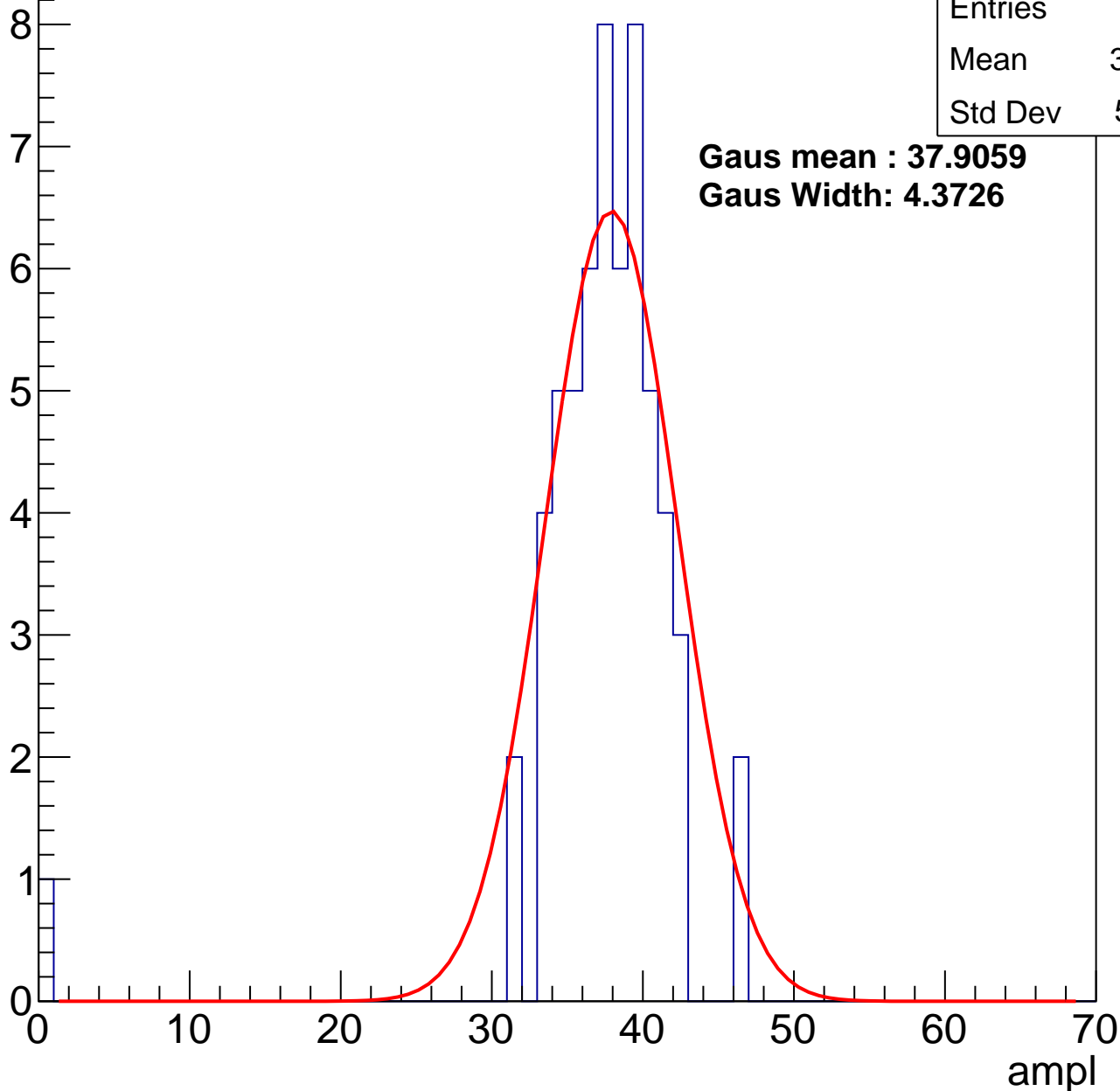
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.83
Std Dev	5.761

**Gaus mean : 37.9059**

**Gaus Width: 4.3726**



# B0L000S, U18-ch84, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	43.88
Std Dev	2.922

**Gaus mean : 44.2247**

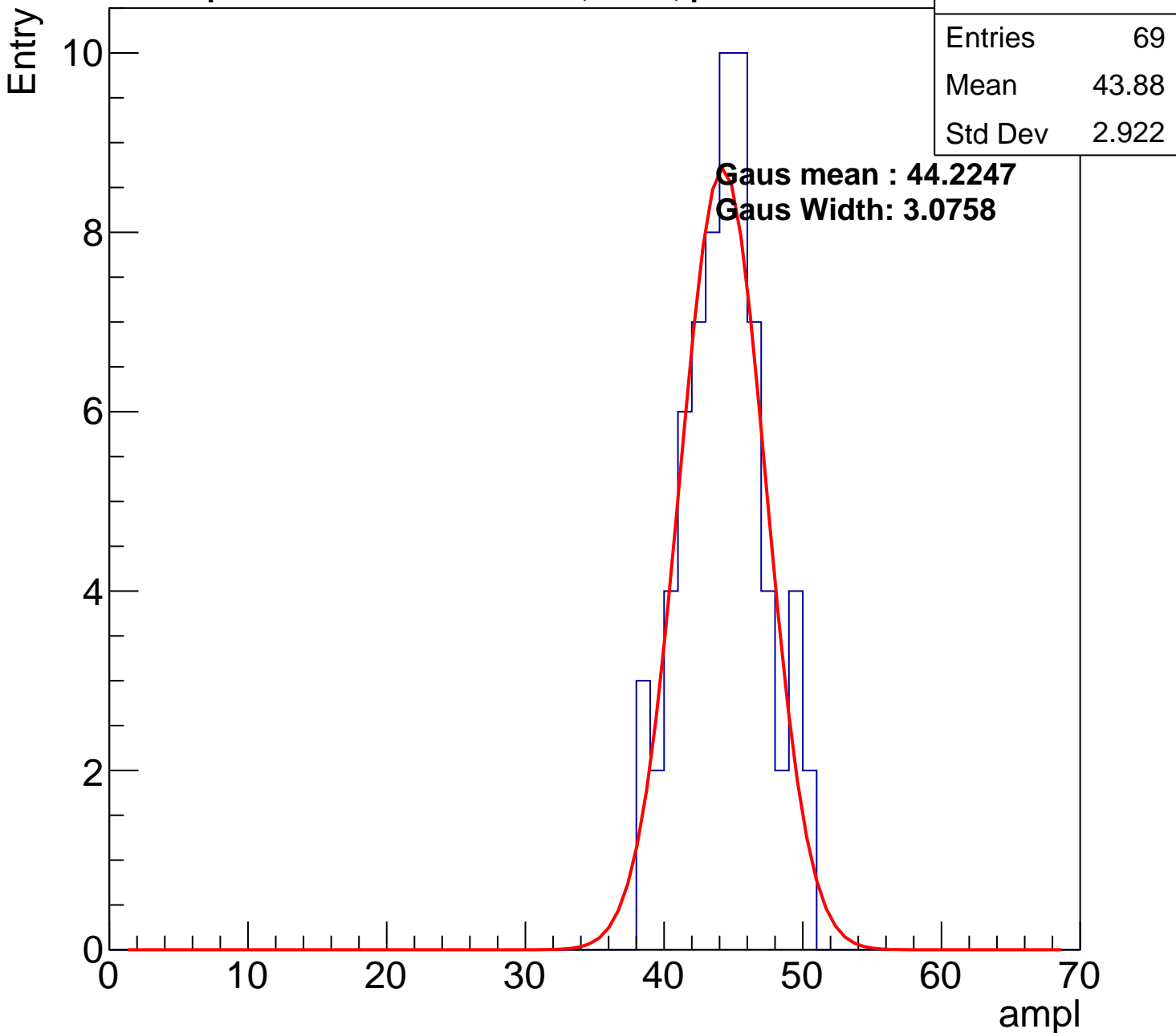
**Gaus Width: 3.0758**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

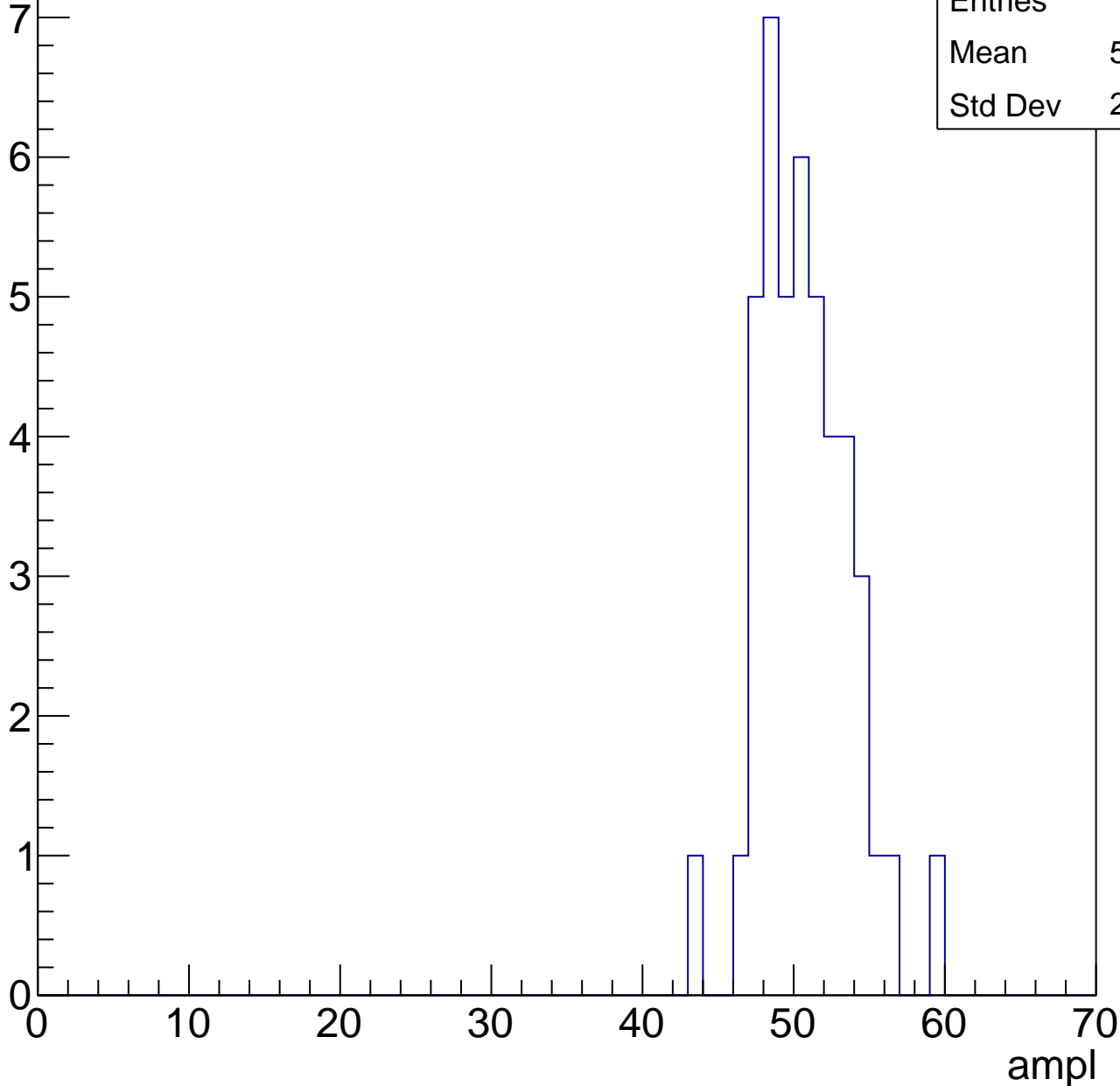


# B0L000S, U18-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	50.27
Std Dev	2.965



# B0L000S, U18-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

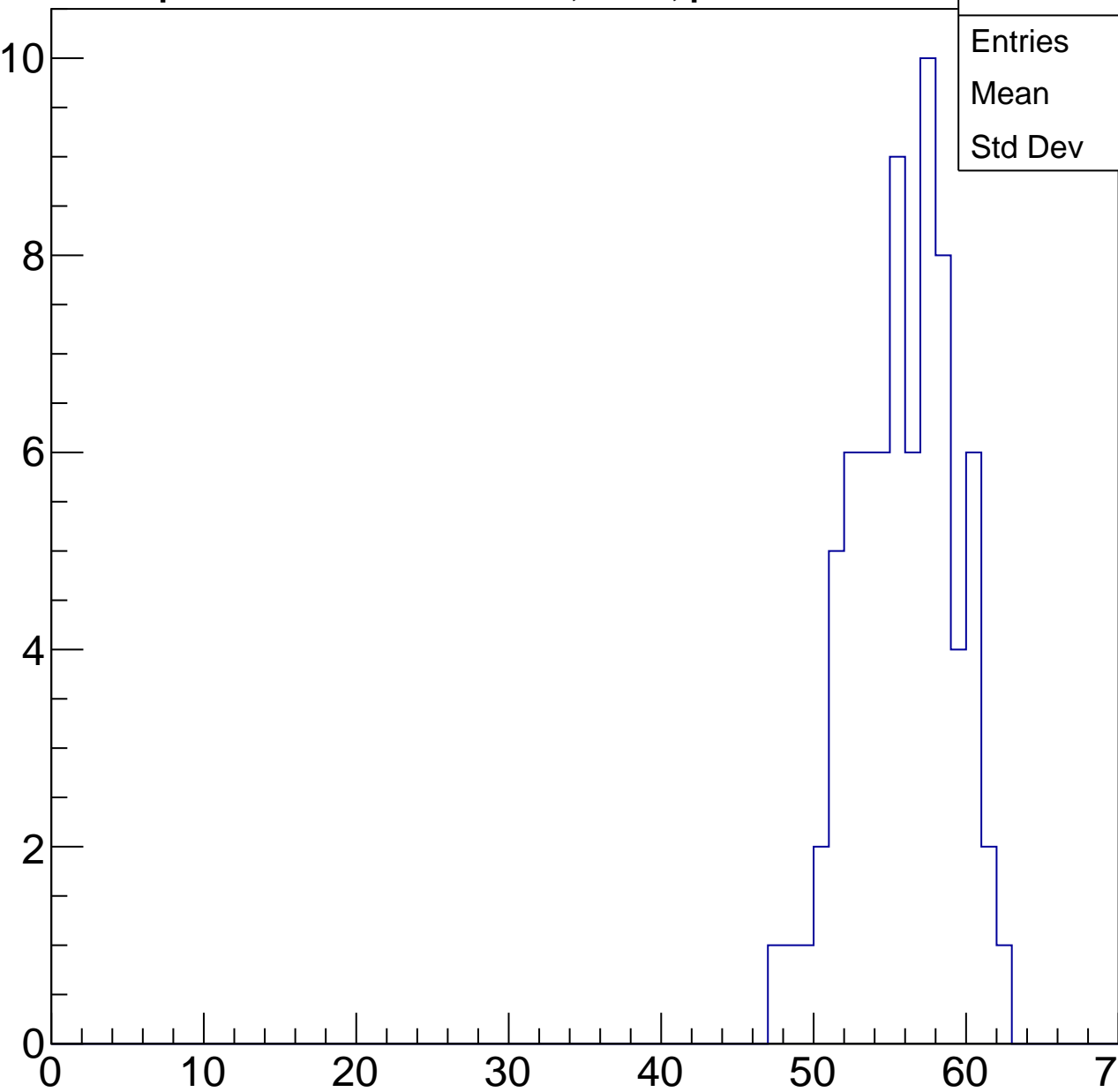
Entries	74
Mean	55.38
Std Dev	3.299

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

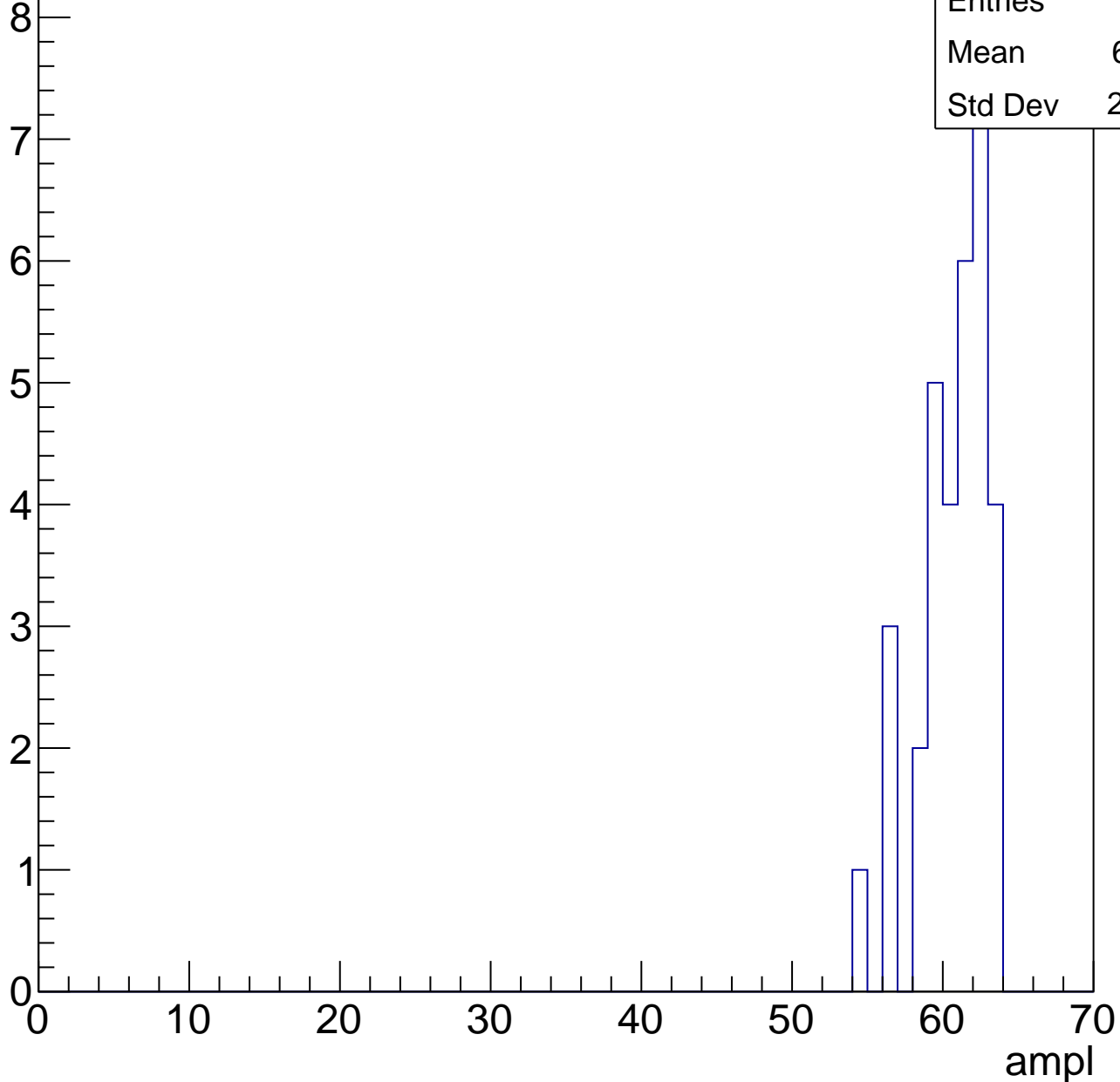


# B0L000S, U18-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	60.21
Std Dev	2.266

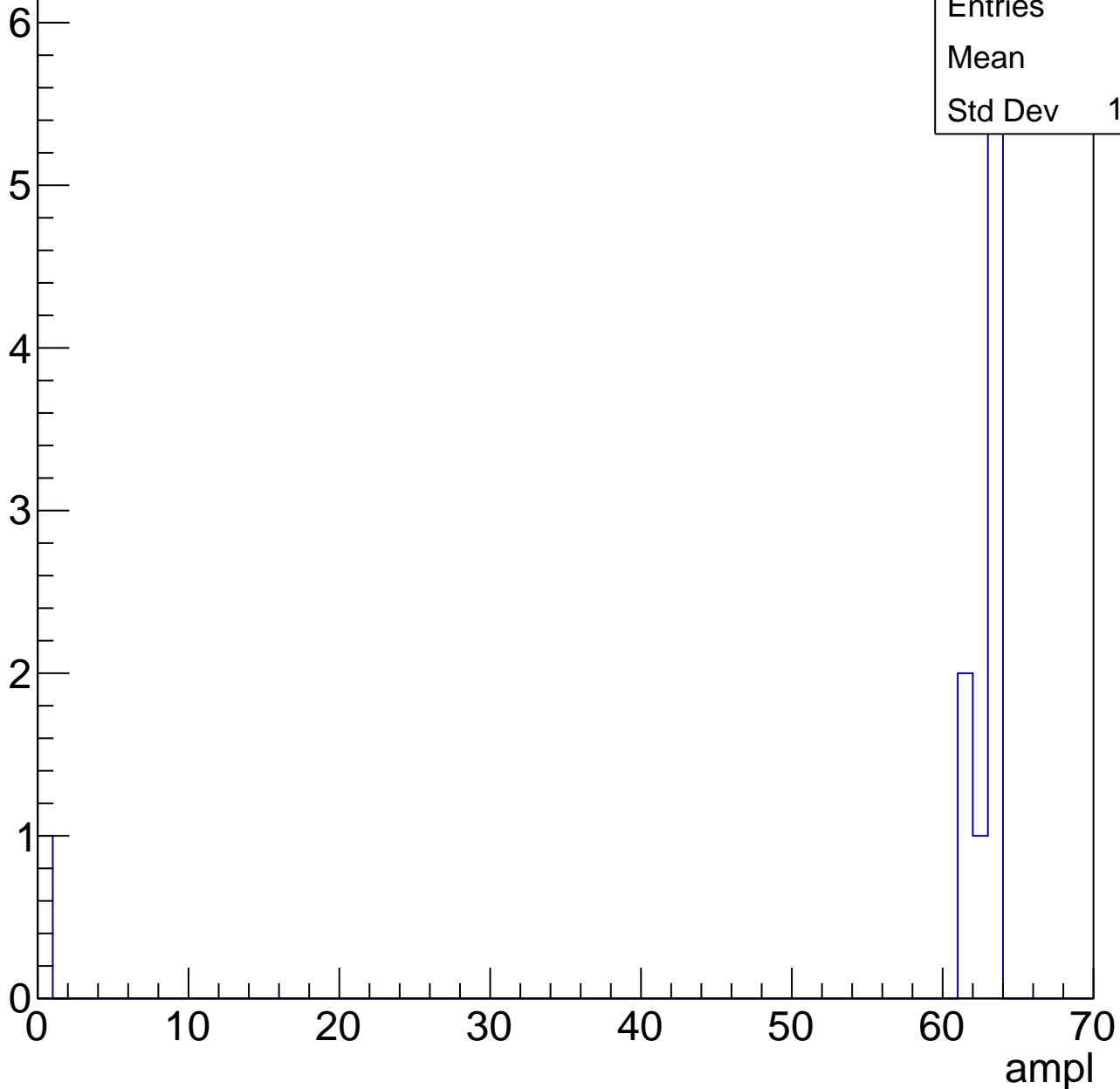


# B0L000S, U18-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	10
Mean	56.2
Std Dev	18.75





# B0L000S, U18-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch85, adc0

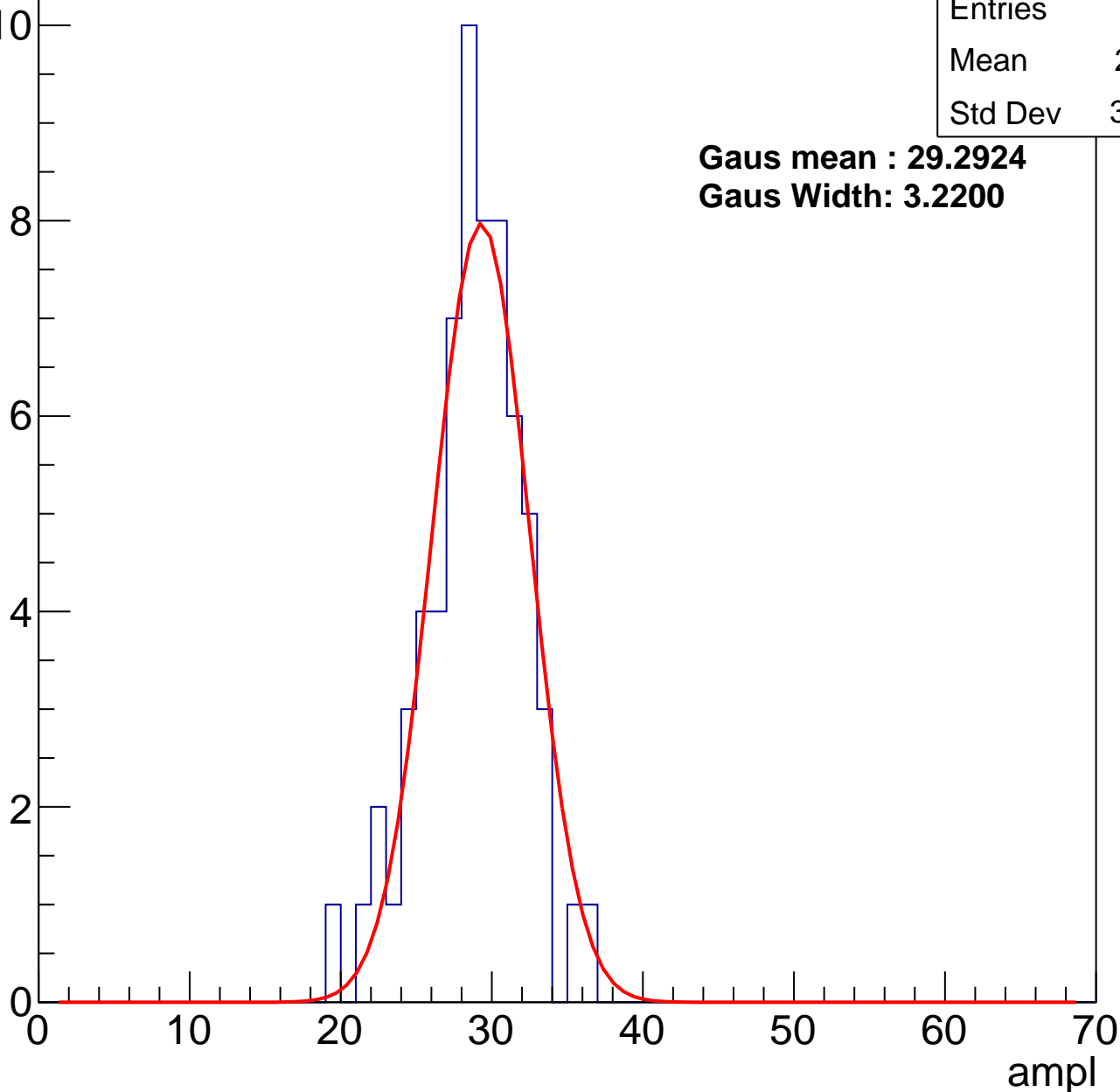
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	28.31
Std Dev	3.258

**Gaus mean : 29.2924**

**Gaus Width: 3.2200**



# B0L000S, U18-ch85, adc1

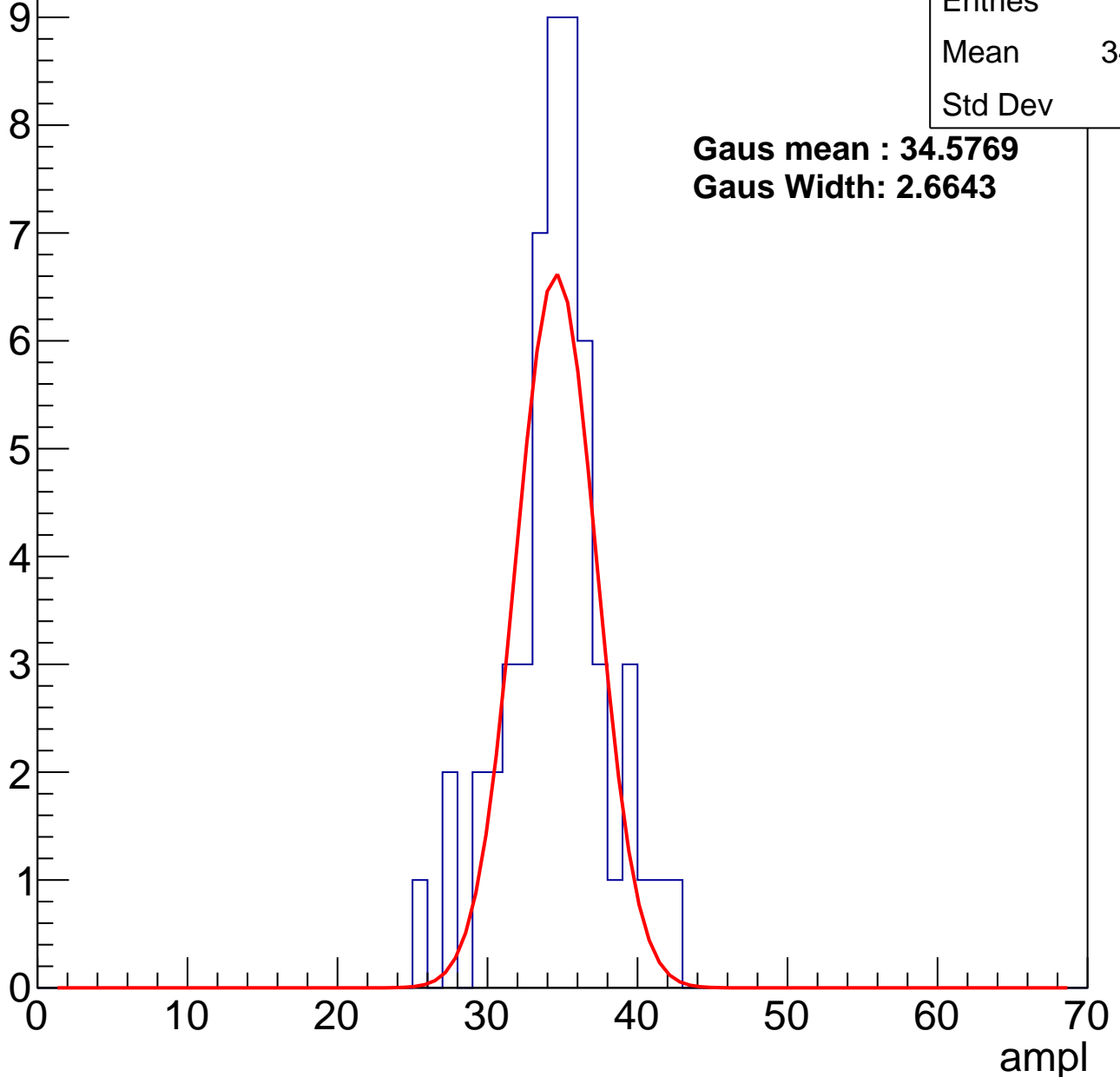
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	34.13
Std Dev	3.35

**Gaus mean : 34.5769**

**Gaus Width: 2.6643**



# B0L000S, U18-ch85, adc2

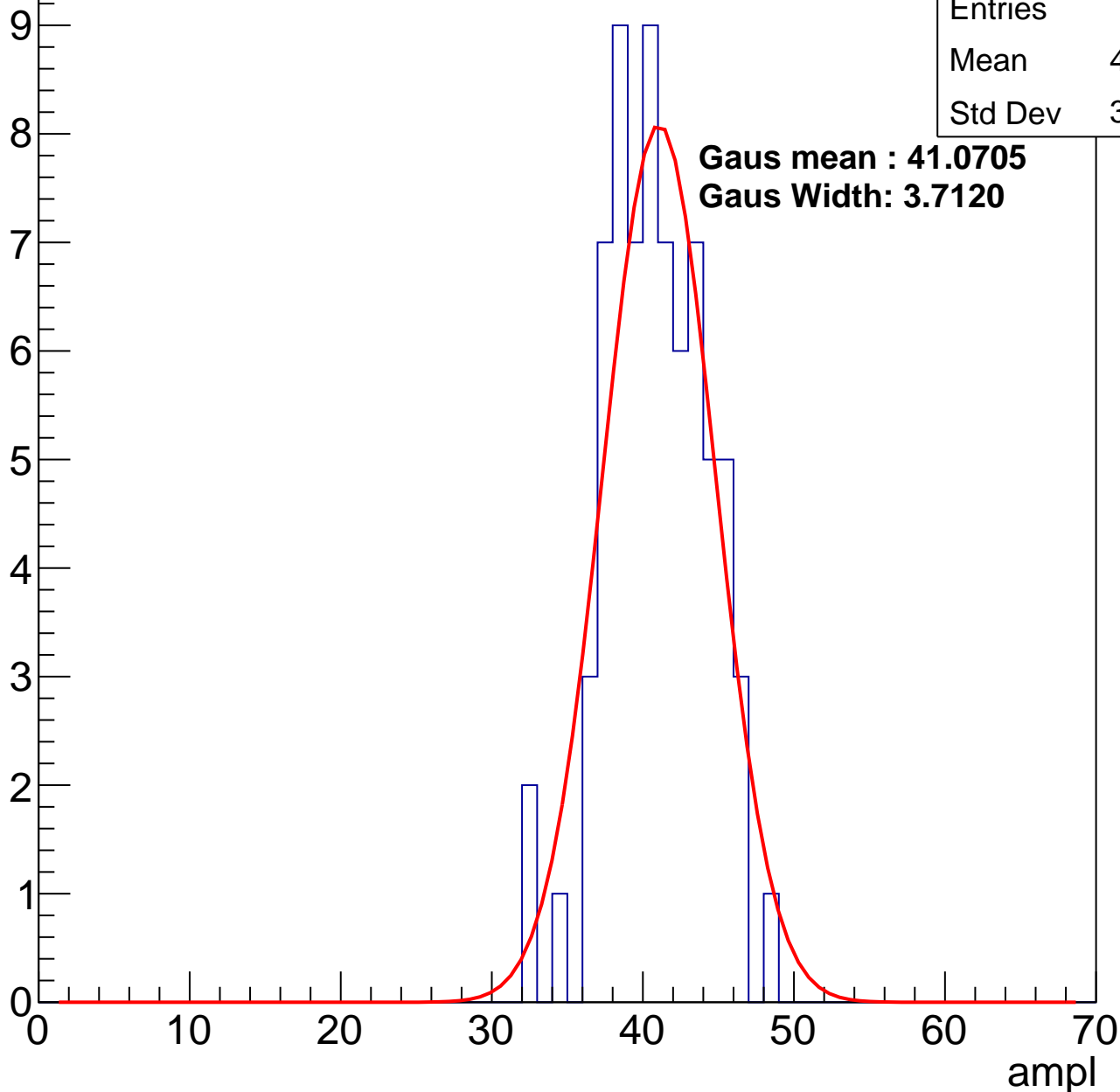
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	40.43
Std Dev	3.282

**Gaus mean : 41.0705**

**Gaus Width: 3.7120**

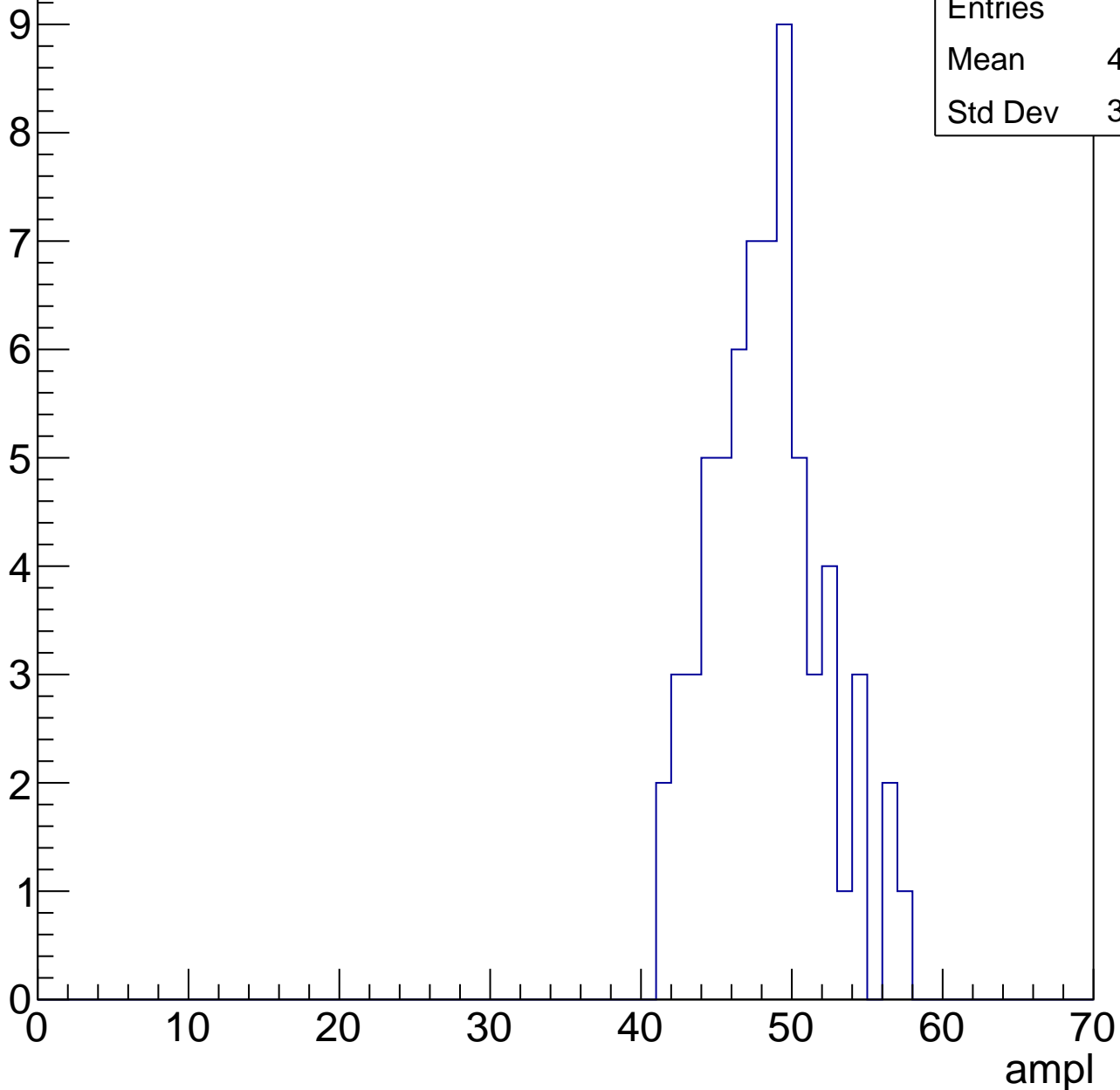


# B0L000S, U18-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	47.86
Std Dev	3.688

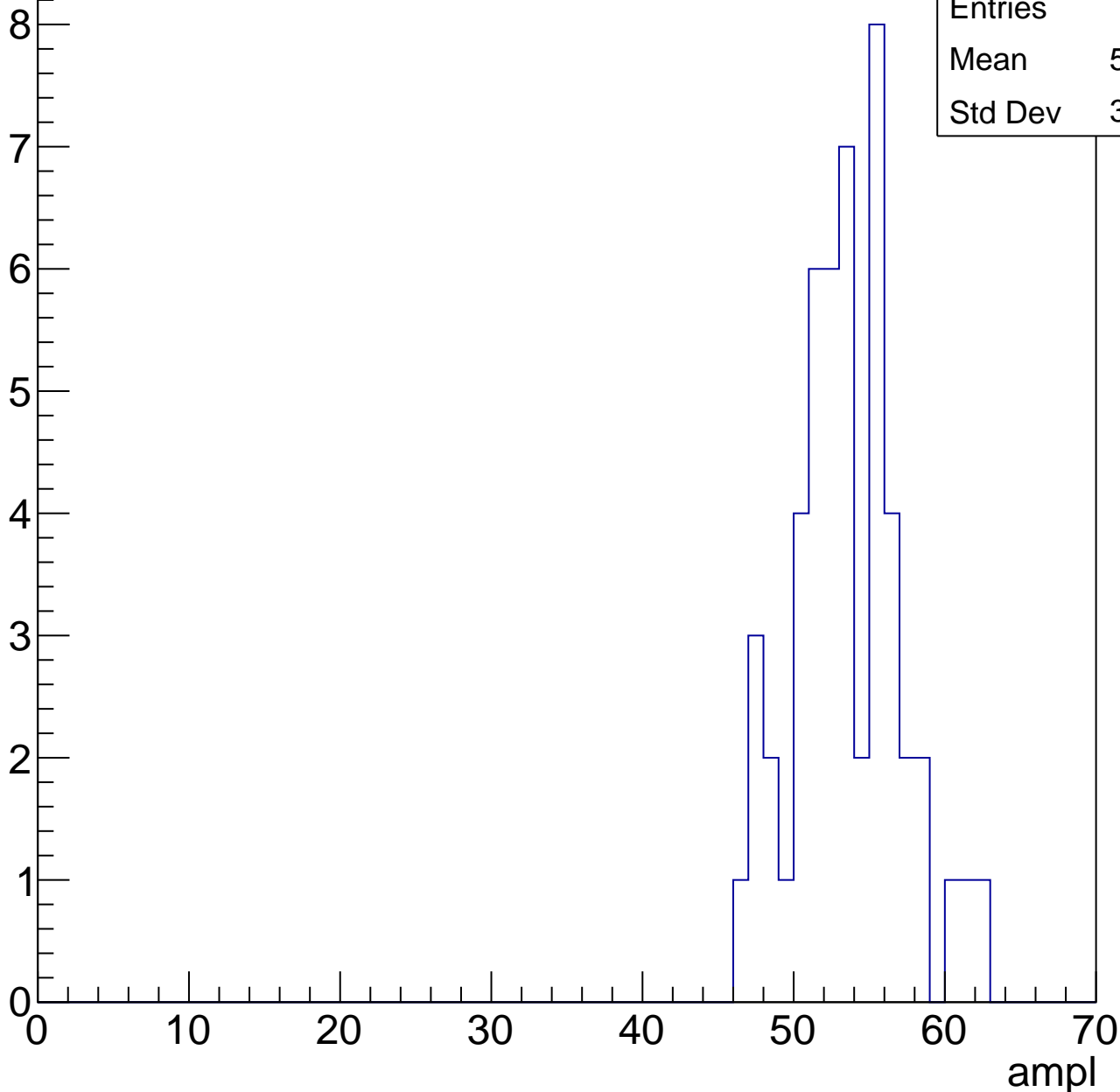


# B0L000S, U18-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

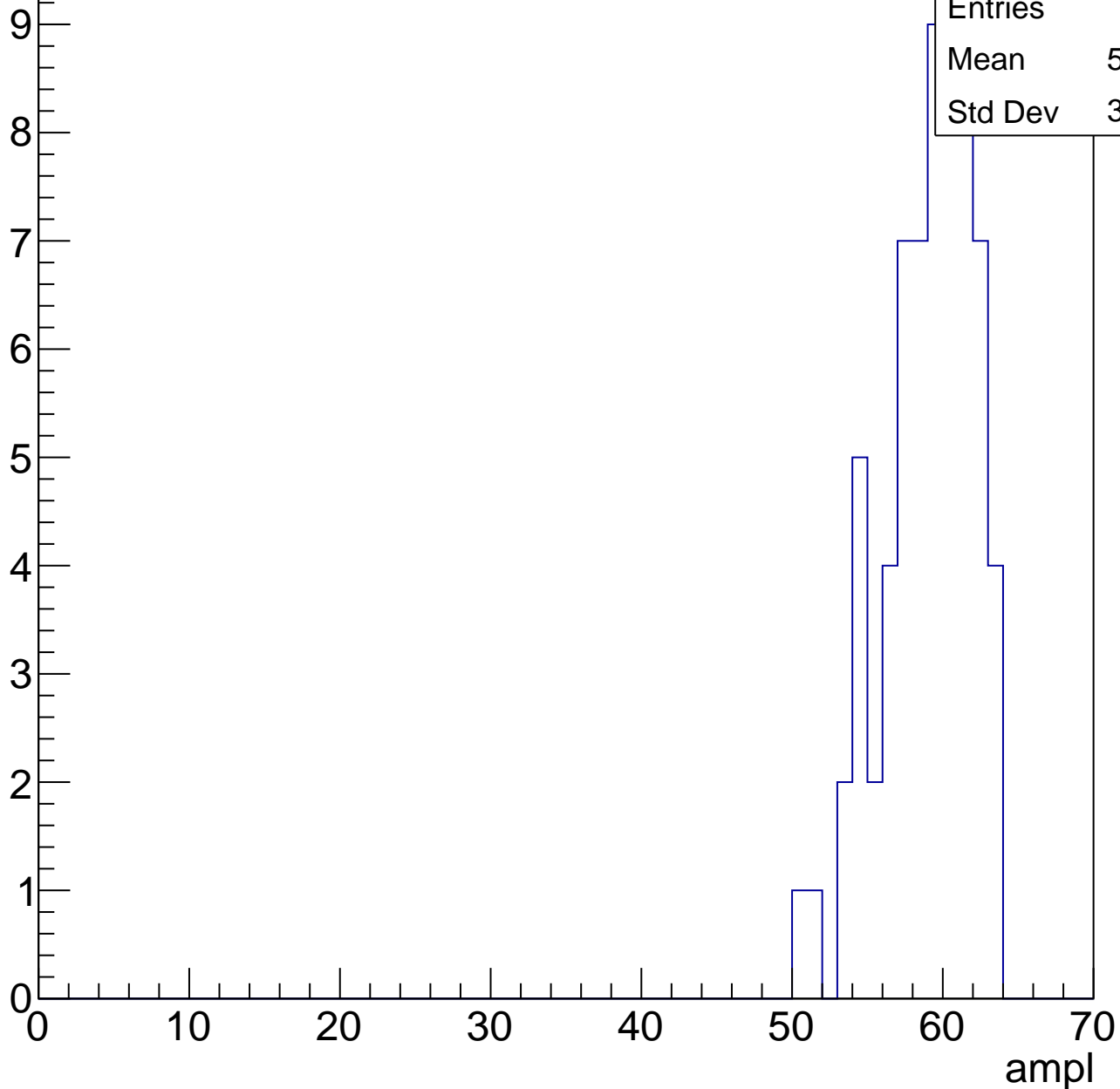
Entries	51
Mean	53.06
Std Dev	3.528



# B0L000S, U18-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

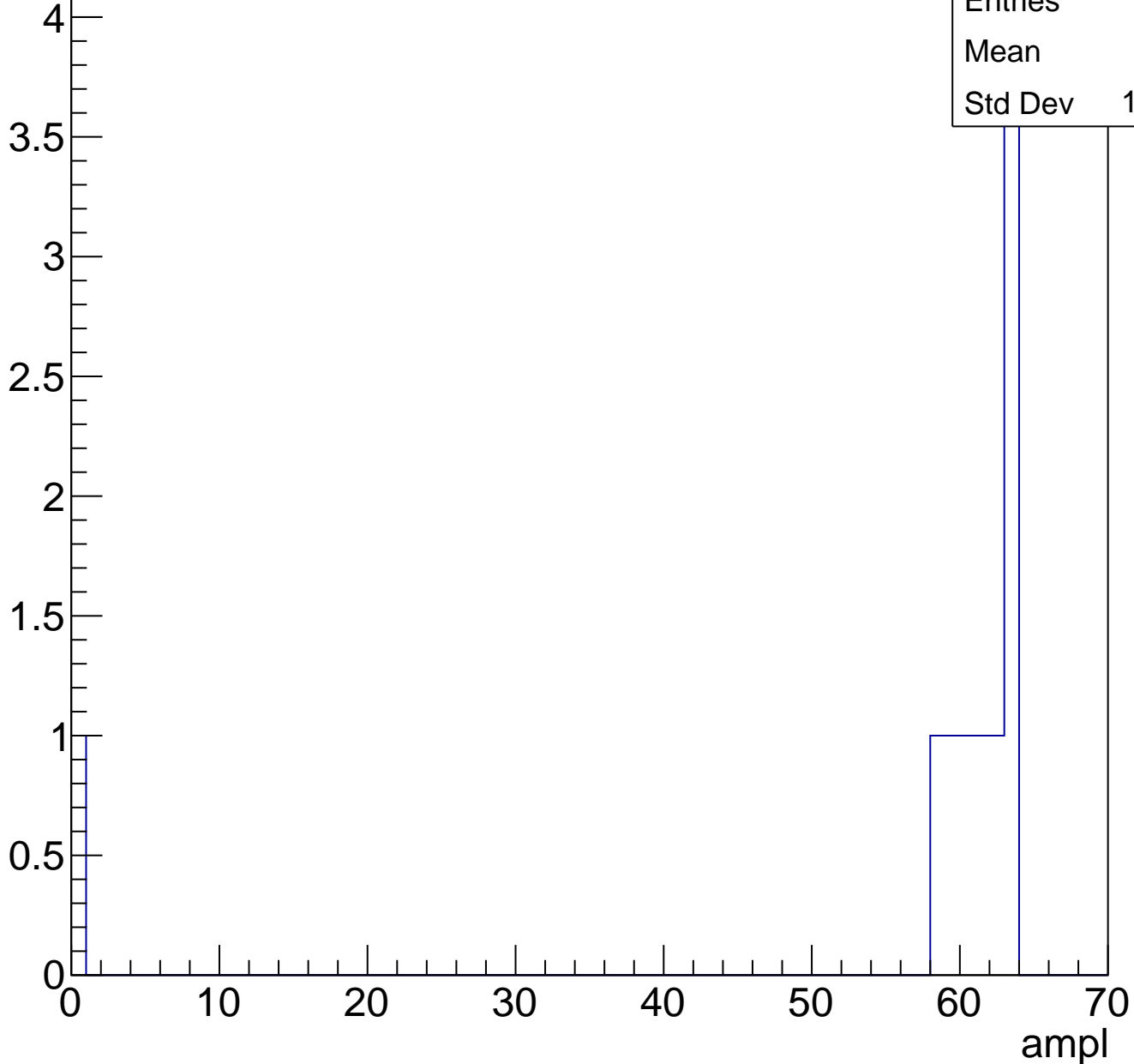
Entry



# B0L000S, U18-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch86, adc0

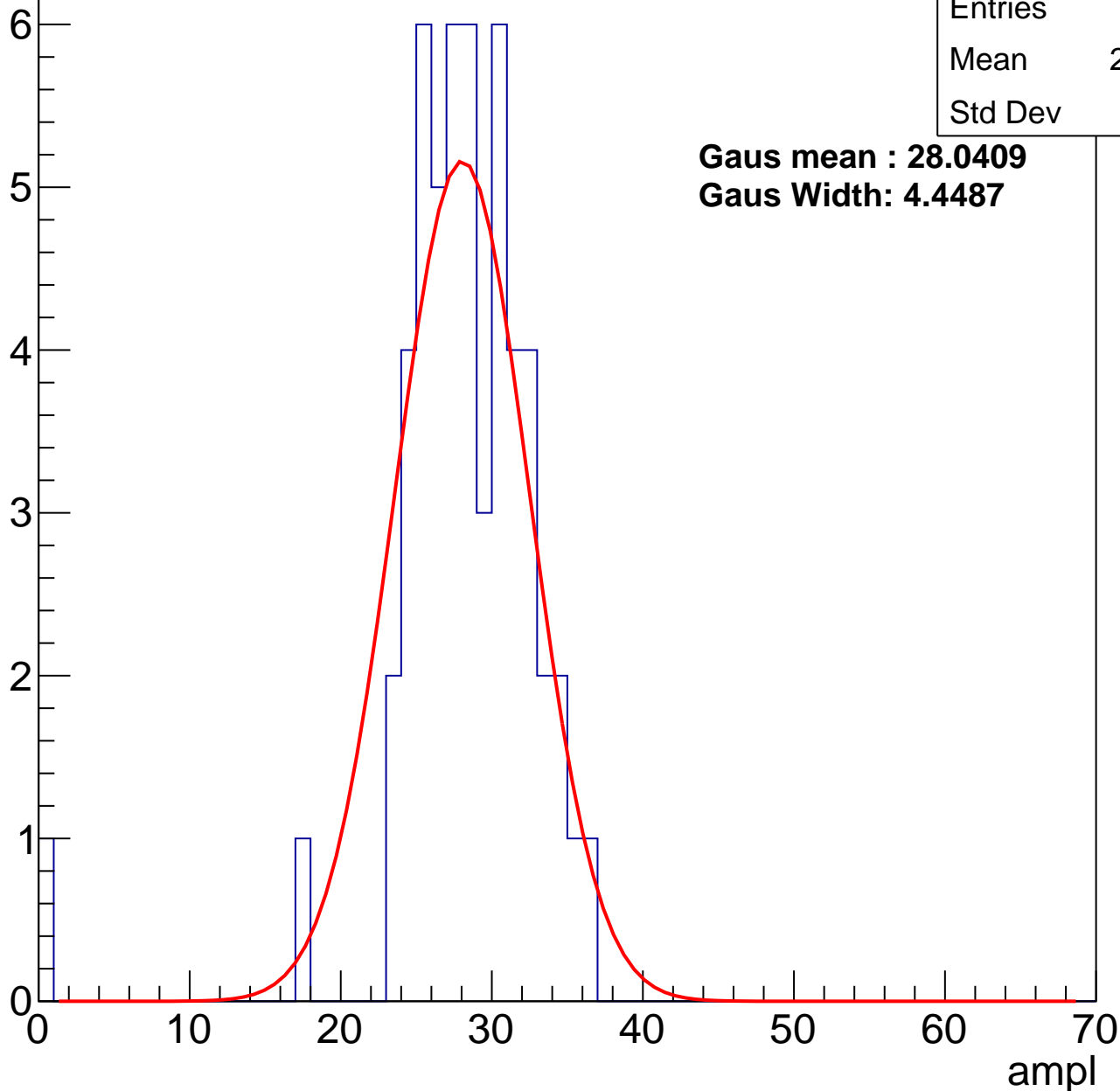
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	27.65
Std Dev	5.19

**Gaus mean : 28.0409**

**Gaus Width: 4.4487**



# B0L000S, U18-ch86, adc1

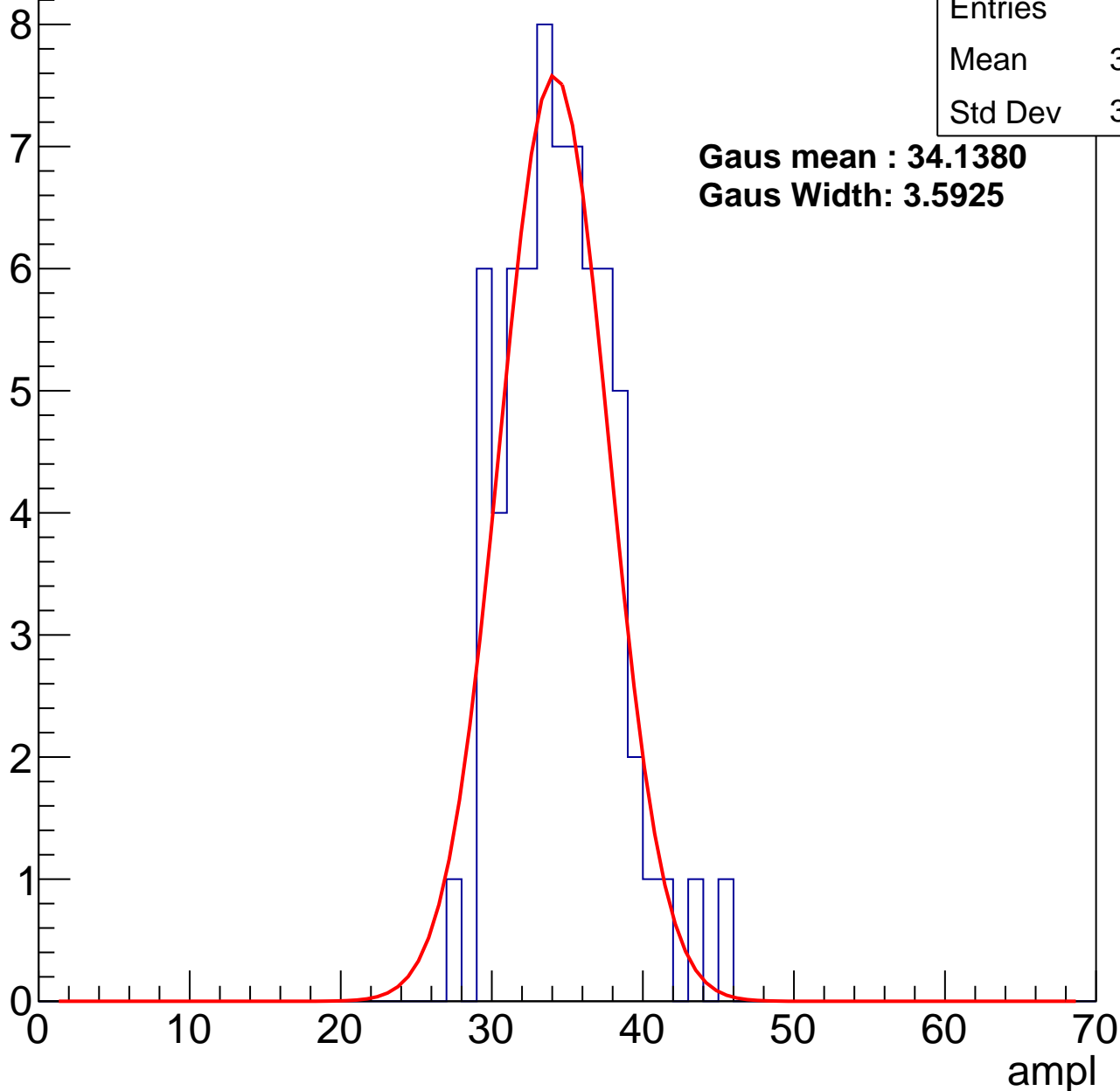
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	34.13
Std Dev	3.535

**Gaus mean : 34.1380**

**Gaus Width: 3.5925**



# B0L000S, U18-ch86, adc2

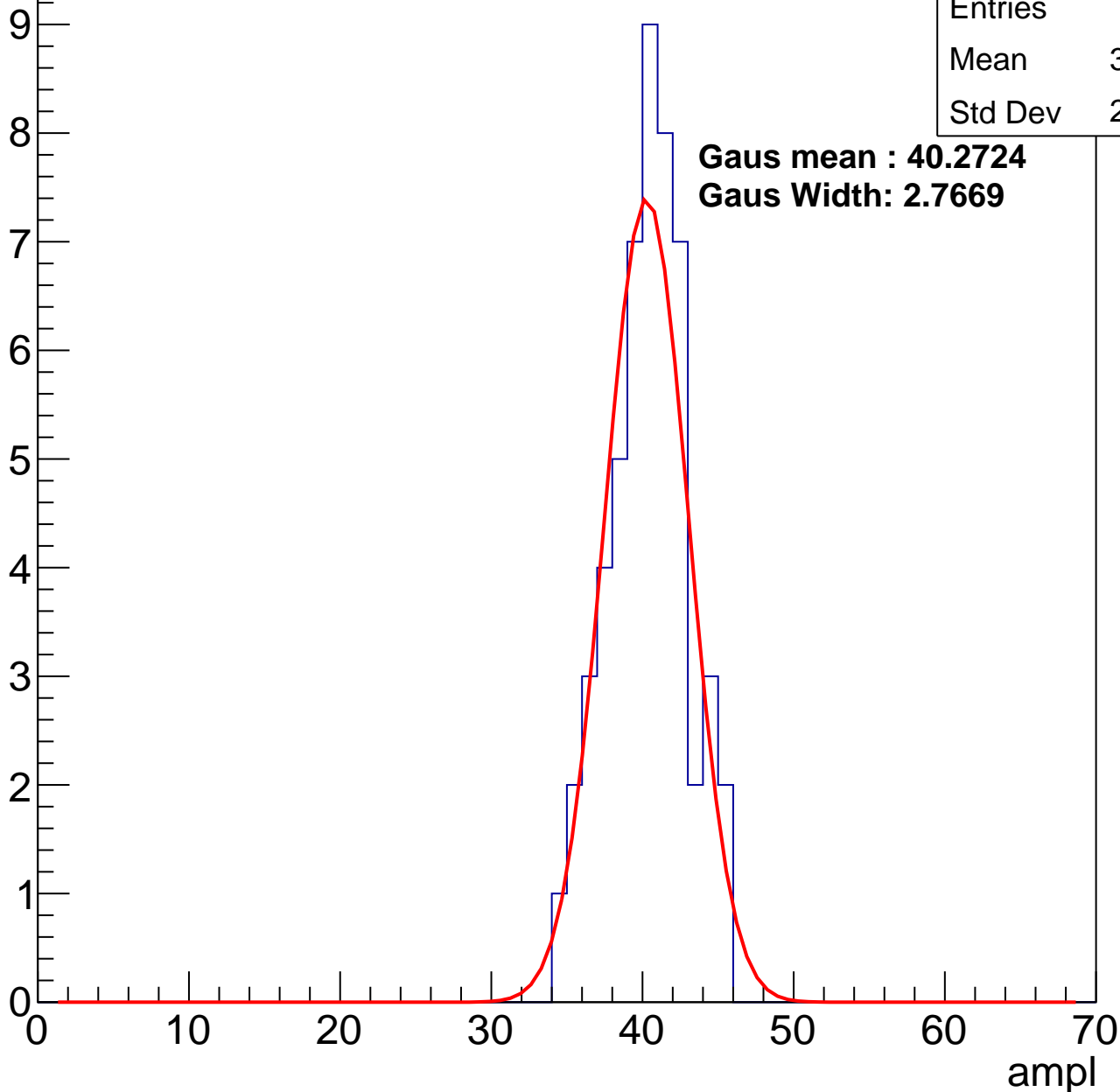
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	39.87
Std Dev	2.563

**Gaus mean : 40.2724**

**Gaus Width: 2.7669**

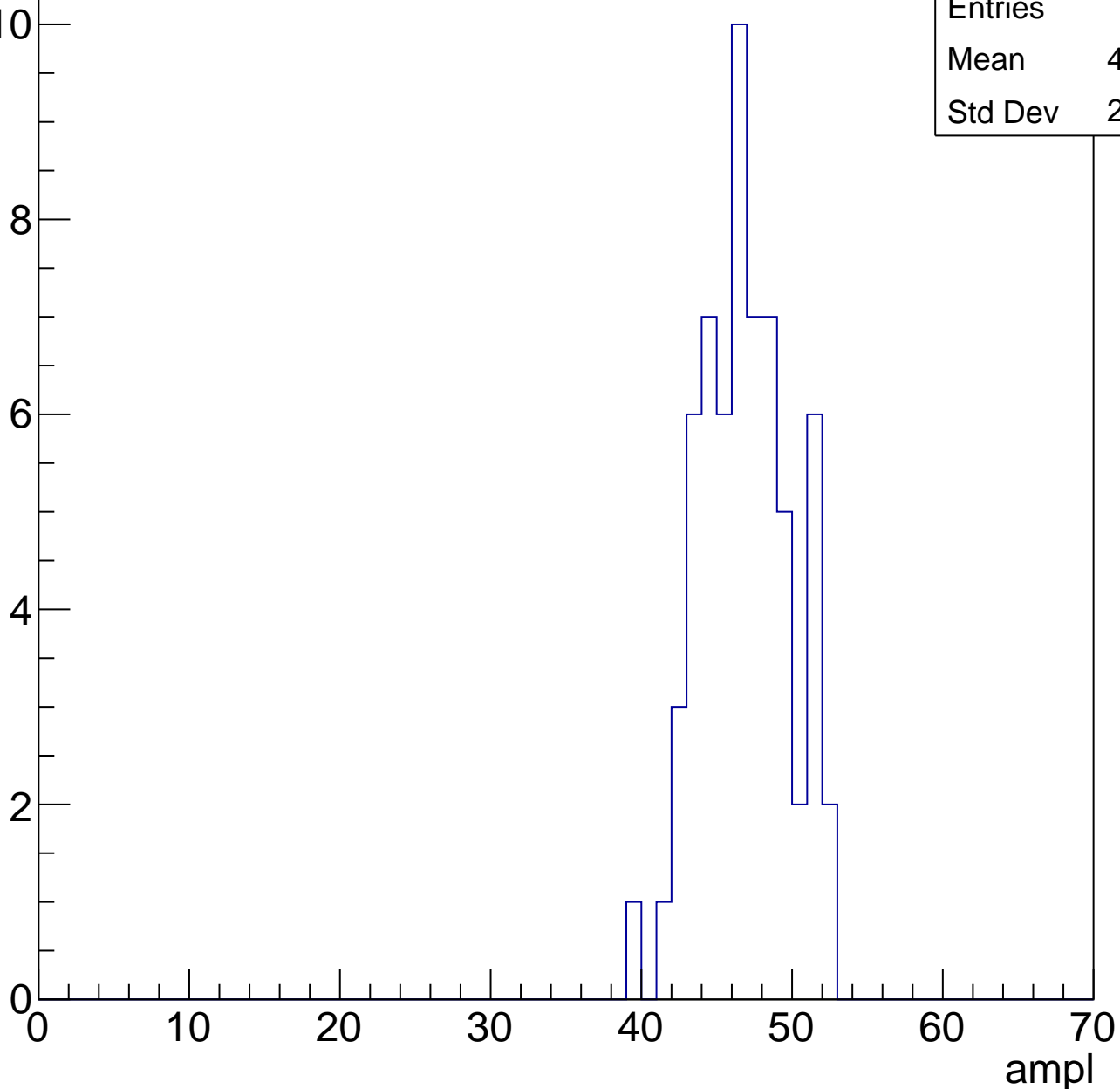


# B0L000S, U18-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	46.38
Std Dev	2.914

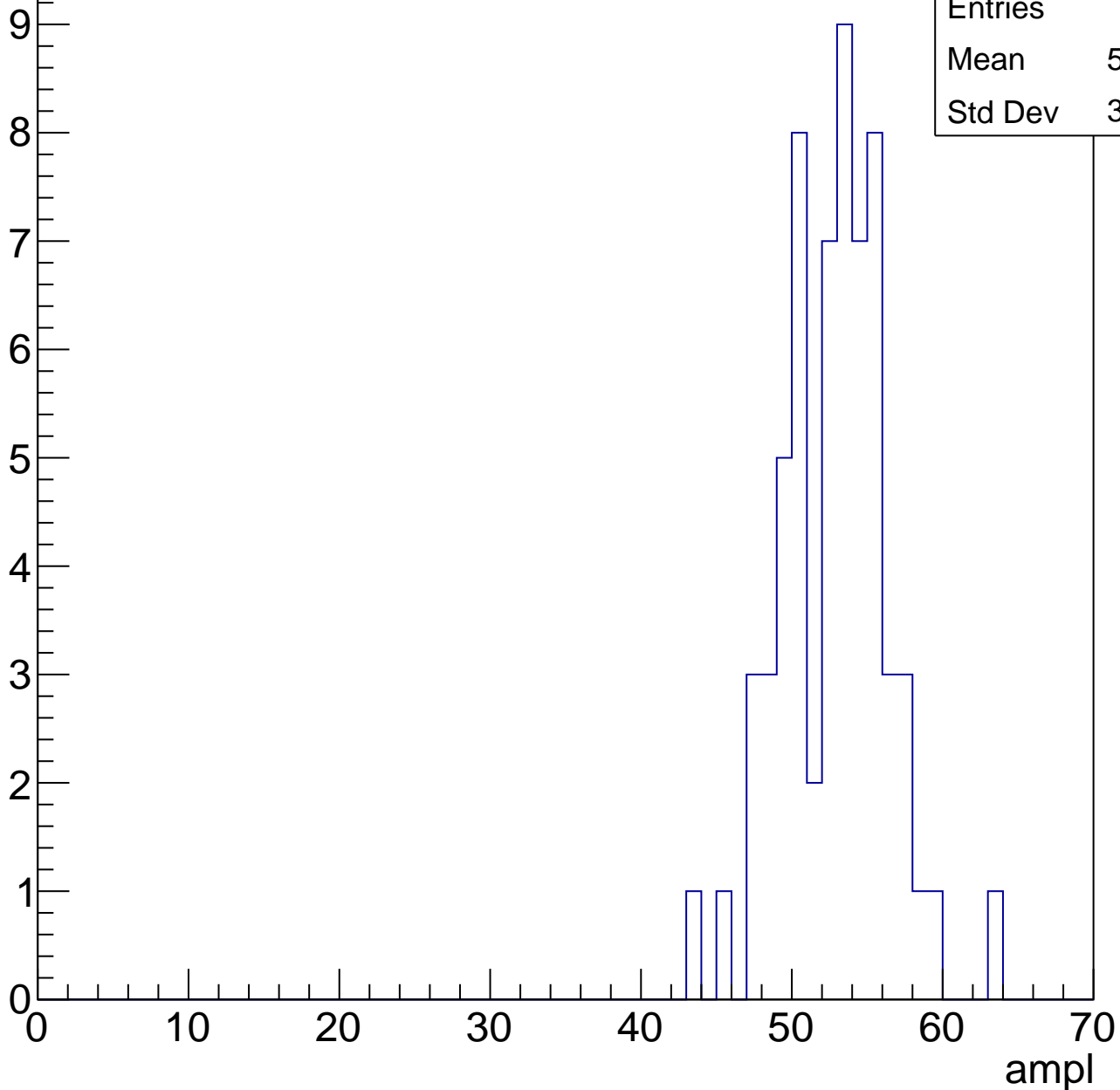


# B0L000S, U18-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	52.35
Std Dev	3.479

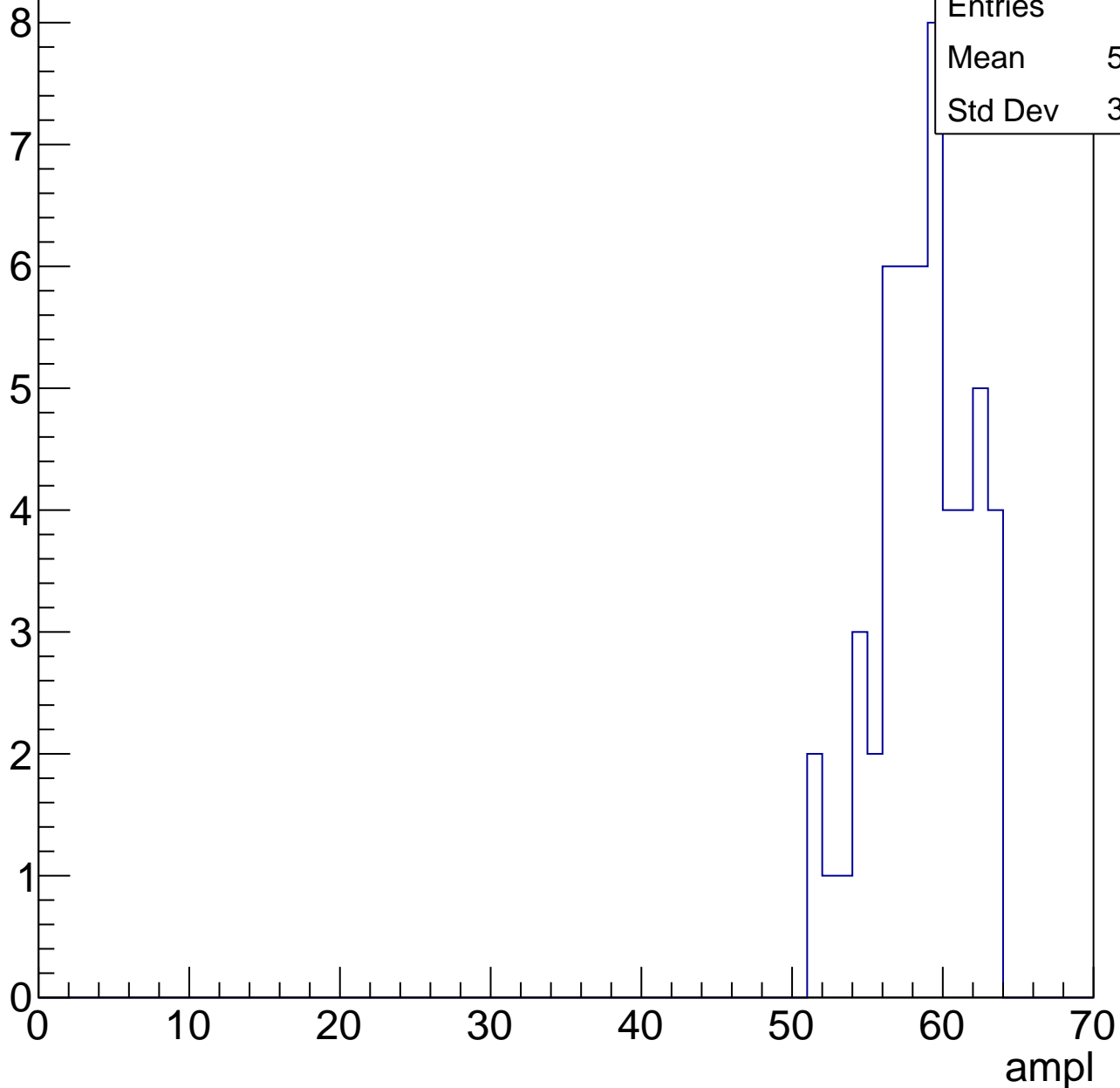


# B0L000S, U18-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.13
Std Dev	3.082

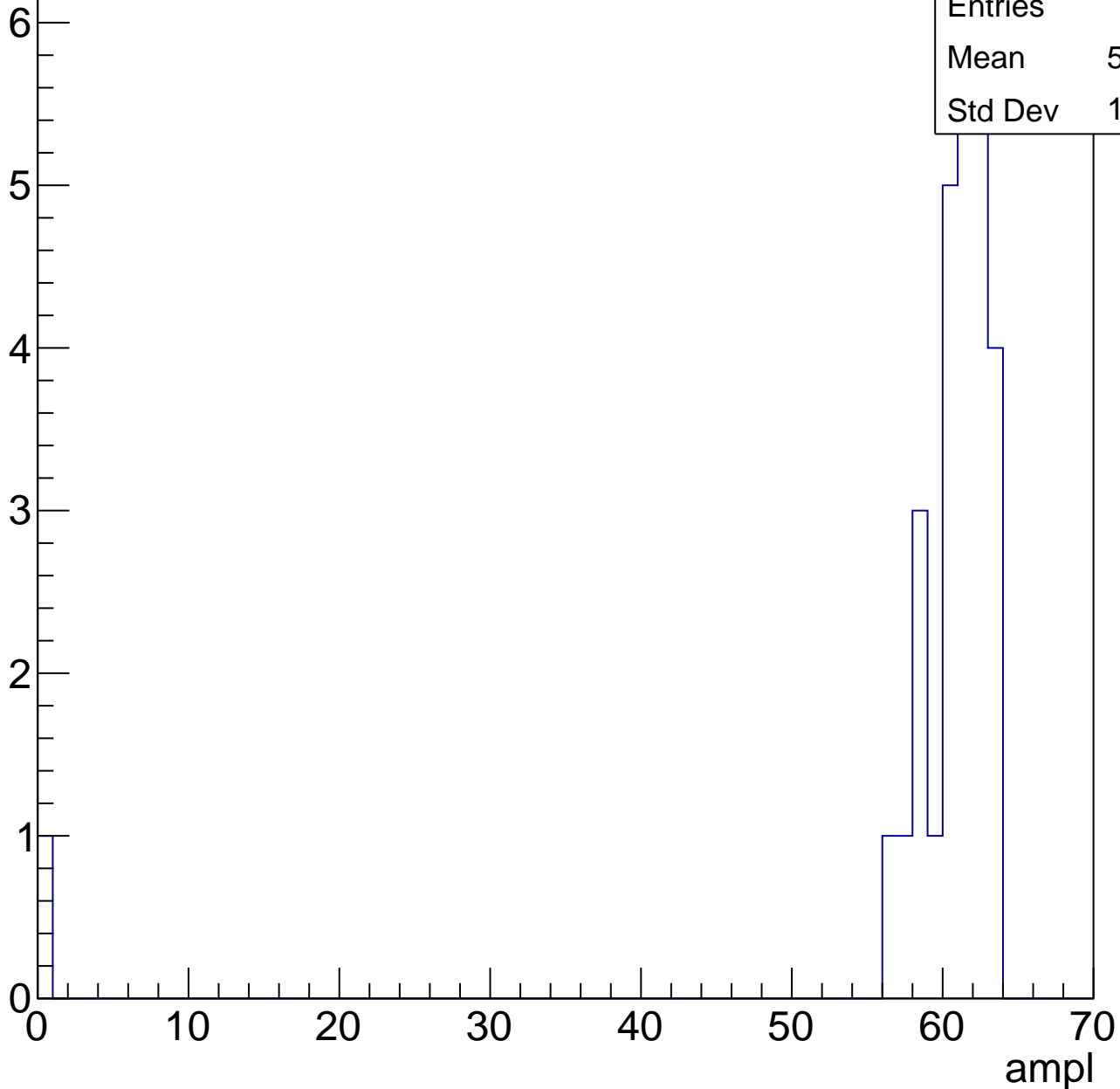


# B0L000S, U18-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.43
Std Dev	11.39

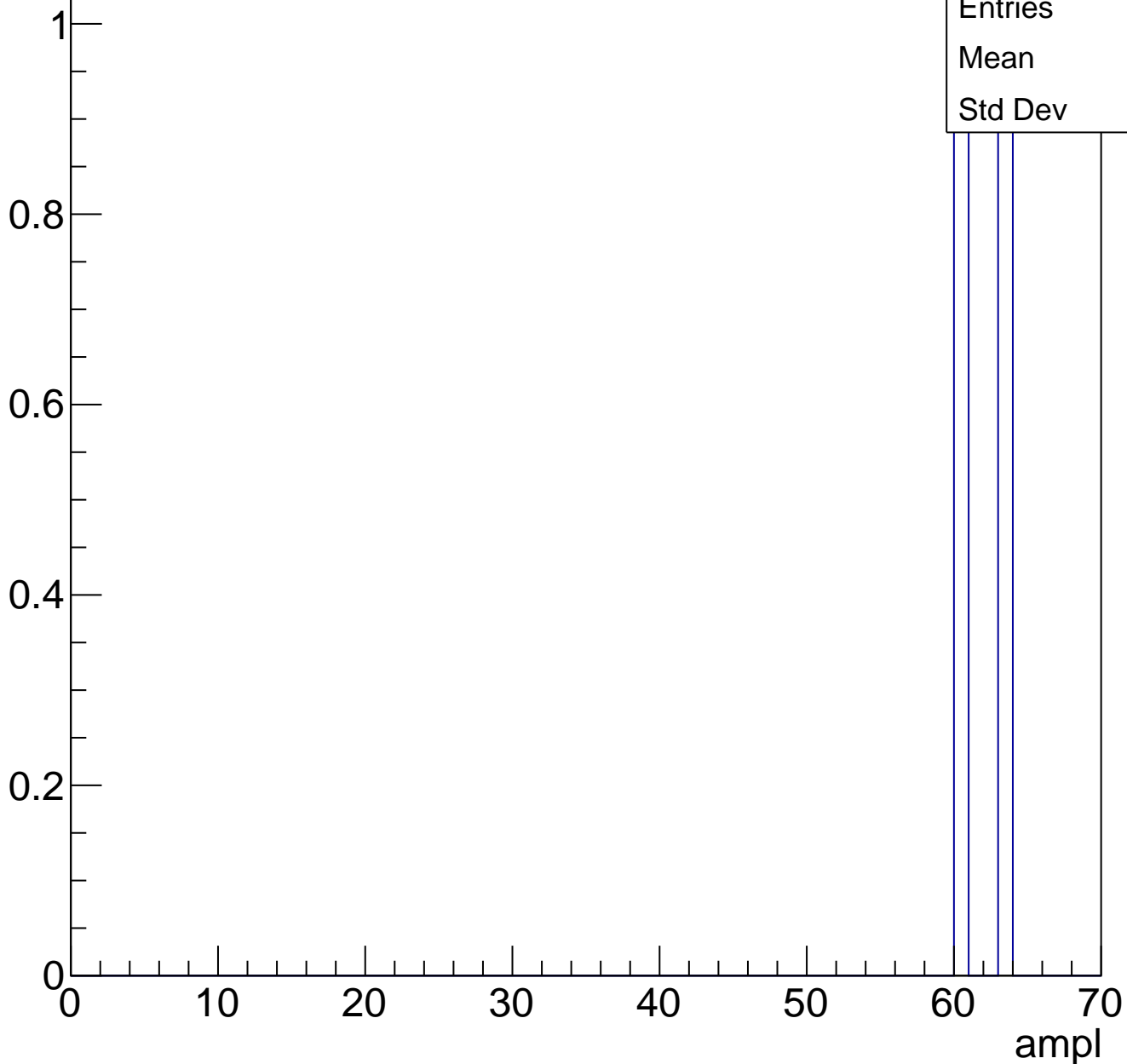




# B0L000S, U18-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch87, adc0

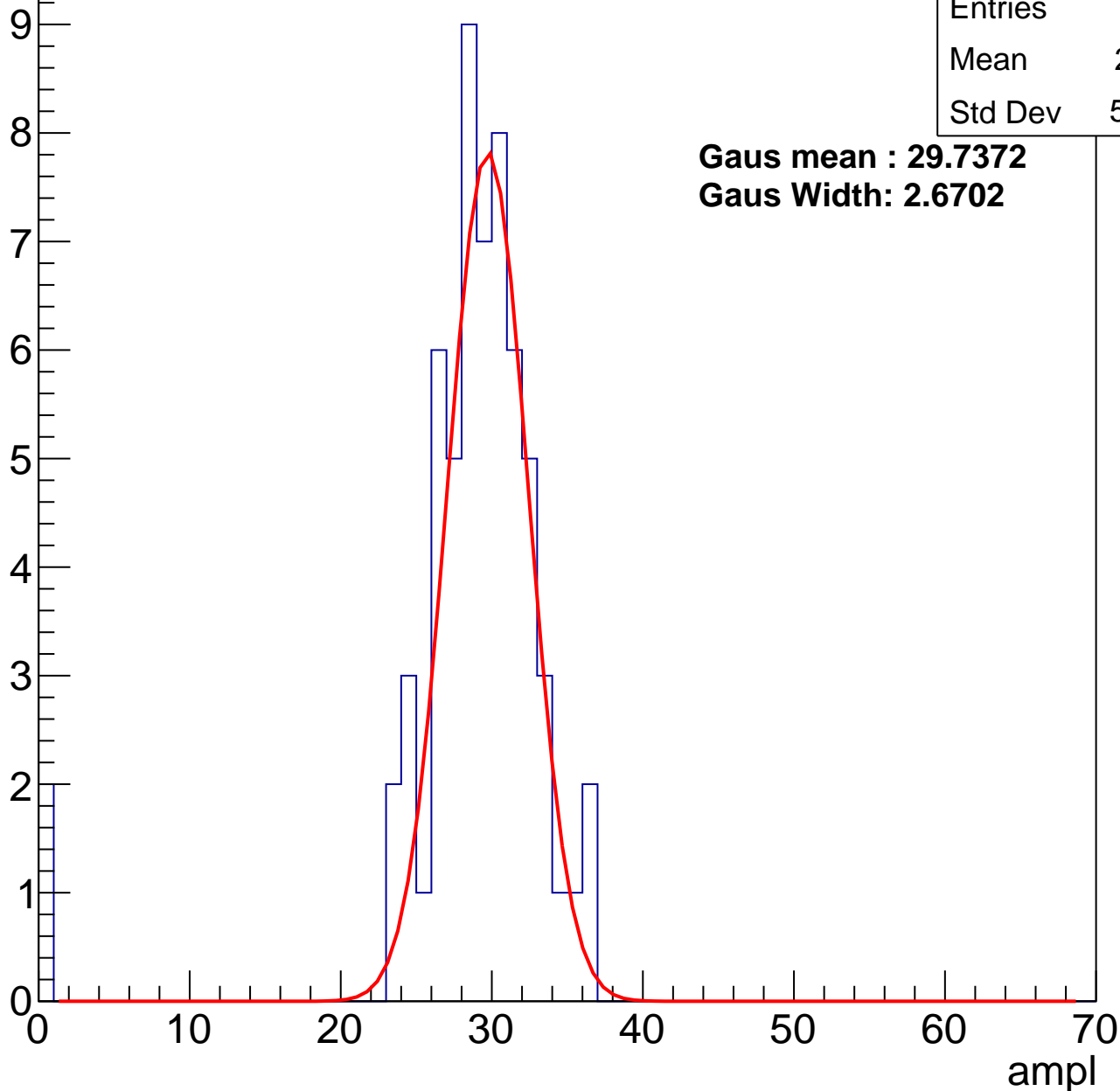
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	28.11
Std Dev	5.956

**Gaus mean : 29.7372**

**Gaus Width: 2.6702**



# B0L000S, U18-ch87, adc1

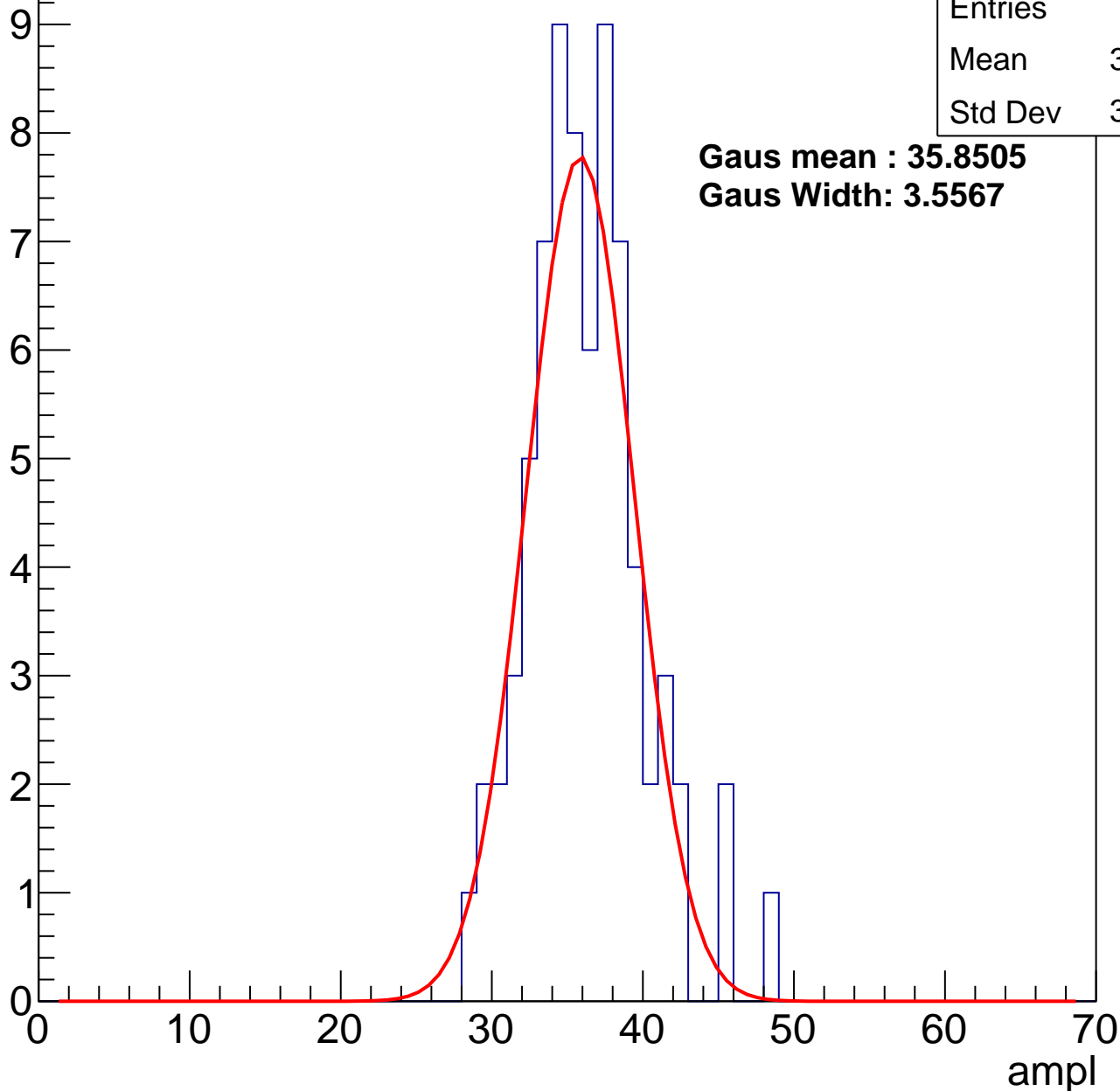
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	35.78
Std Dev	3.793

**Gaus mean : 35.8505**

**Gaus Width: 3.5567**



# B0L000S, U18-ch87, adc2

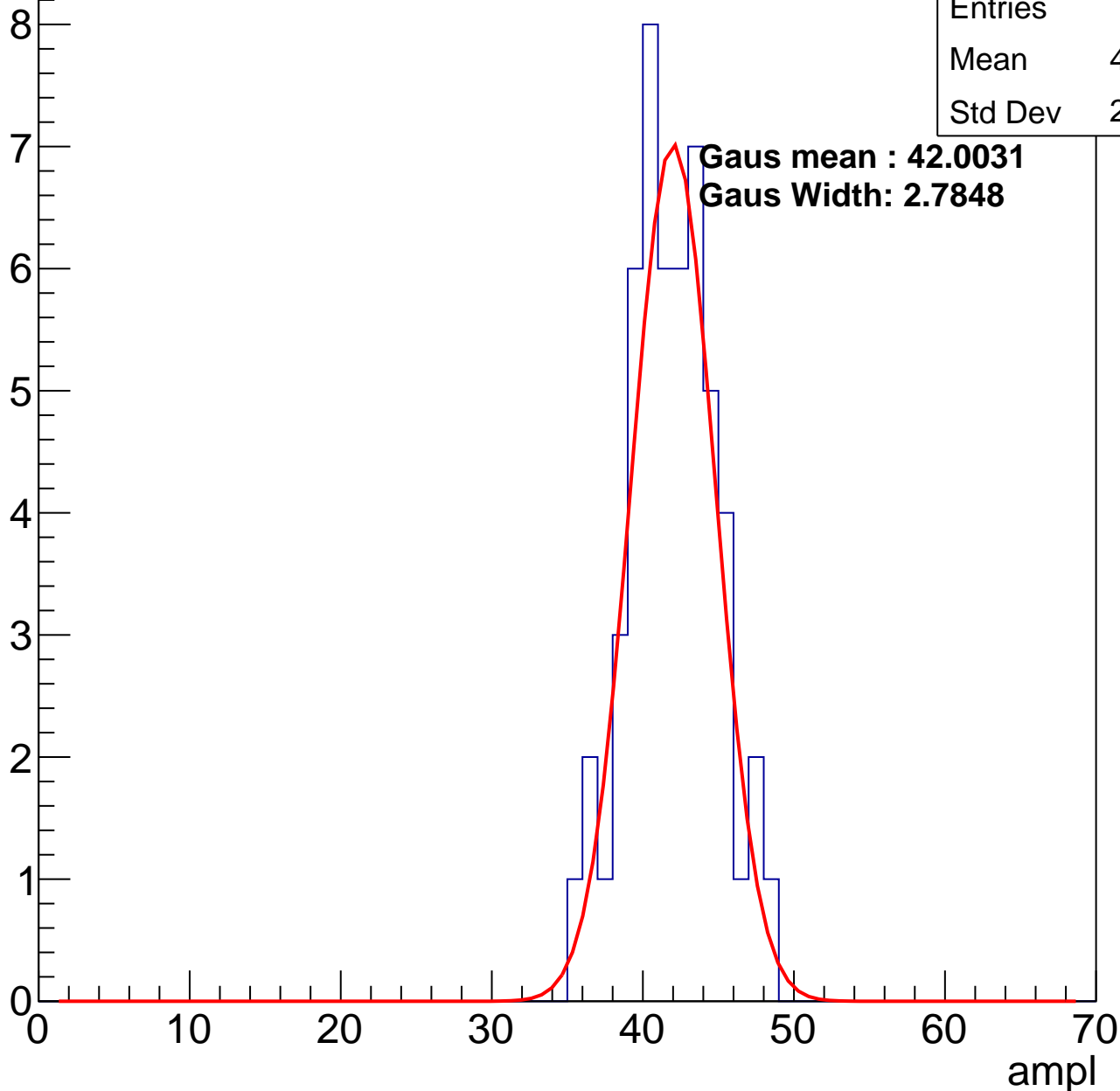
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	41.49
Std Dev	2.872

**Gaus mean : 42.0031**

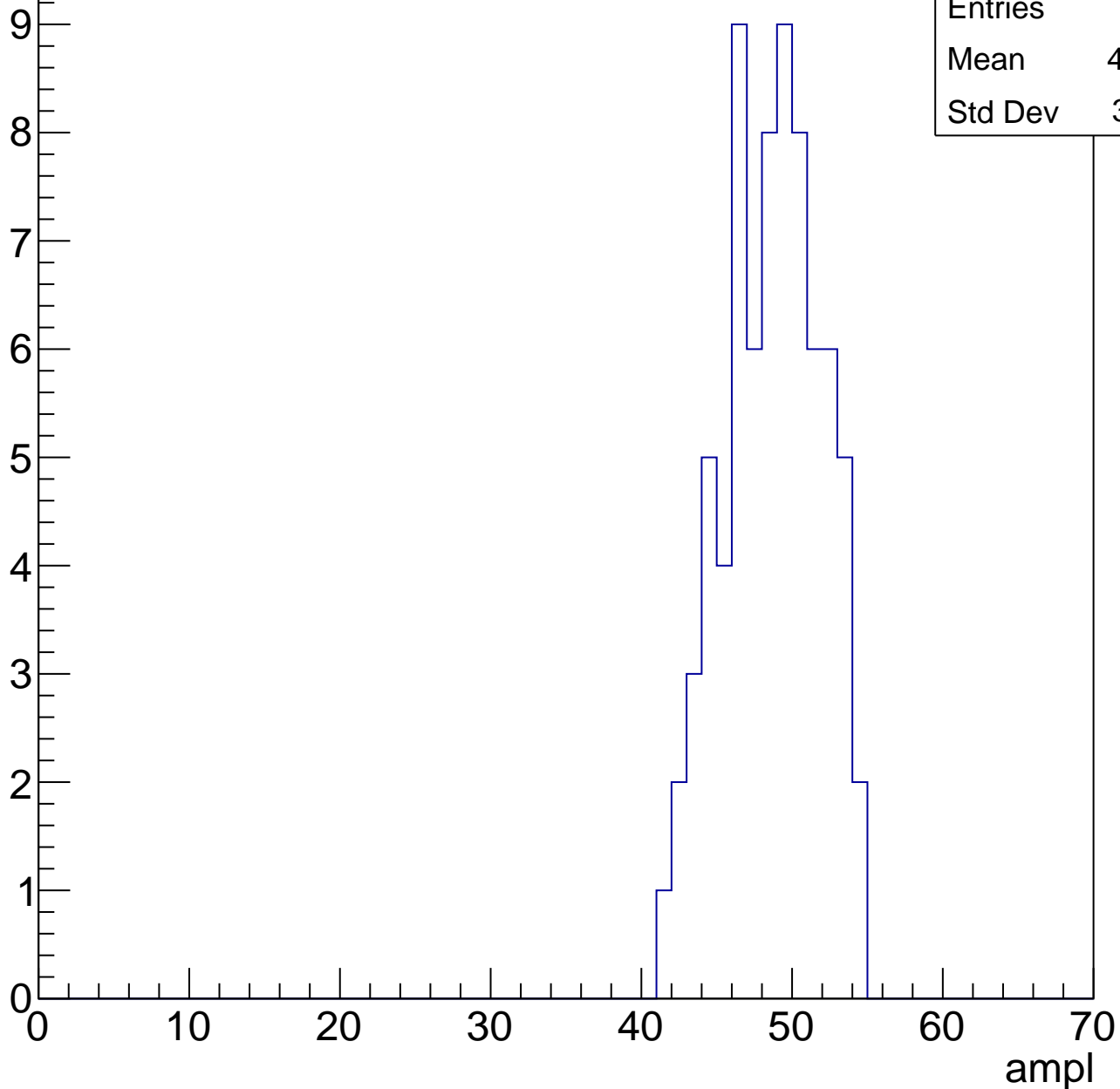
**Gaus Width: 2.7848**



# B0L000S, U18-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	74
Mean	48.19
Std Dev	3.161

# B0L000S, U18-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	55.33
Std Dev	3.206

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

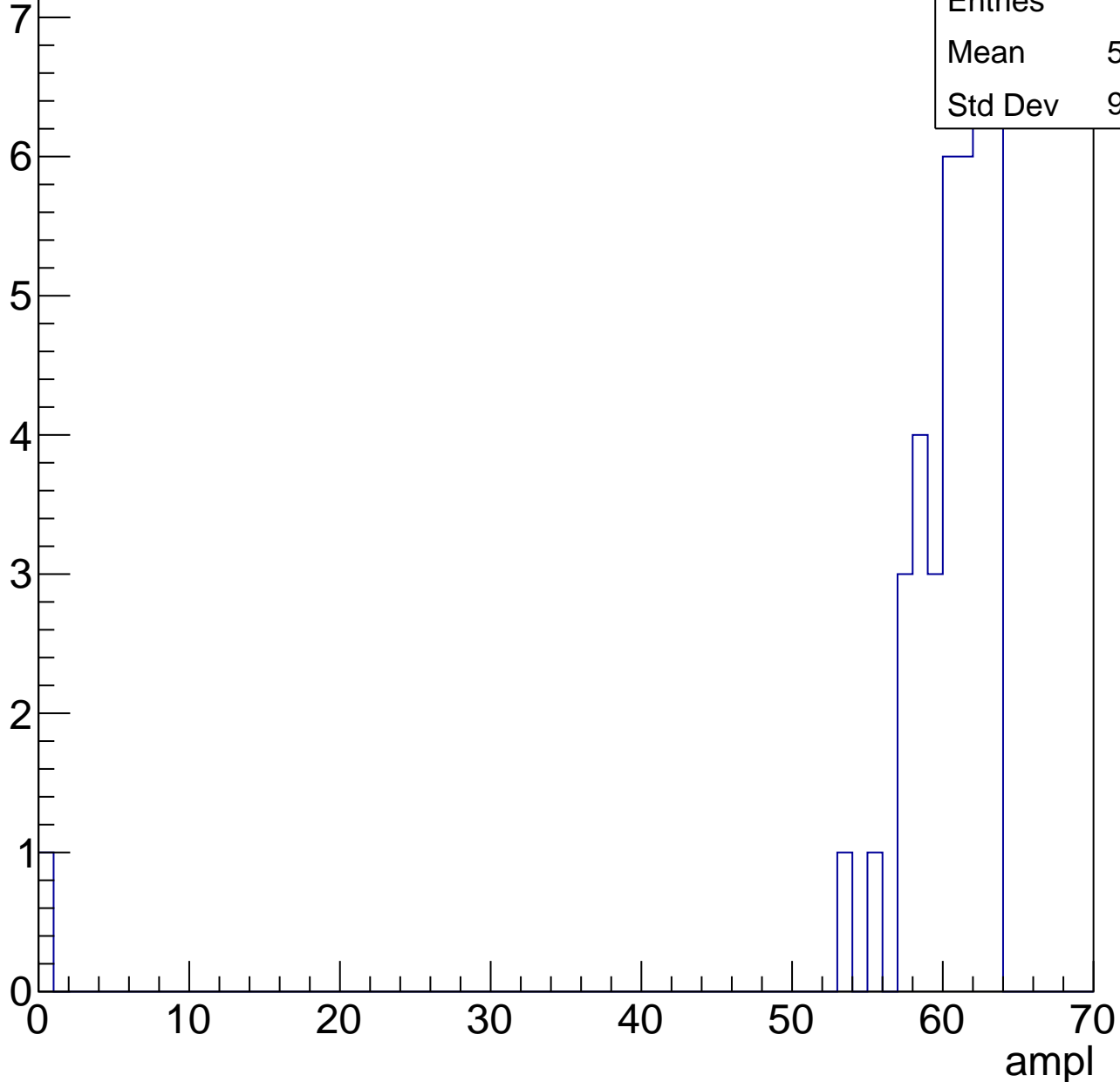
ampl

ampl

# B0L000S, U18-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

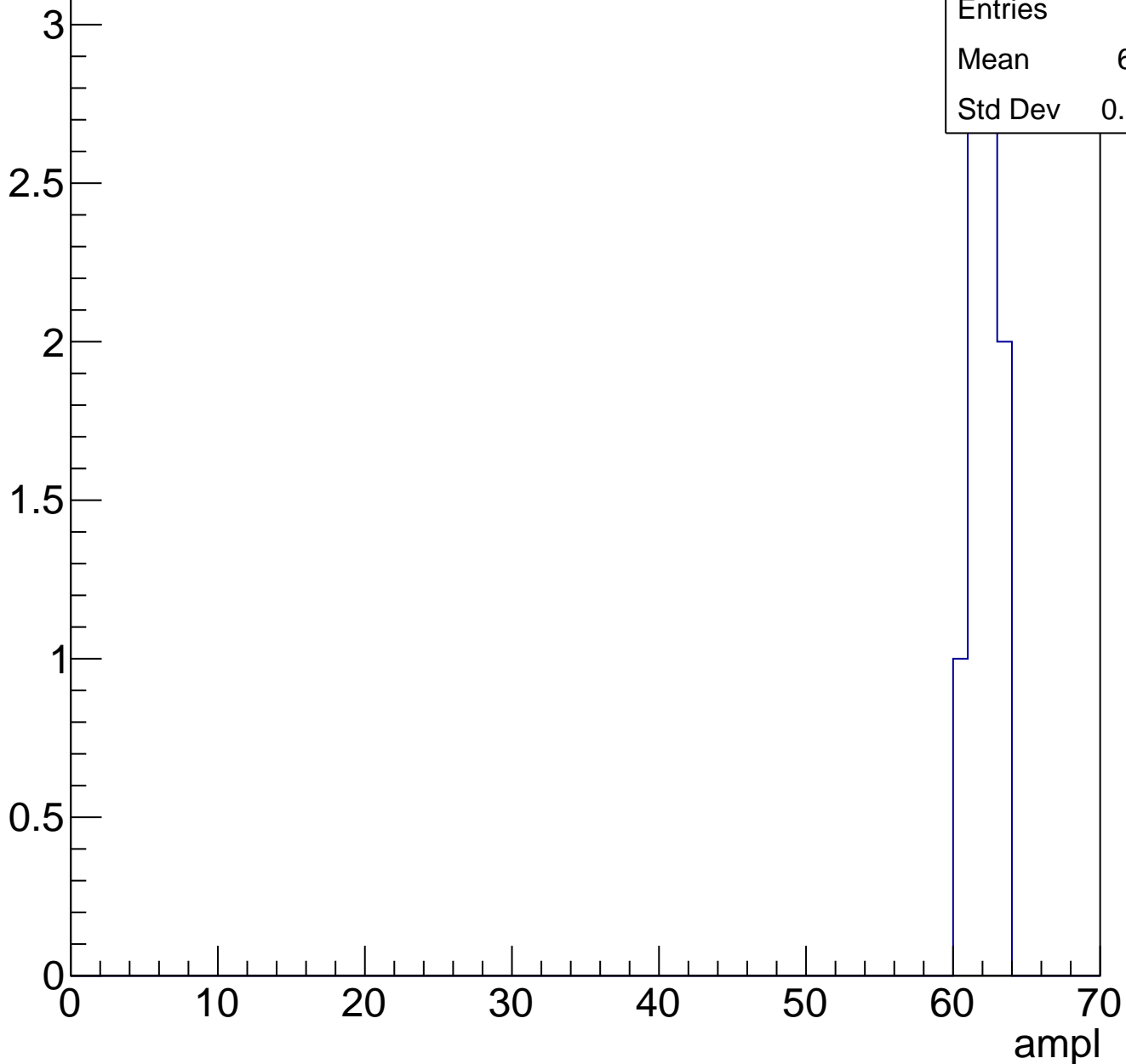
Entry



# B0L000S, U18-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch88, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.16
Std Dev	4.916

**Gaus mean : 29.8726**

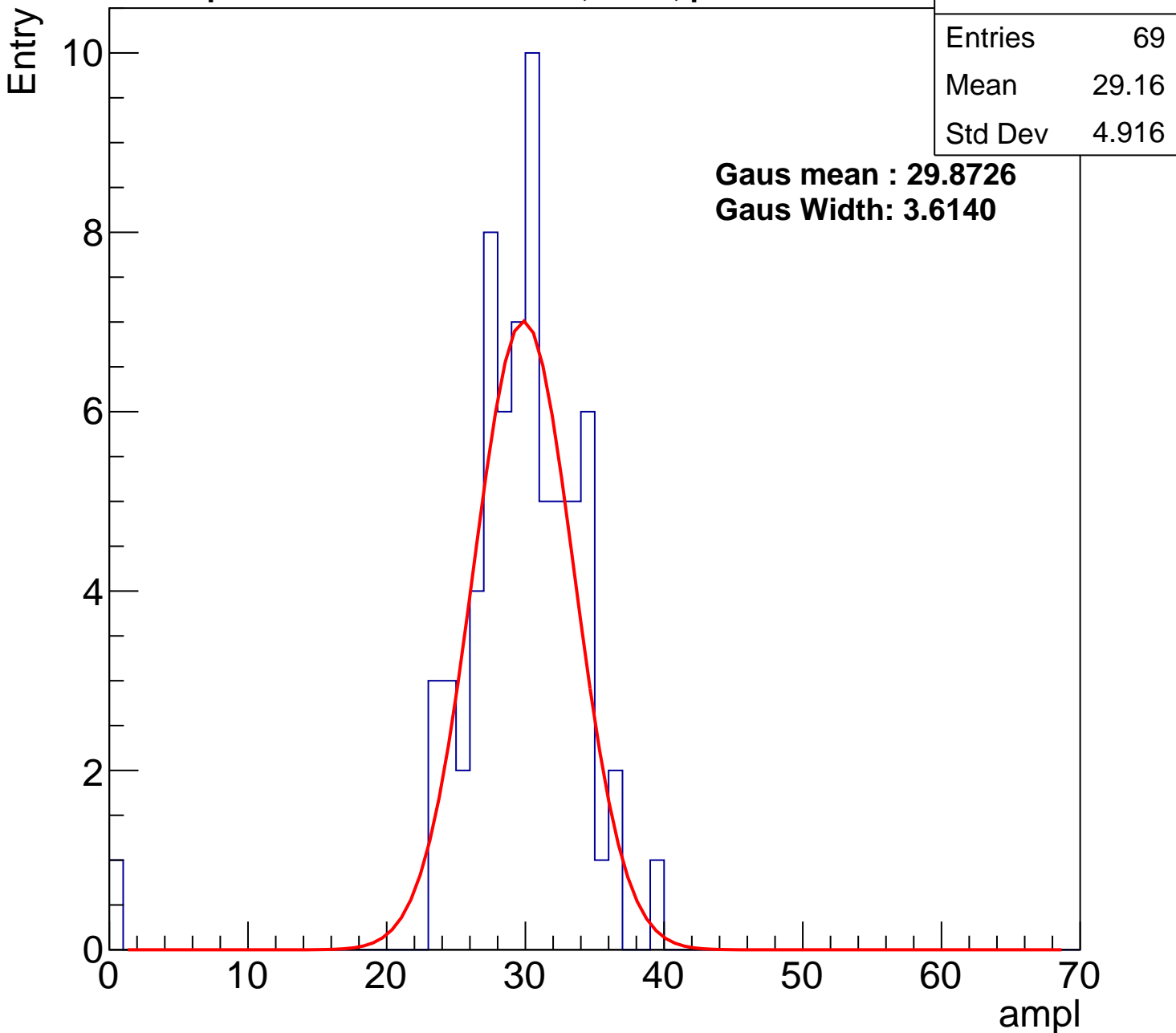
**Gaus Width: 3.6140**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch88, adc1

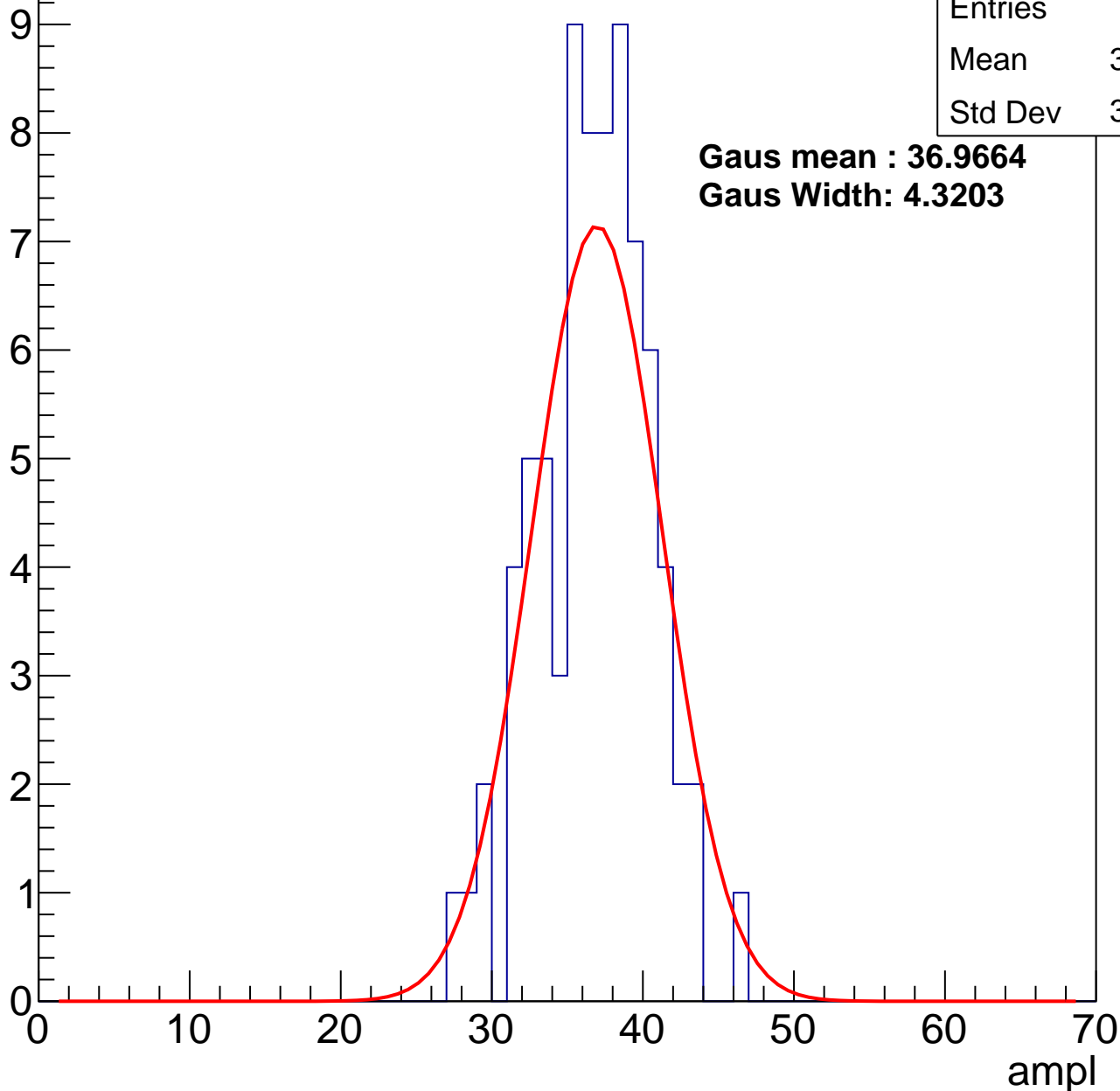
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	36.34
Std Dev	3.698

**Gaus mean : 36.9664**

**Gaus Width: 4.3203**



# B0L000S, U18-ch88, adc2

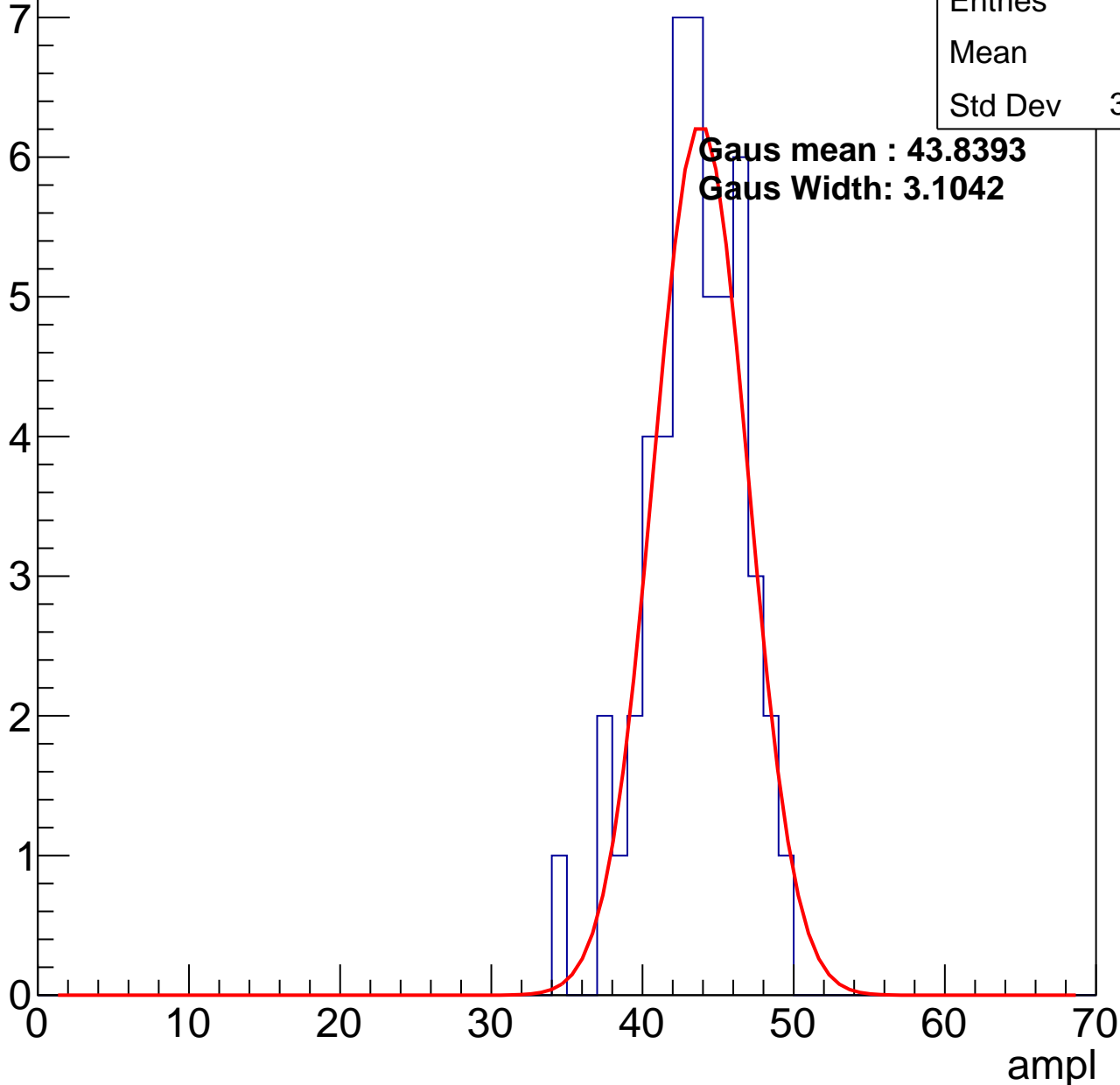
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	43
Std Dev	3.105

**Gaus mean : 43.8393**

**Gaus Width: 3.1042**

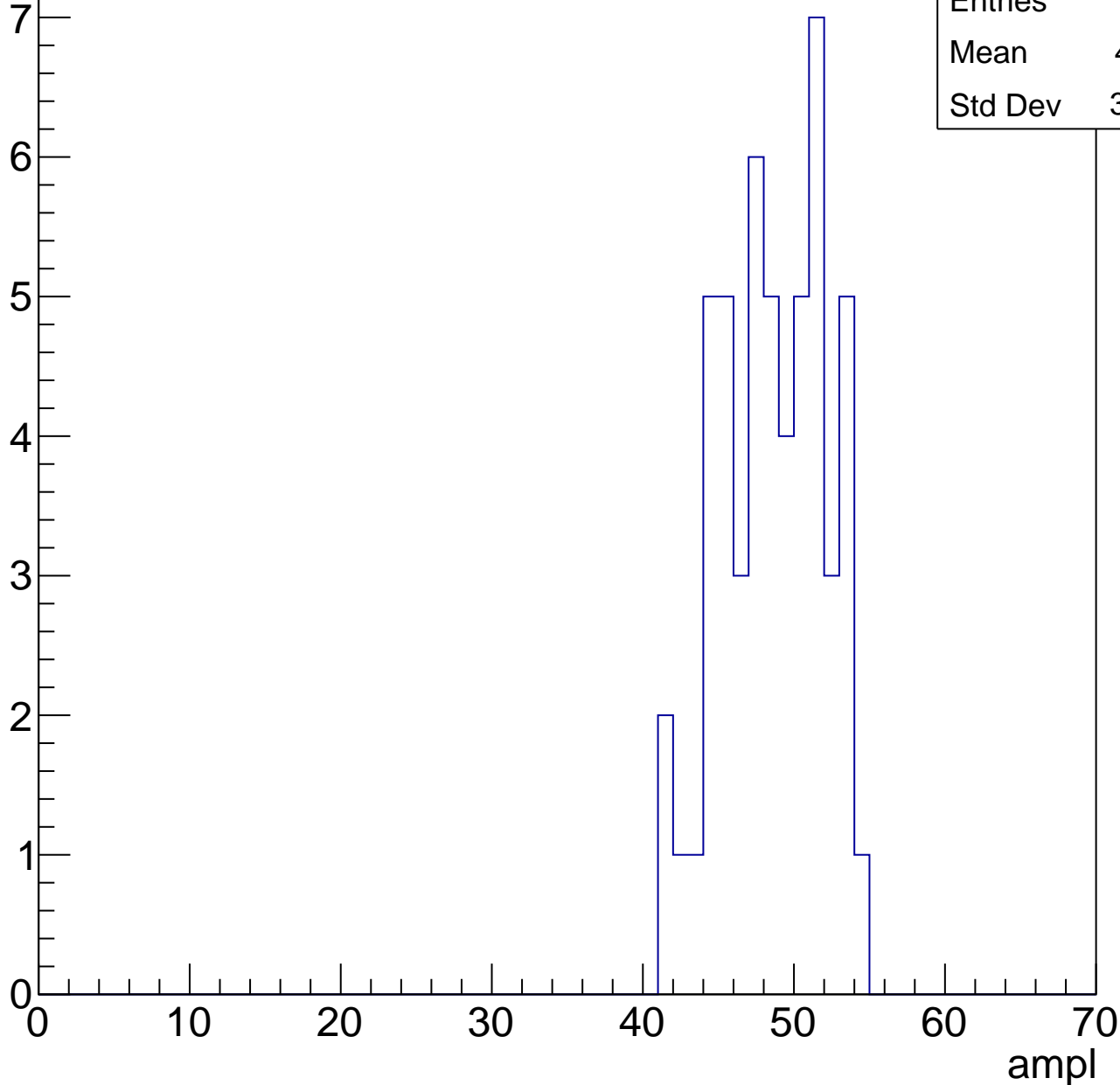


# B0L000S, U18-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	48.11
Std Dev	3.357

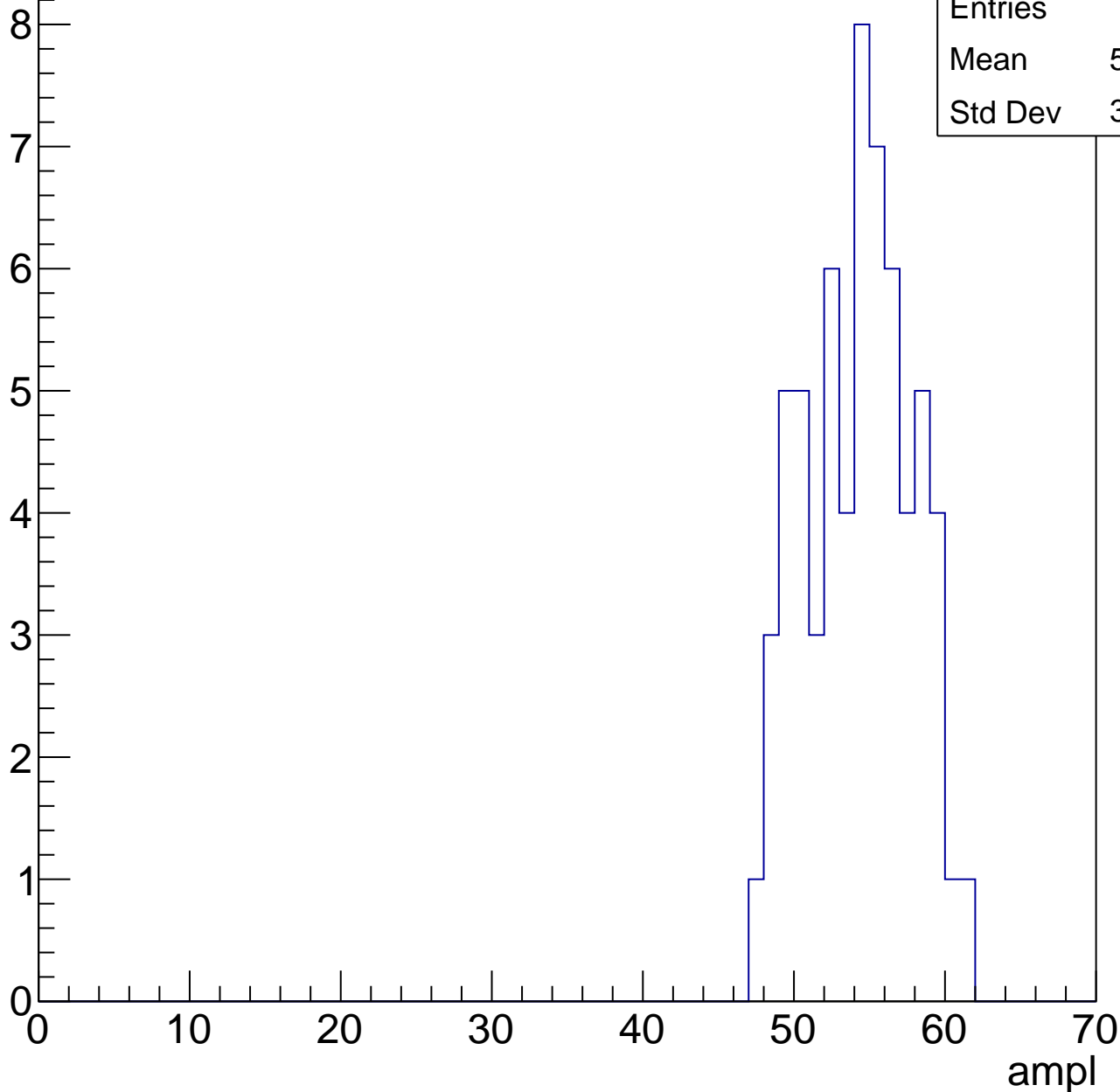


# B0L000S, U18-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

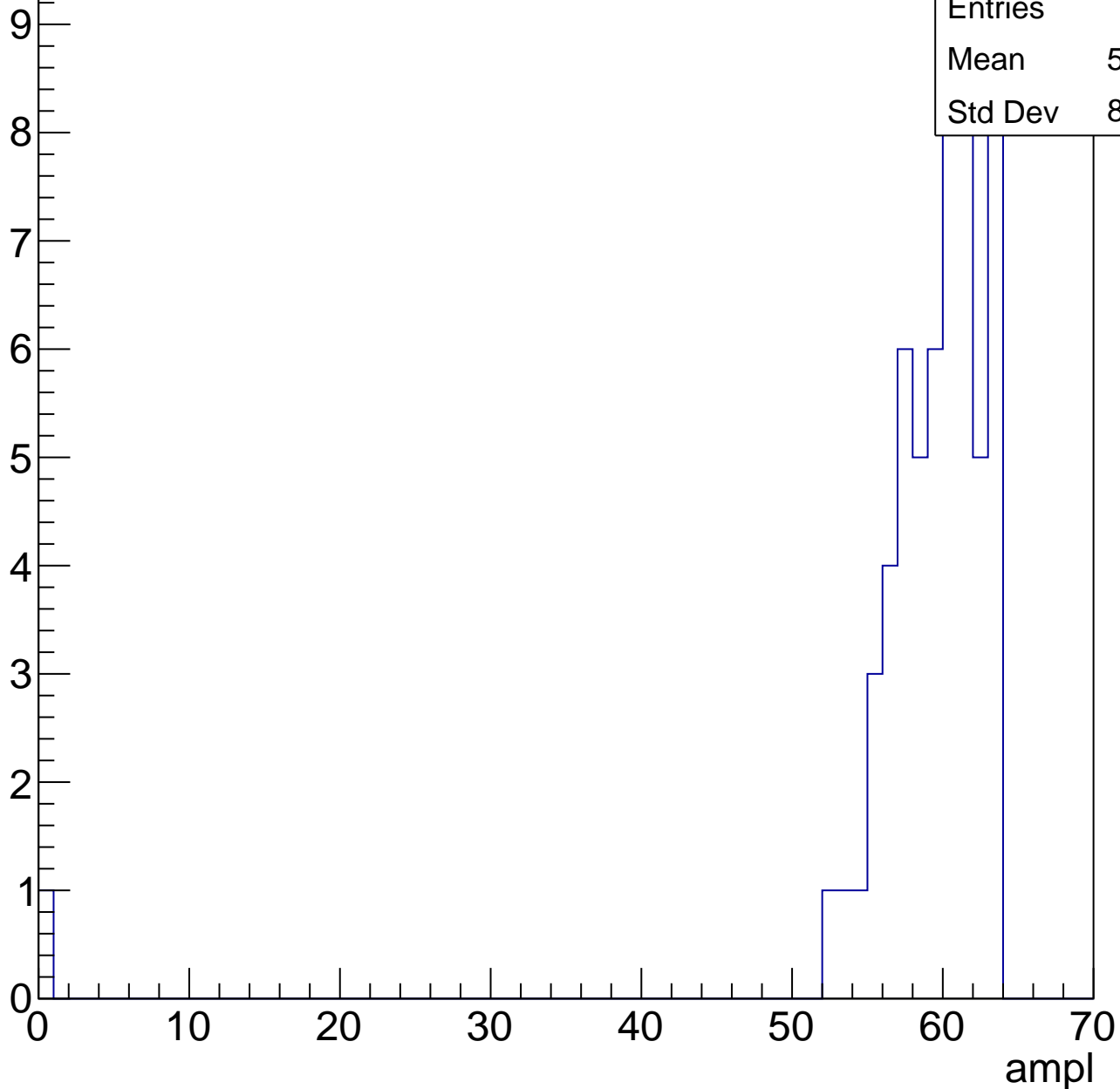
Entries	63
Mean	53.83
Std Dev	3.444



# B0L000S, U18-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

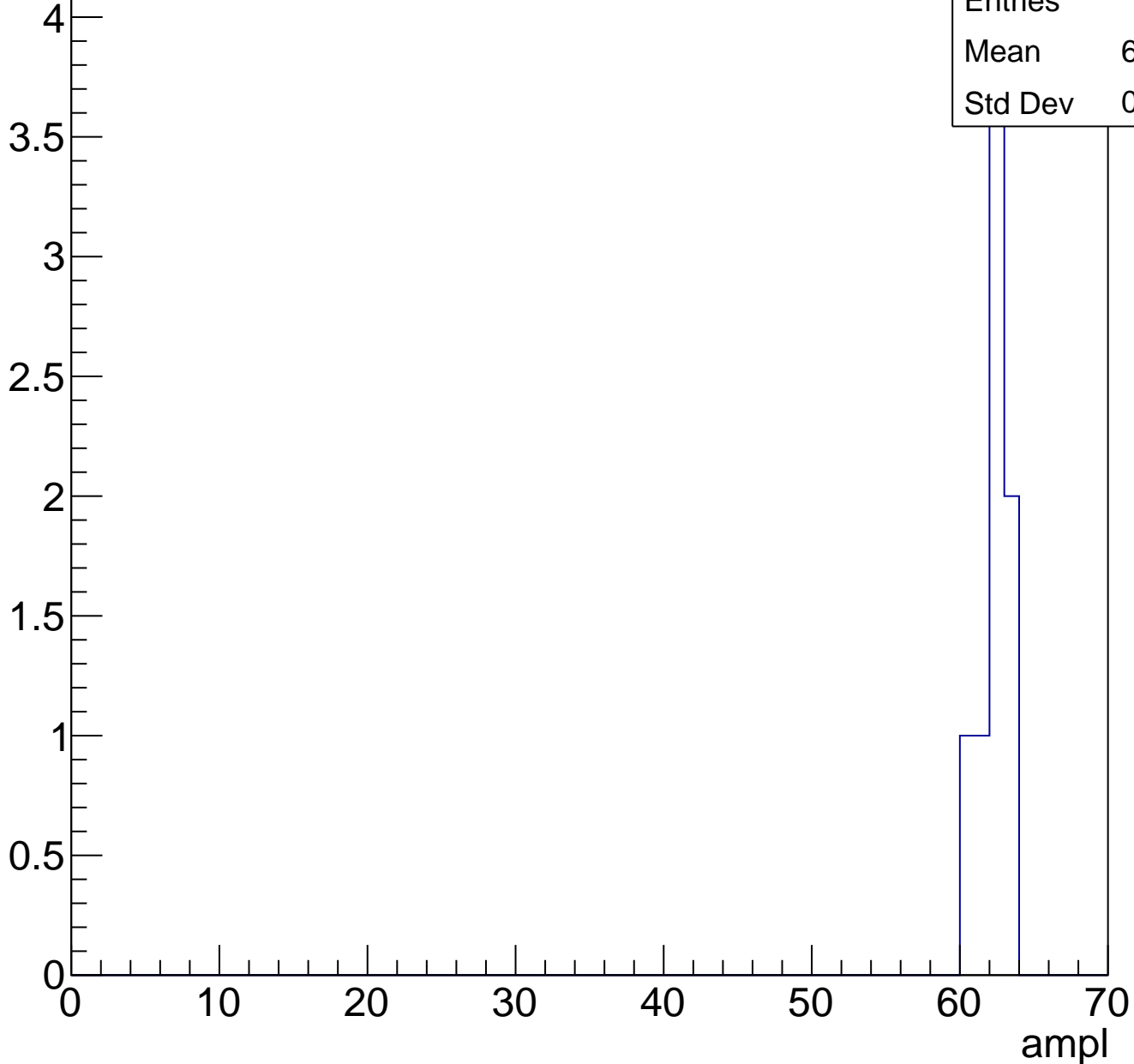
Entry



# B0L000S, U18-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

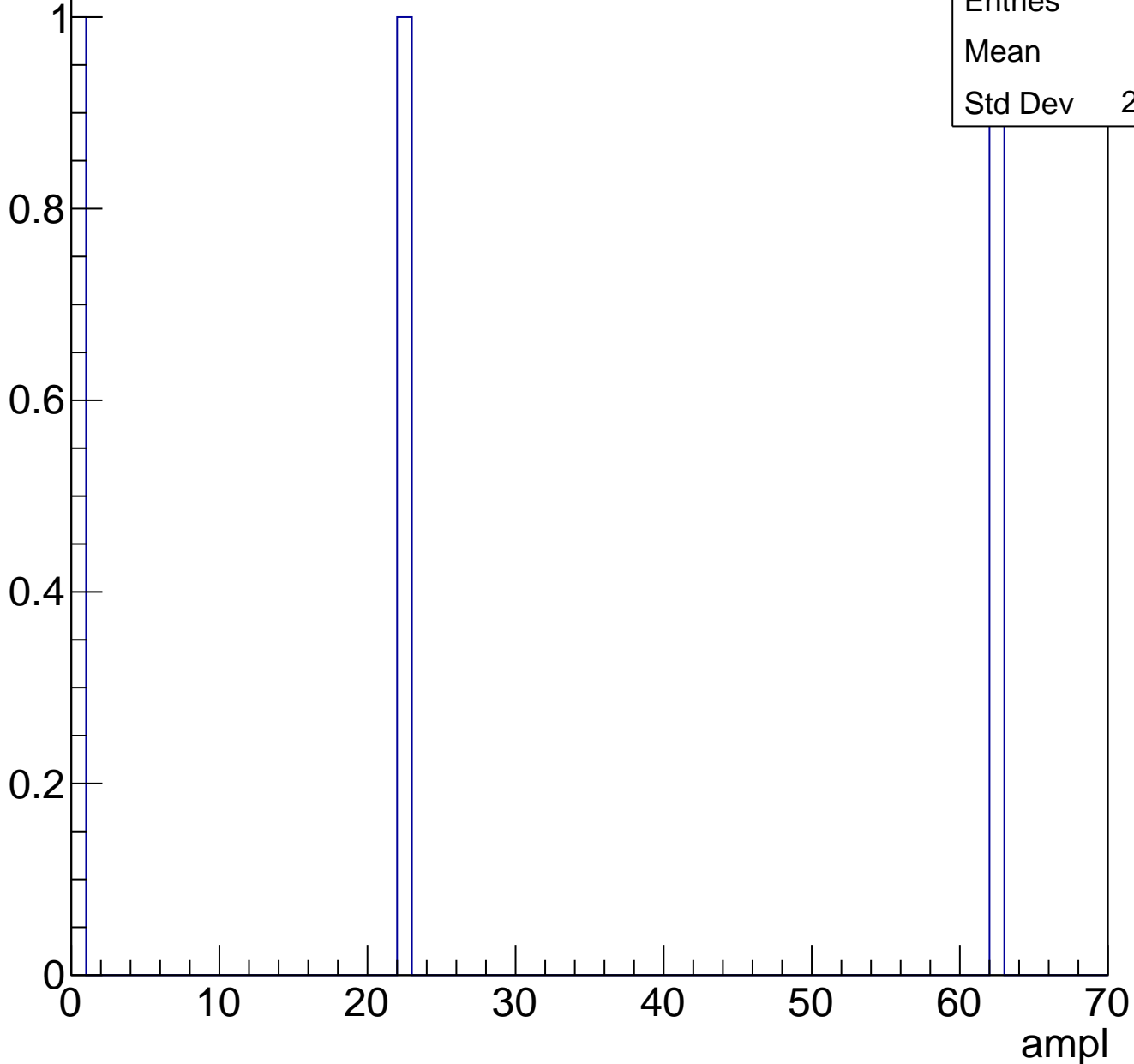




# B0L000S, U18-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28
Std Dev	25.66

# B0L000S, U18-ch89, adc0

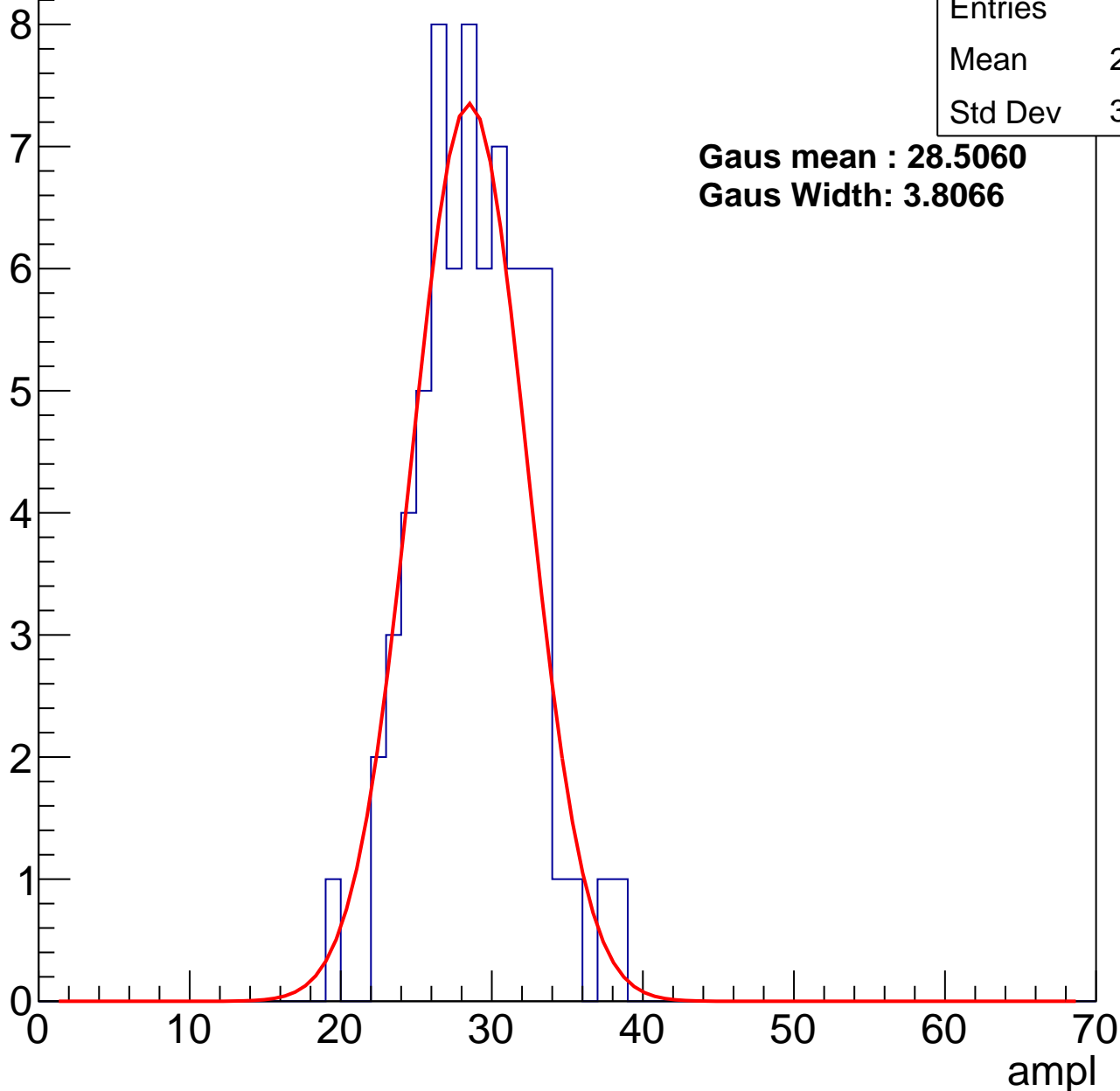
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	28.49
Std Dev	3.648

**Gaus mean : 28.5060**

**Gaus Width: 3.8066**



# B0L000S, U18-ch89, adc1

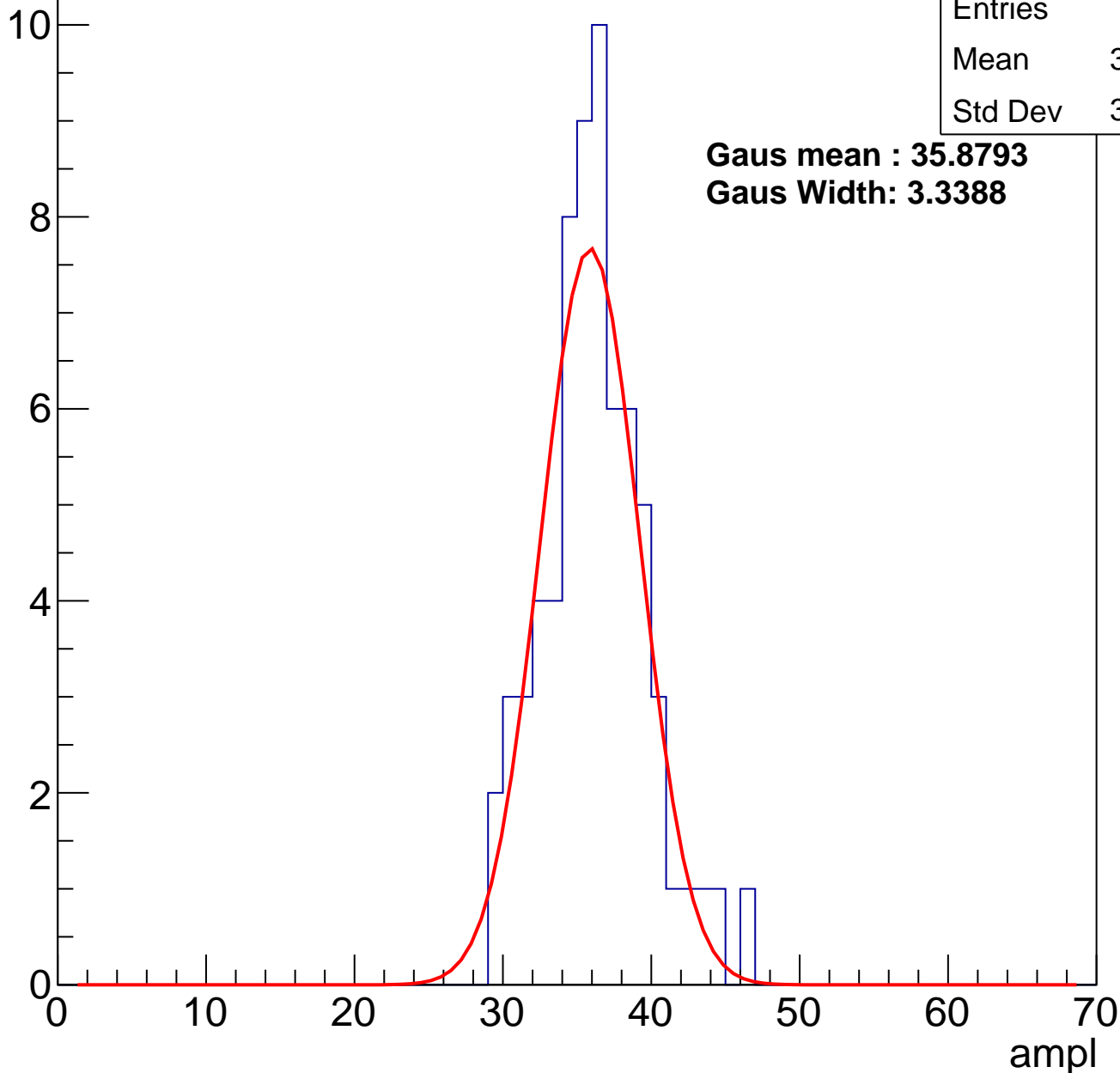
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	35.72
Std Dev	3.459

**Gaus mean : 35.8793**

**Gaus Width: 3.3388**

Entry



# B0L000S, U18-ch89, adc2

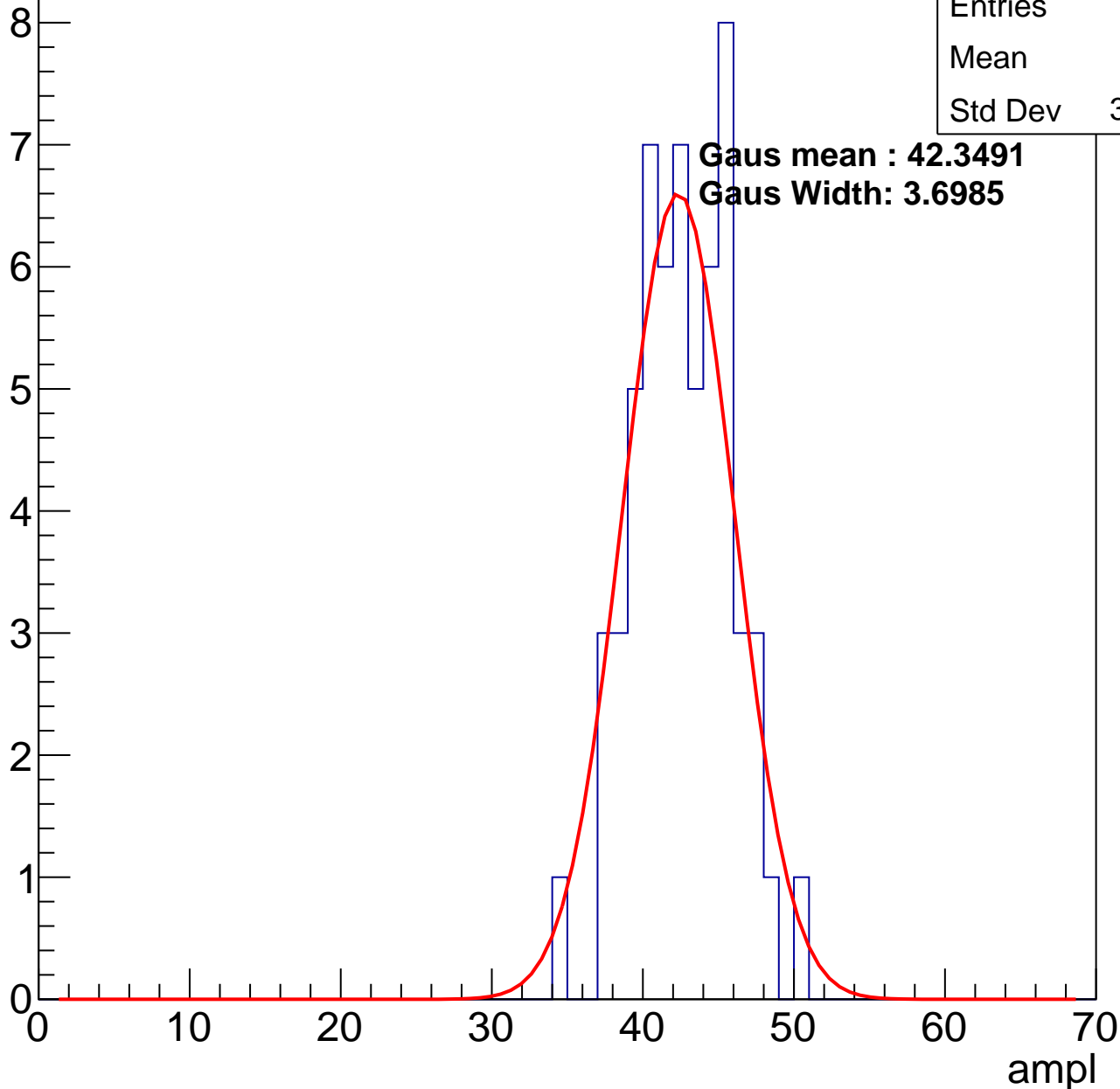
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.2
Std Dev	3.156

**Gaus mean : 42.3491**

**Gaus Width: 3.6985**

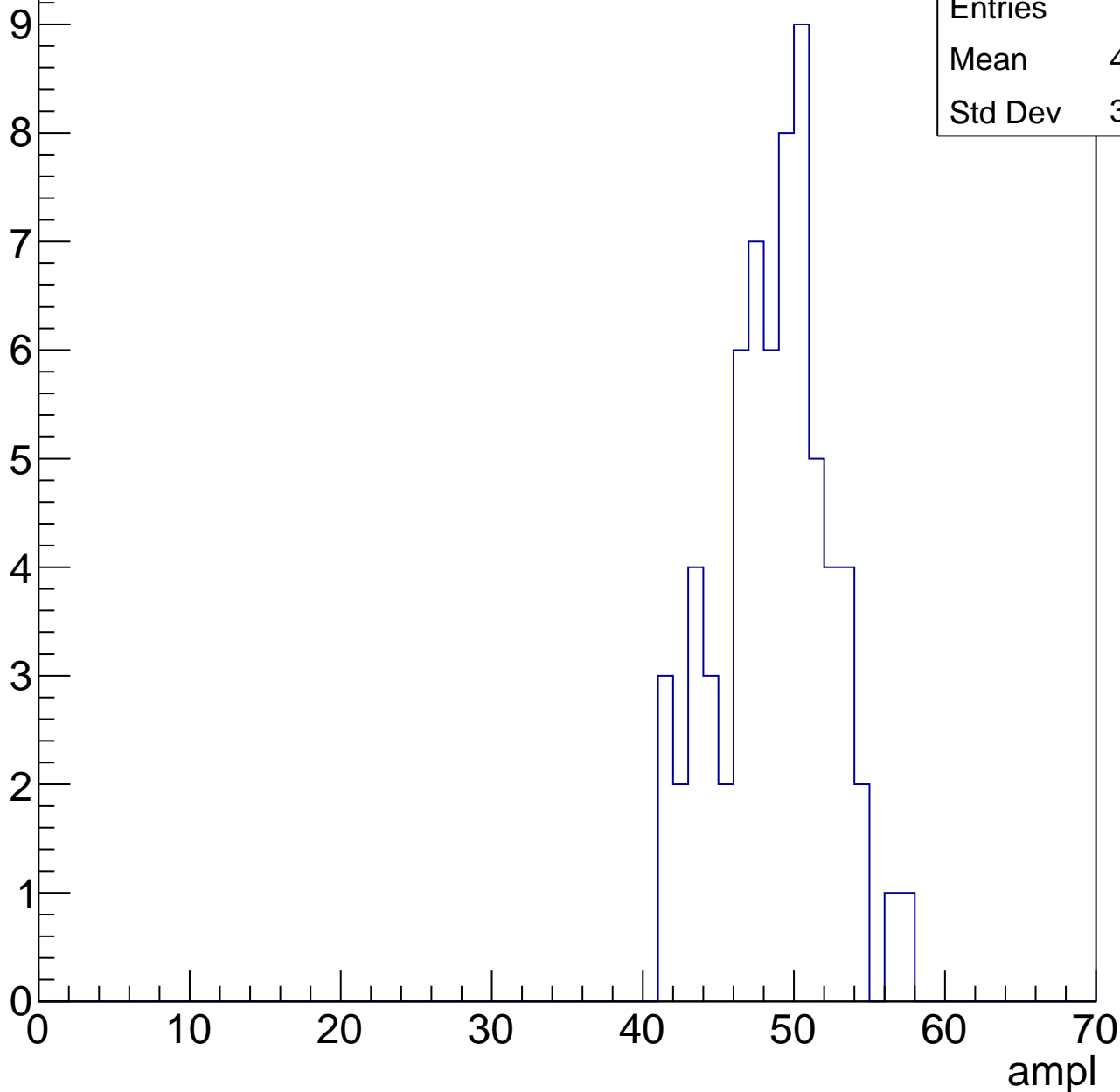


# B0L000S, U18-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.24
Std Dev	3.629

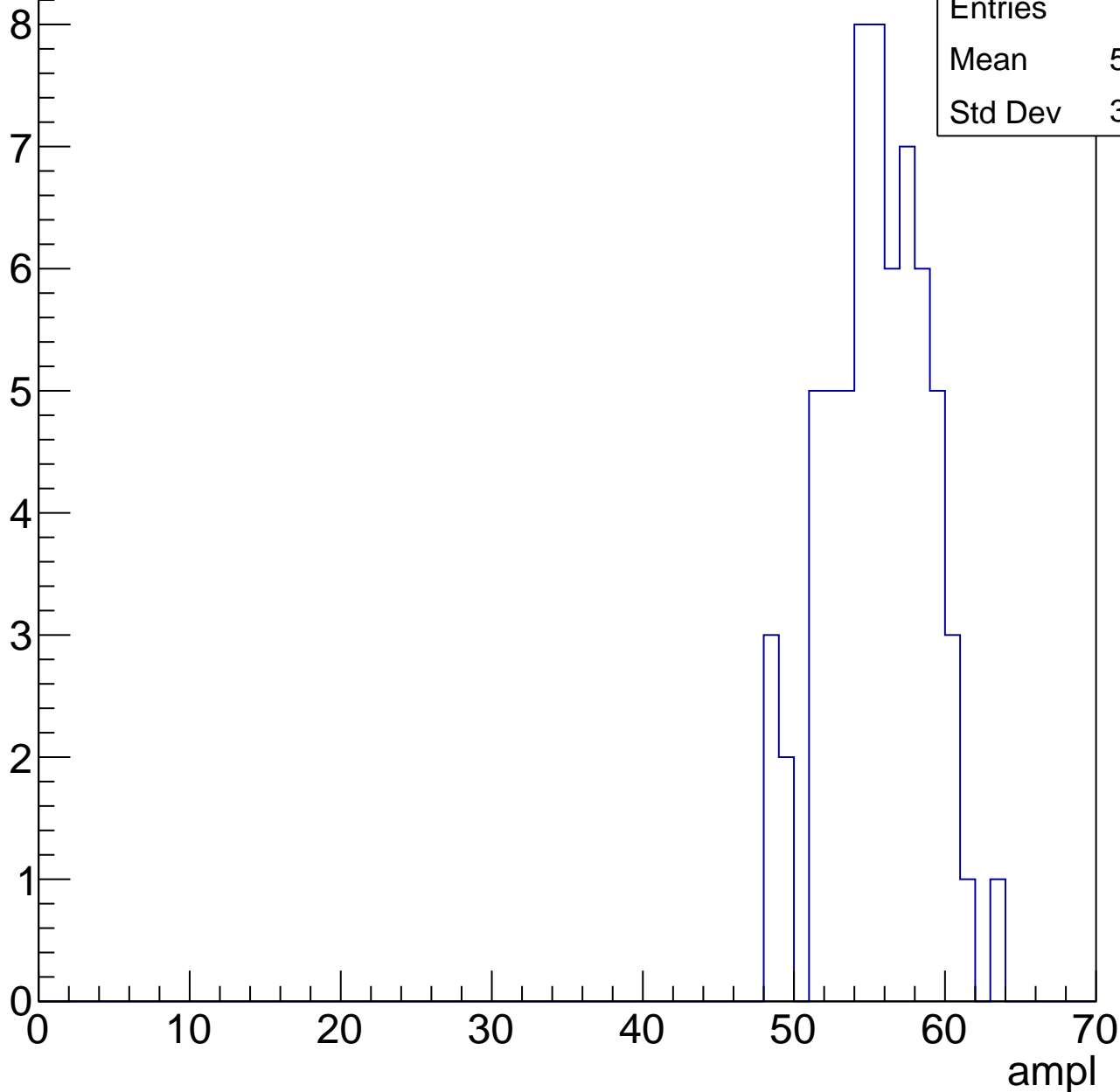


# B0L000S, U18-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	55.02
Std Dev	3.317

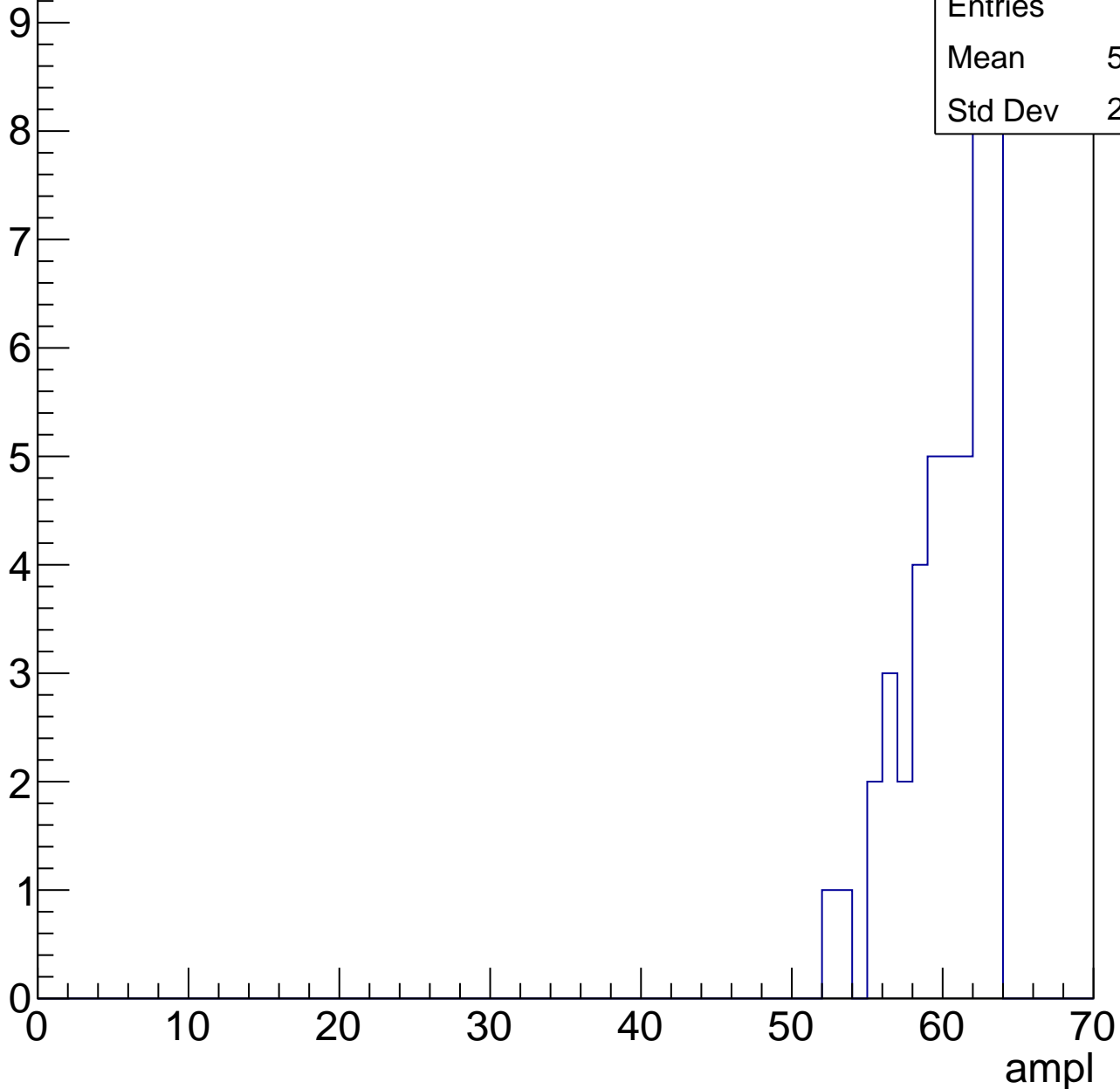


# B0L000S, U18-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

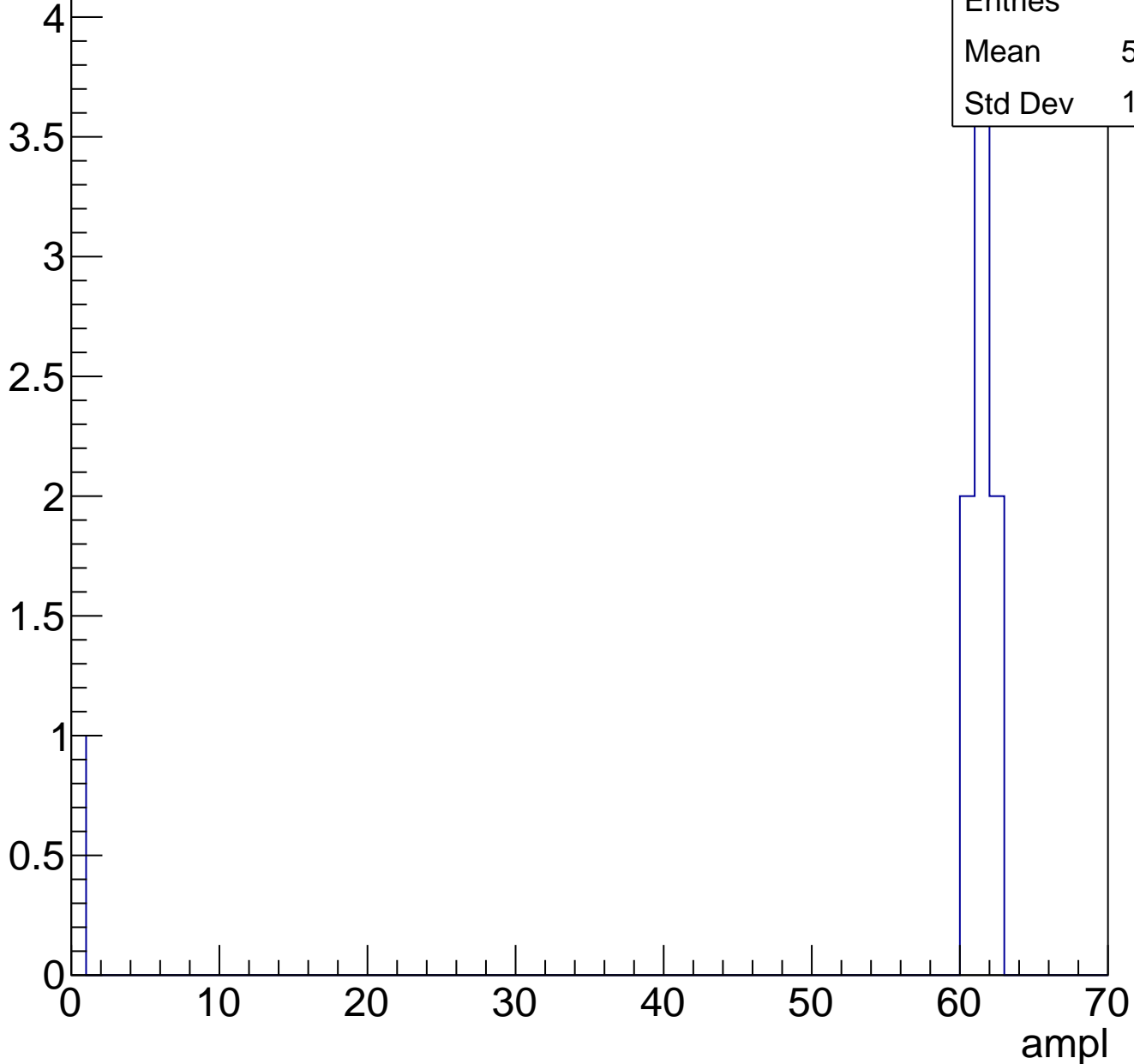
Entries	45
Mean	59.82
Std Dev	2.854



# B0L000S, U18-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	9
Mean	54.22
Std Dev	19.18



# B0L000S, U18-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch90, adc0

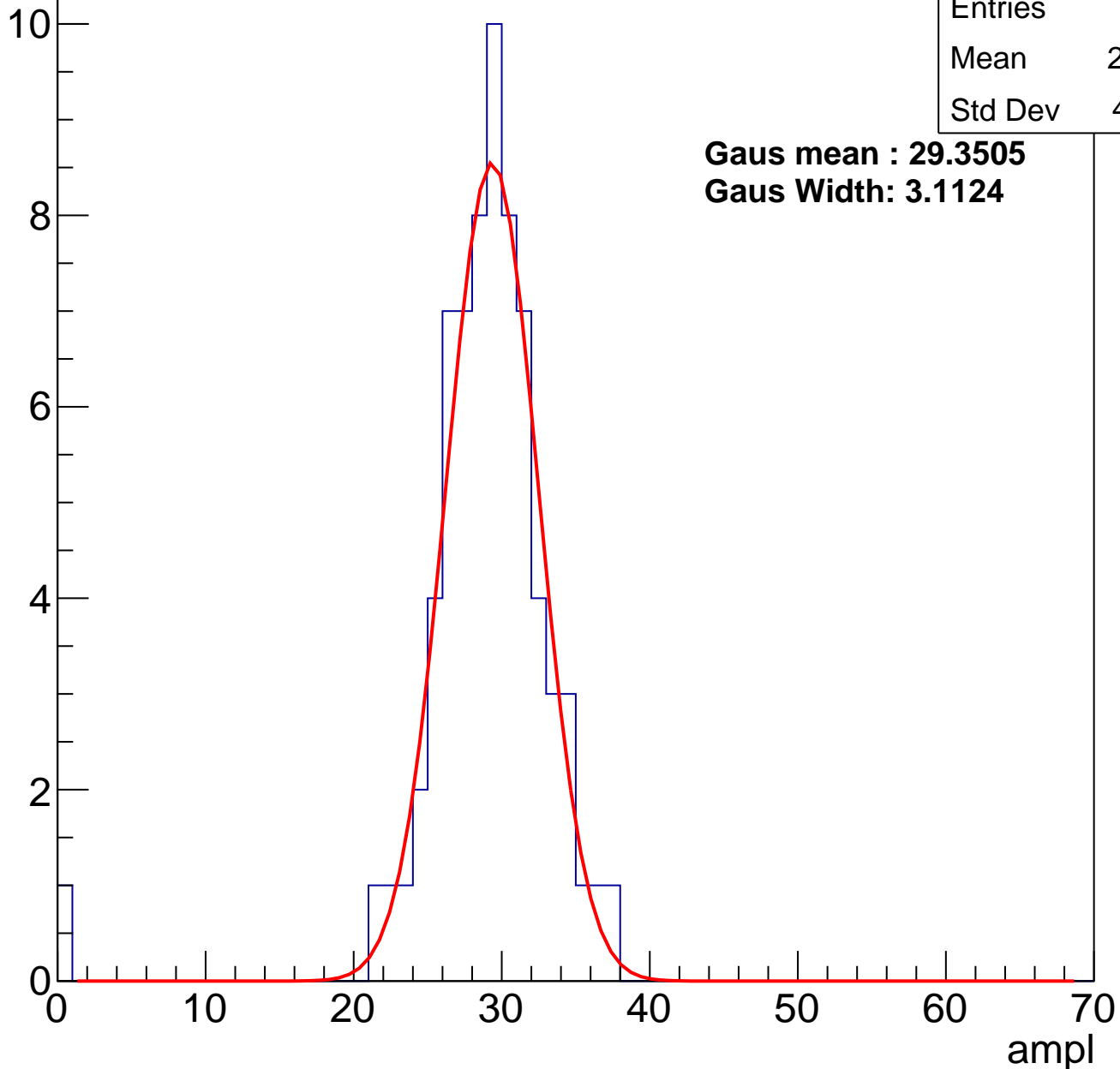
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	28.47
Std Dev	4.671

**Gaus mean : 29.3505**

**Gaus Width: 3.1124**

Entry



# B0L000S, U18-ch90, adc1

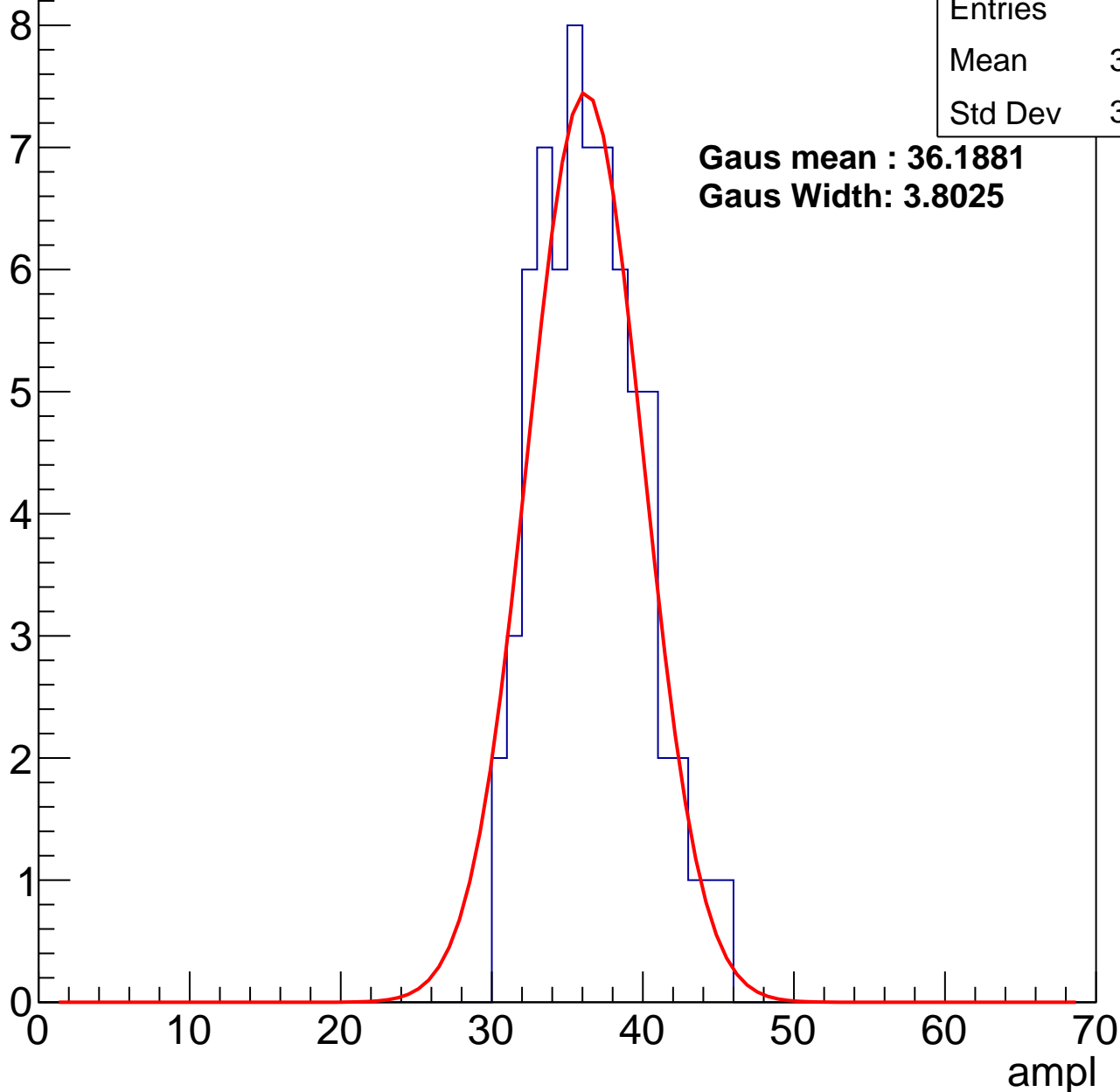
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.12
Std Dev	3.424

**Gaus mean : 36.1881**

**Gaus Width: 3.8025**



# B0L000S, U18-ch90, adc2

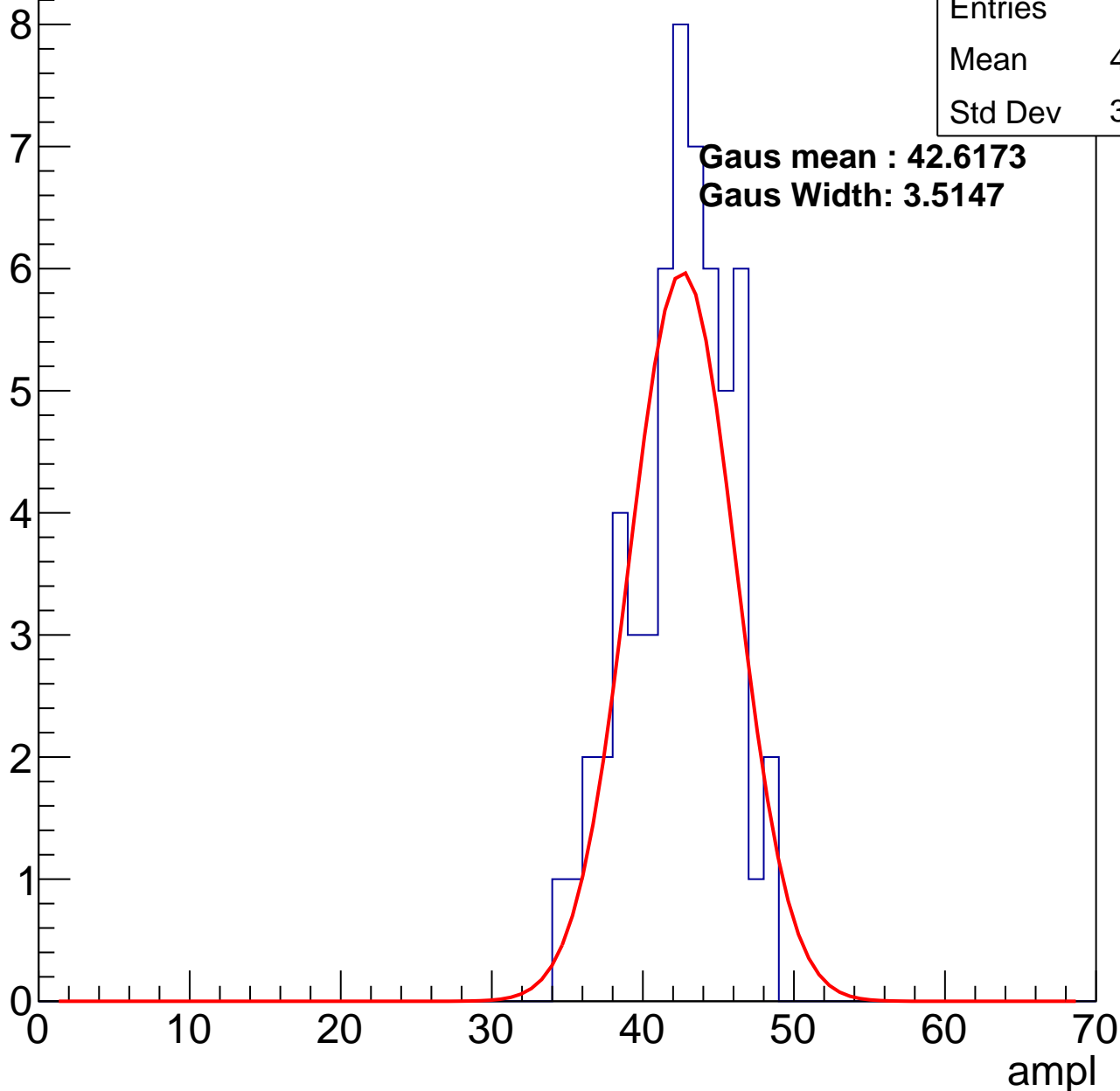
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	42.02
Std Dev	3.279

**Gaus mean : 42.6173**

**Gaus Width: 3.5147**

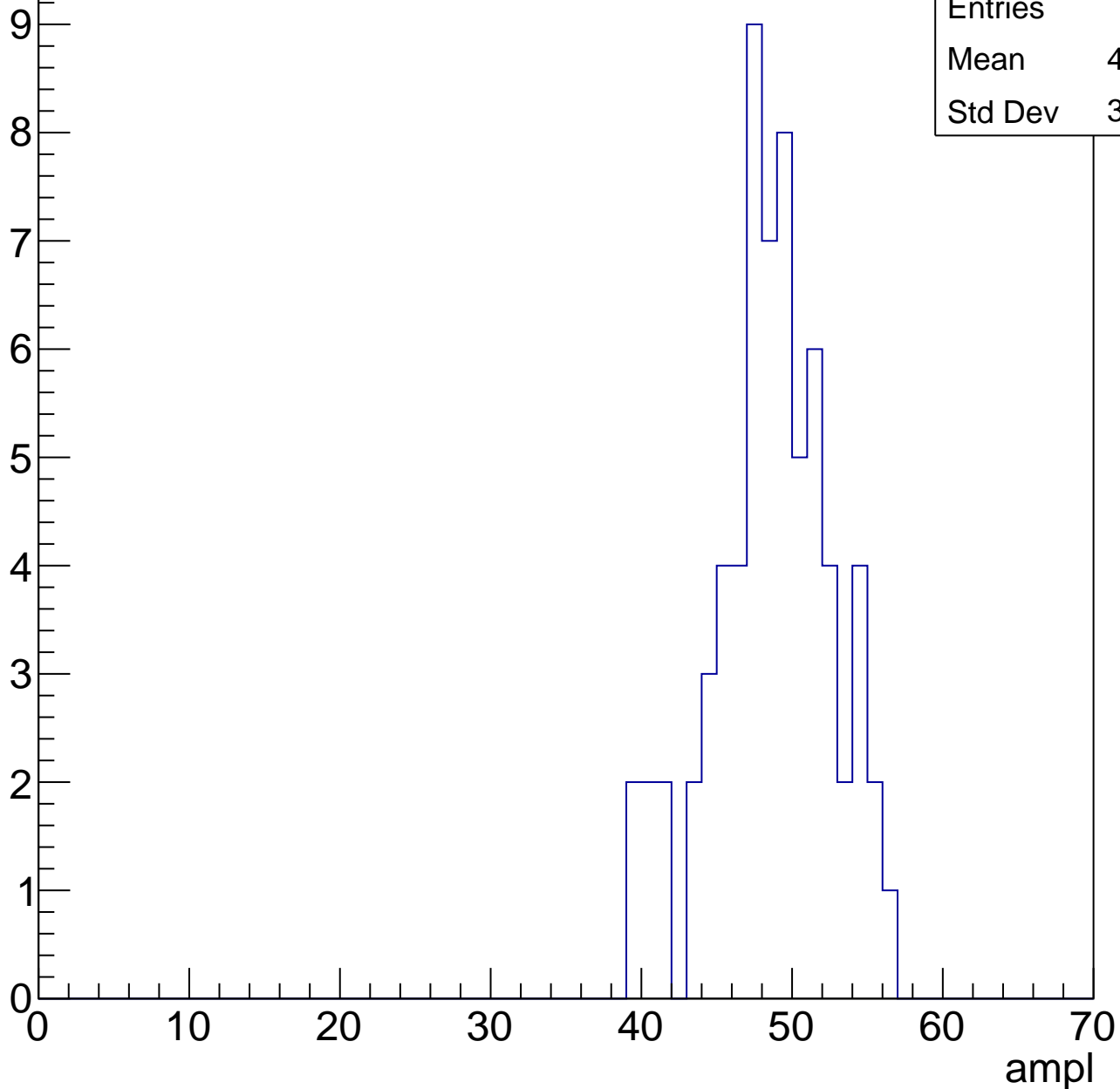


# B0L000S, U18-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.13
Std Dev	3.955

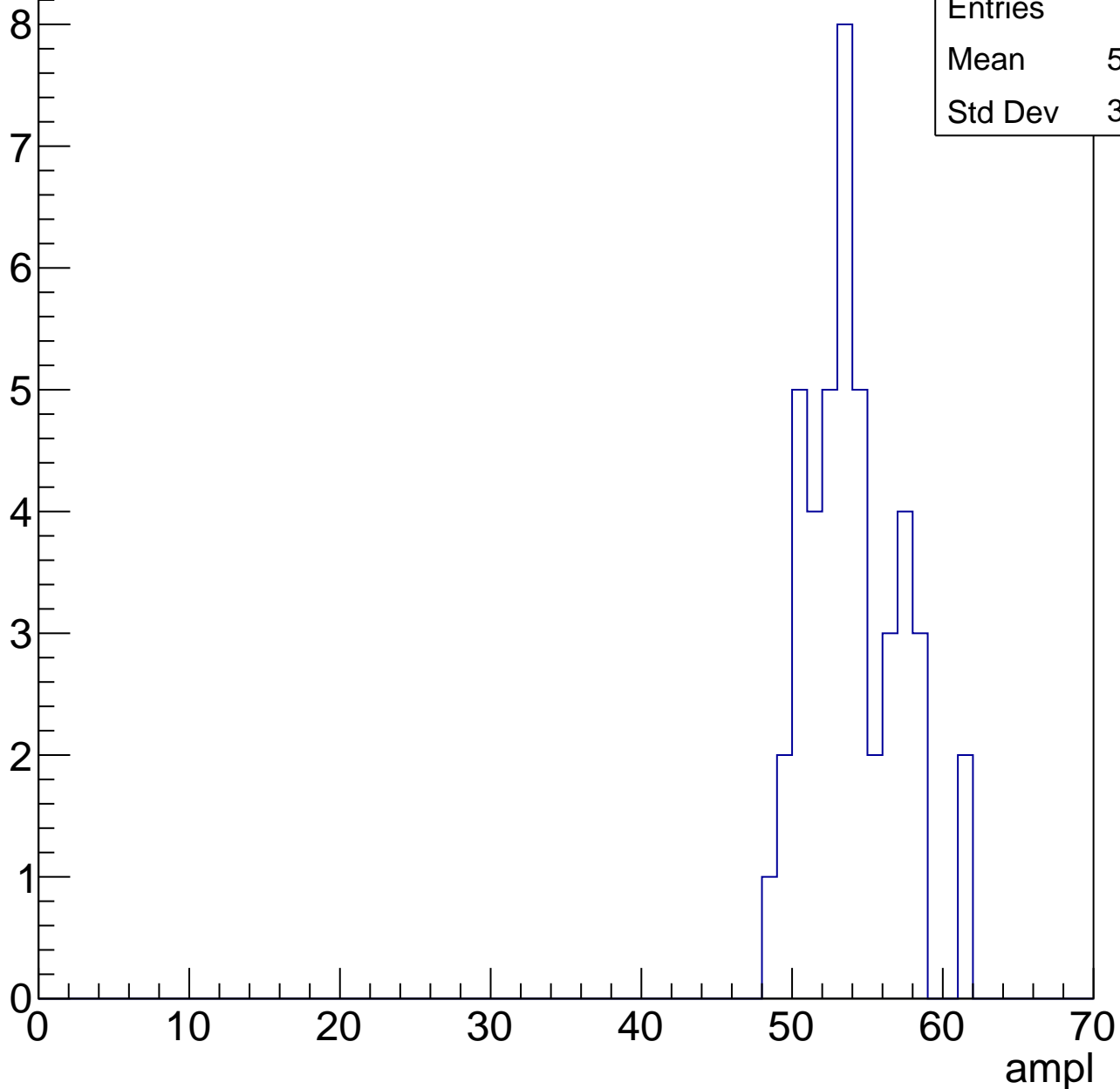


# B0L000S, U18-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

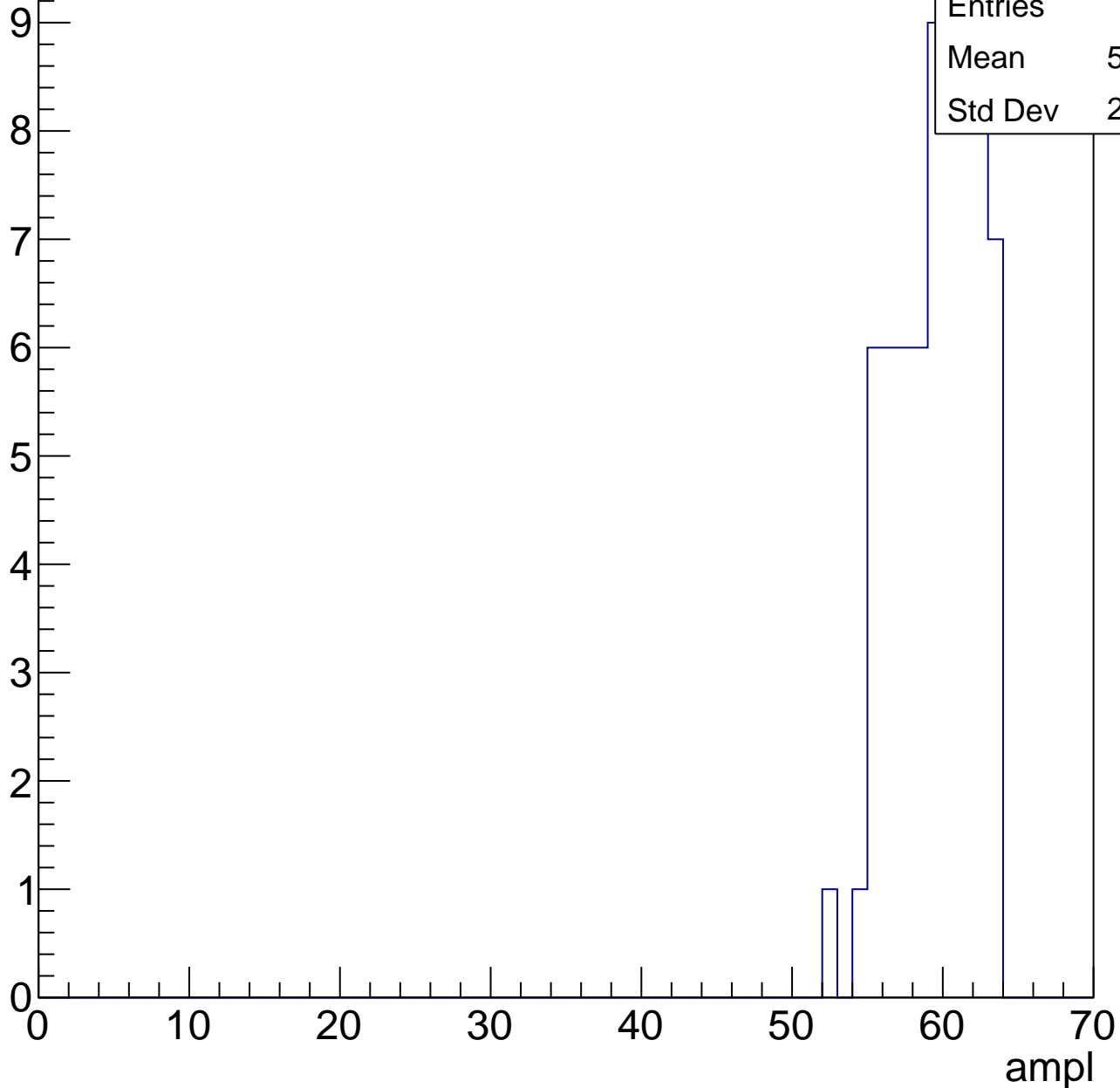
Entries	44
Mean	53.55
Std Dev	3.078



# B0L000S, U18-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

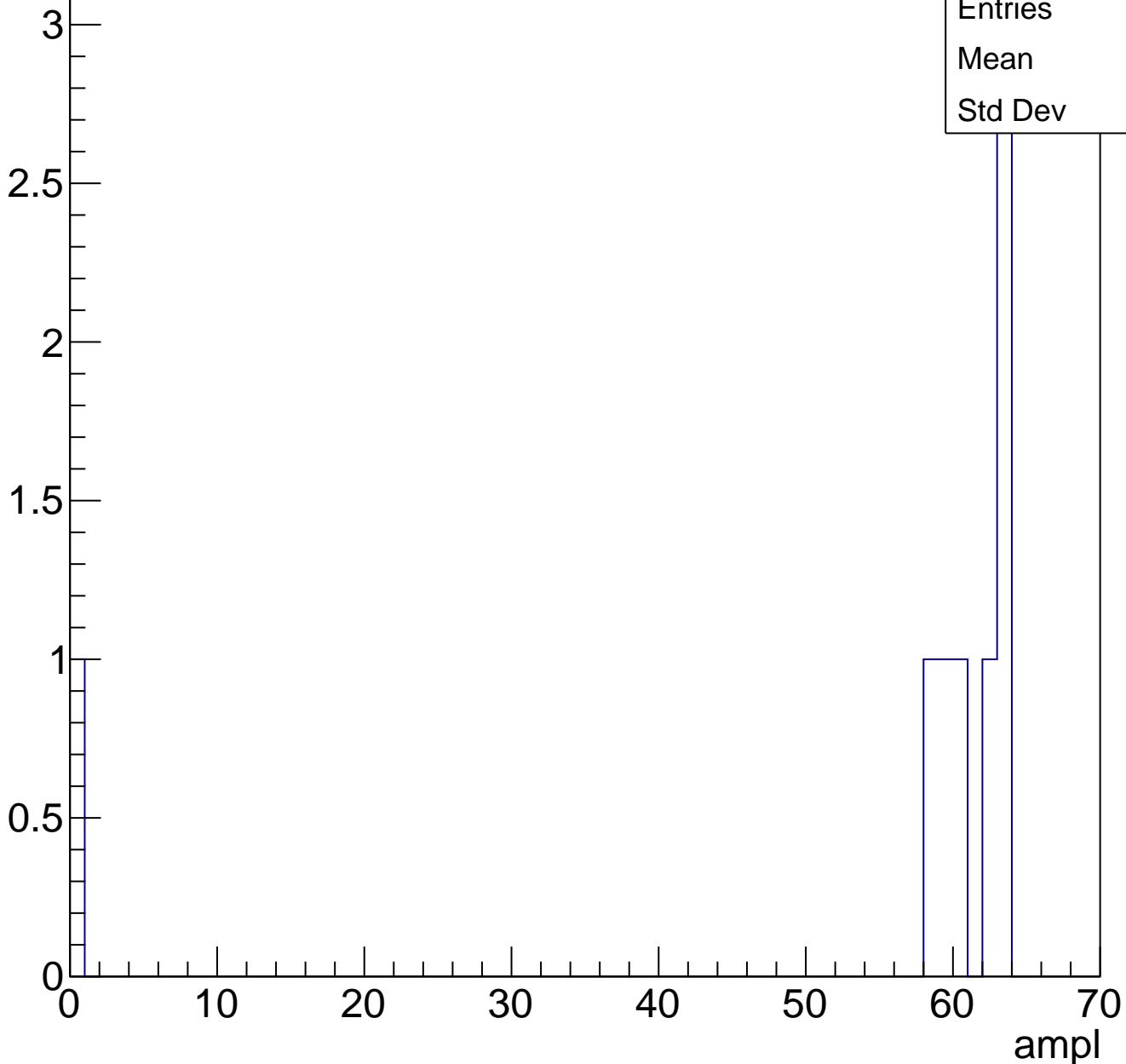
Entry



# B0L000S, U18-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B0L000S, U18-ch91, adc0

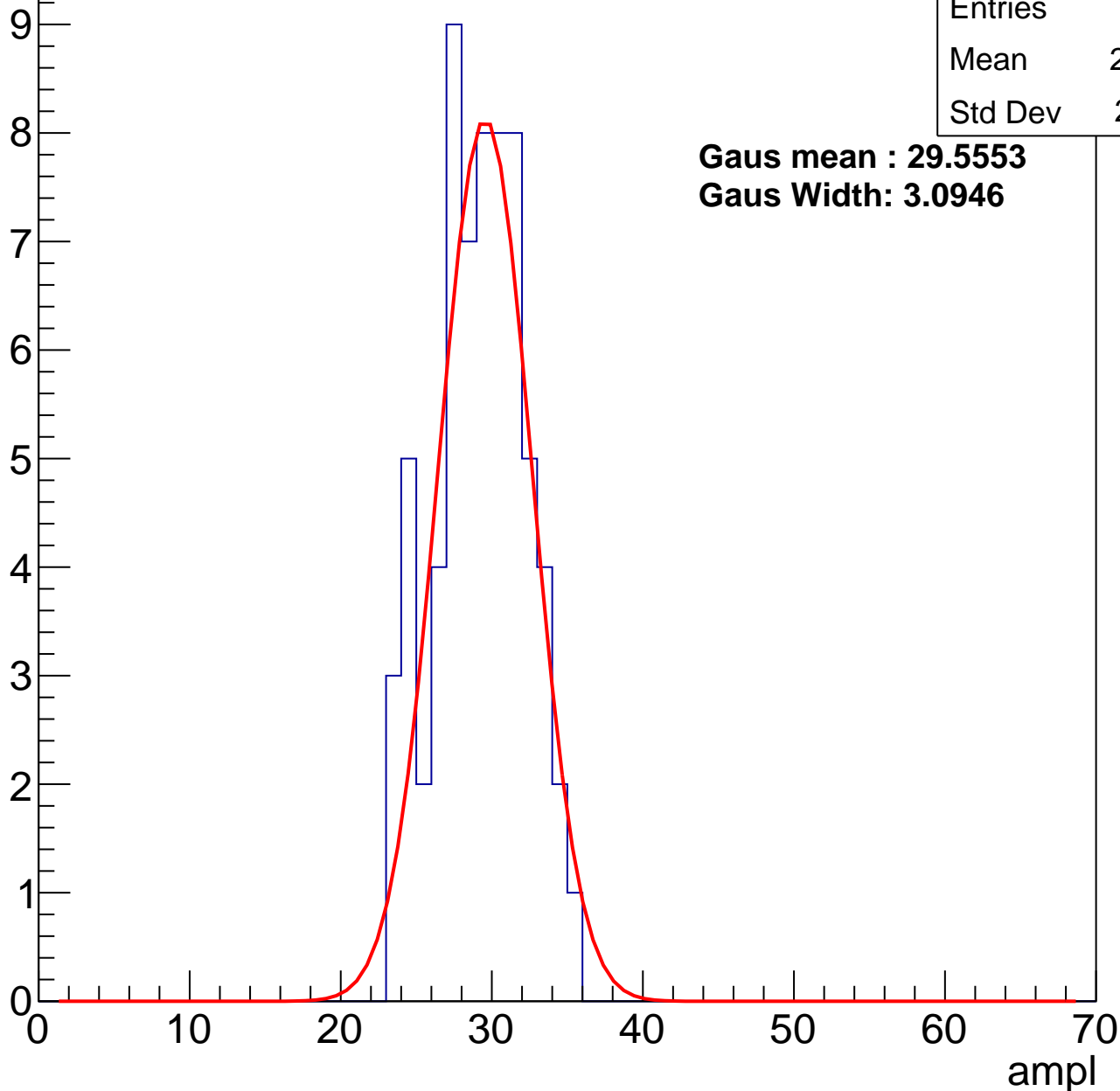
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.74
Std Dev	2.951

**Gaus mean : 29.5553**

**Gaus Width: 3.0946**



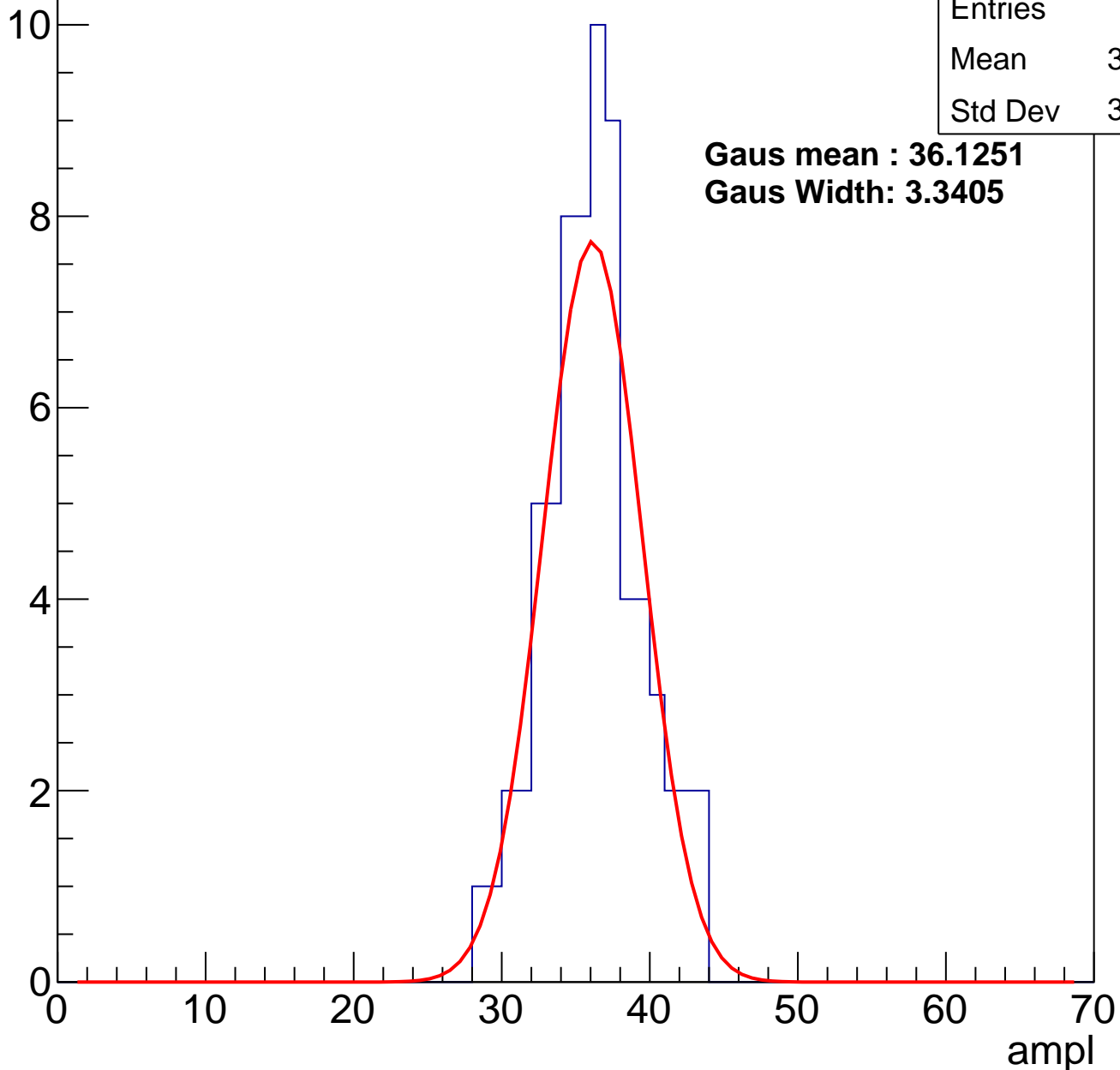
# B0L000S, U18-ch91, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	35.72
Std Dev	3.262

**Gaus mean : 36.1251**  
**Gaus Width: 3.3405**

Entry

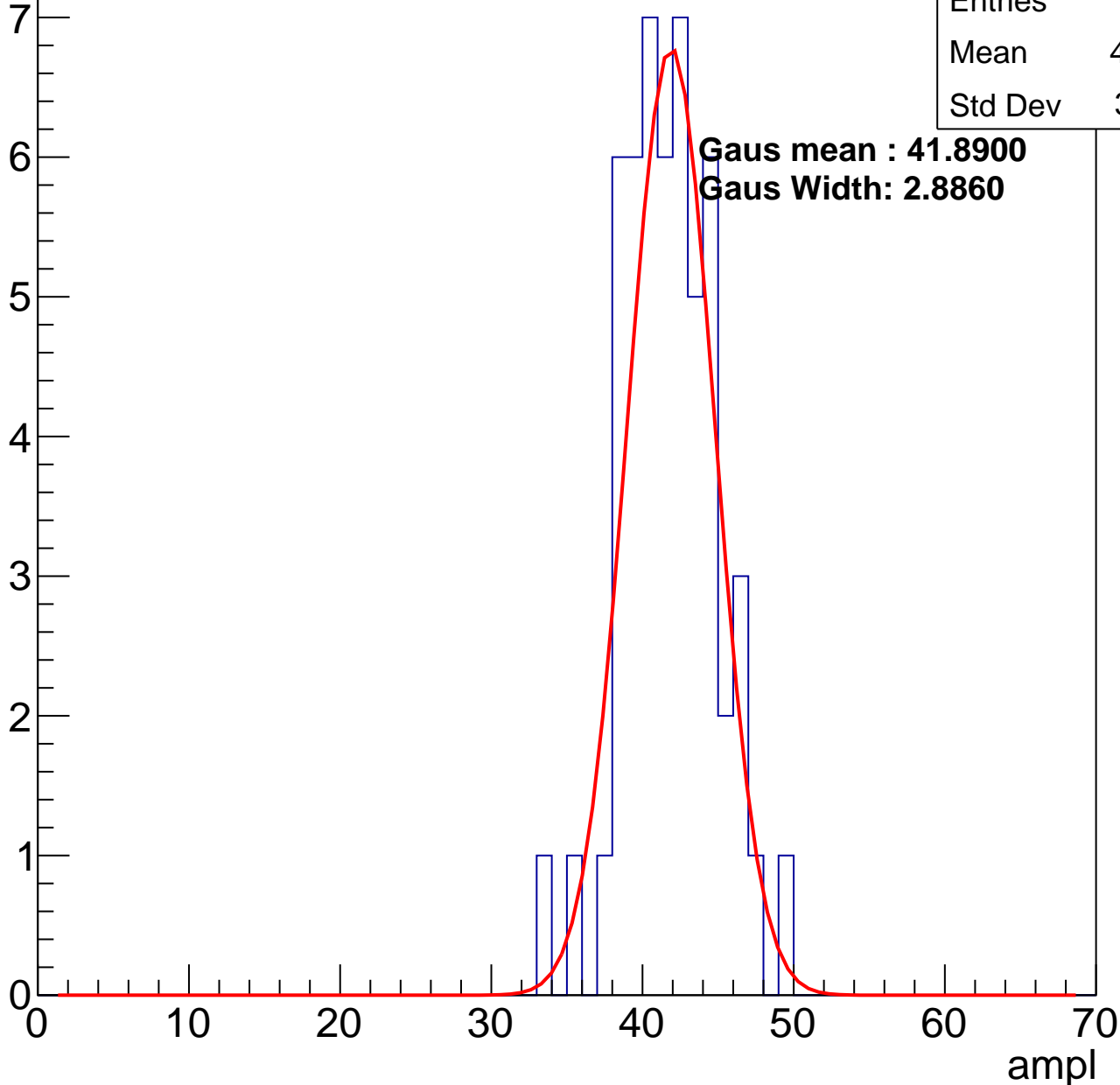


# B0L000S, U18-ch91, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

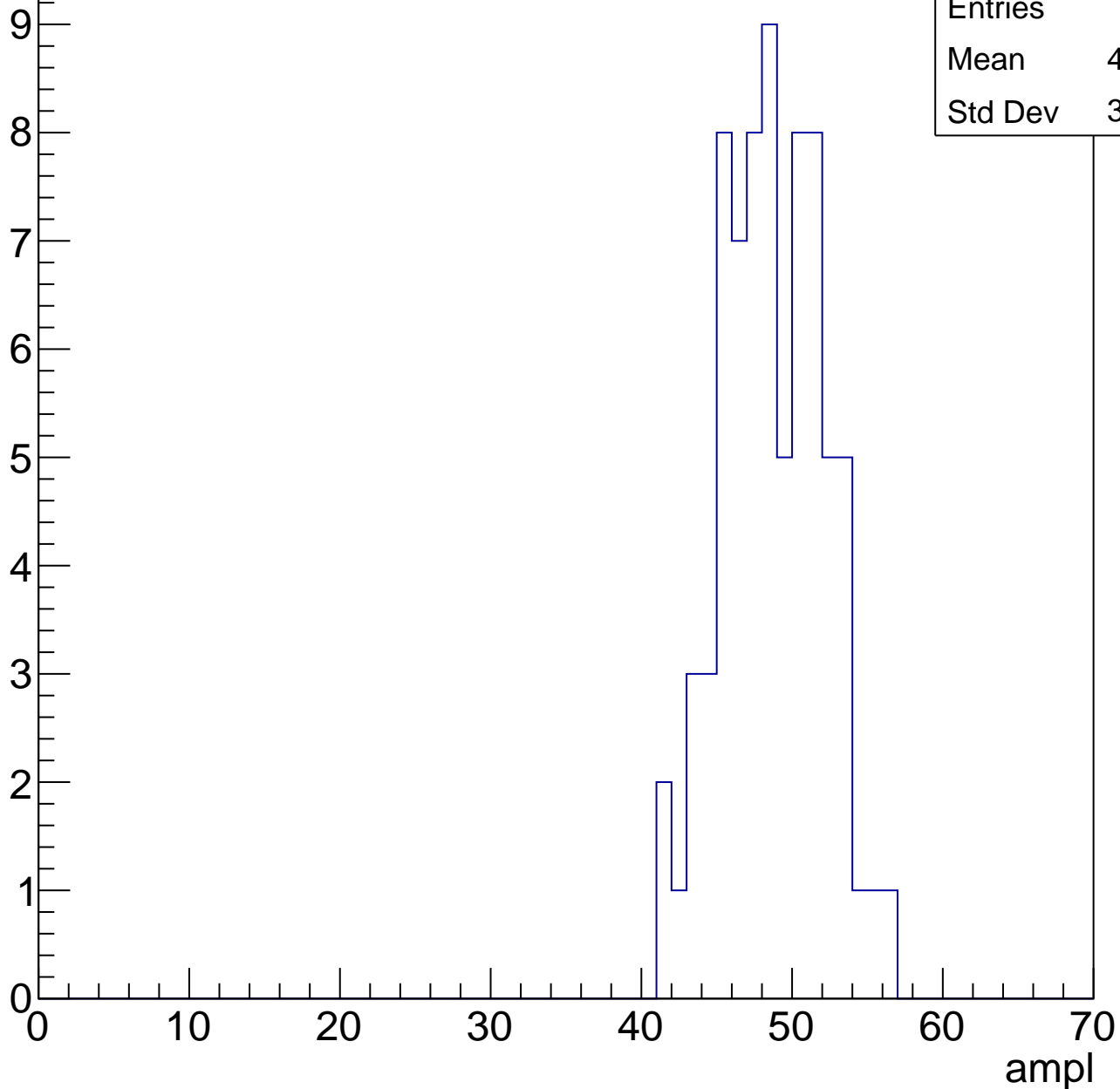
Entries	53
Mean	41.32
Std Dev	3.021



# B0L000S, U18-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



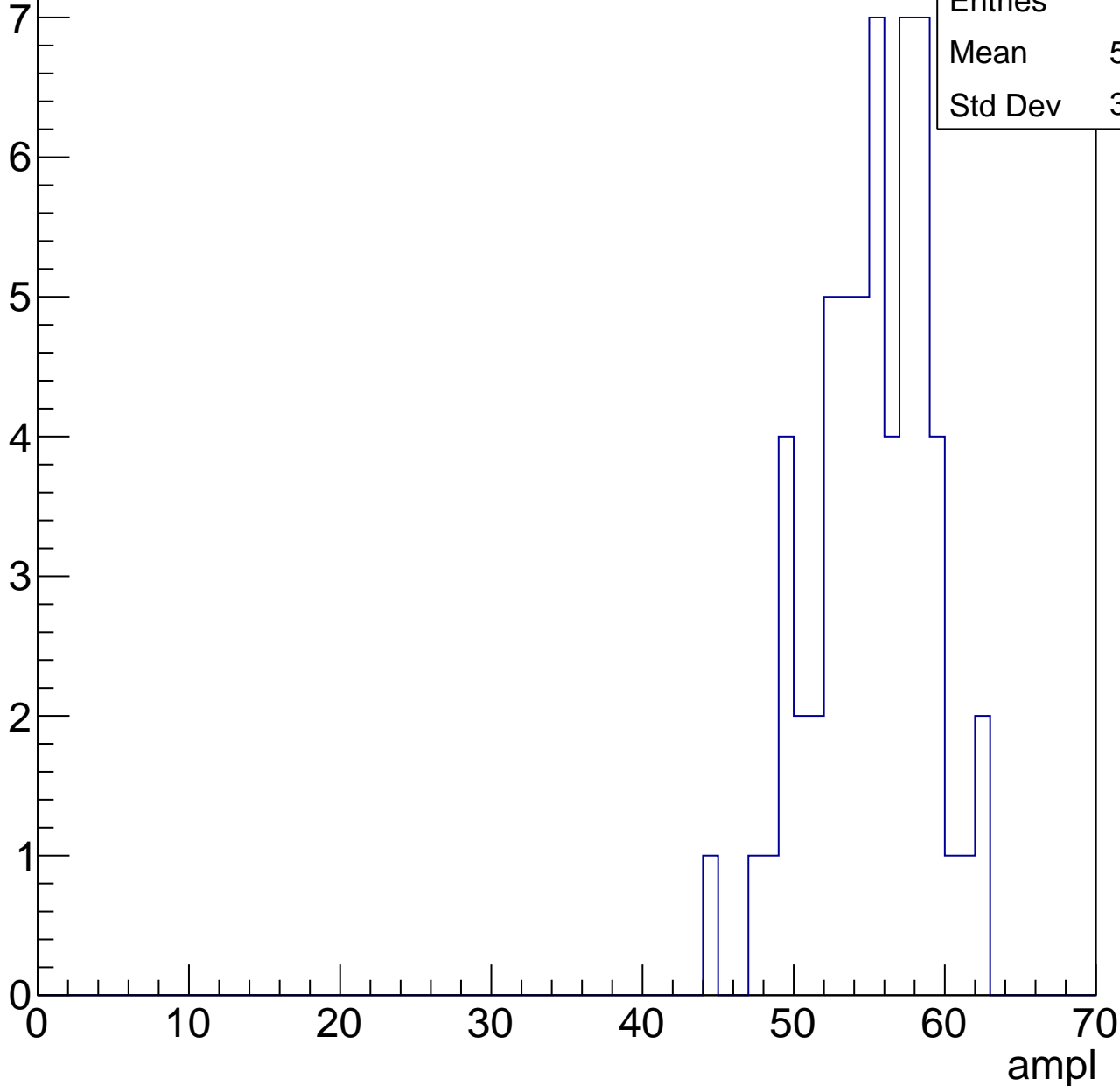
Entries	75
Mean	48.24
Std Dev	3.314

# B0L000S, U18-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.69
Std Dev	3.779

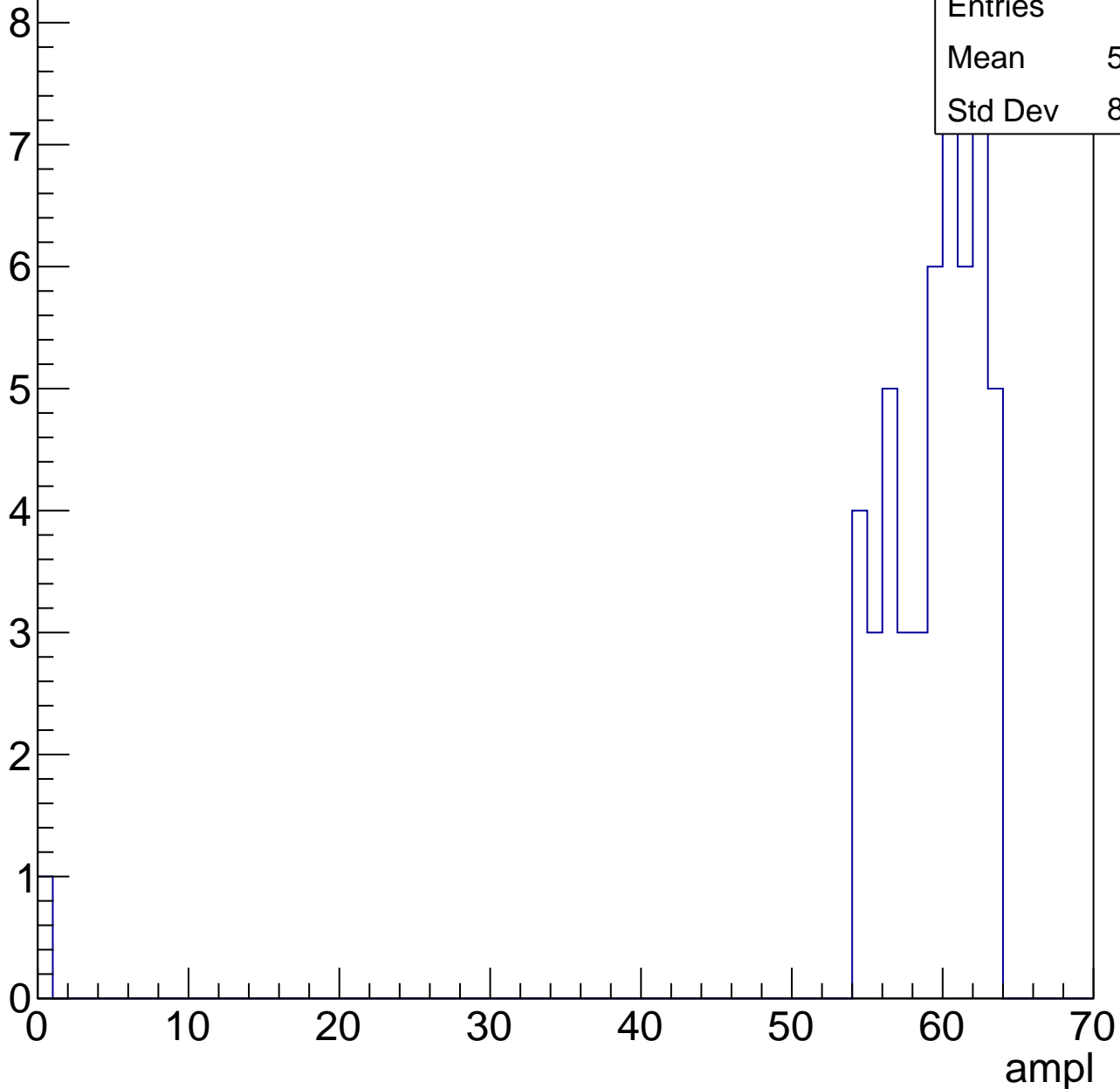


# B0L000S, U18-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.02
Std Dev	8.574

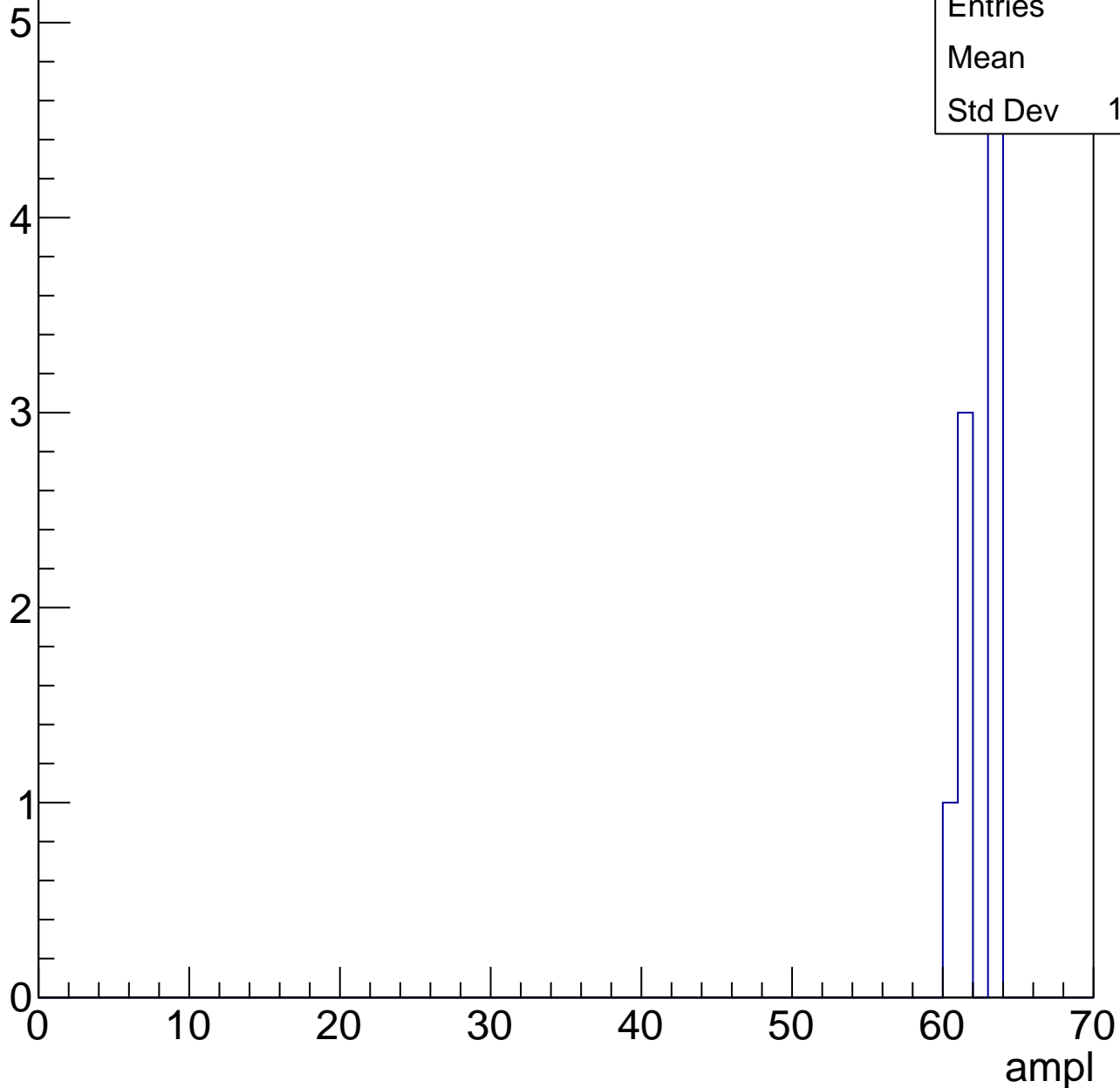


# B0L000S, U18-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62
Std Dev	1.155





# B0L000S, U18-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch92, adc0

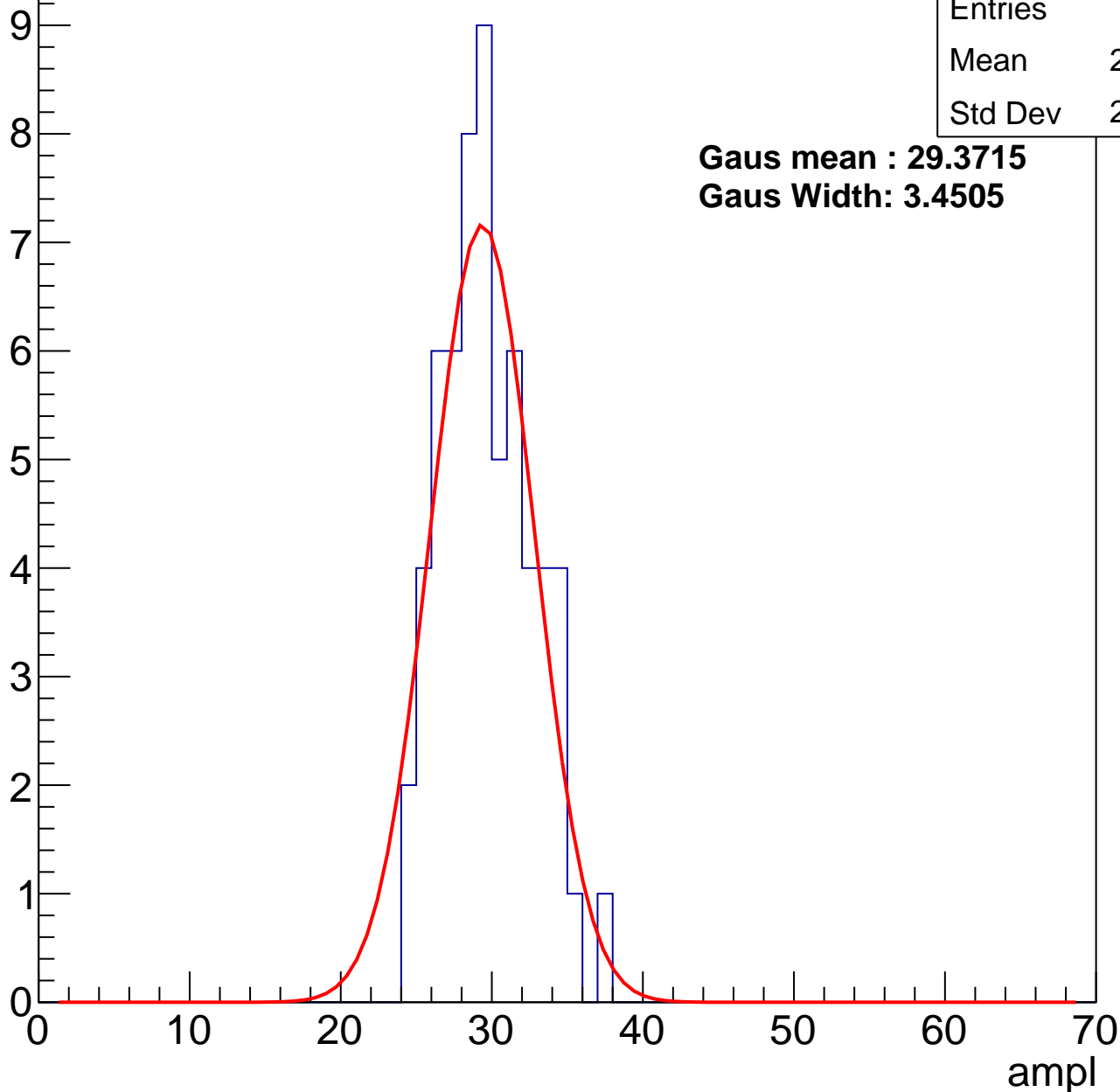
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	29.25
Std Dev	2.959

**Gaus mean : 29.3715**

**Gaus Width: 3.4505**



# B0L000S, U18-ch92, adc1

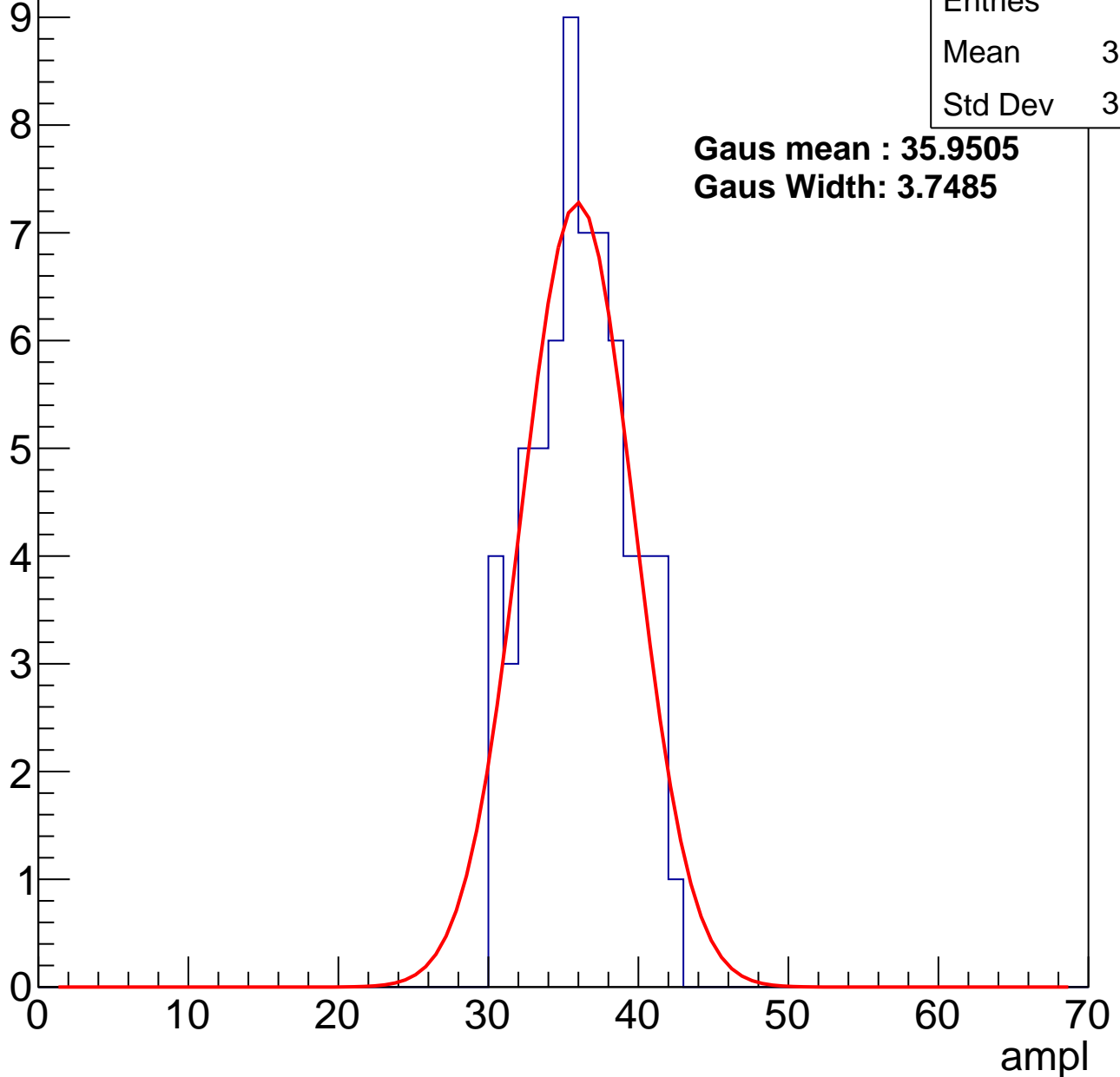
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.66
Std Dev	3.129

**Gaus mean : 35.9505**

**Gaus Width: 3.7485**



# B0L000S, U18-ch92, adc2

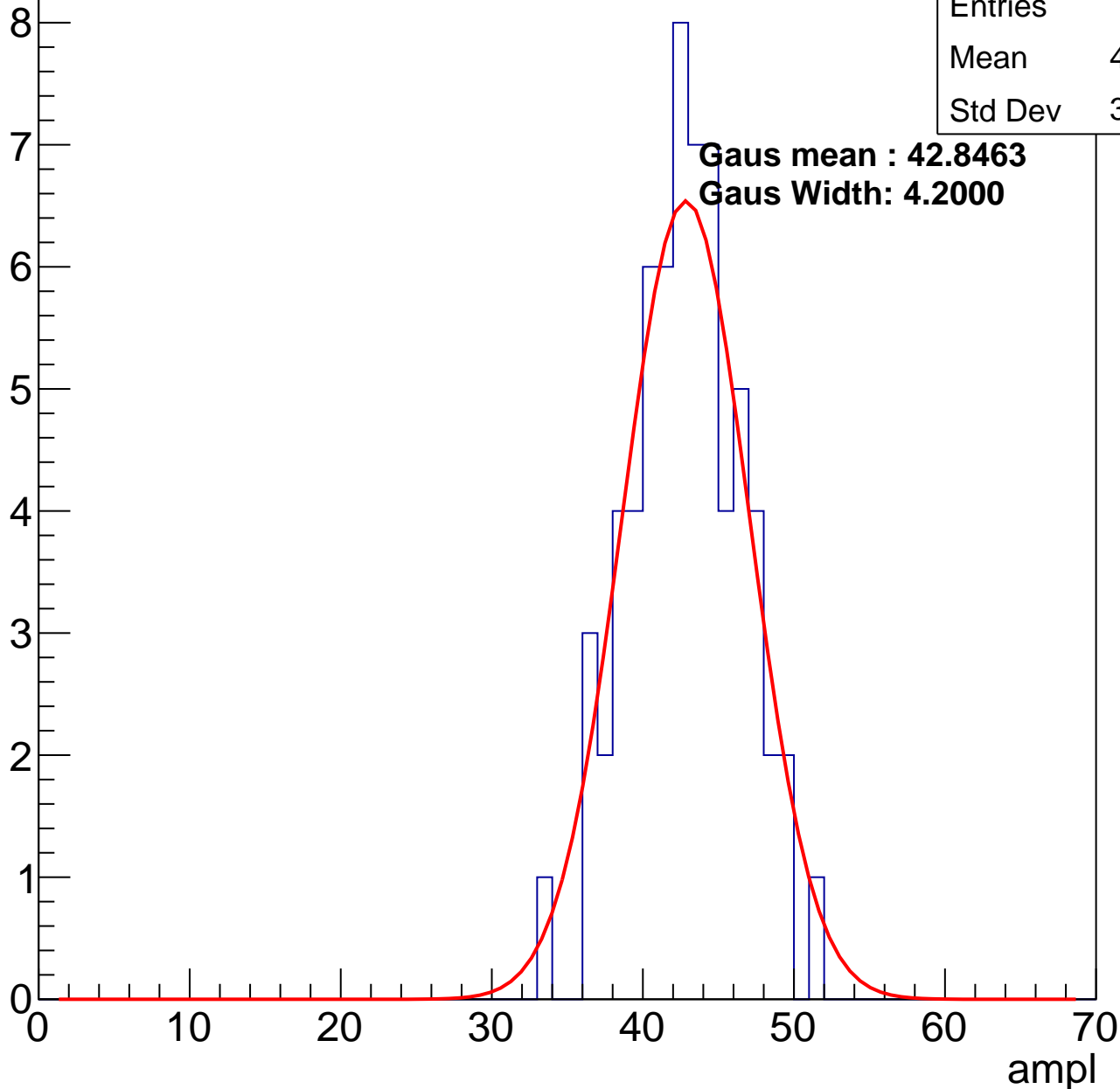
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	42.38
Std Dev	3.613

**Gaus mean : 42.8463**

**Gaus Width: 4.2000**

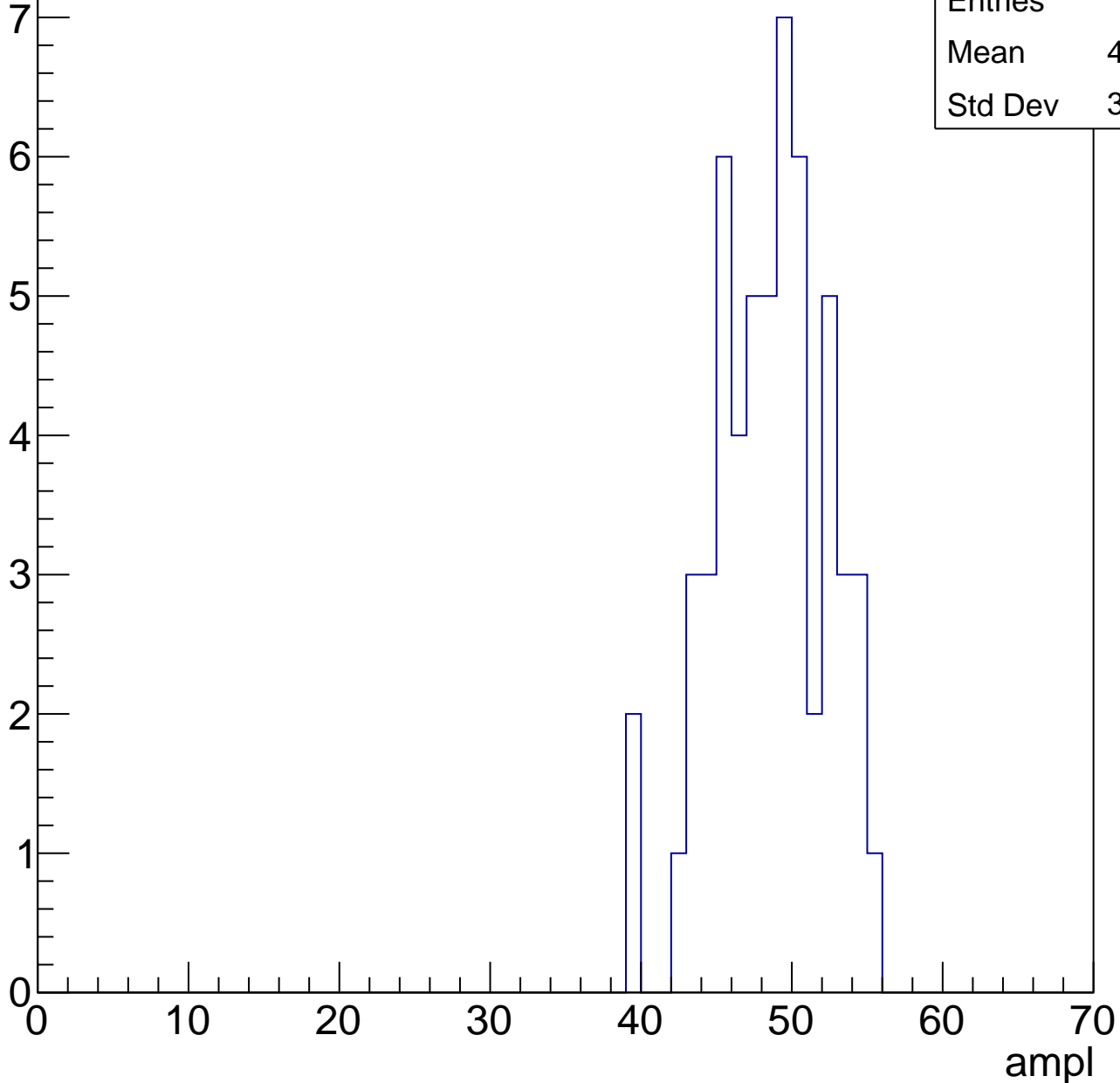


# B0L000S, U18-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	48.05
Std Dev	3.676

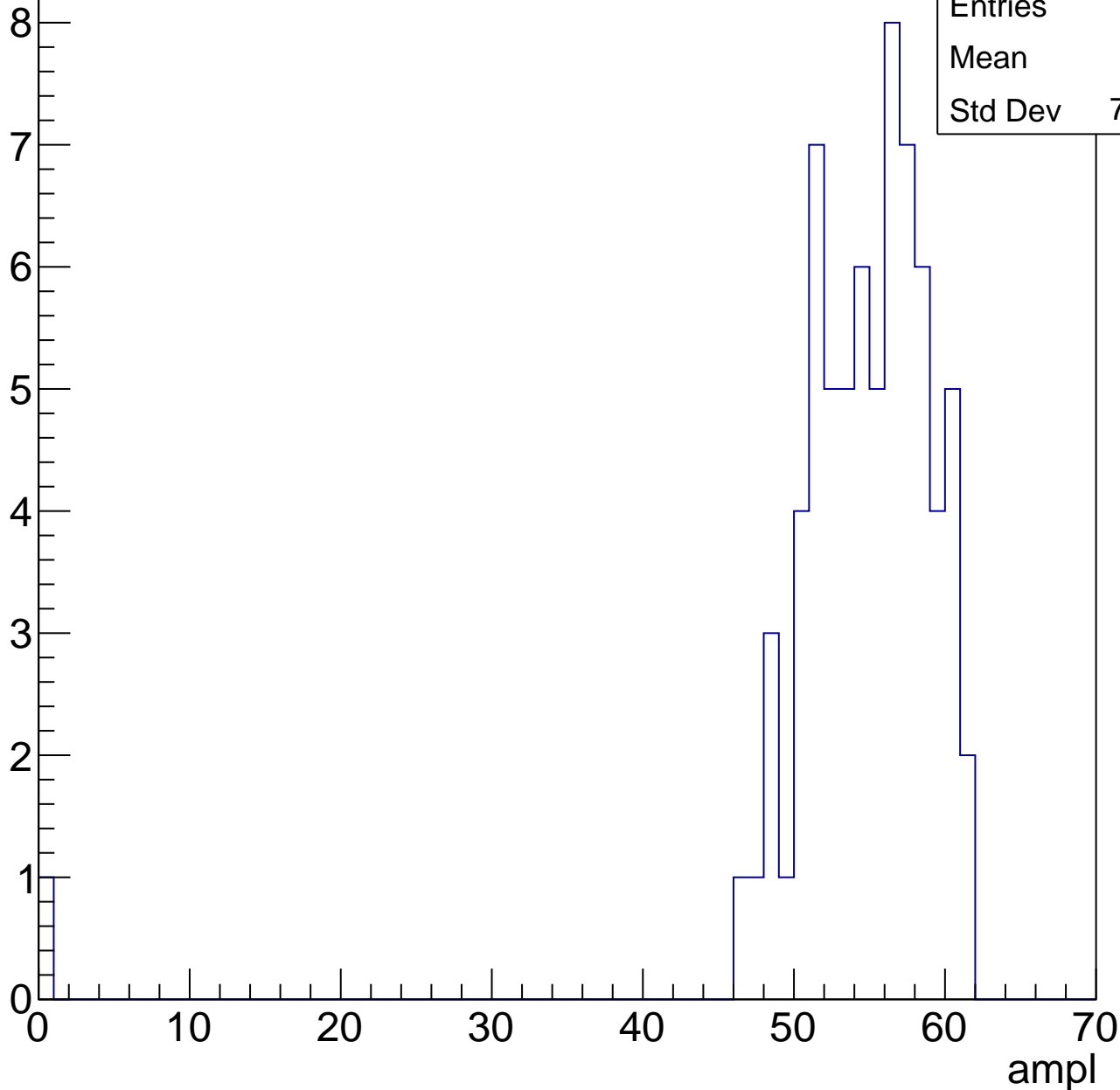


# B0L000S, U18-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	53.8
Std Dev	7.397

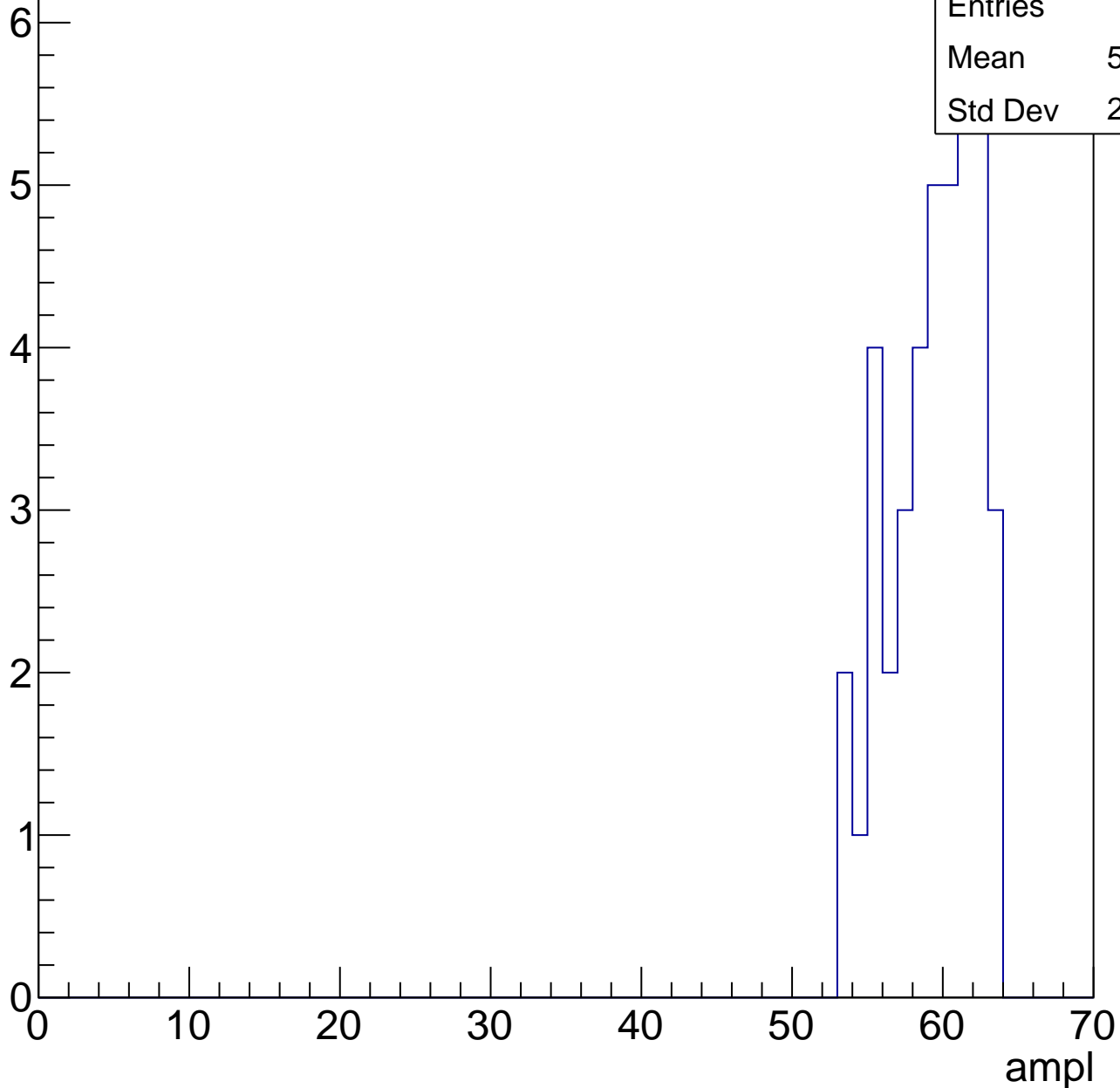


# B0L000S, U18-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

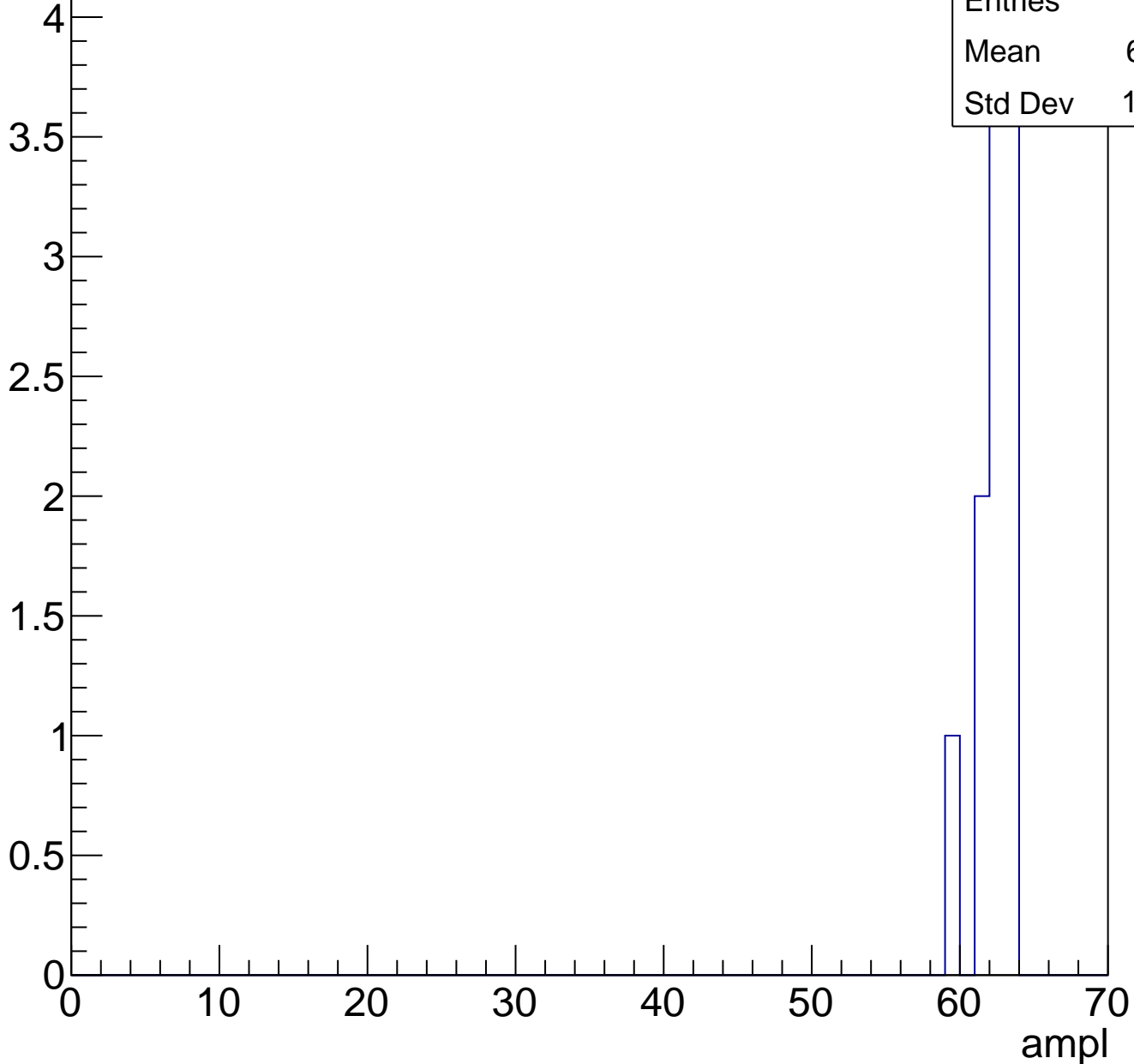
Entries	41
Mean	58.95
Std Dev	2.819



# B0L000S, U18-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



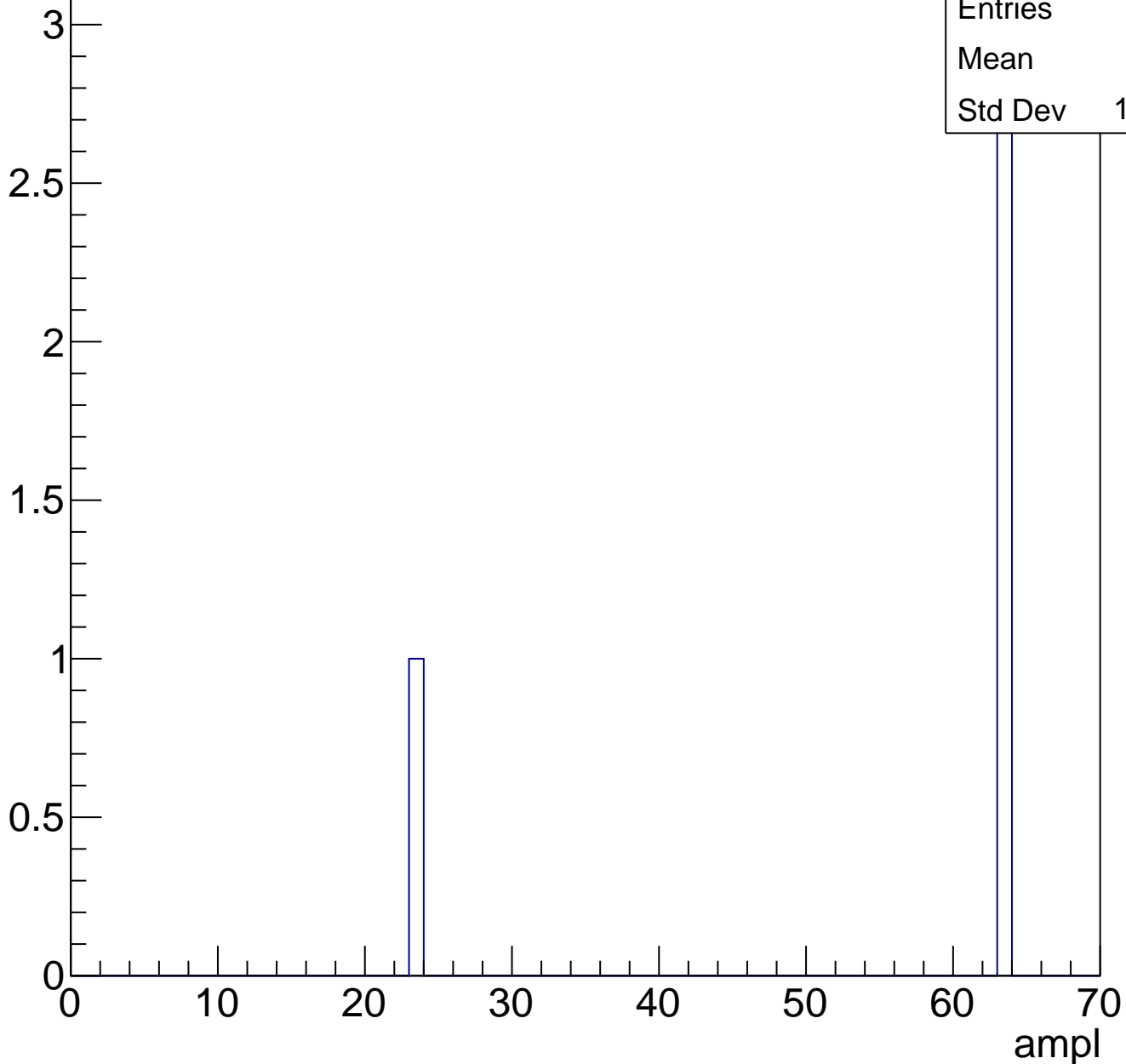
Entries	11
Mean	61.91
Std Dev	1.164



# B0L000S, U18-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch93, adc0

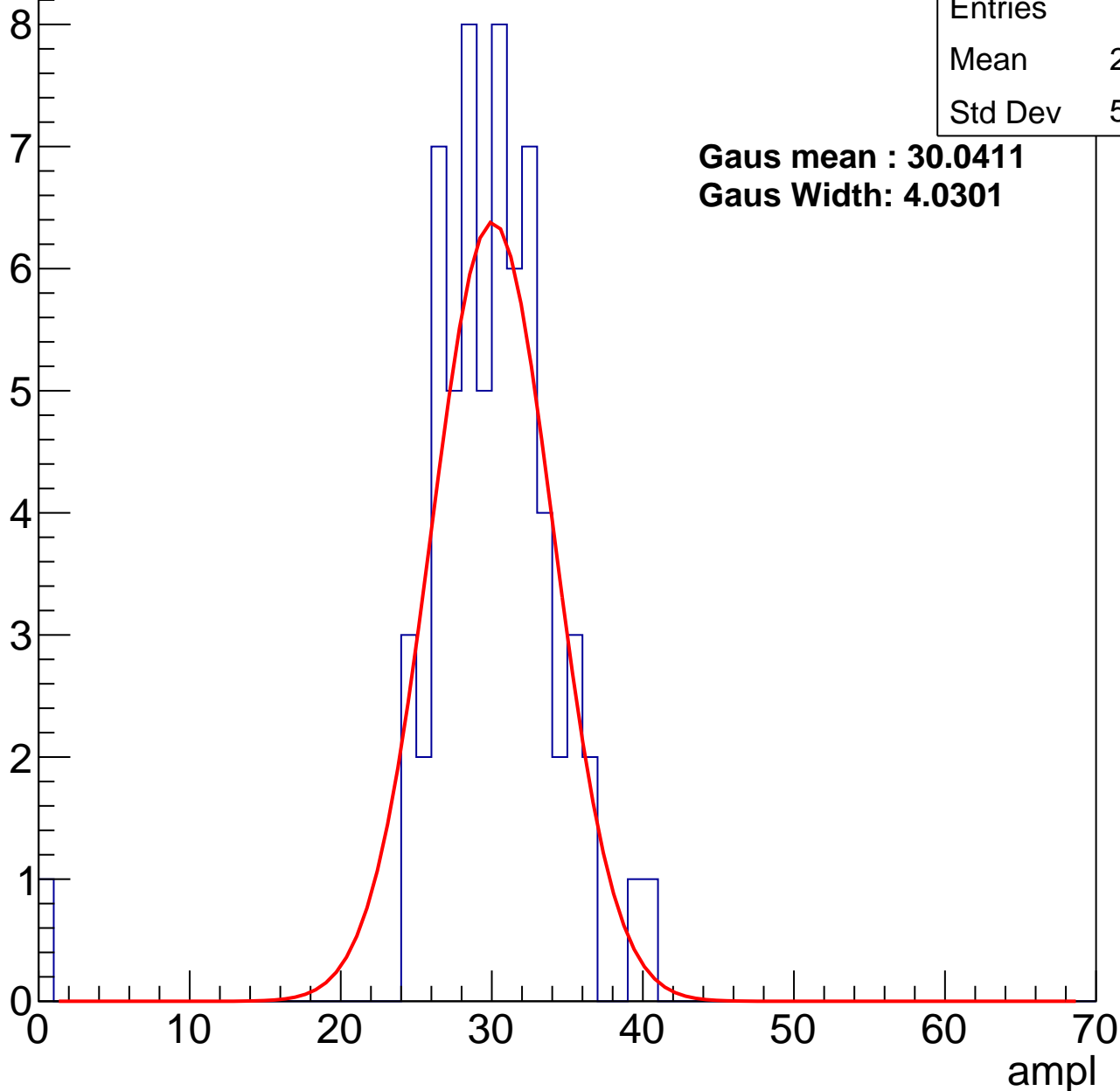
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.45
Std Dev	5.057

**Gaus mean : 30.0411**

**Gaus Width: 4.0301**



# B0L000S, U18-ch93, adc1

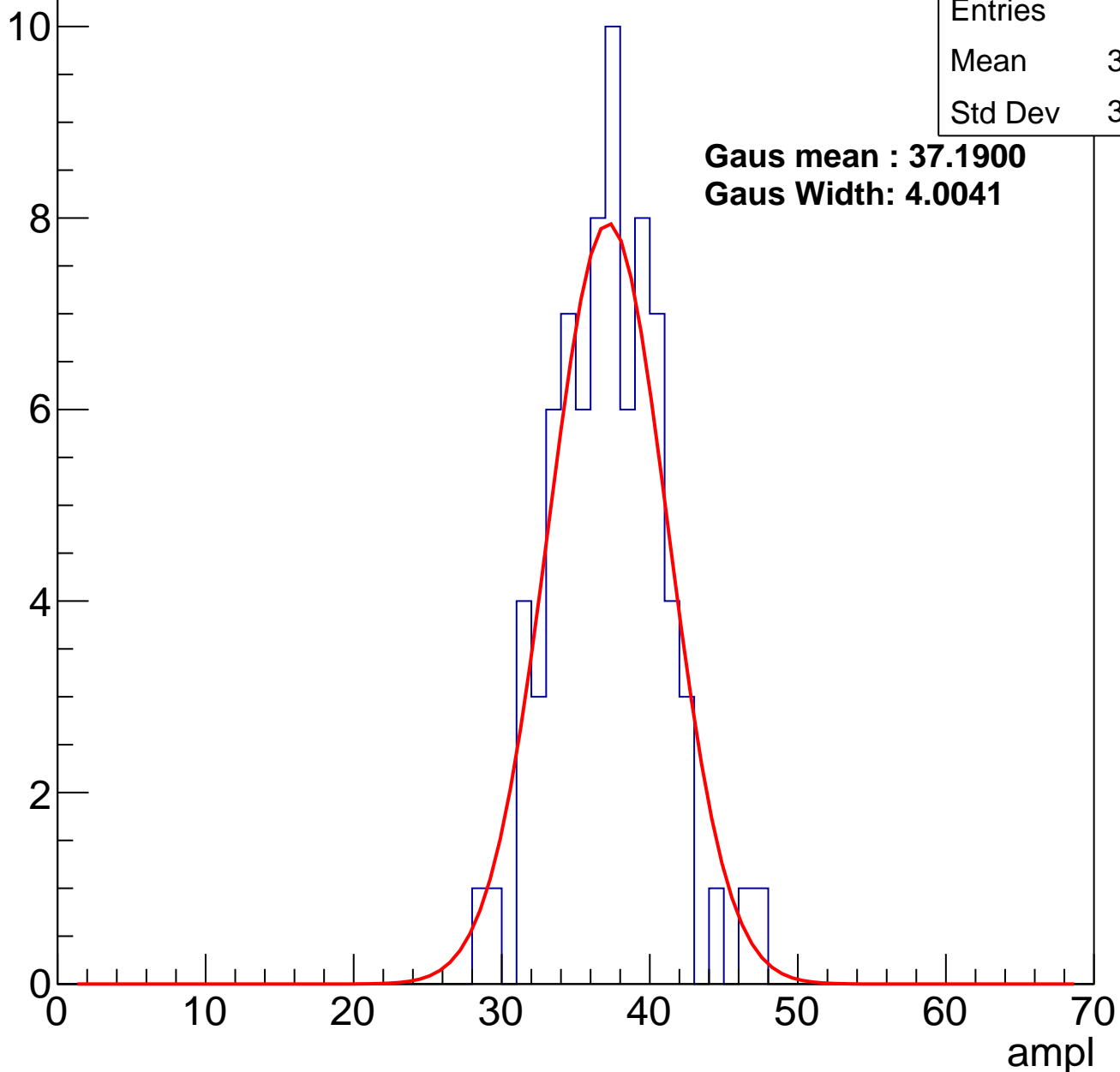
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	36.73
Std Dev	3.638

**Gaus mean : 37.1900**

**Gaus Width: 4.0041**

Entry



# B0L000S, U18-ch93, adc2

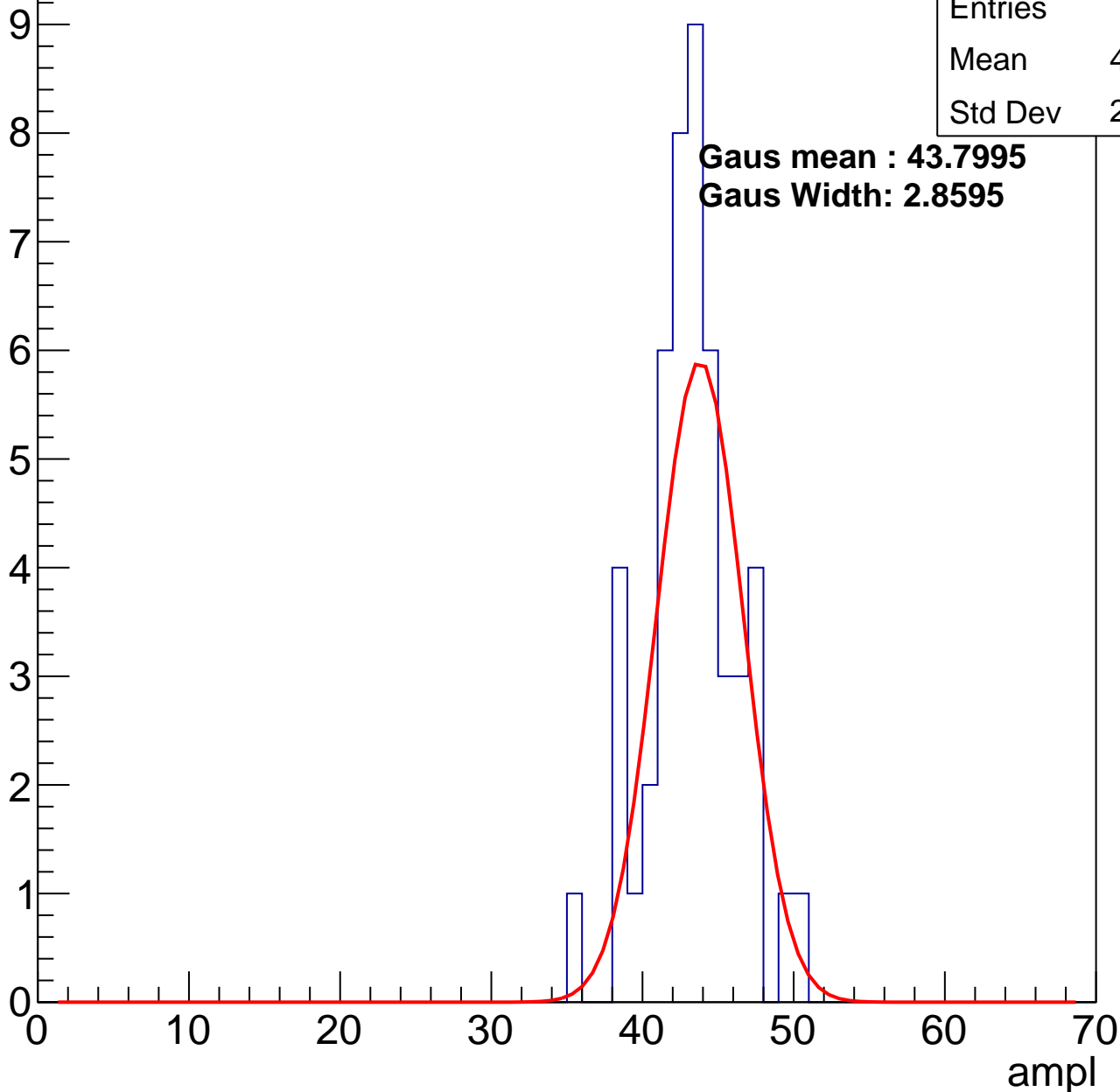
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	42.84
Std Dev	2.937

**Gaus mean : 43.7995**

**Gaus Width: 2.8595**

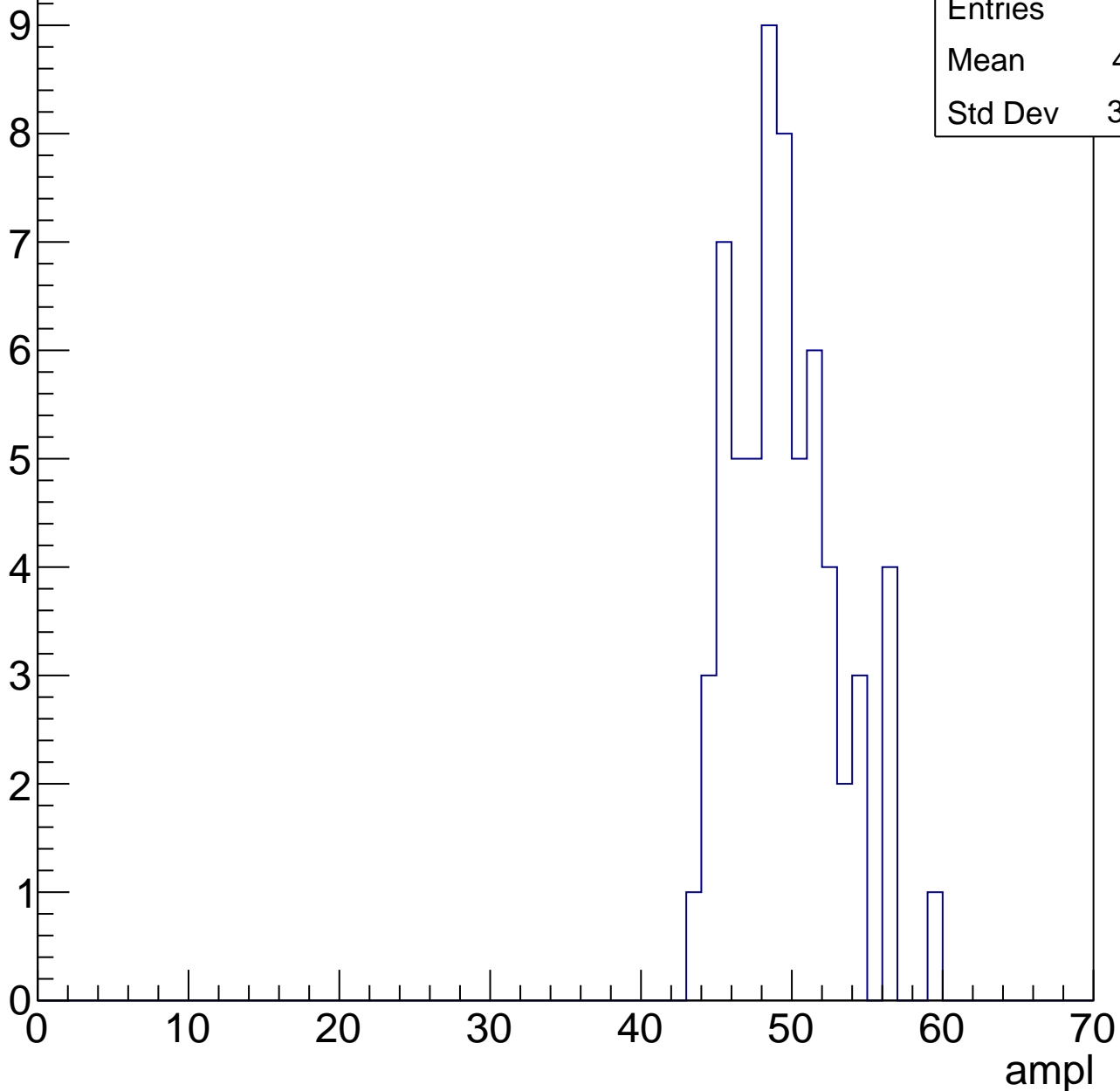


# B0L000S, U18-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	49.11
Std Dev	3.483

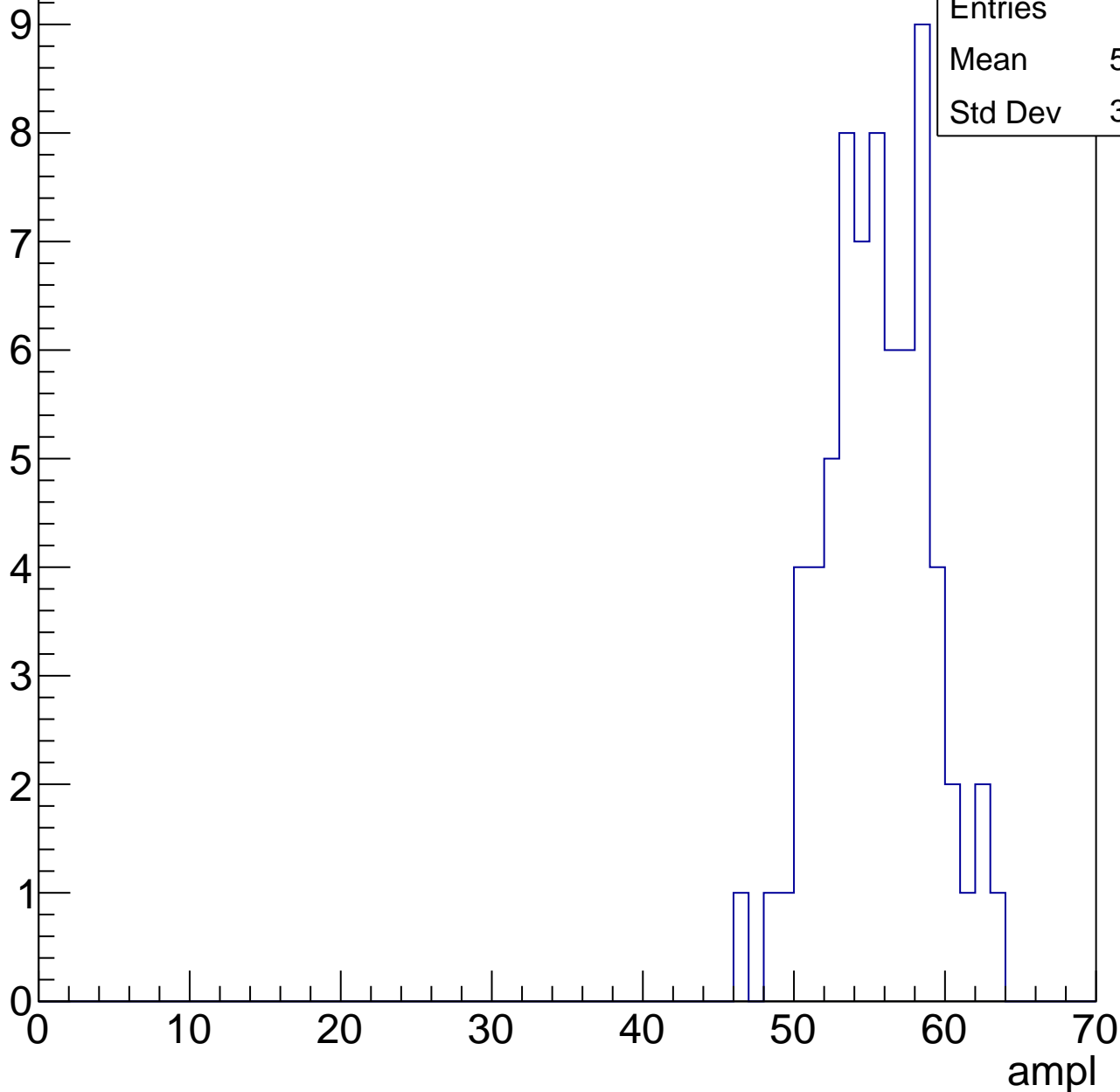


# B0L000S, U18-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	55.04
Std Dev	3.458

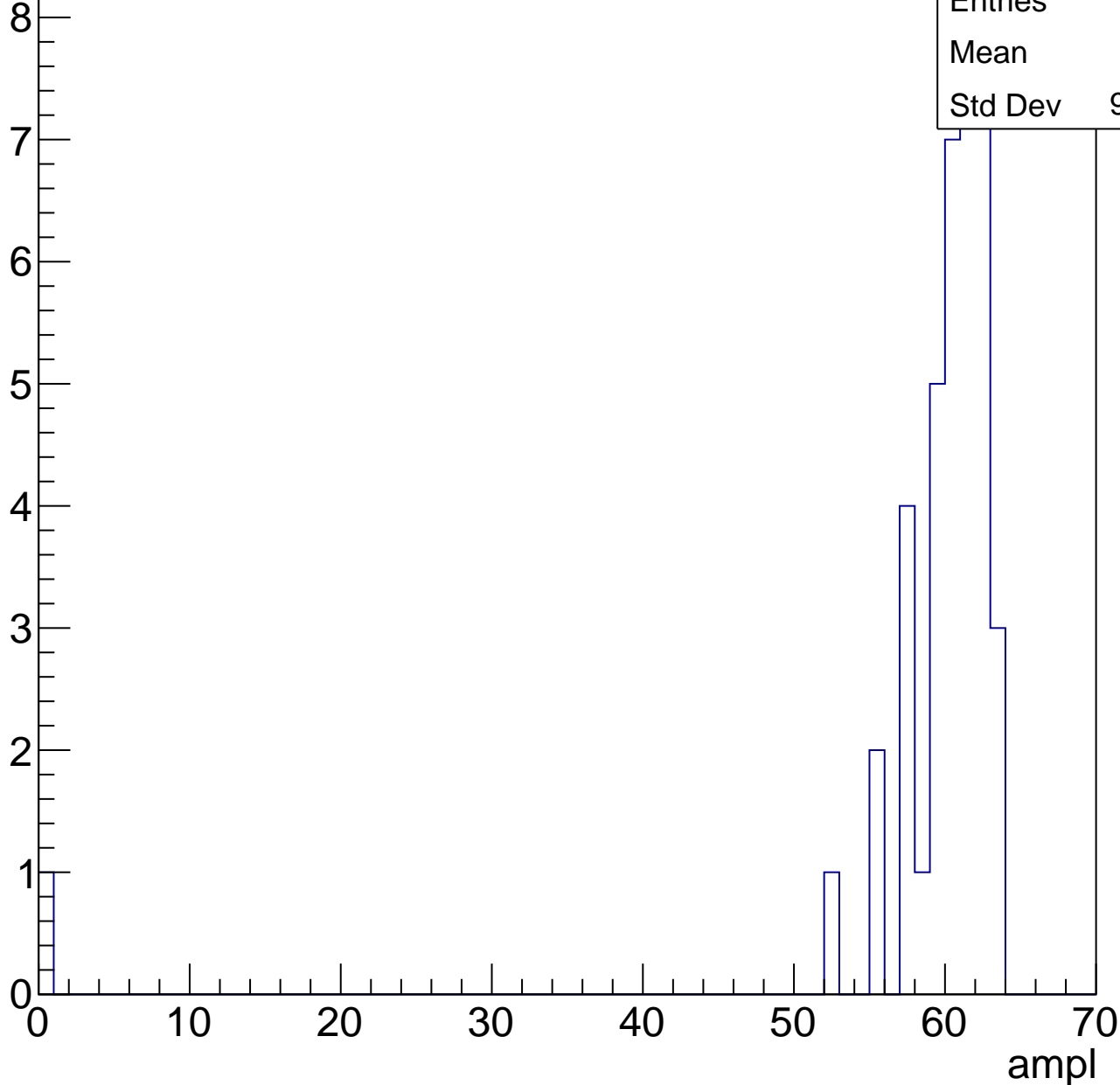


# B0L000S, U18-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.4
Std Dev	9.648

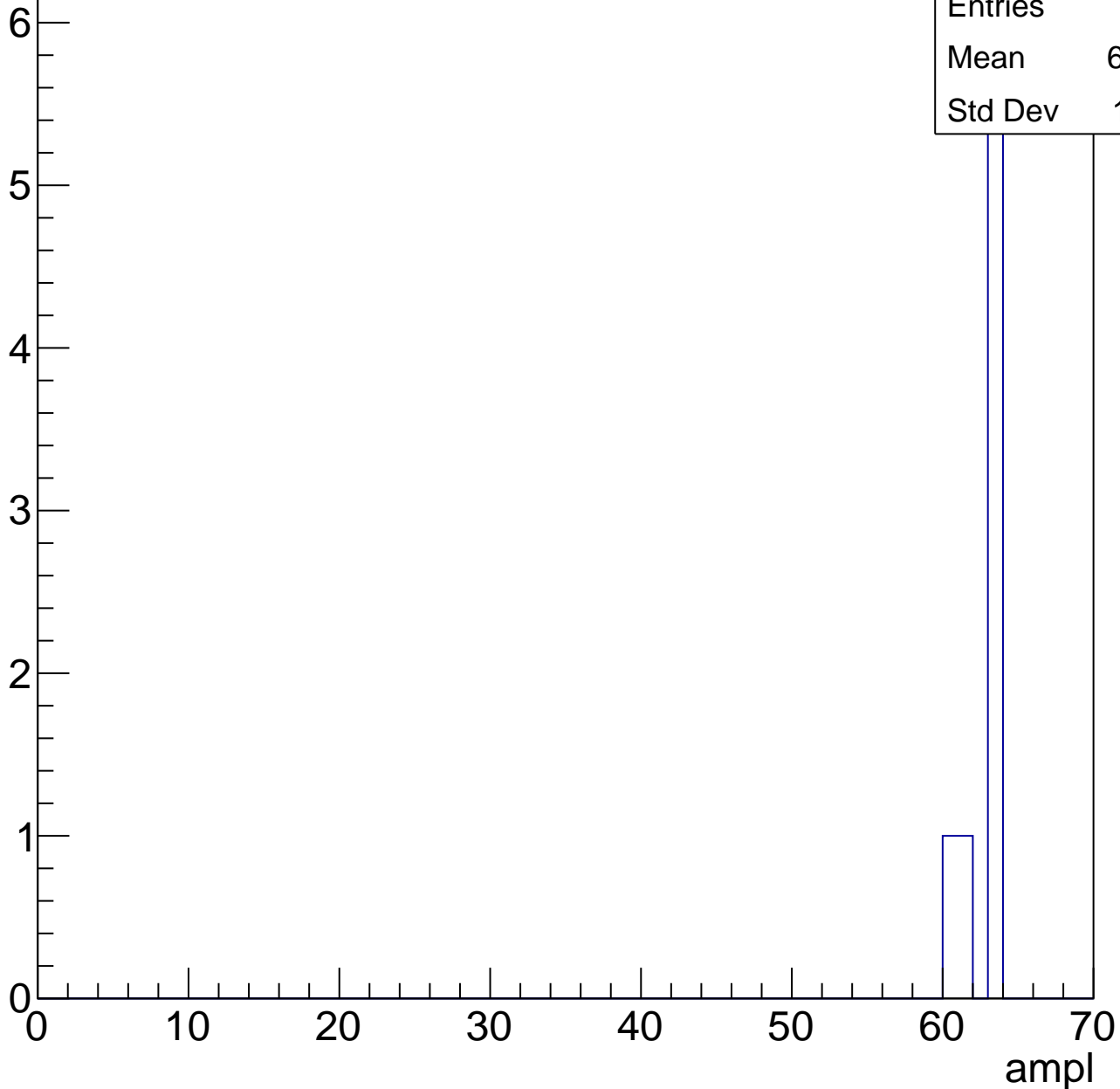


# B0L000S, U18-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	62.38
Std Dev	1.111





# B0L000S, U18-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch94, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	28.19
Std Dev	4.648

**Gaus mean : 29.3195**

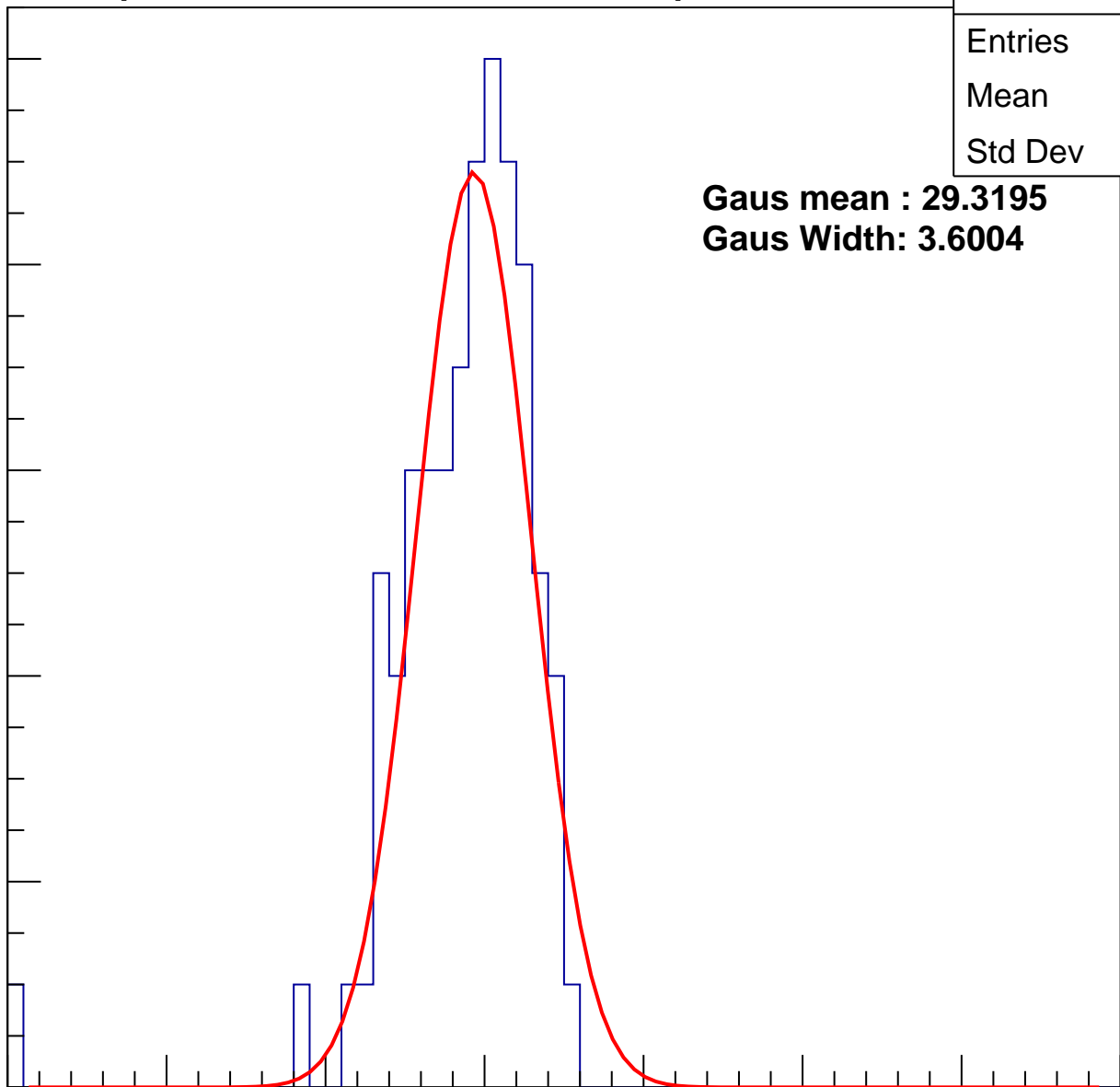
**Gaus Width: 3.6004**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch94, adc1

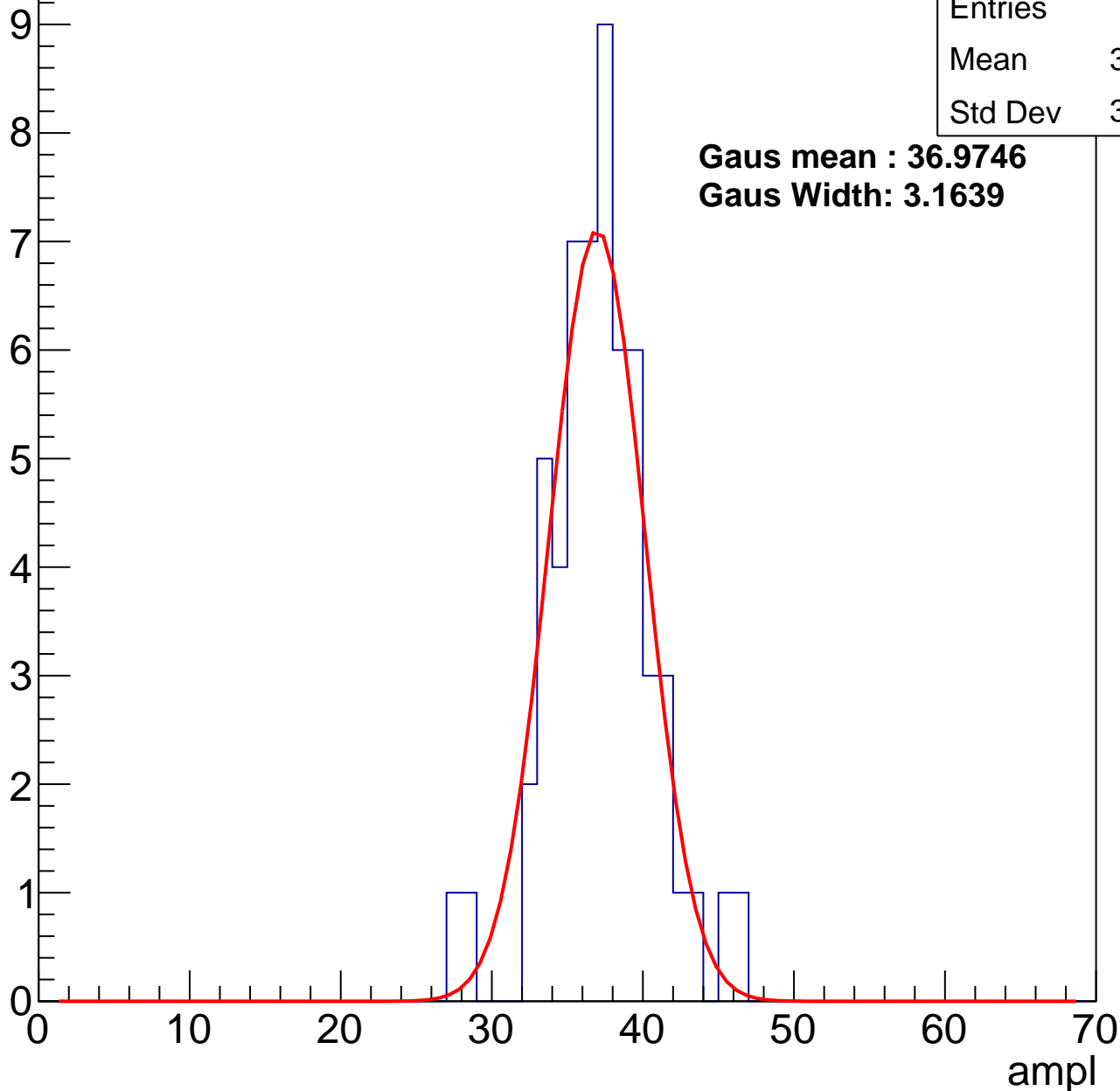
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	36.74
Std Dev	3.447

**Gaus mean : 36.9746**

**Gaus Width: 3.1639**



# B0L000S, U18-ch94, adc2

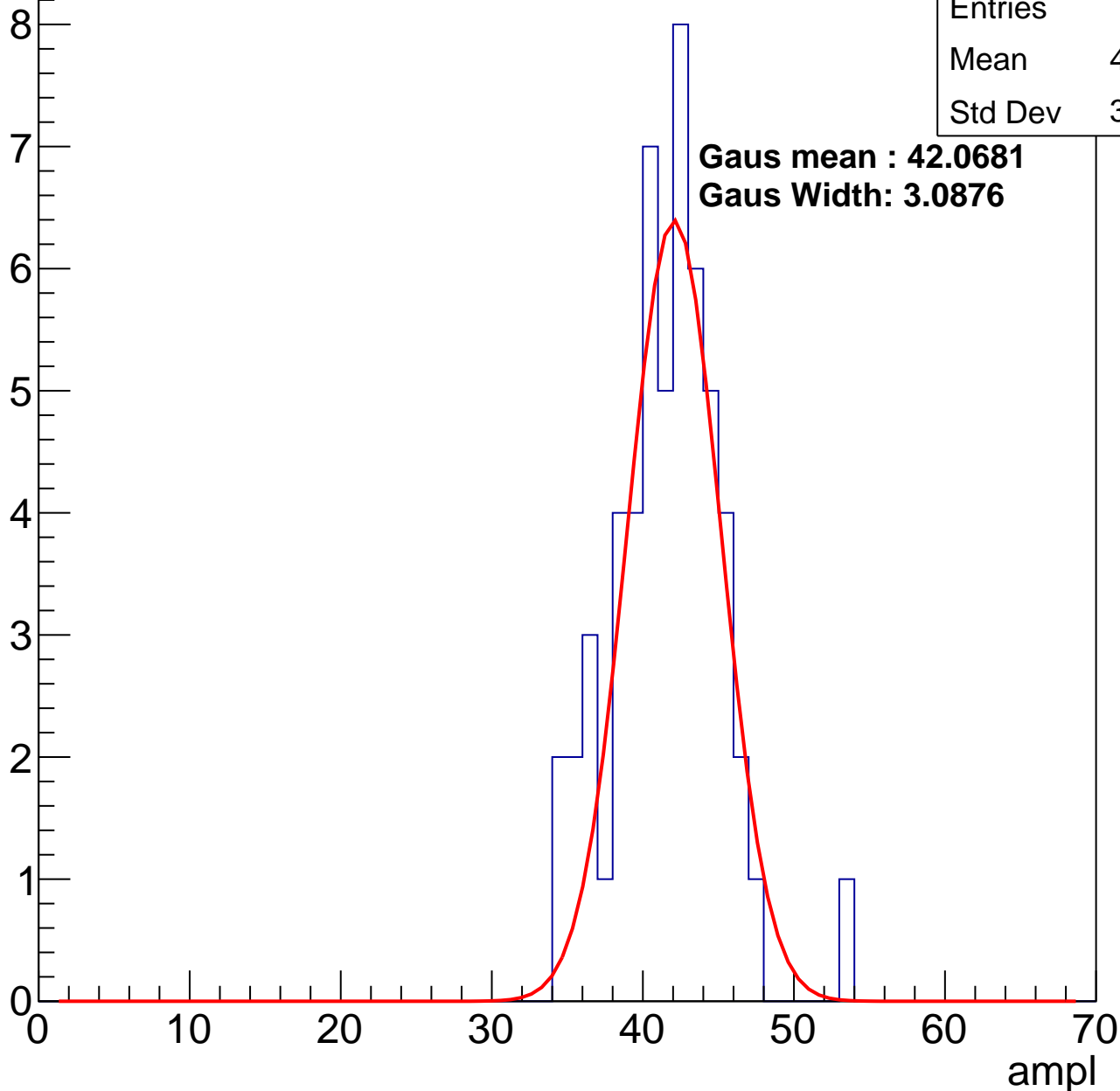
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	41.13
Std Dev	3.542

**Gaus mean : 42.0681**

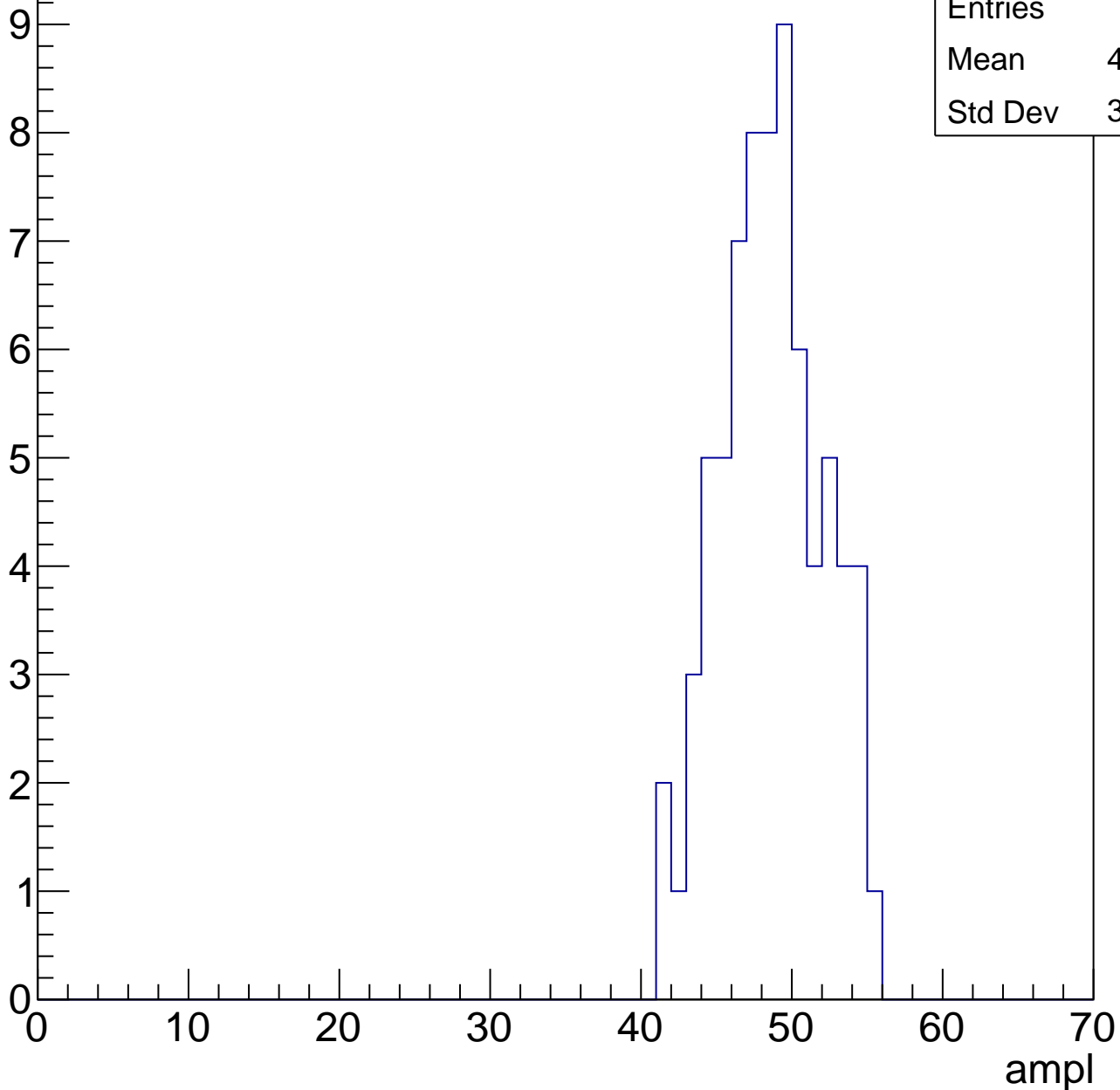
**Gaus Width: 3.0876**



# B0L000S, U18-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

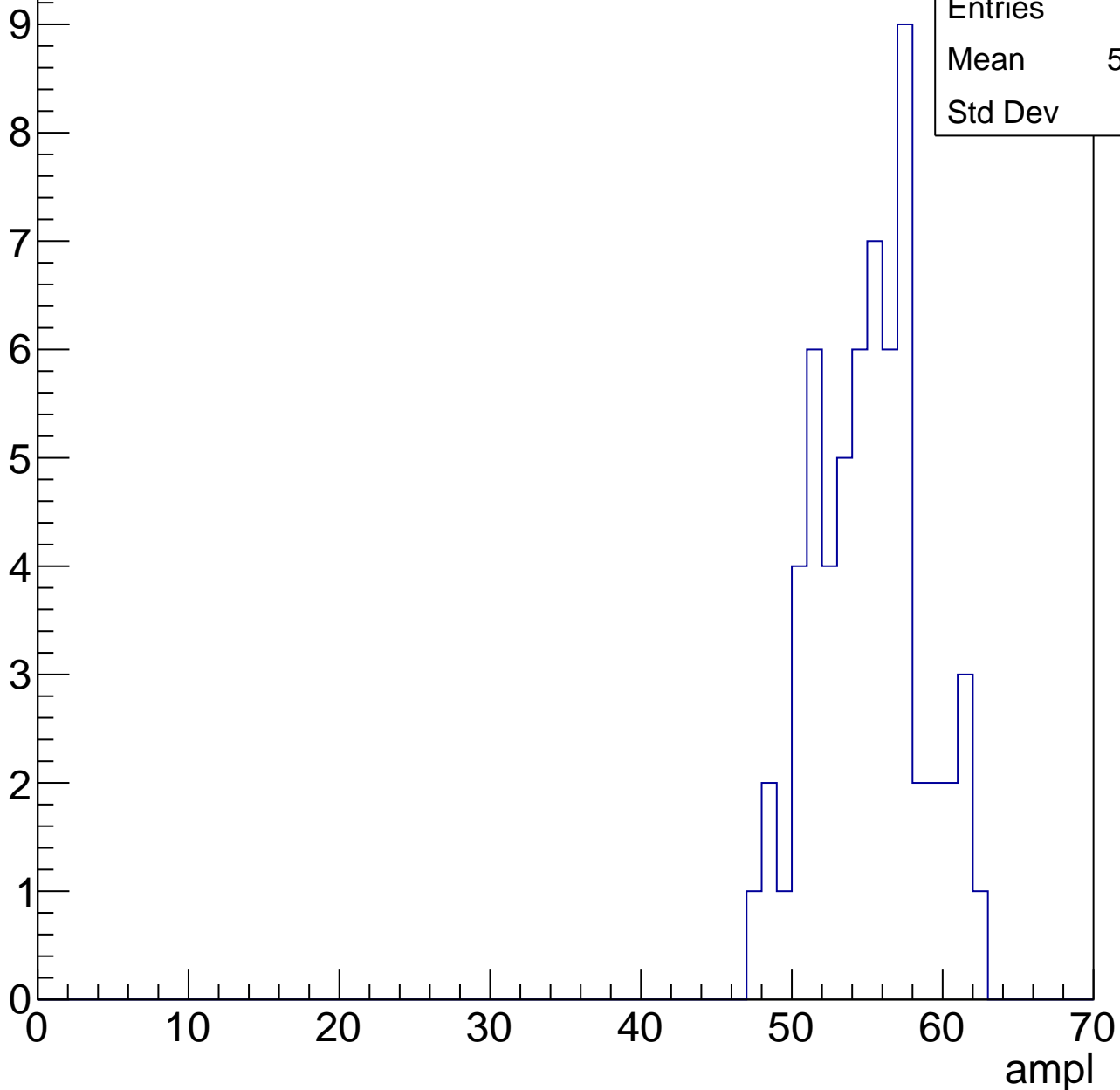


# B0L000S, U18-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

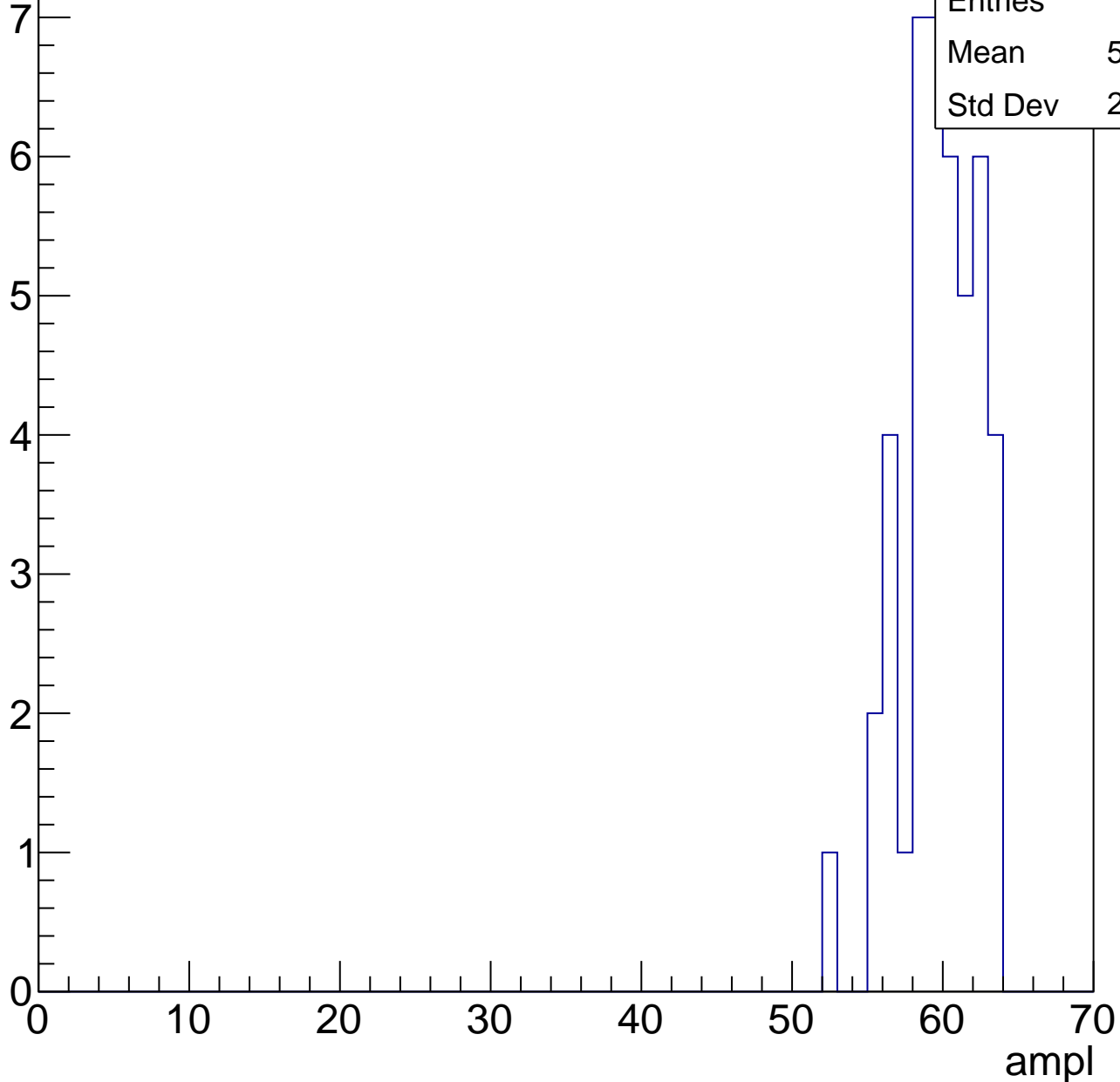
Entries	61
Mean	54.56
Std Dev	3.49



# B0L000S, U18-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

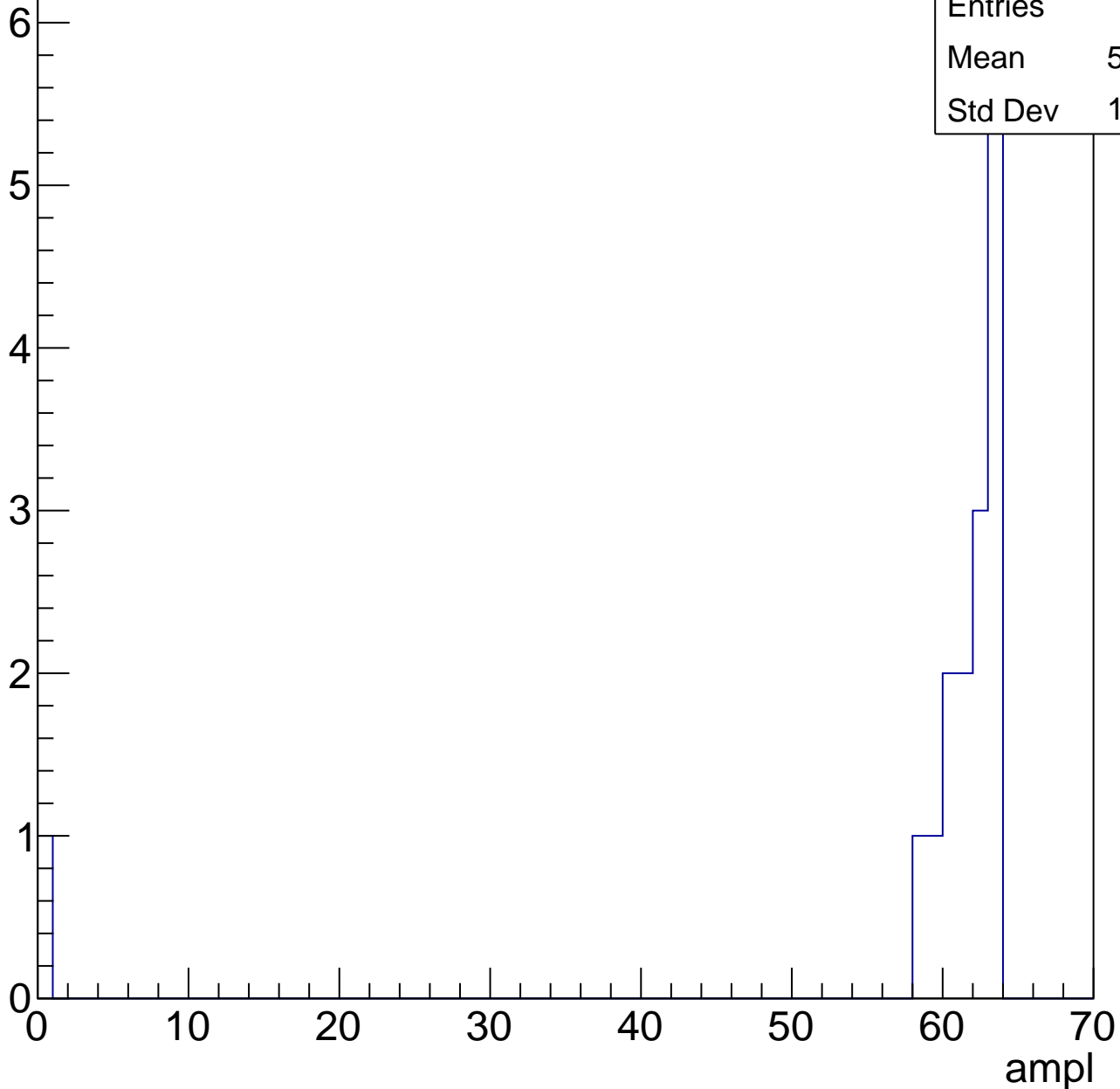


# B0L000S, U18-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.69
Std Dev	14.97





# B0L000S, U18-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch95, adc0

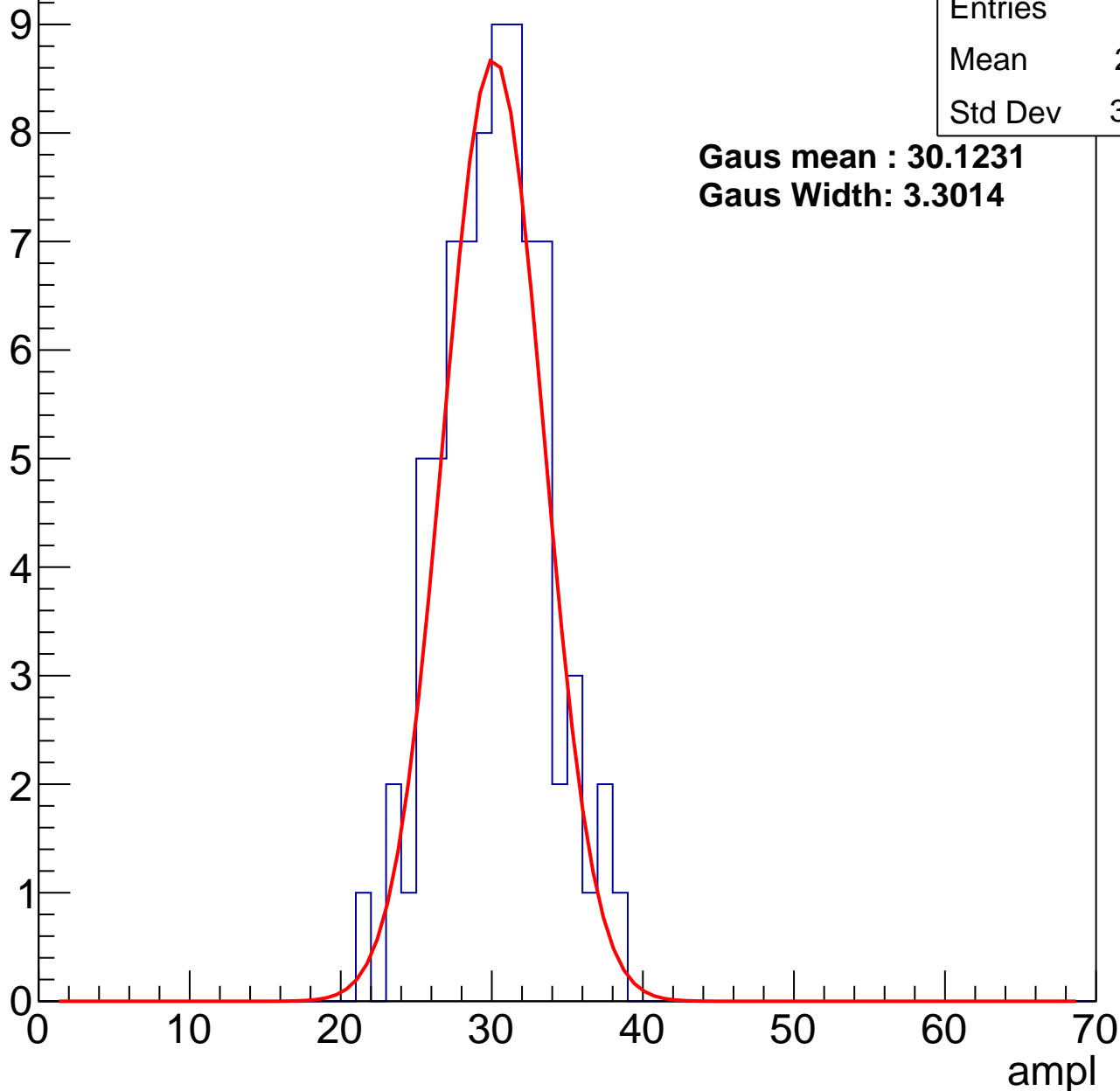
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	29.71
Std Dev	3.452

**Gaus mean : 30.1231**

**Gaus Width: 3.3014**



# B0L000S, U18-ch95, adc1

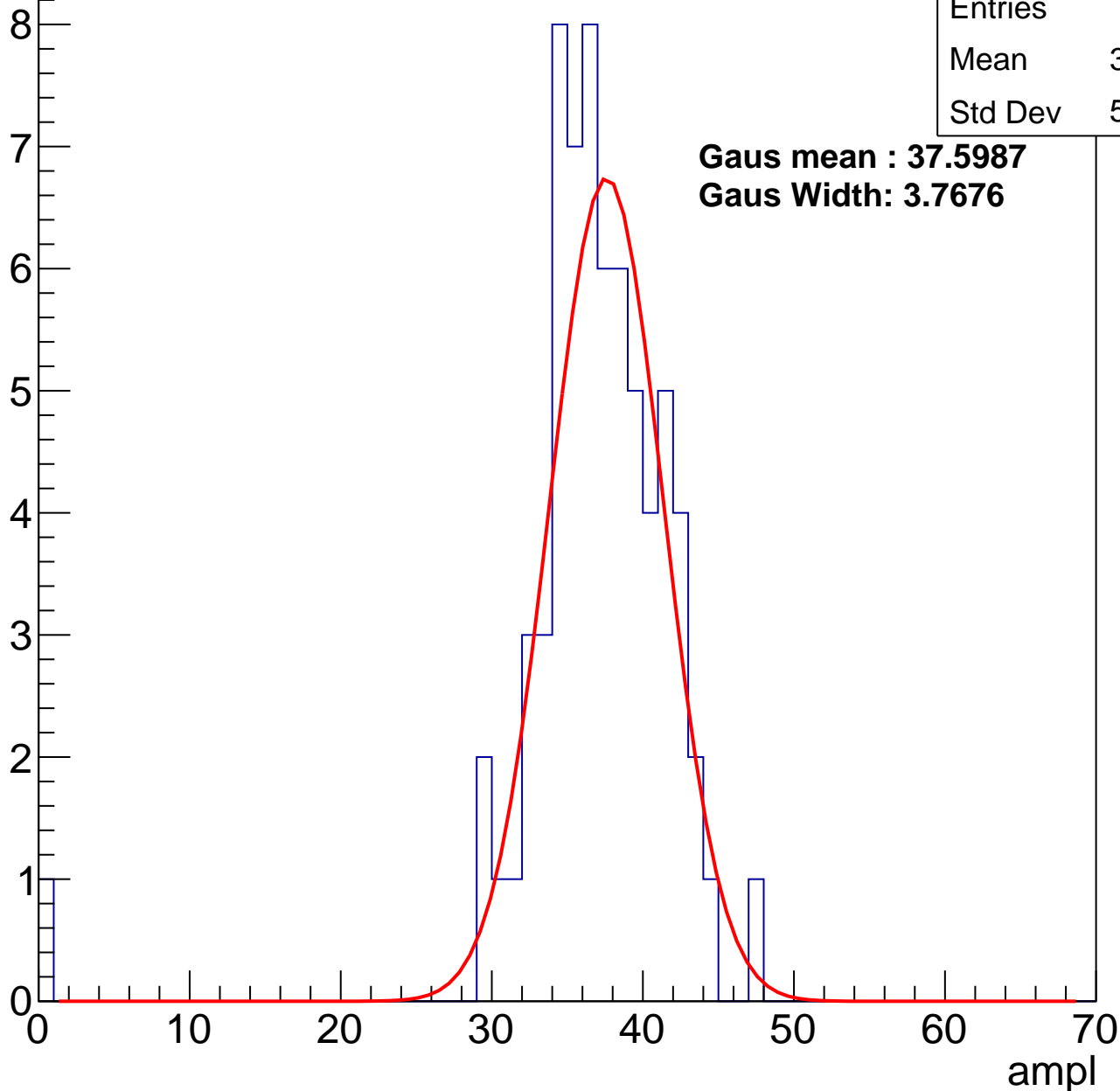
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.38
Std Dev	5.752

**Gaus mean : 37.5987**

**Gaus Width: 3.7676**



# B0L000S, U18-ch95, adc2

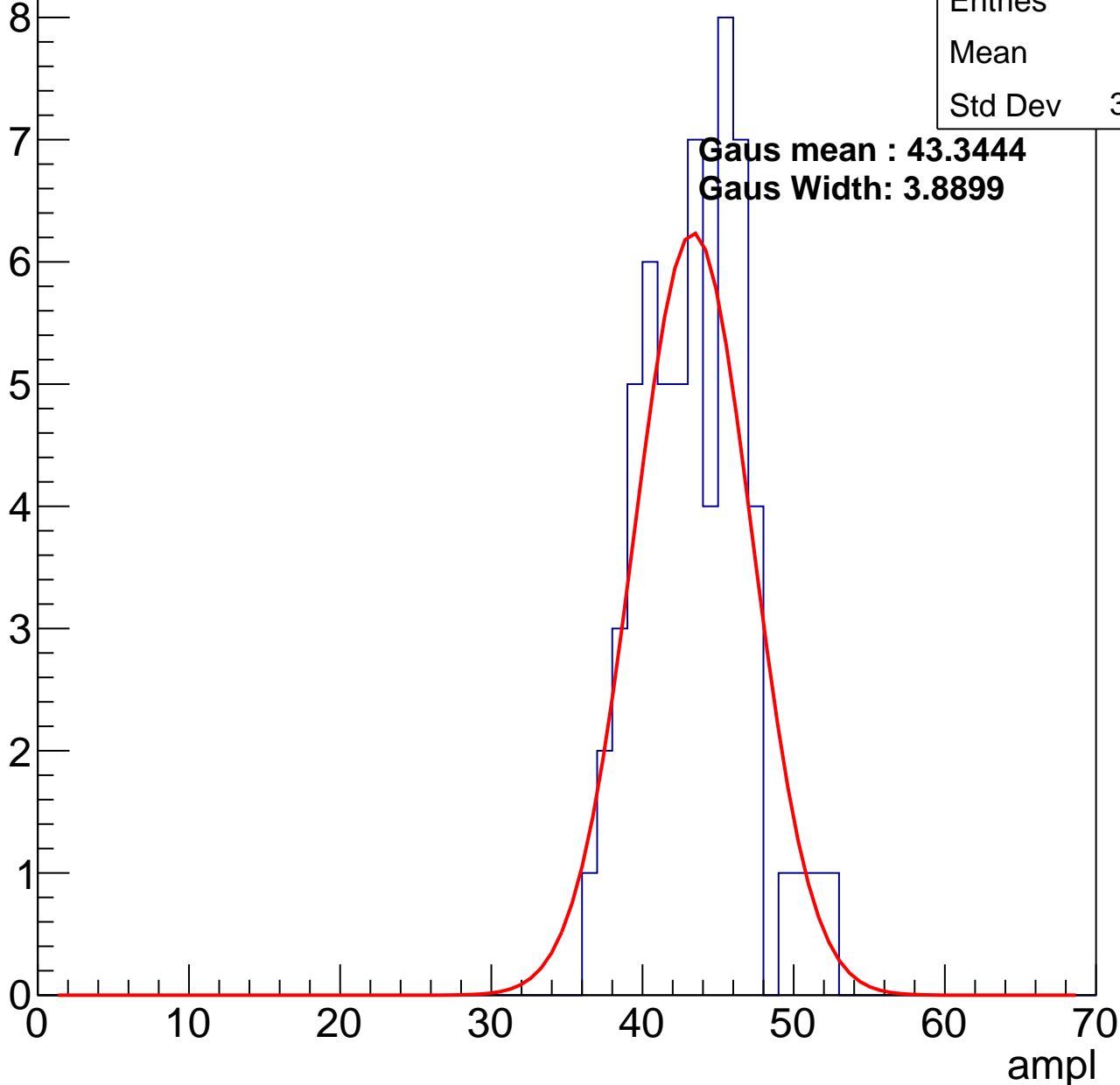
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43
Std Dev	3.502

**Gaus mean : 43.3444**

**Gaus Width: 3.8899**



# B0L000S, U18-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

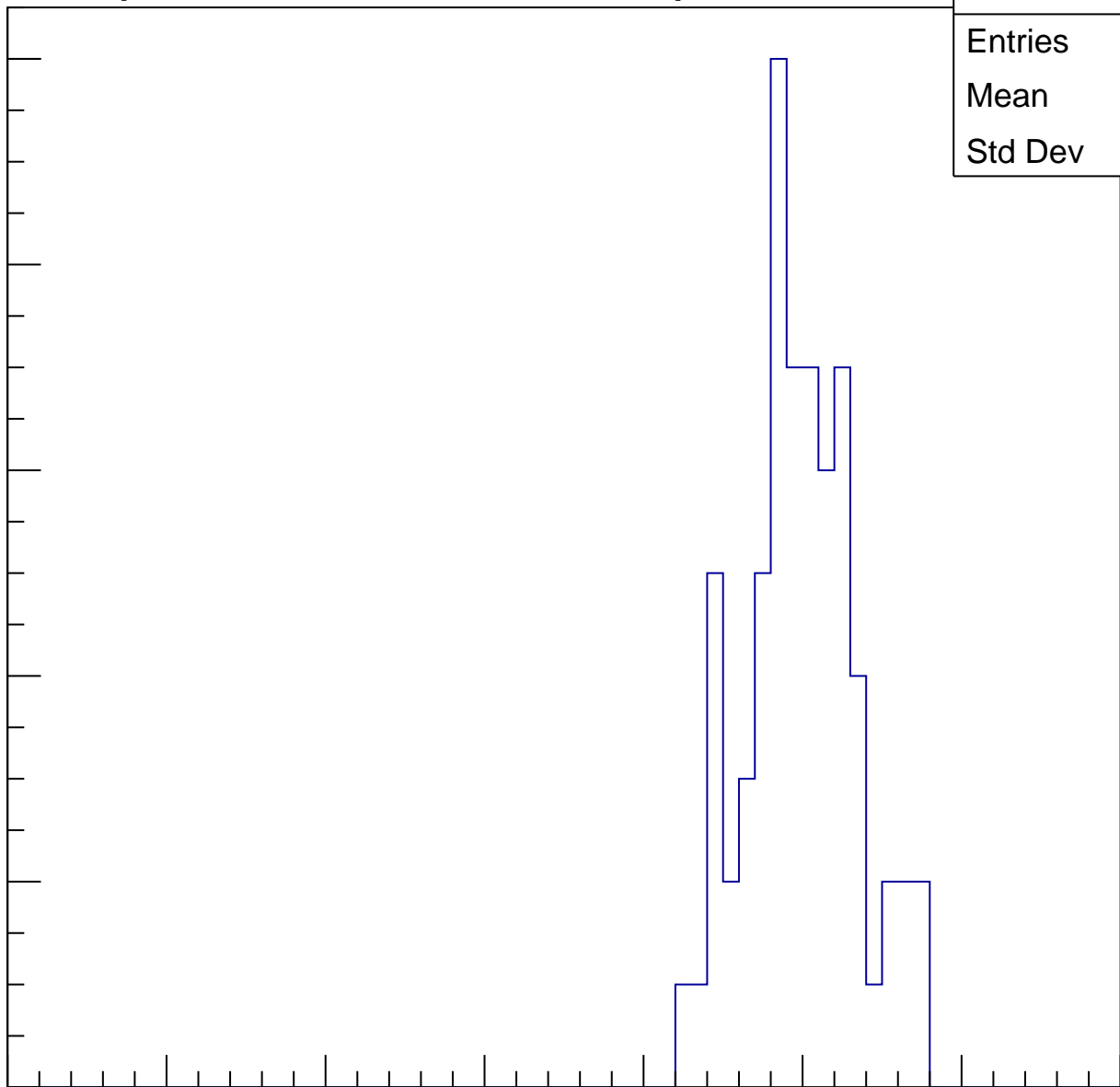
Entries	65
Mean	49.43
Std Dev	3.437

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

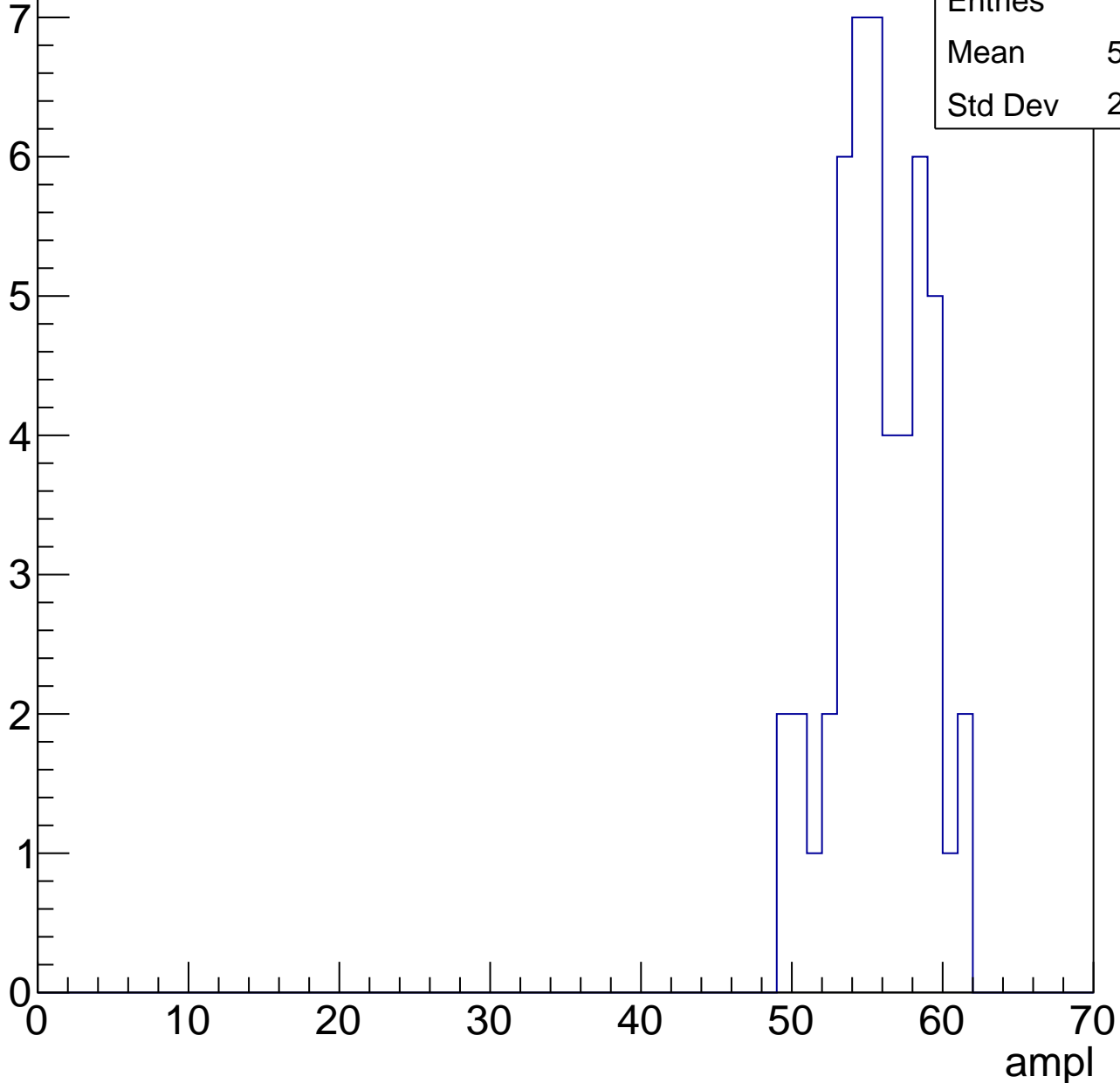


# B0L000S, U18-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

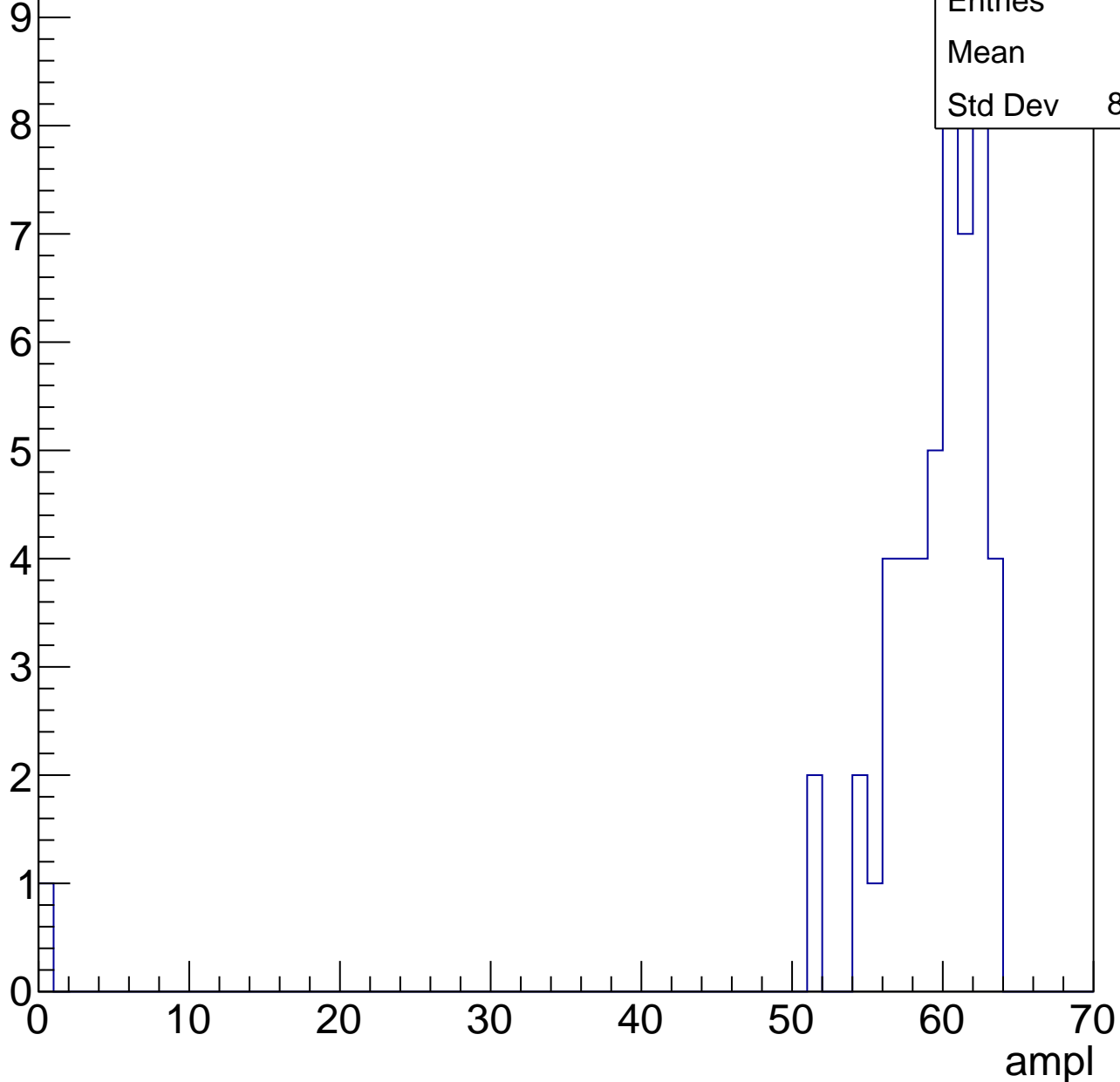
Entries	49
Mean	55.33
Std Dev	2.972



# B0L000S, U18-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

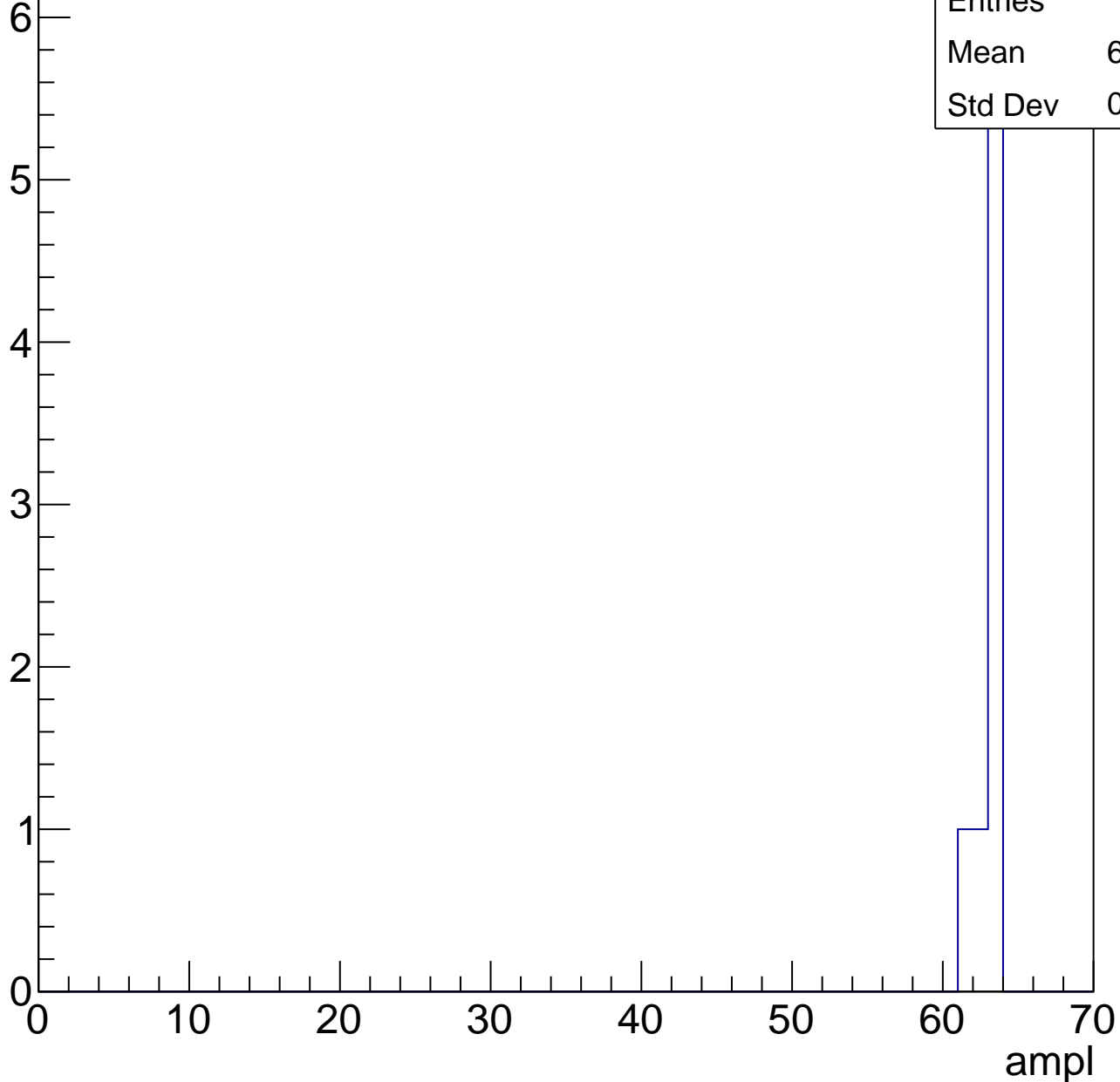


Entries	52
Mean	58.1
Std Dev	8.627

# B0L000S, U18-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	8
Mean	62.62
Std Dev	0.696



# B0L000S, U18-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch96, adc0

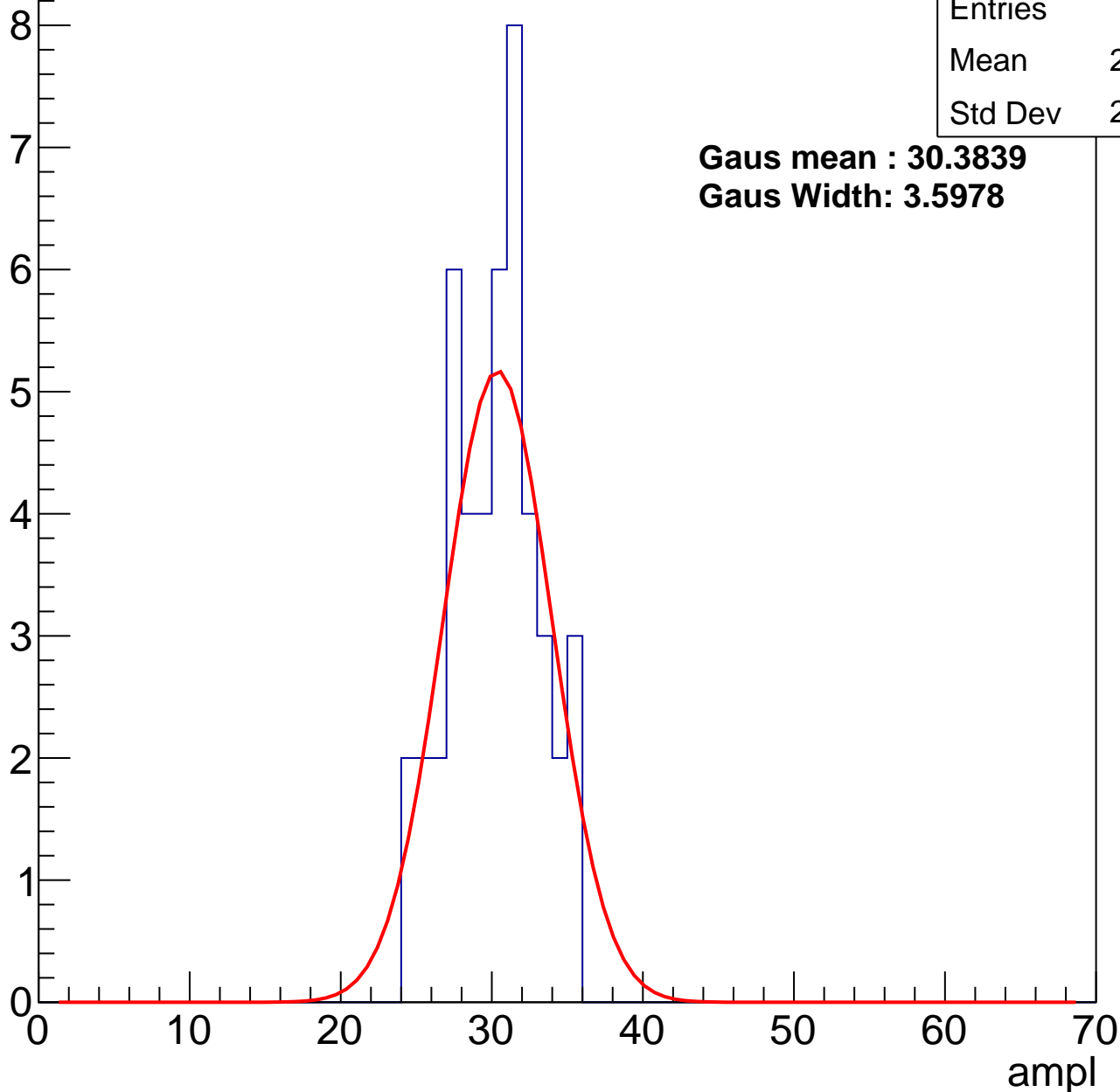
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	29.74
Std Dev	2.885

**Gaus mean : 30.3839**

**Gaus Width: 3.5978**



# B0L000S, U18-ch96, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	35.58
Std Dev	3.394

**Gaus mean : 36.3219**

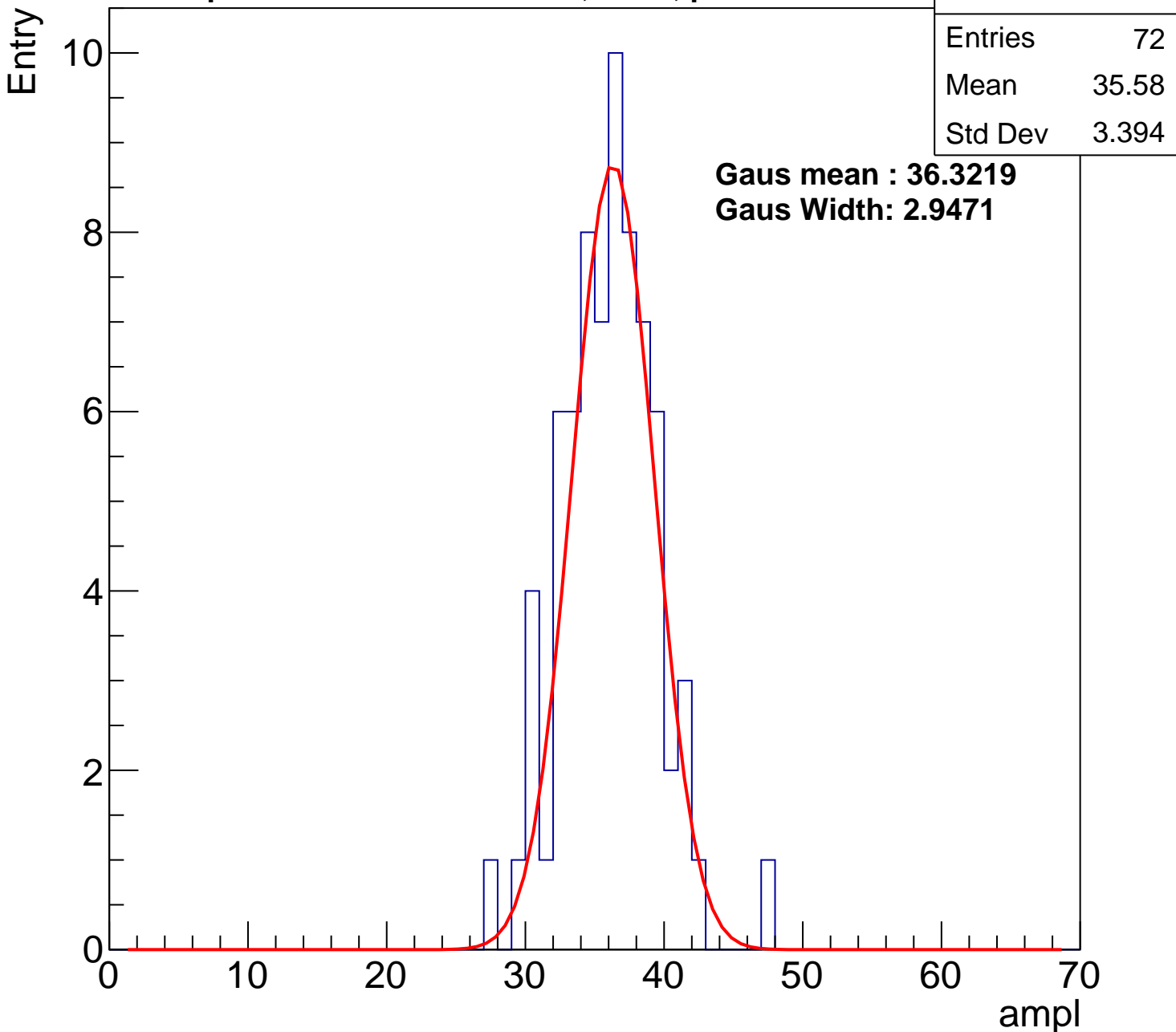
**Gaus Width: 2.9471**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch96, adc2

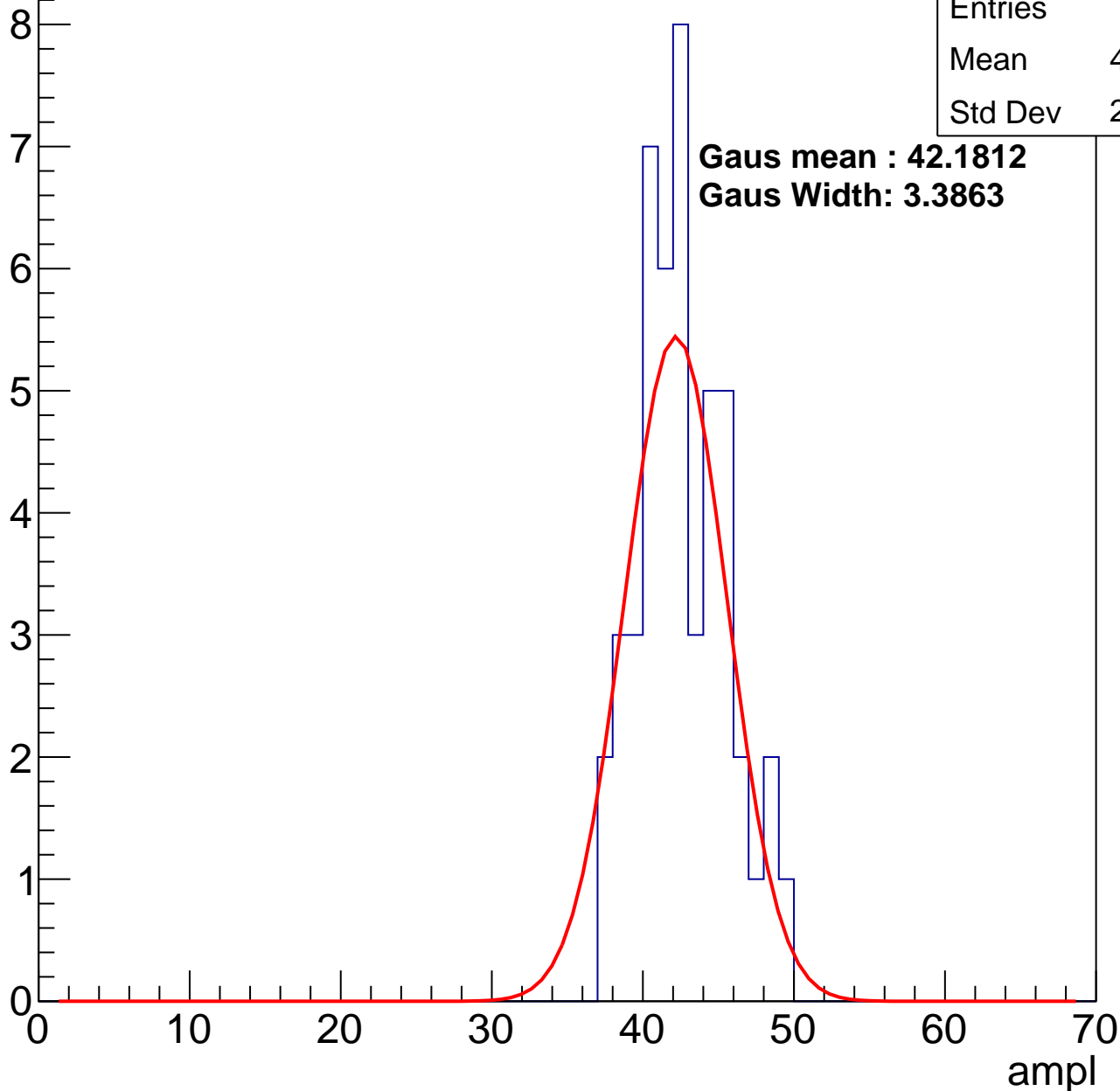
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	42.19
Std Dev	2.899

**Gaus mean : 42.1812**

**Gaus Width: 3.3863**

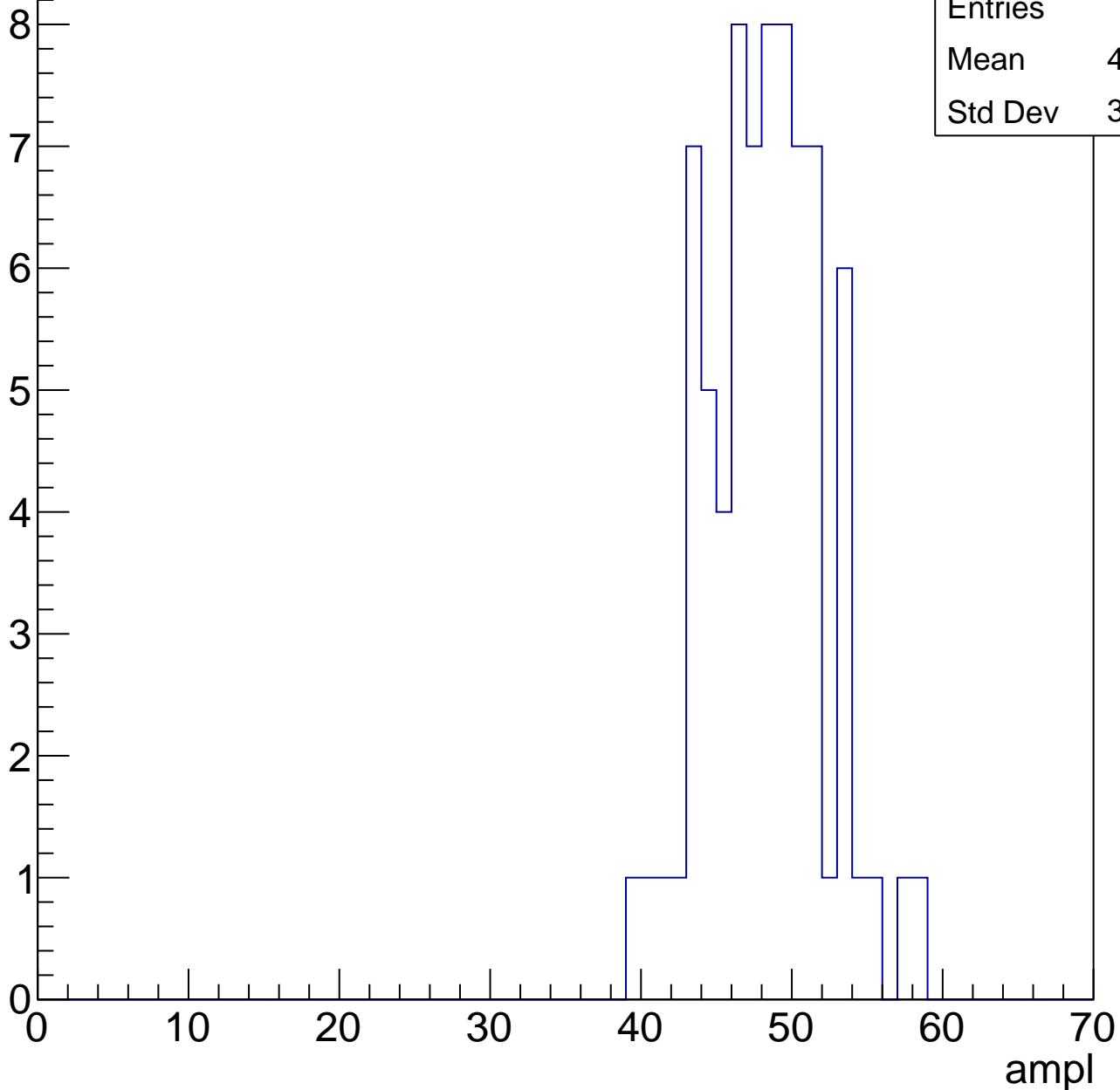


# B0L000S, U18-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	47.86
Std Dev	3.786

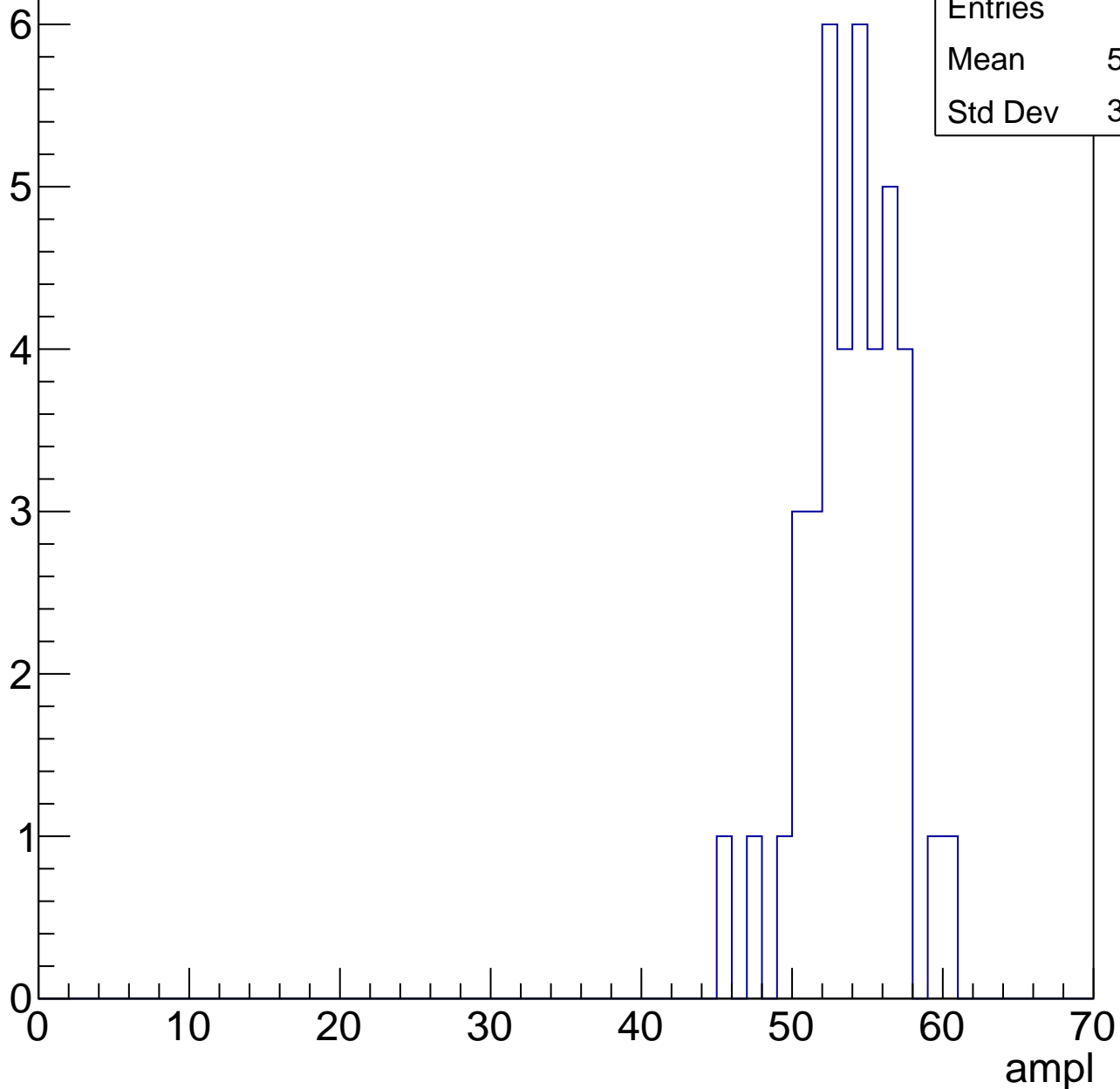


# B0L000S, U18-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	53.48
Std Dev	3.033



# B0L000S, U18-ch96, adc5

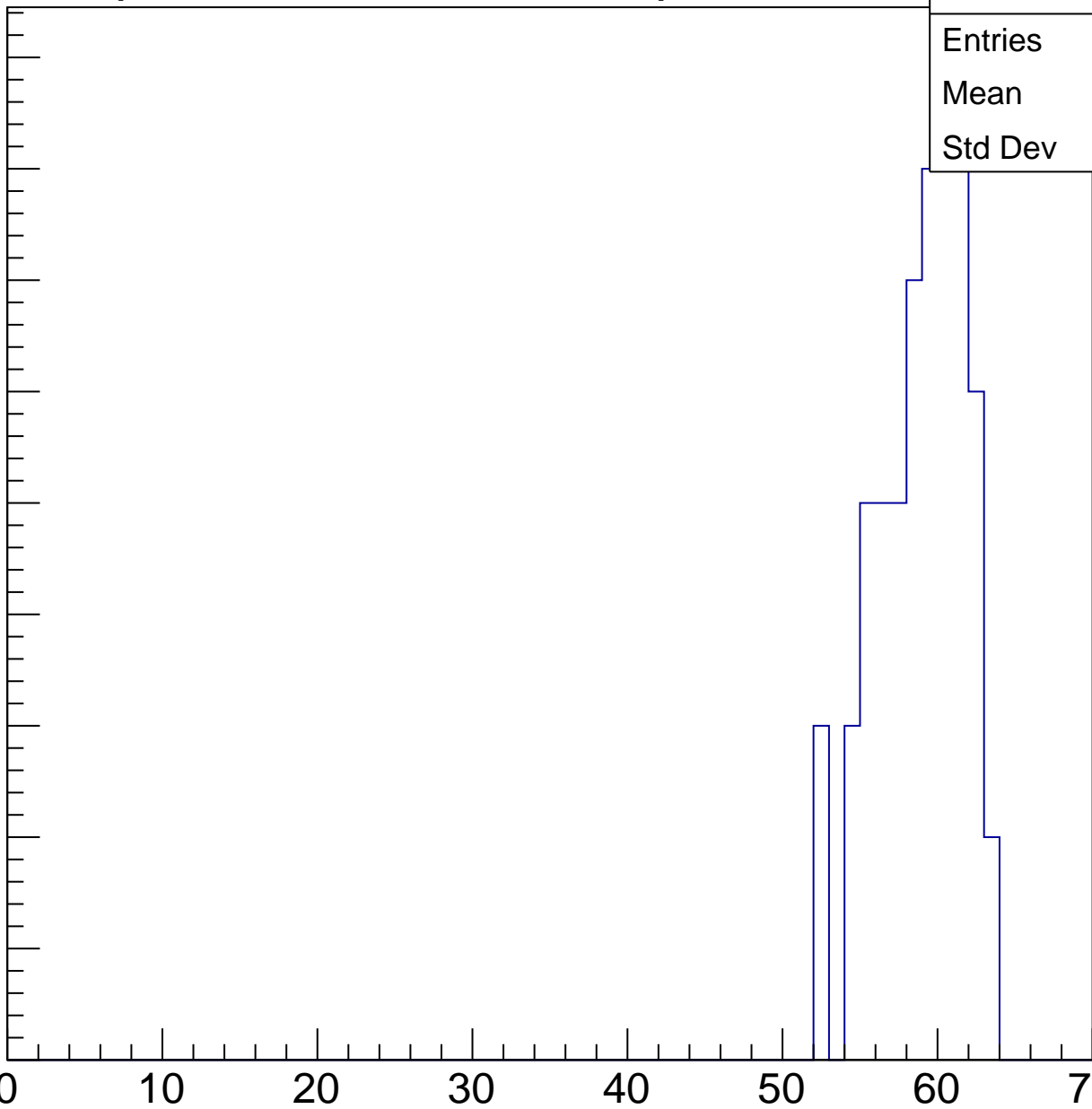
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	61
Mean	58.41
Std Dev	2.813

ampl

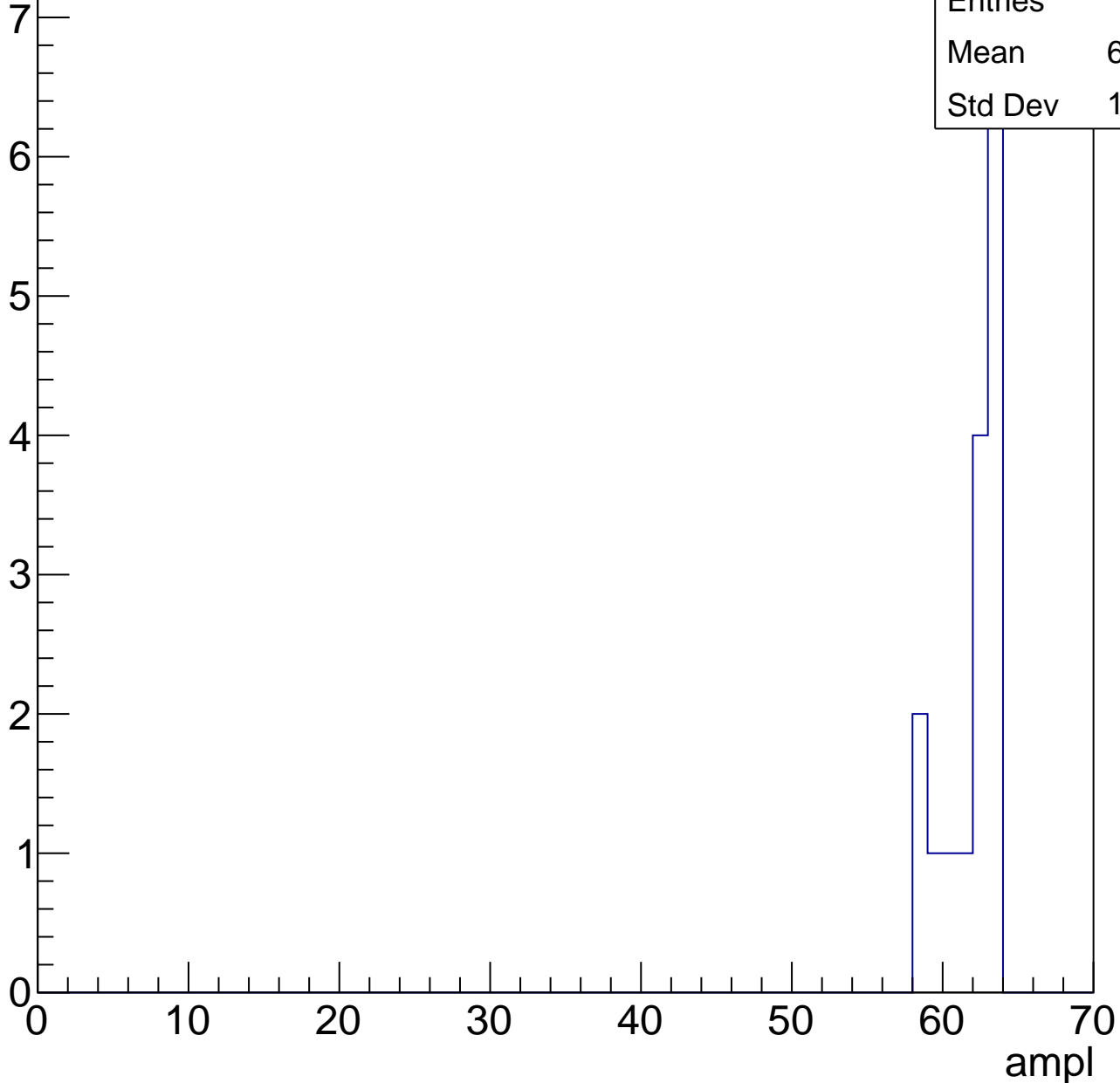


# B0L000S, U18-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.56
Std Dev	1.767

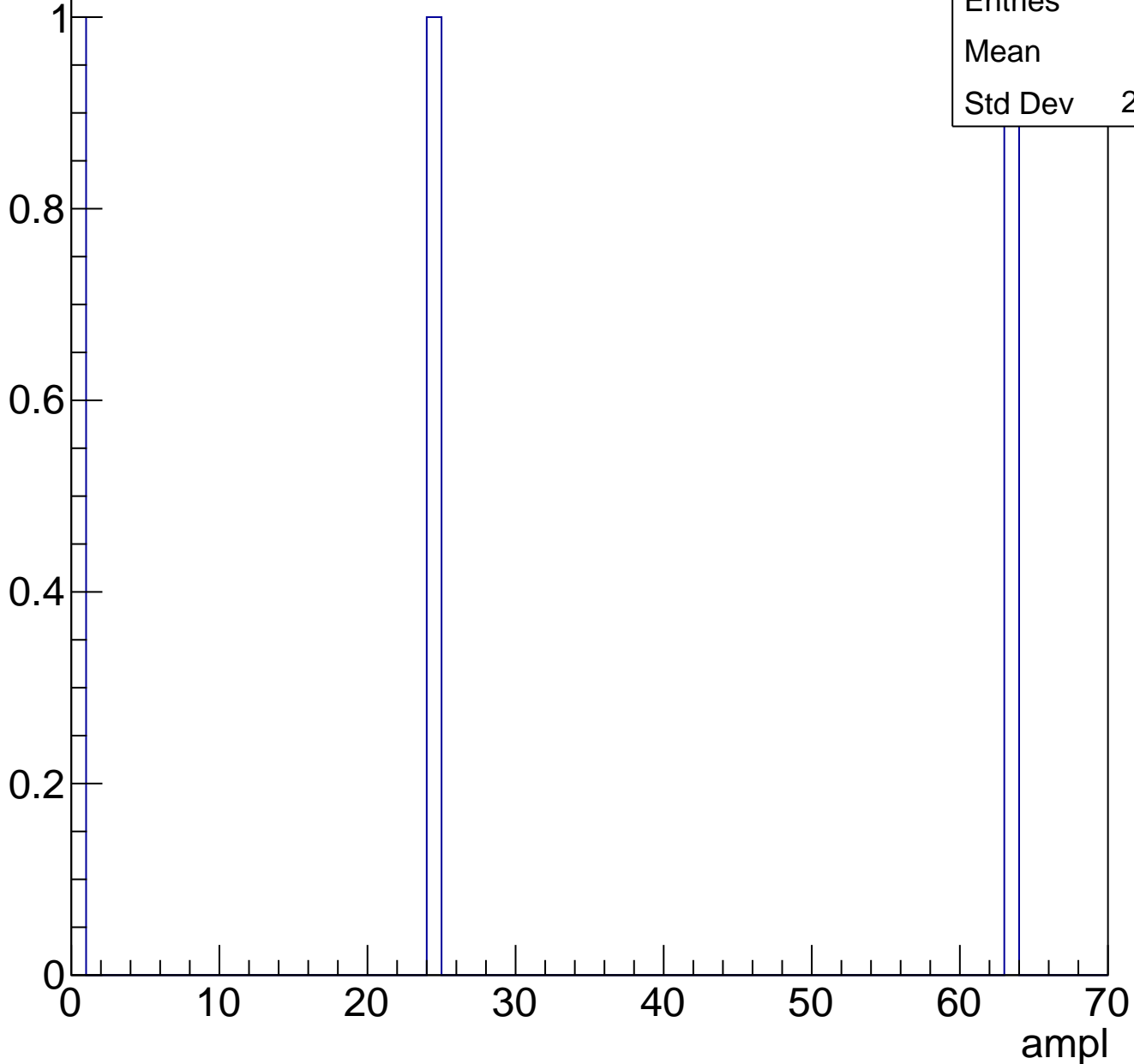




# B0L000S, U18-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch97, adc0

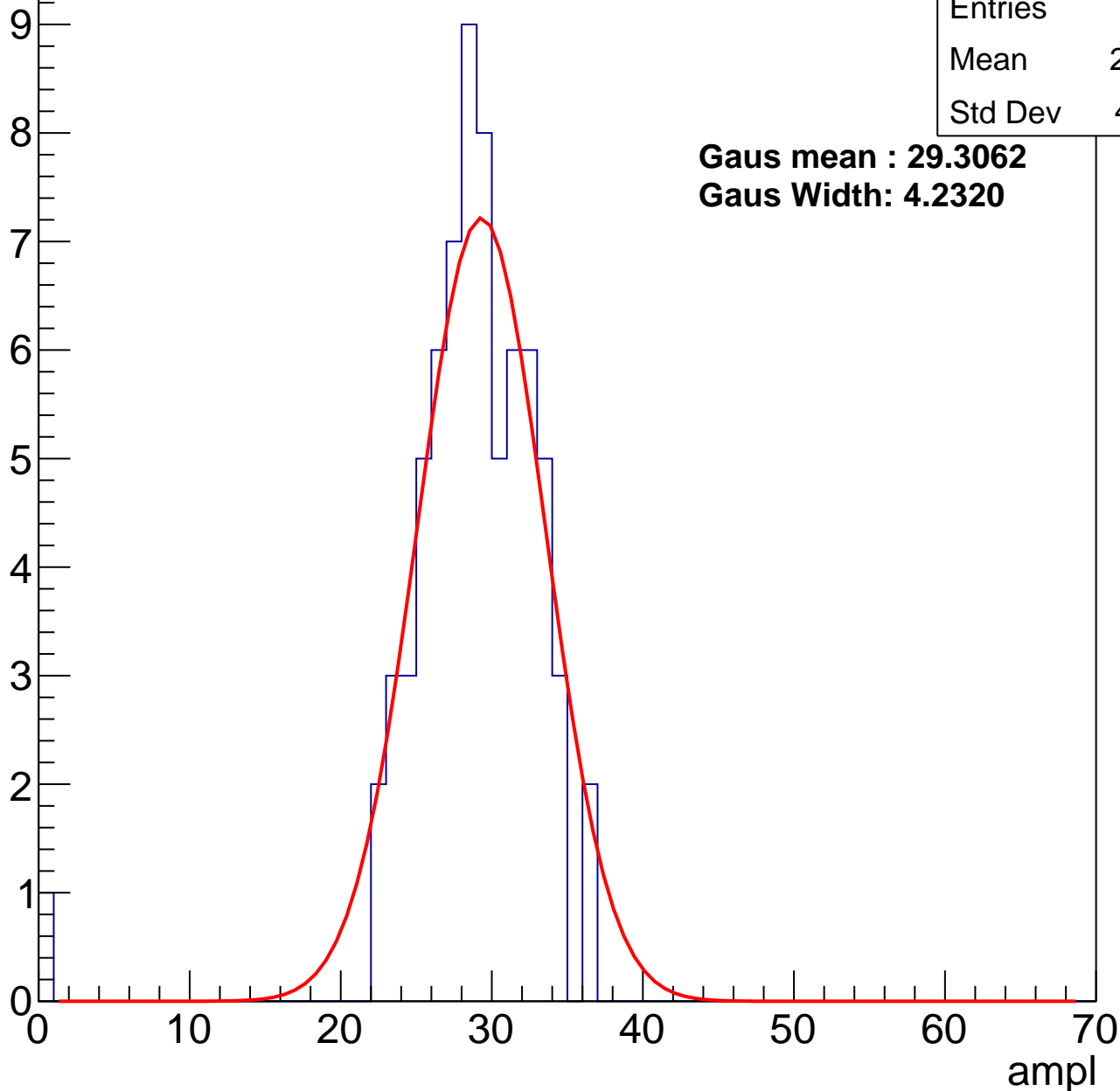
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	28.25
Std Dev	4.731

**Gaus mean : 29.3062**

**Gaus Width: 4.2320**



# B0L000S, U18-ch97, adc1

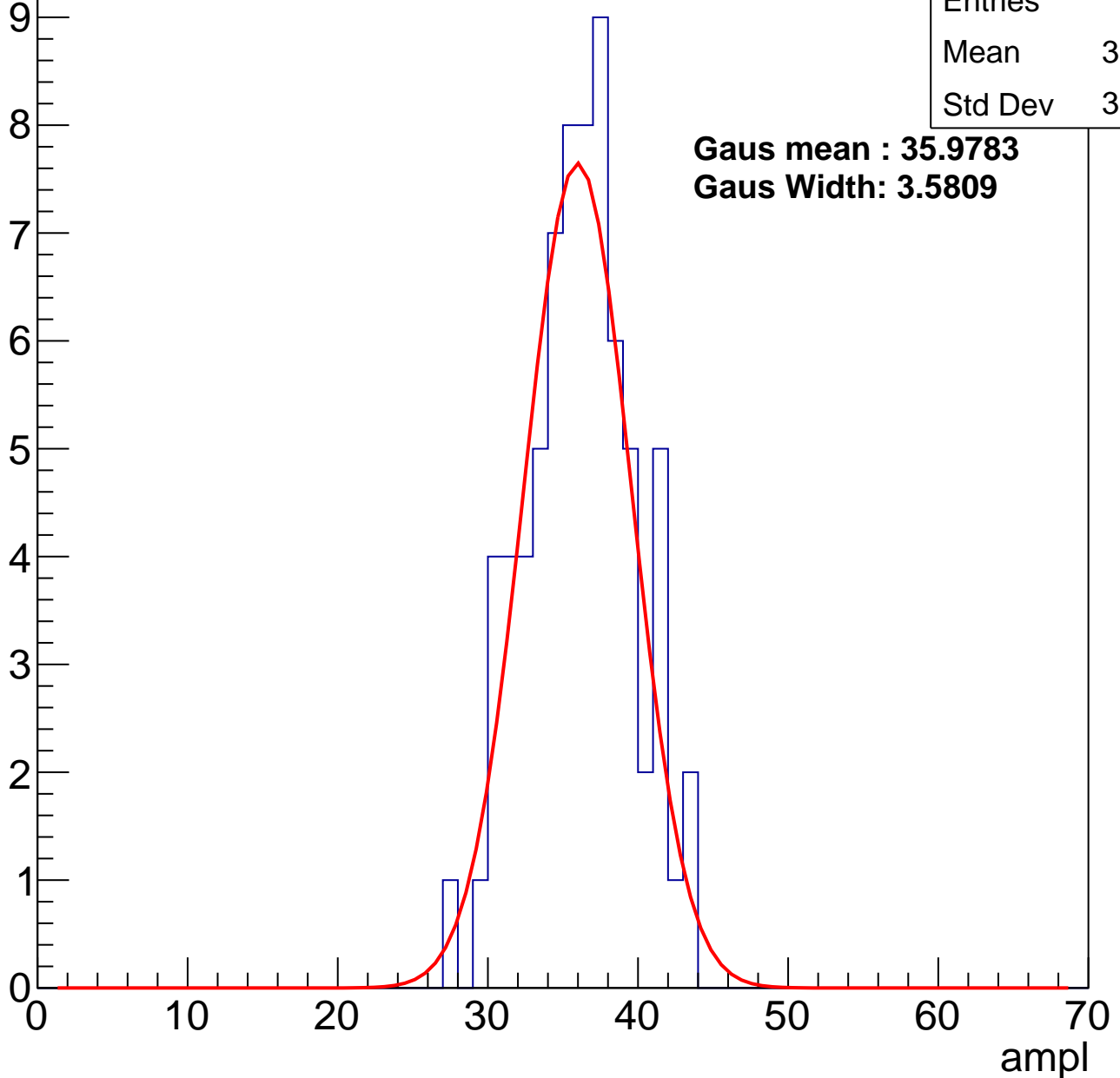
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	35.67
Std Dev	3.496

**Gaus mean : 35.9783**

**Gaus Width: 3.5809**



# B0L000S, U18-ch97, adc2

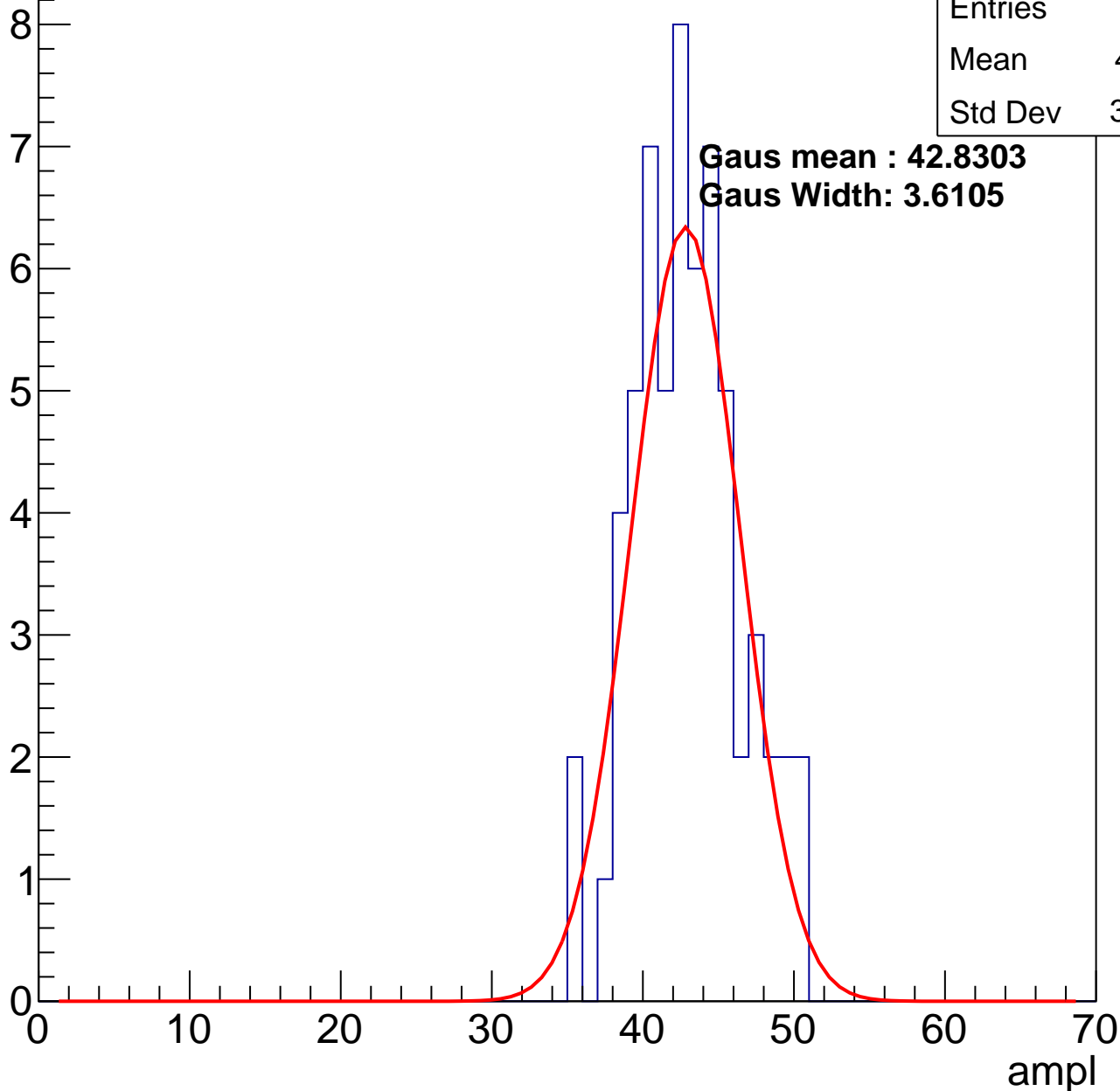
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.51
Std Dev	3.467

**Gaus mean : 42.8303**

**Gaus Width: 3.6105**

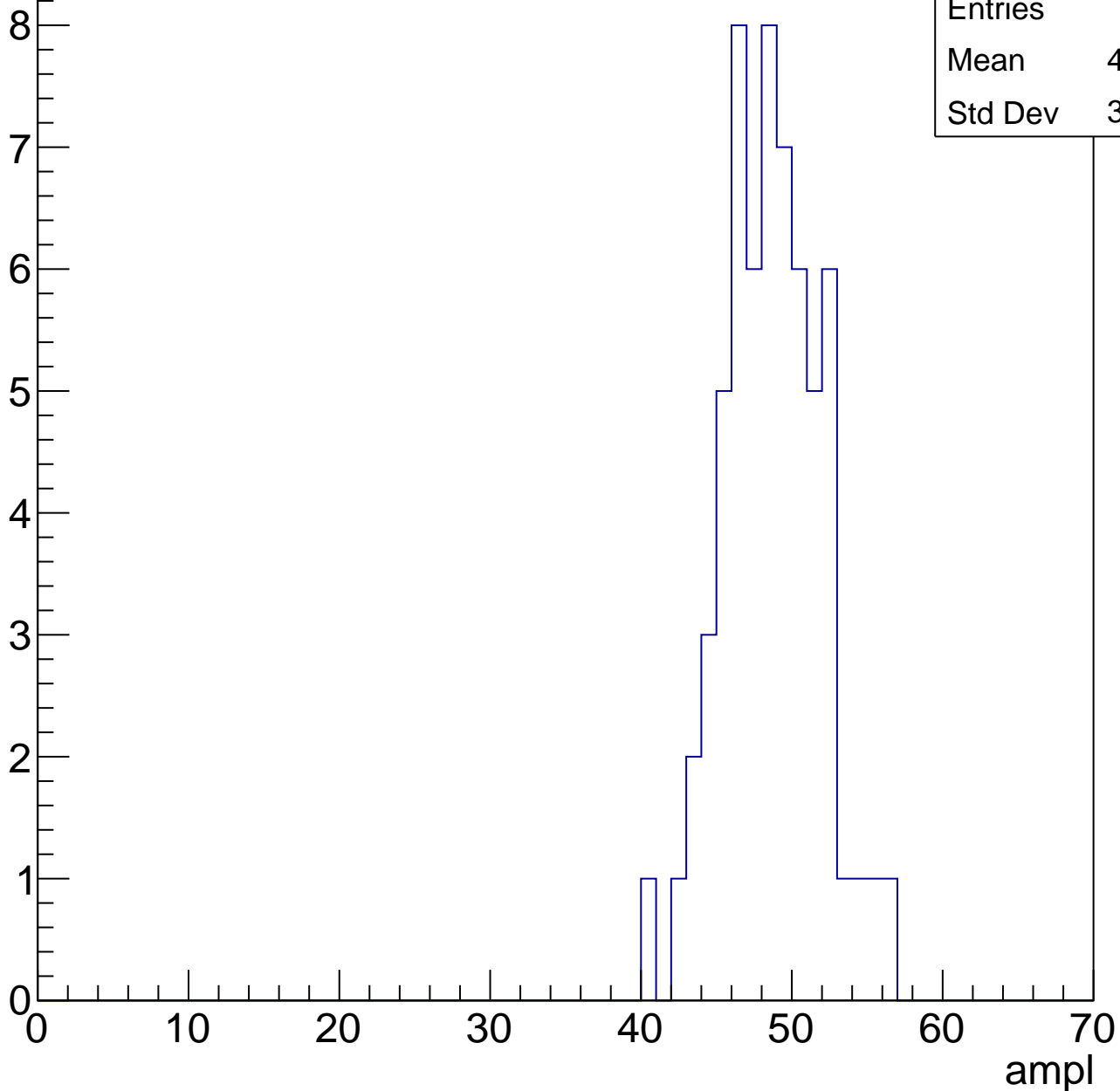


# B0L000S, U18-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	48.18
Std Dev	3.175

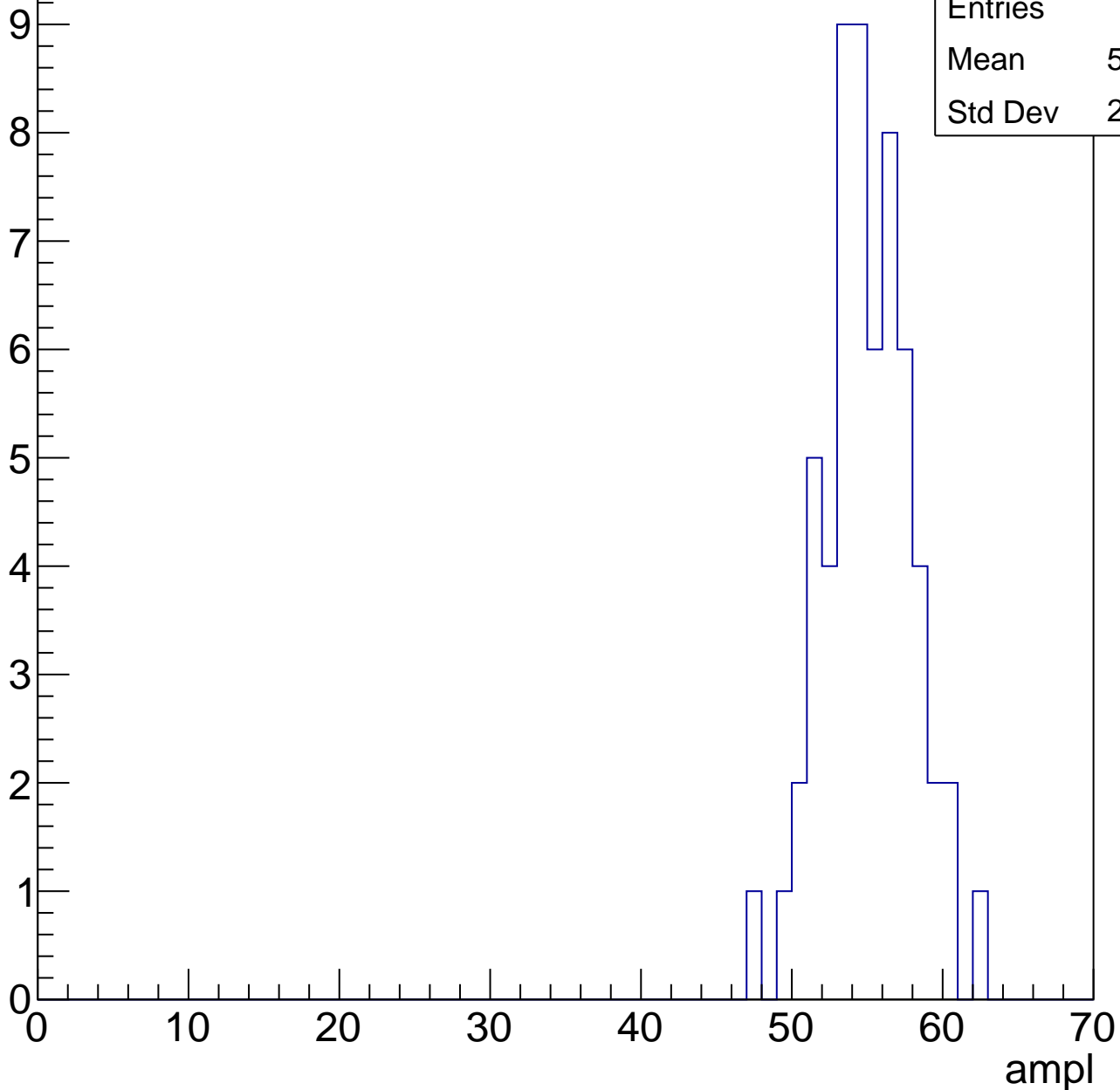


# B0L000S, U18-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.57
Std Dev	2.883

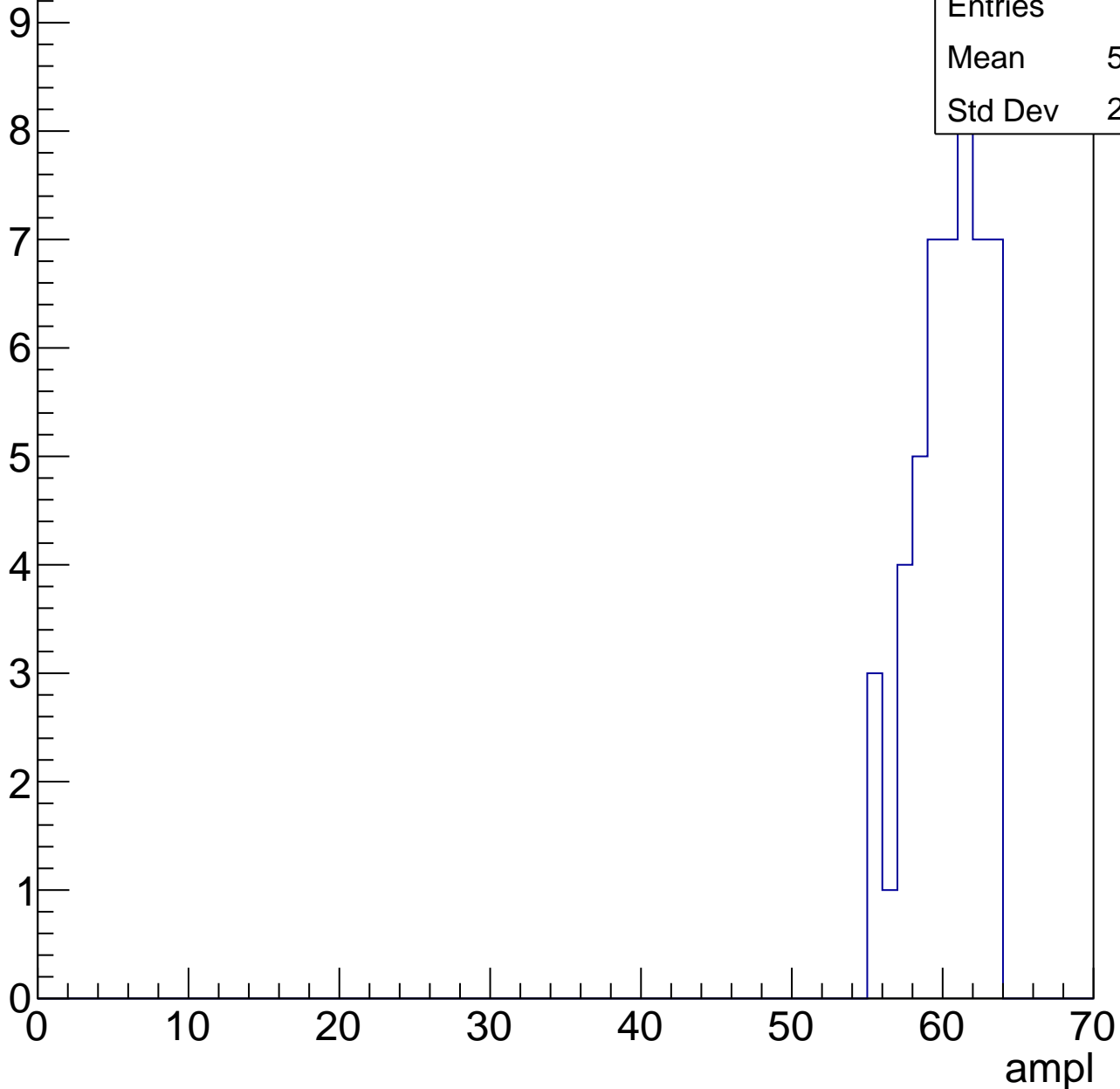


# B0L000S, U18-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

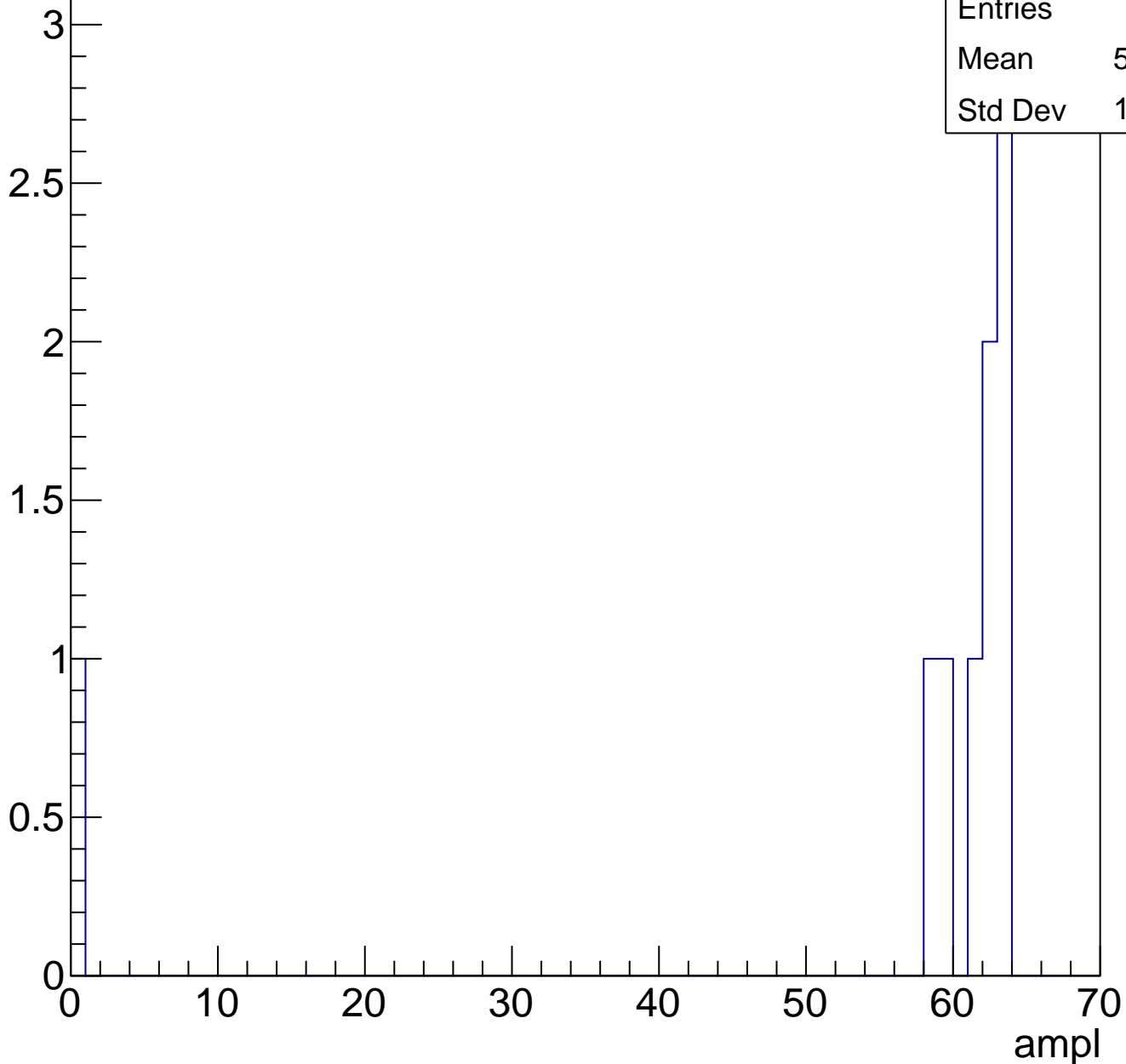
Entries	50
Mean	59.92
Std Dev	2.252



# B0L000S, U18-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

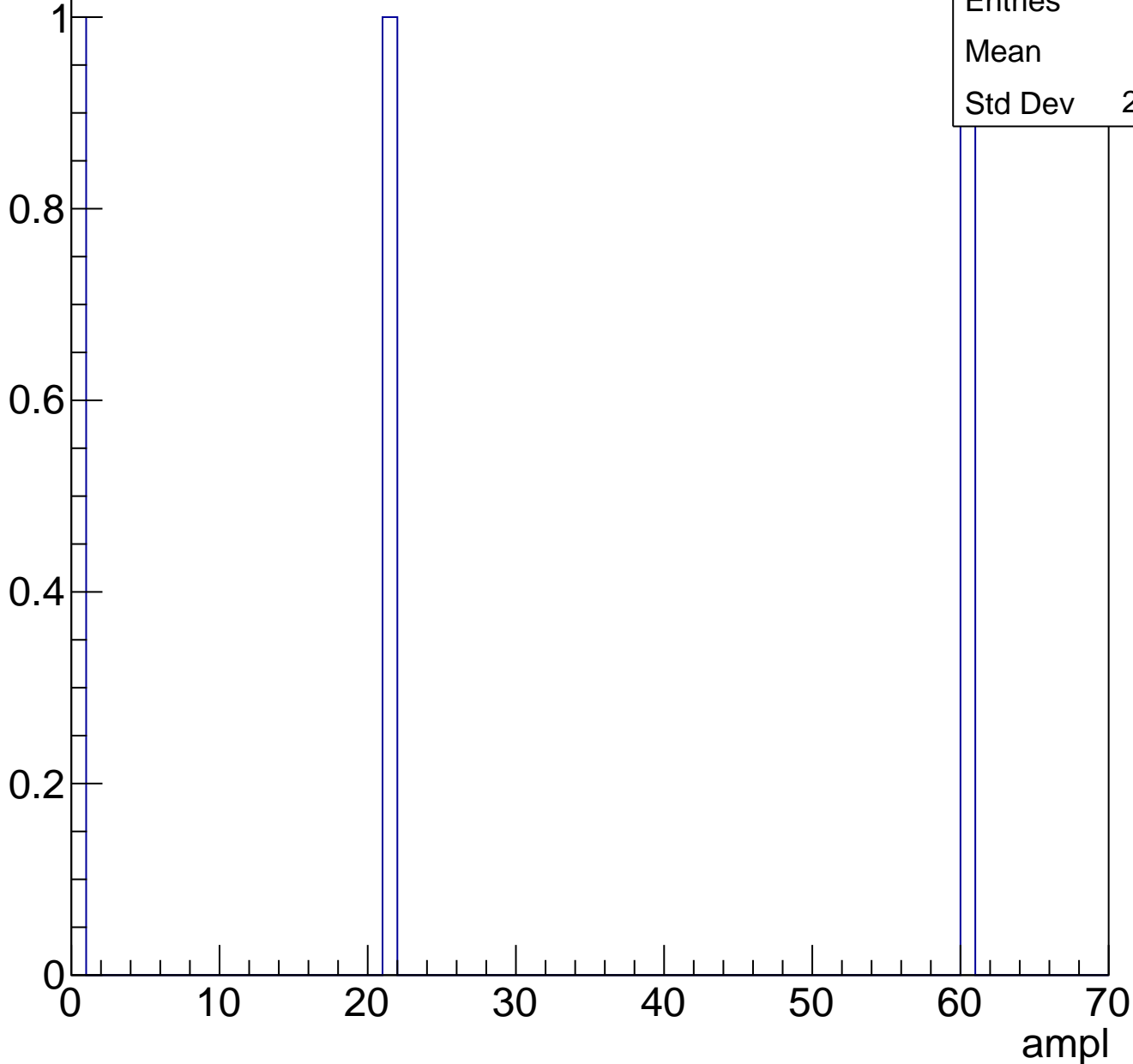




# B0L000S, U18-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch98, adc0

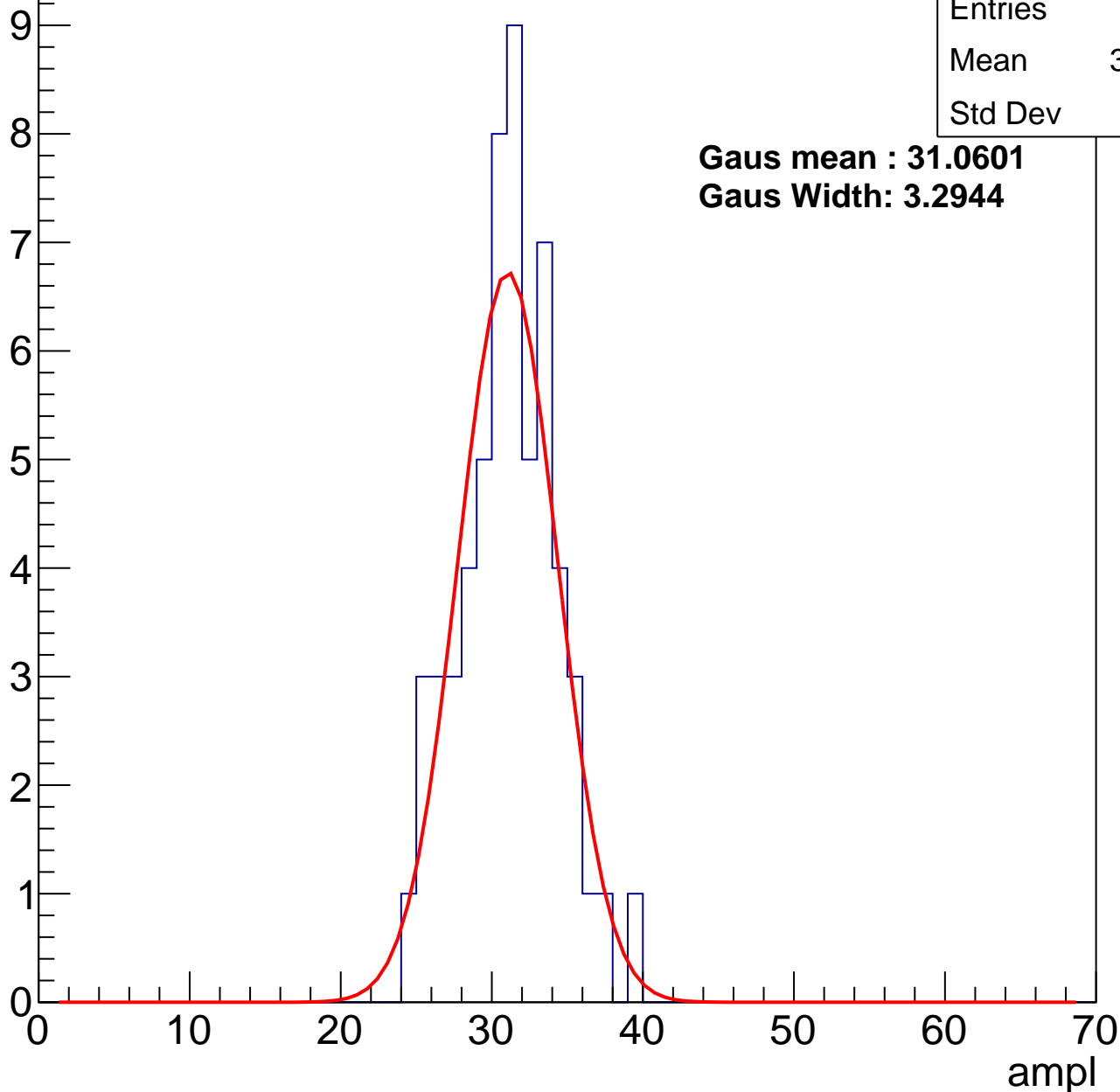
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	30.66
Std Dev	3.16

**Gaus mean : 31.0601**

**Gaus Width: 3.2944**



# B0L000S, U18-ch98, adc1

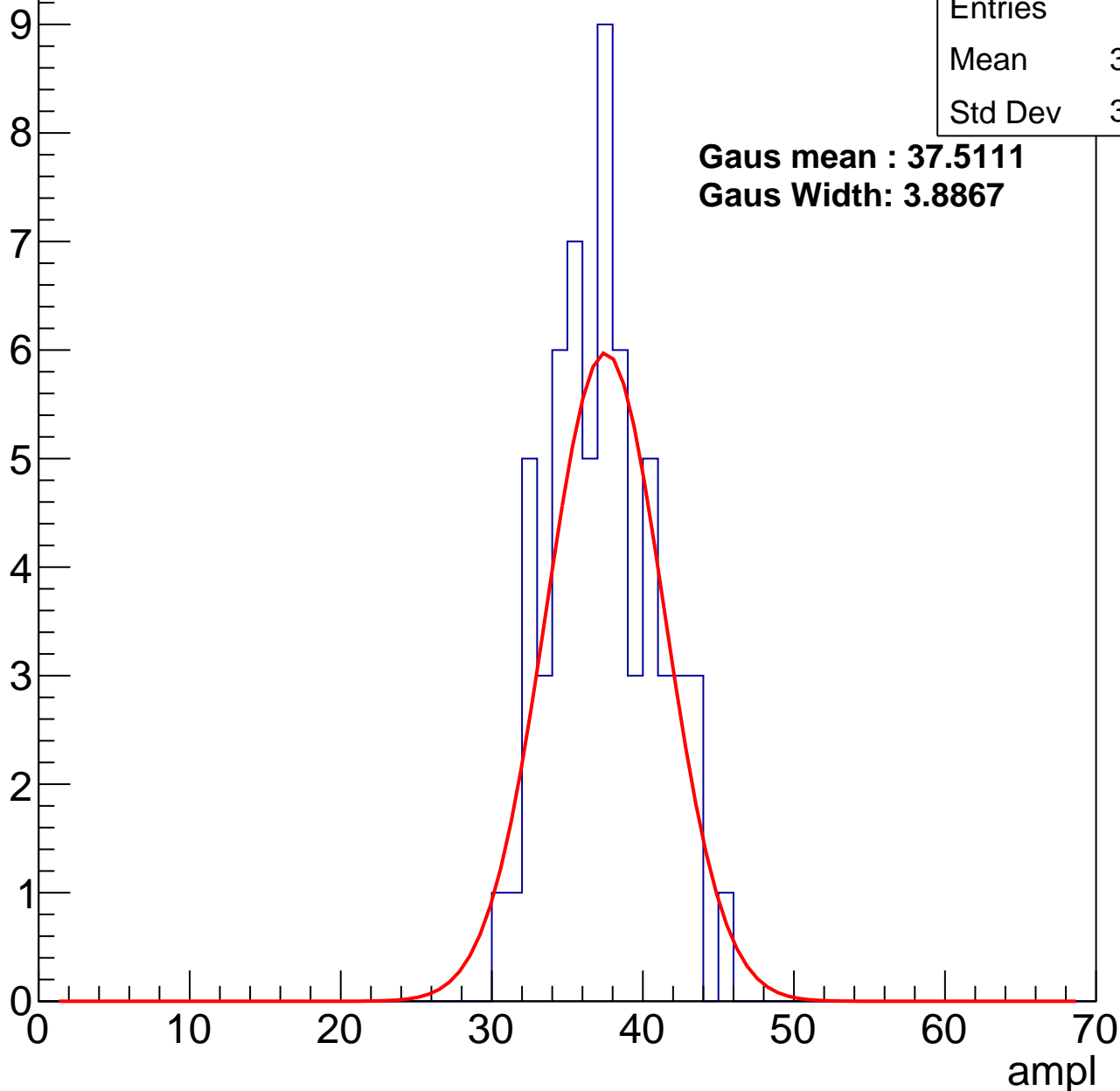
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.89
Std Dev	3.393

**Gaus mean : 37.5111**

**Gaus Width: 3.8867**

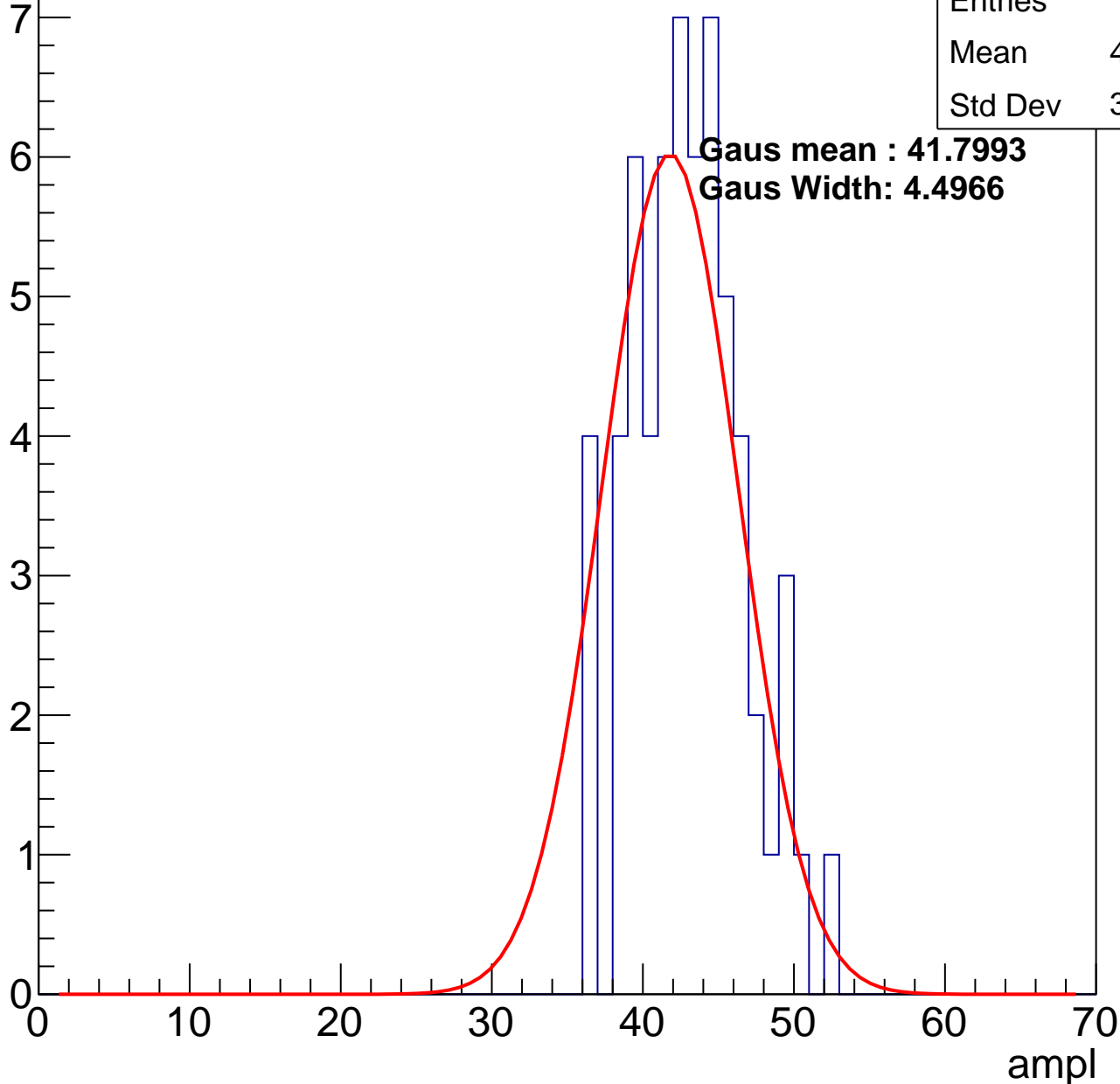


# B0L000S, U18-ch98, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

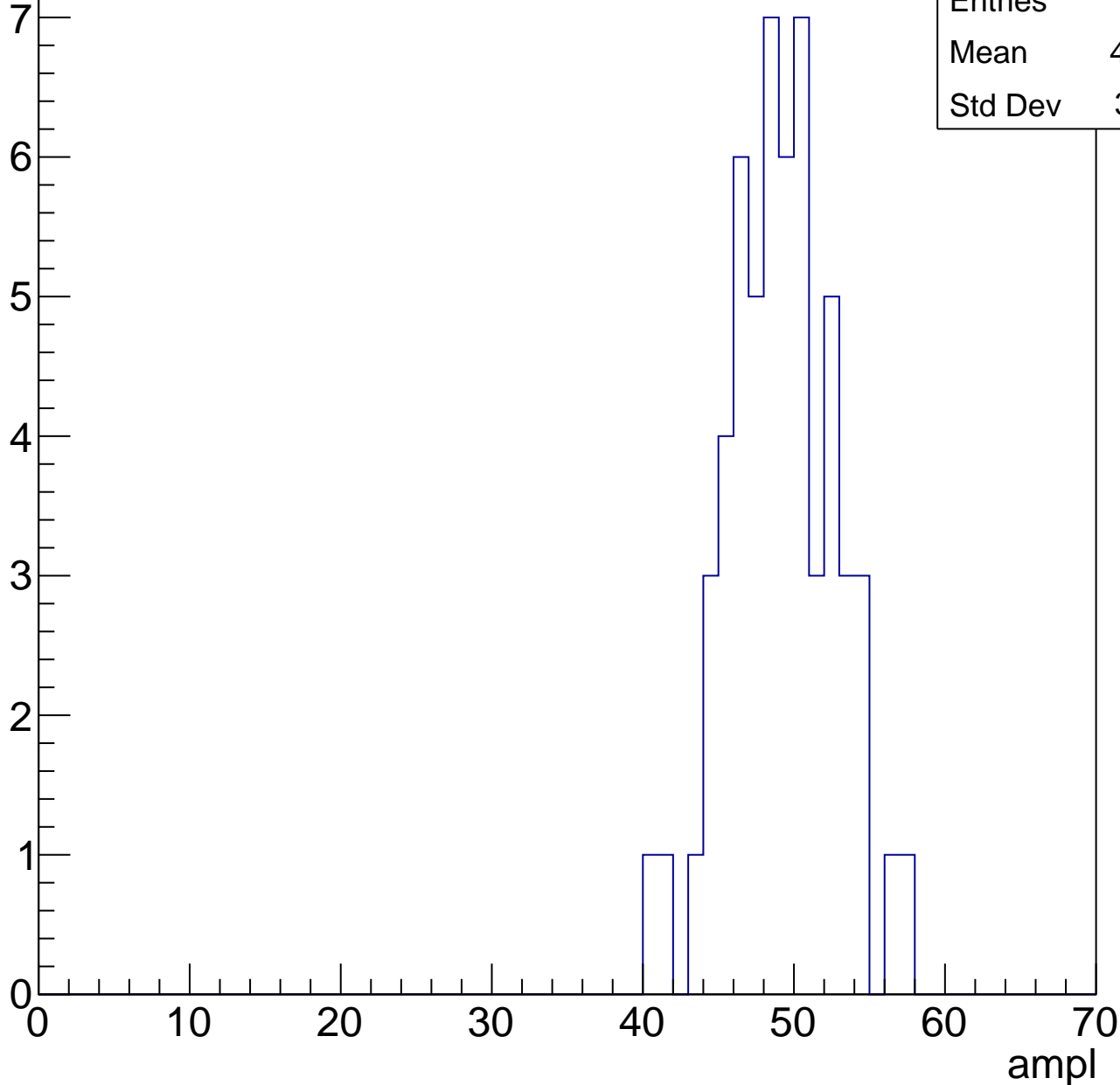
Entries	61
Mean	42.56
Std Dev	3.633



# B0L000S, U18-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

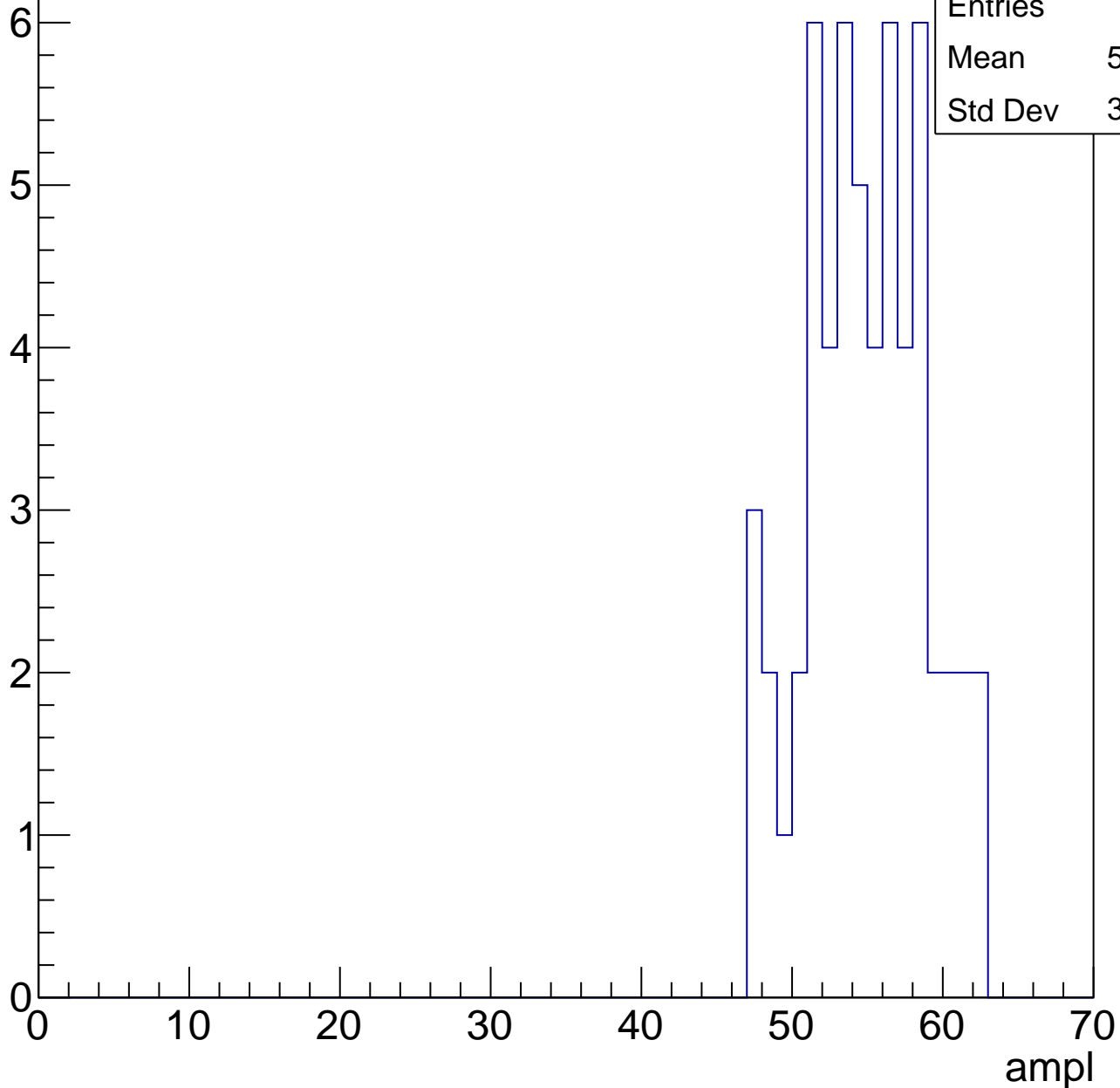
Entry



# B0L000S, U18-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

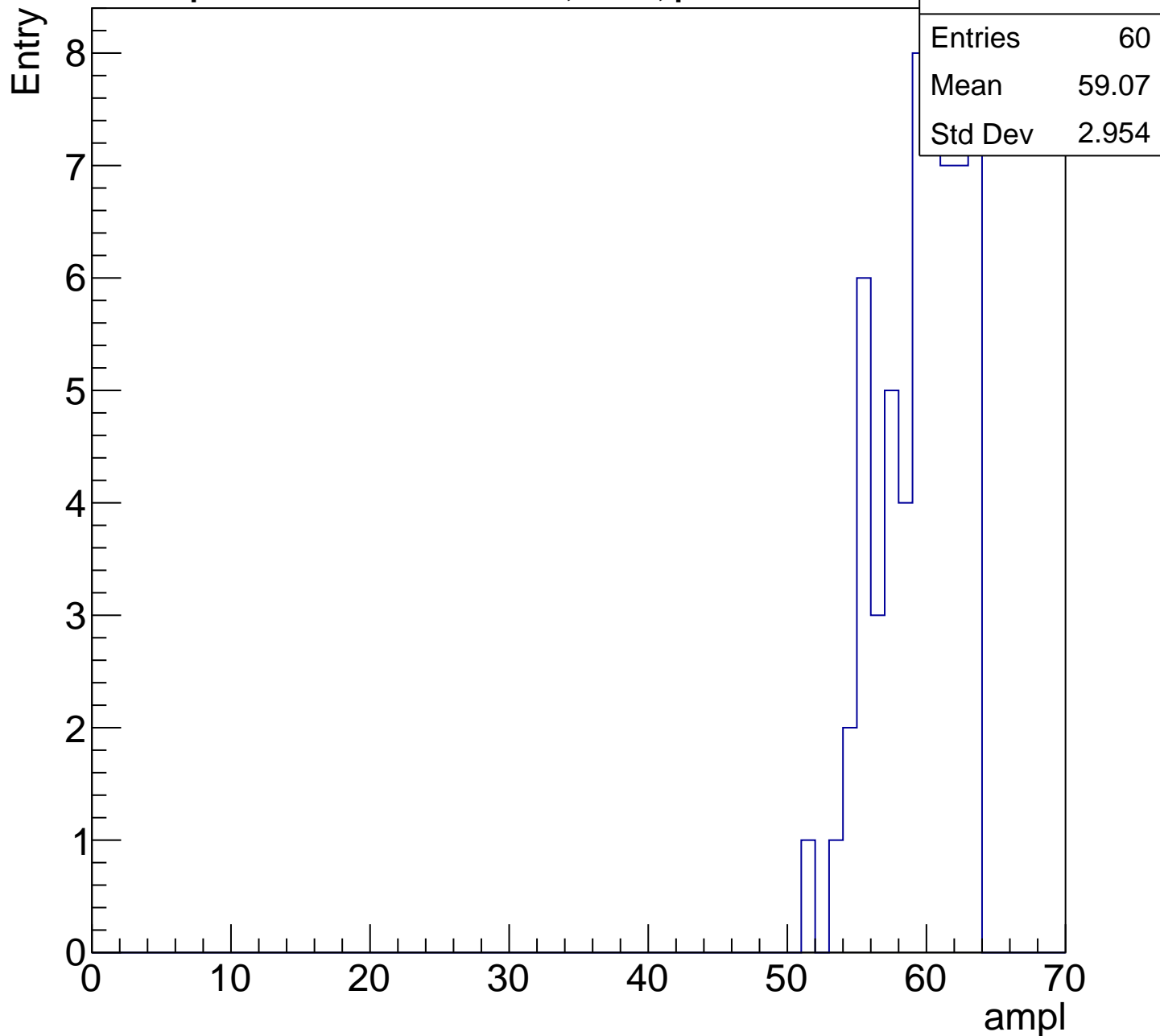
Entry



Entries	57
Mean	54.46
Std Dev	3.857

# B0L000S, U18-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U18-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

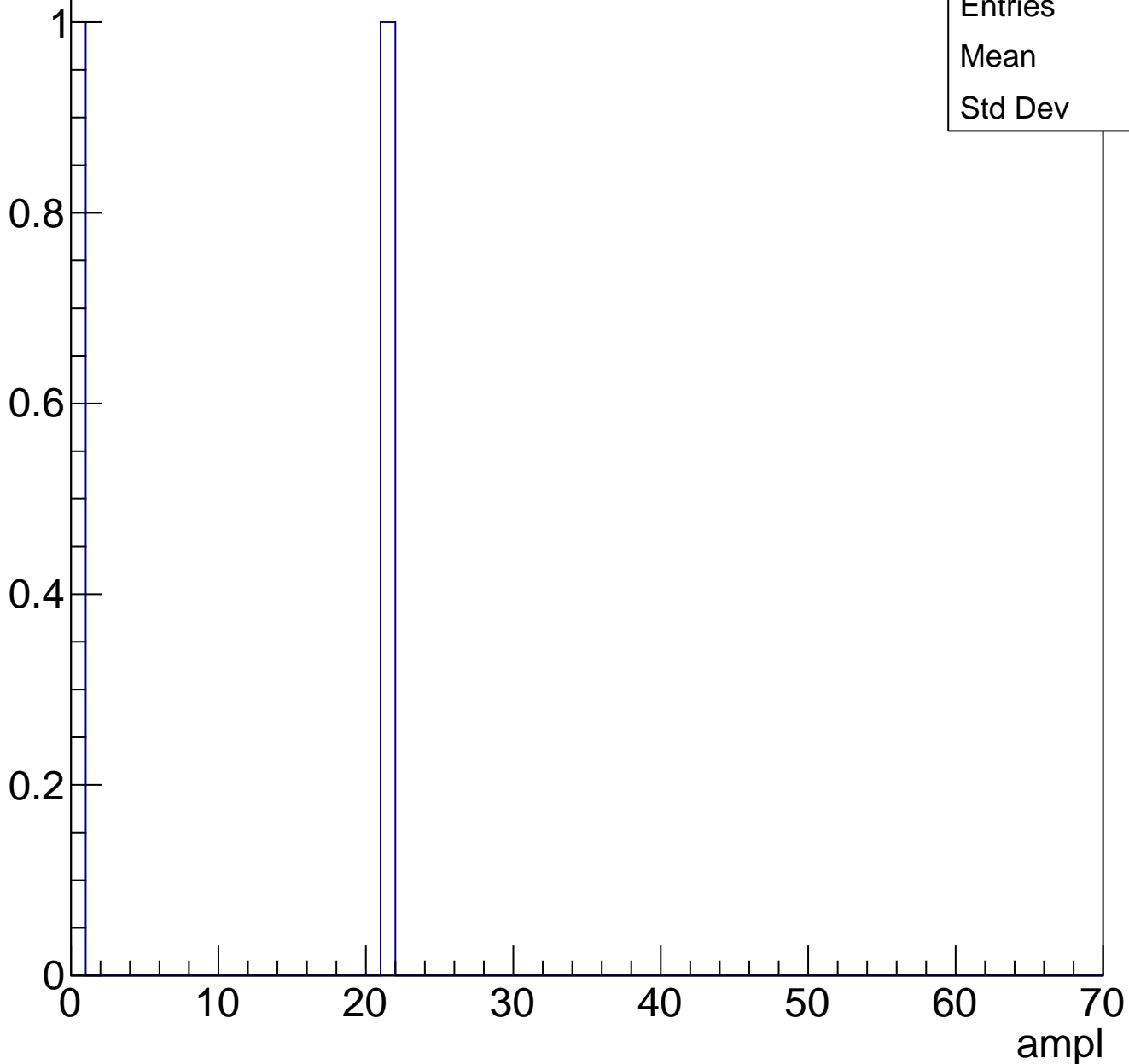




# B0L000S, U18-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch99, adc0

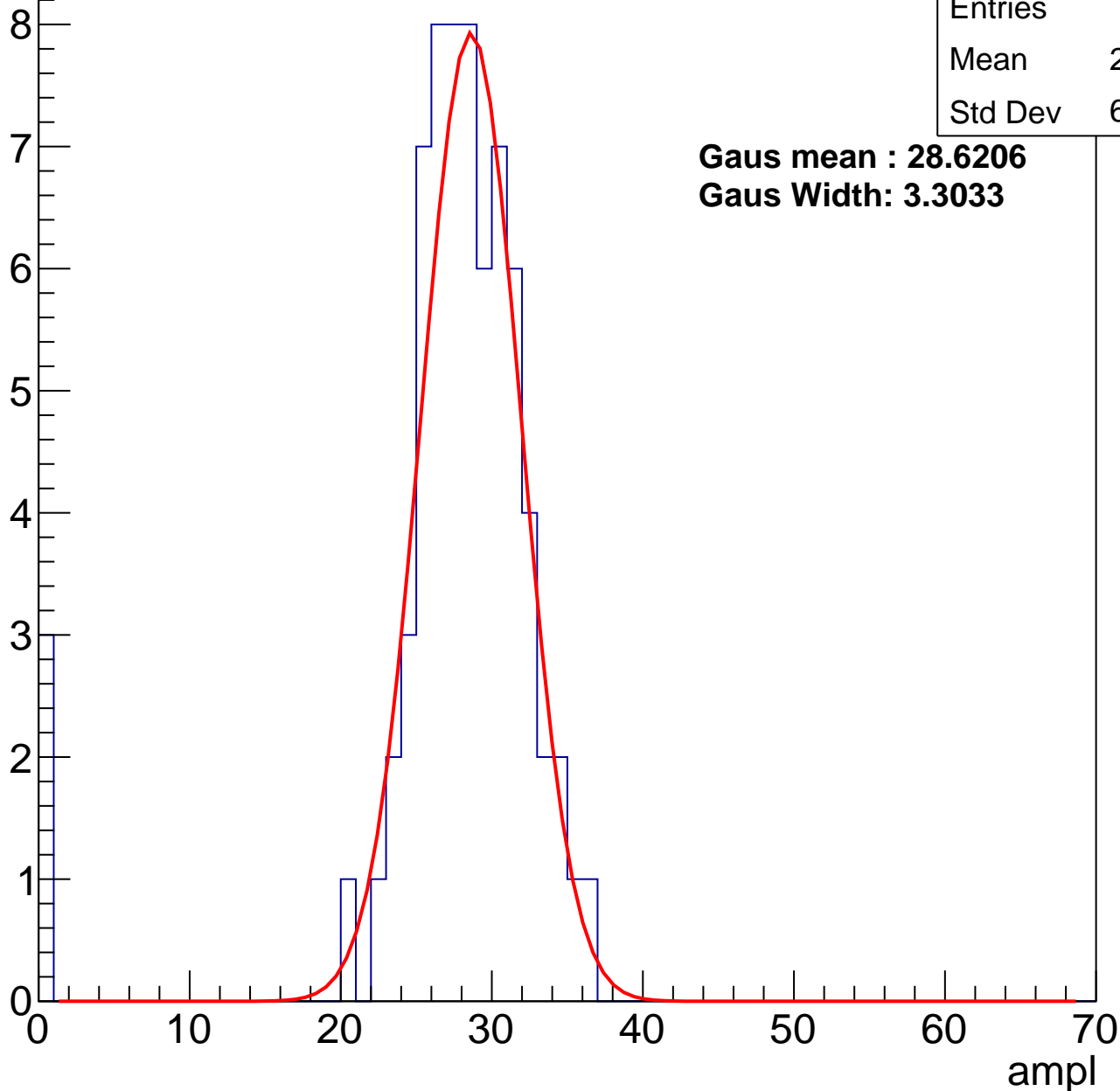
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	26.94
Std Dev	6.505

**Gaus mean : 28.6206**

**Gaus Width: 3.3033**



# B0L000S, U18-ch99, adc1

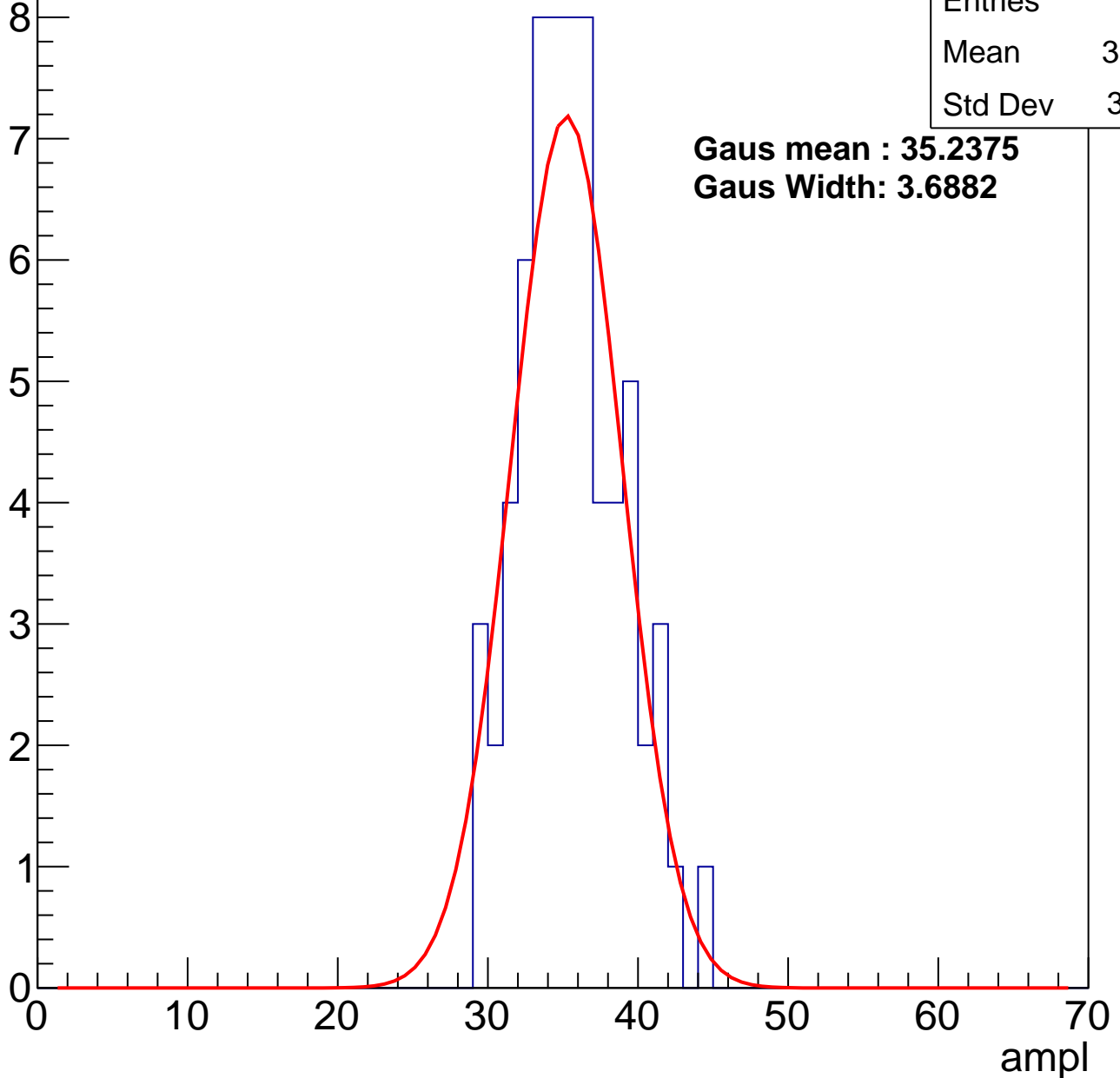
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	35.09
Std Dev	3.331

**Gaus mean : 35.2375**

**Gaus Width: 3.6882**



# B0L000S, U18-ch99, adc2

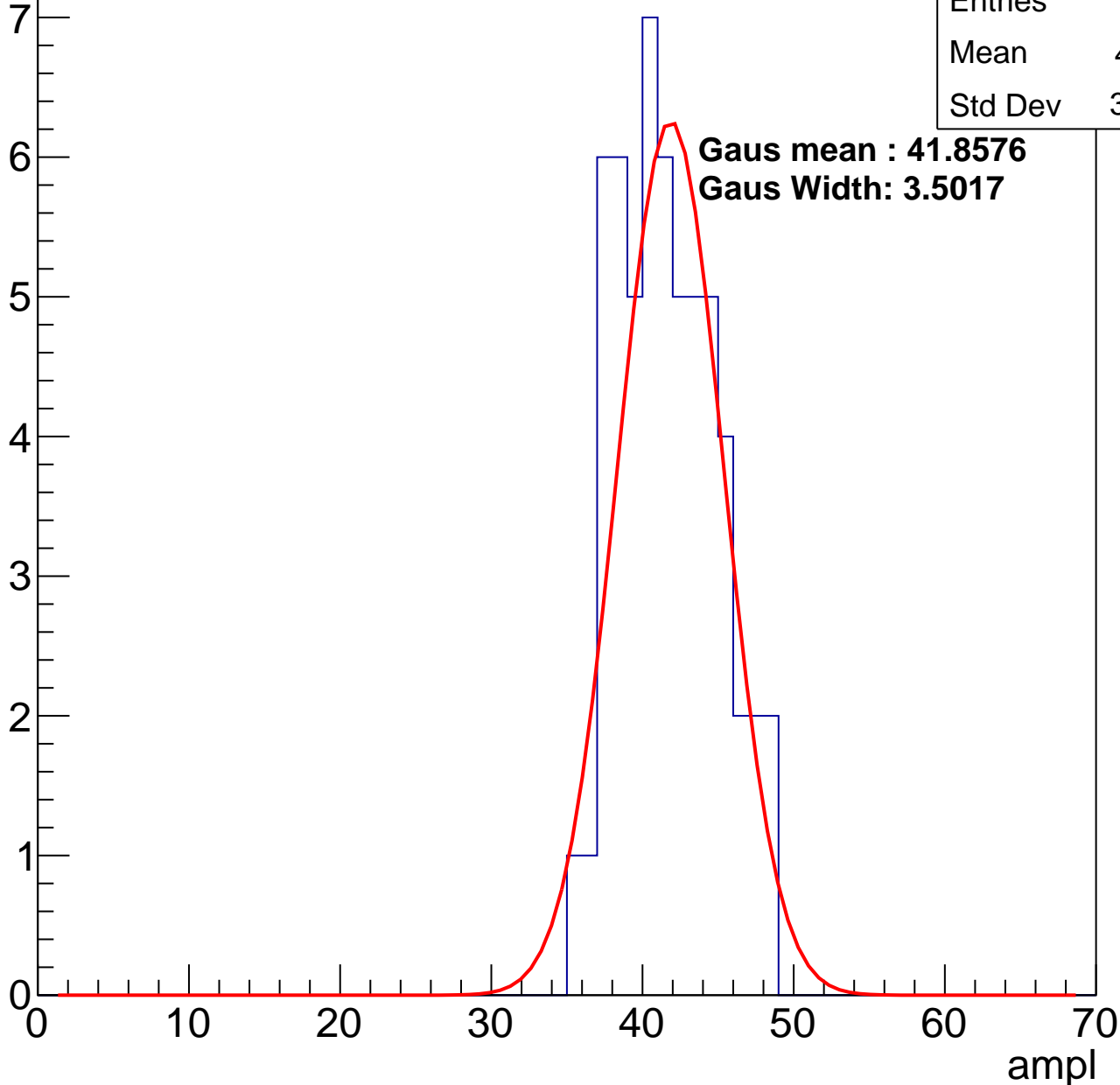
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	41.21
Std Dev	3.216

**Gaus mean : 41.8576**

**Gaus Width: 3.5017**

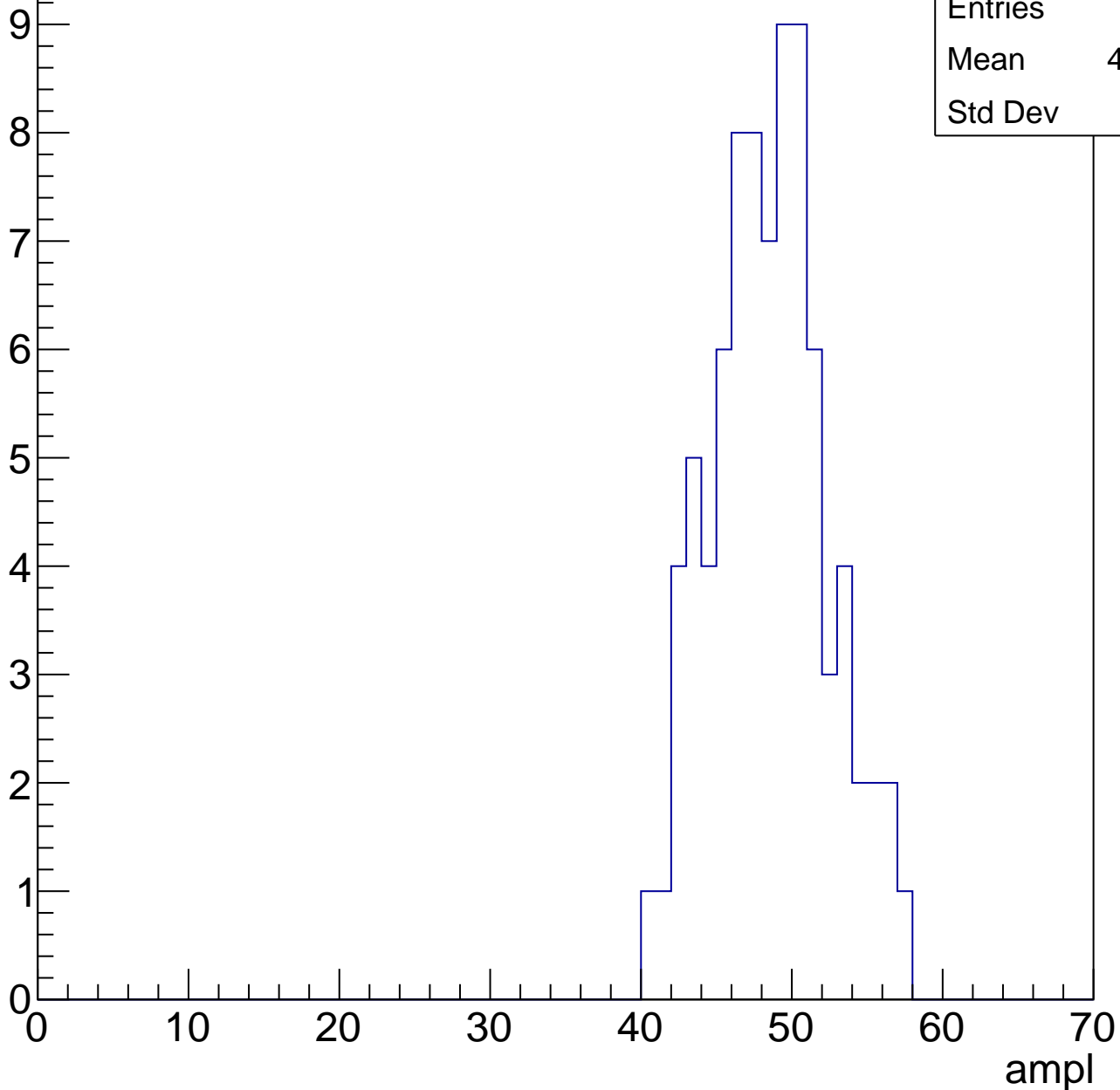


# B0L000S, U18-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	48.07
Std Dev	3.77

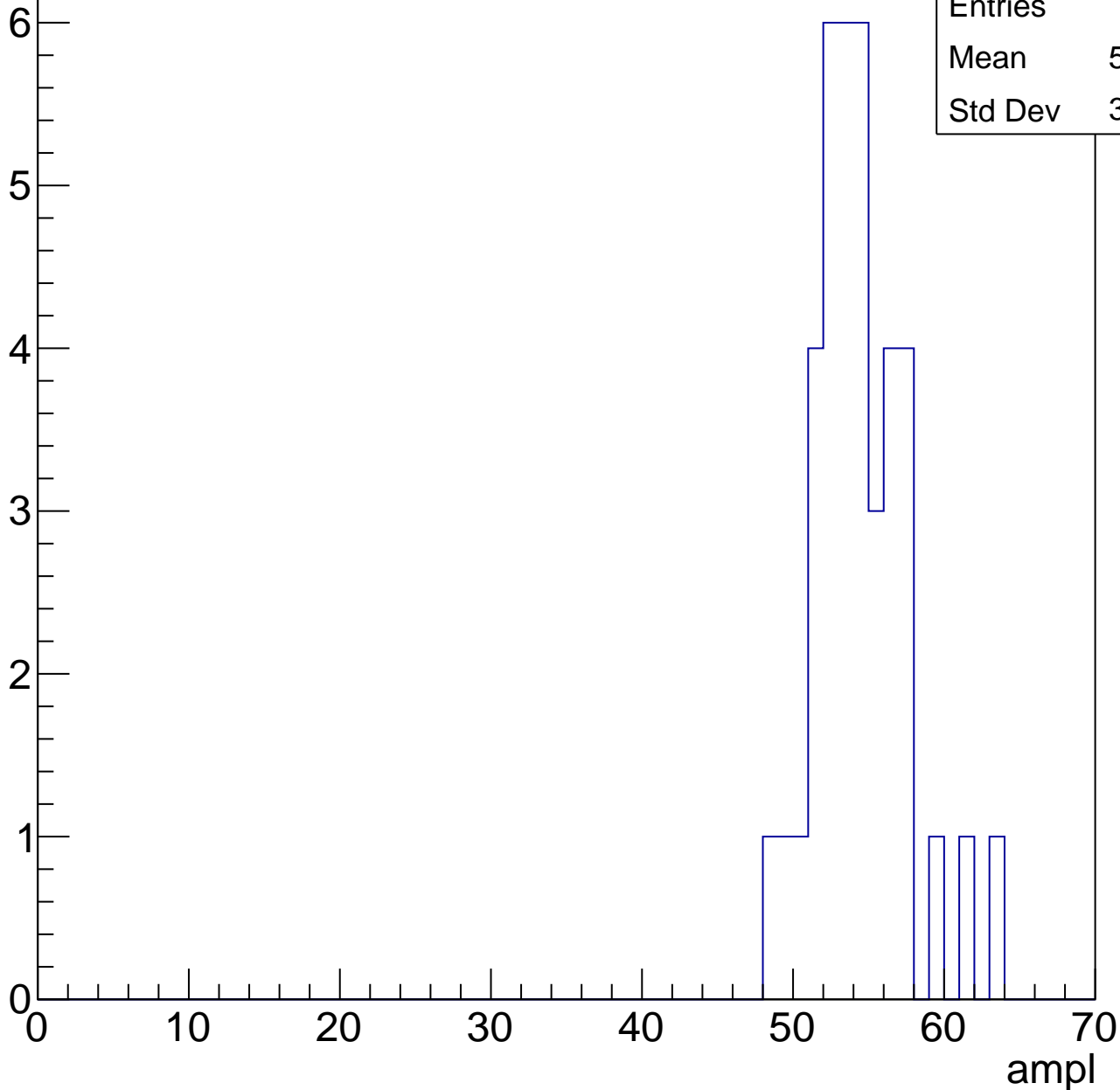


# B0L000S, U18-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

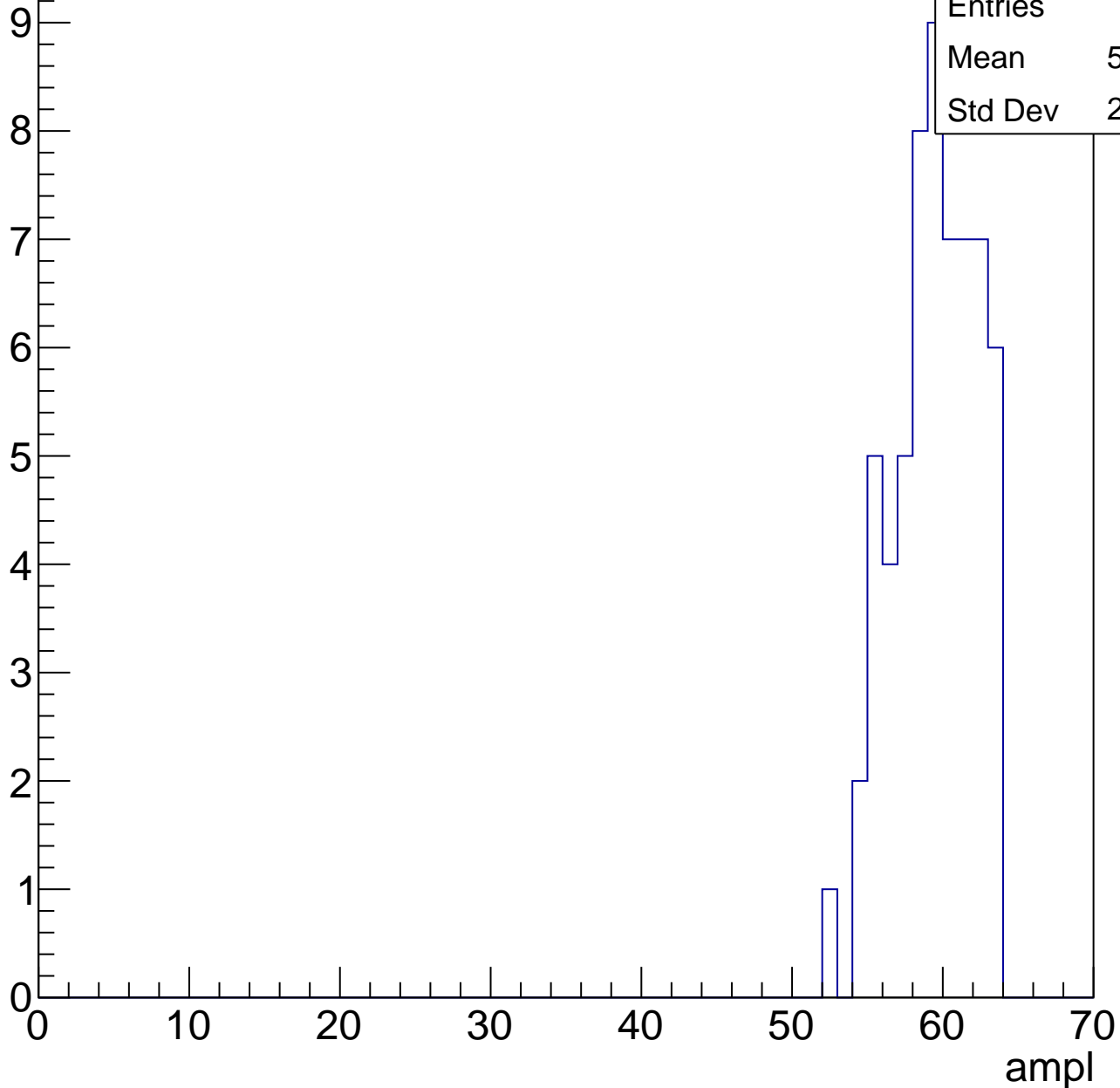
Entries	39
Mean	53.97
Std Dev	3.008



# B0L000S, U18-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

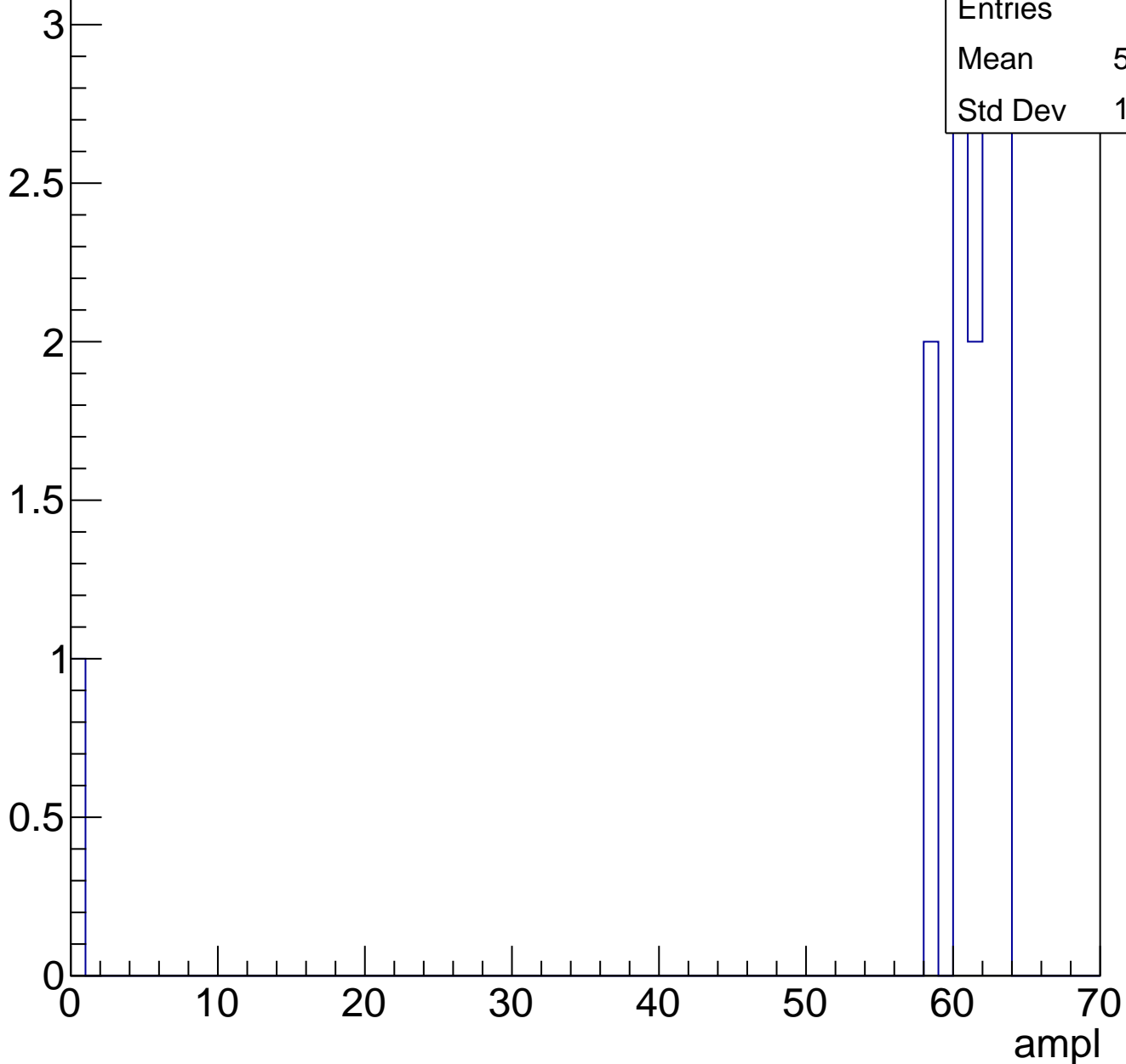
Entry



# B0L000S, U18-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	26.57
Std Dev	5.55

**Gaus mean : 28.3029**

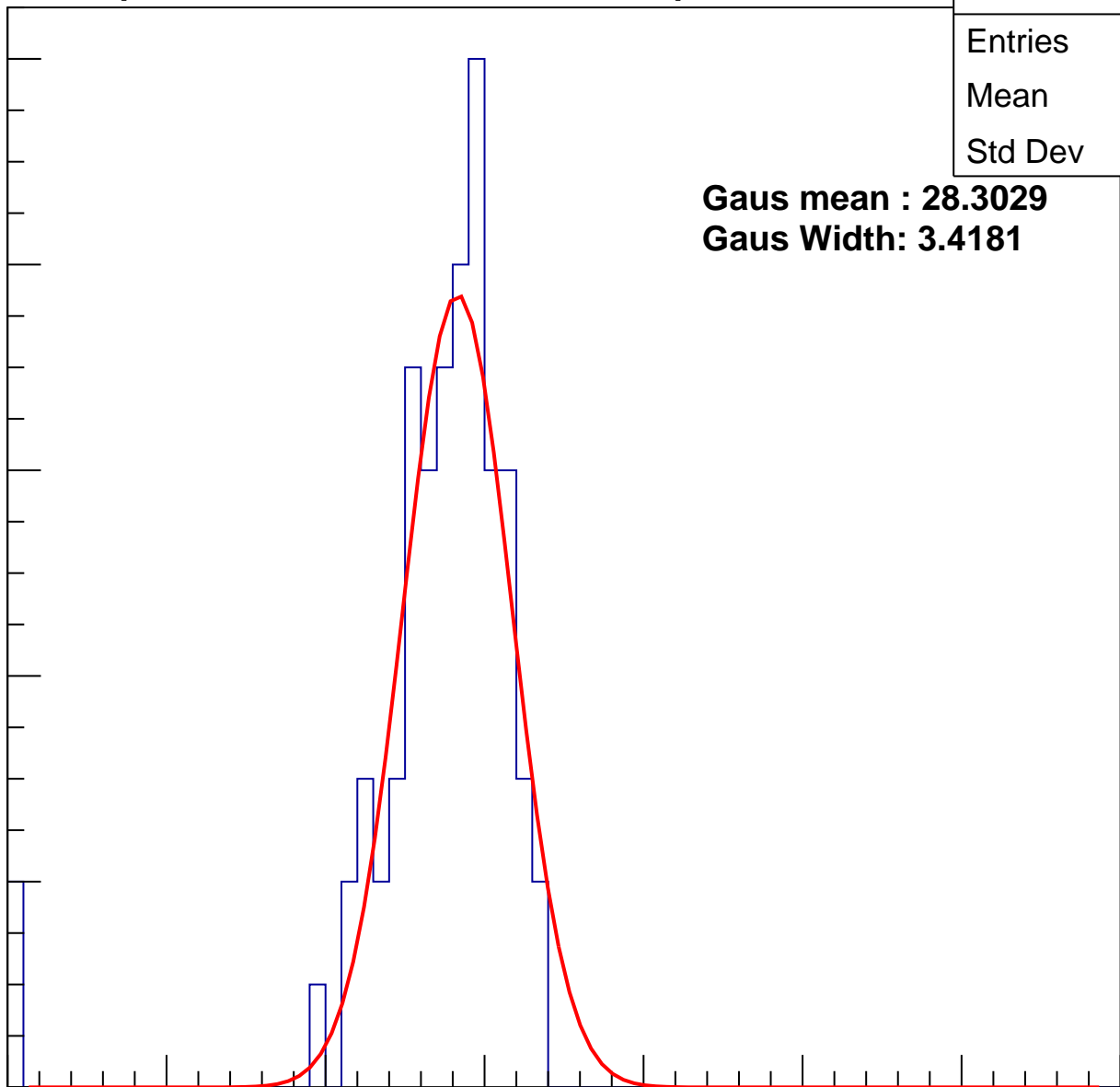
**Gaus Width: 3.4181**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch100, adc1

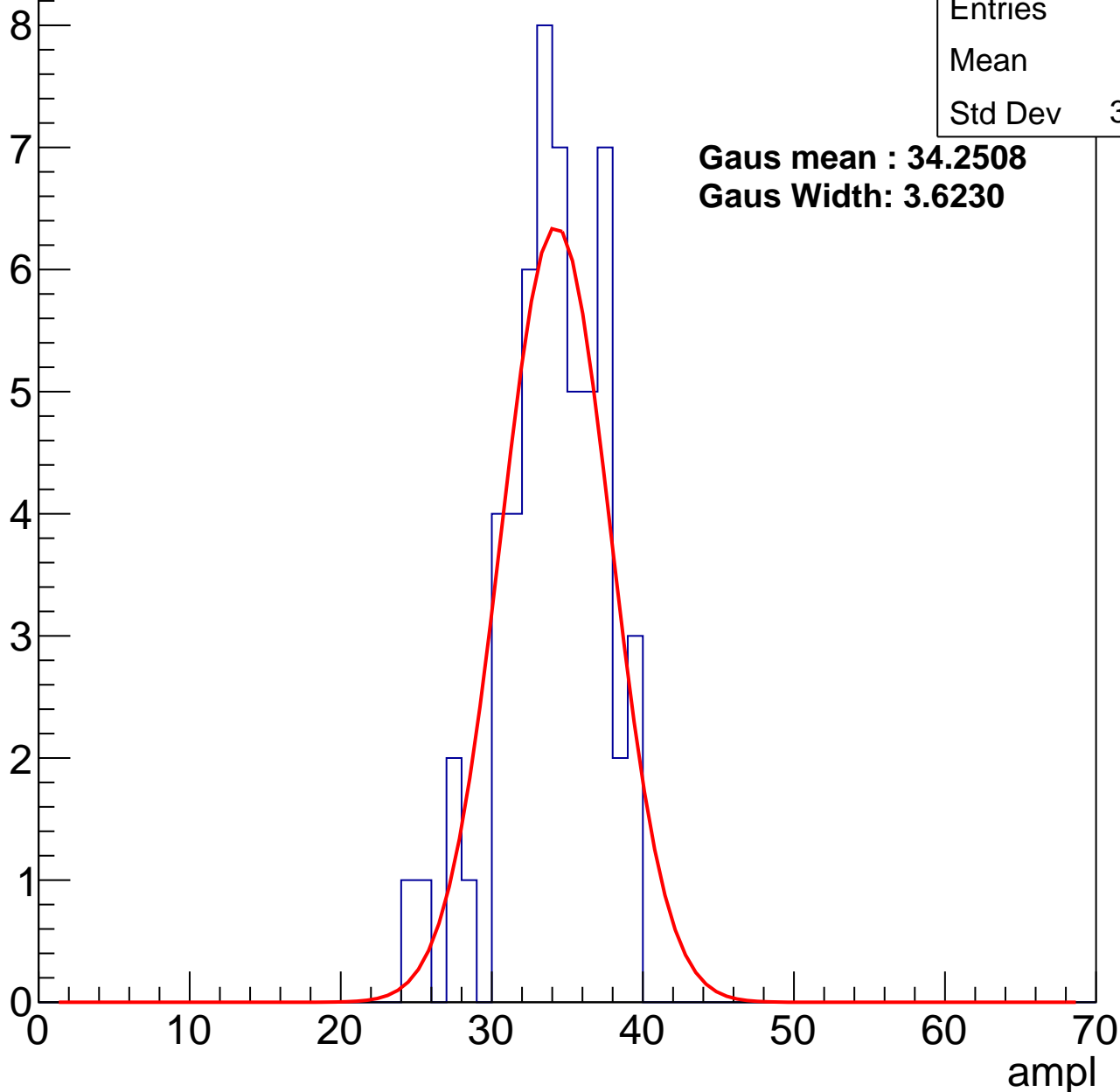
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	33.5
Std Dev	3.349

**Gaus mean : 34.2508**

**Gaus Width: 3.6230**



# B0L000S, U18-ch100, adc2

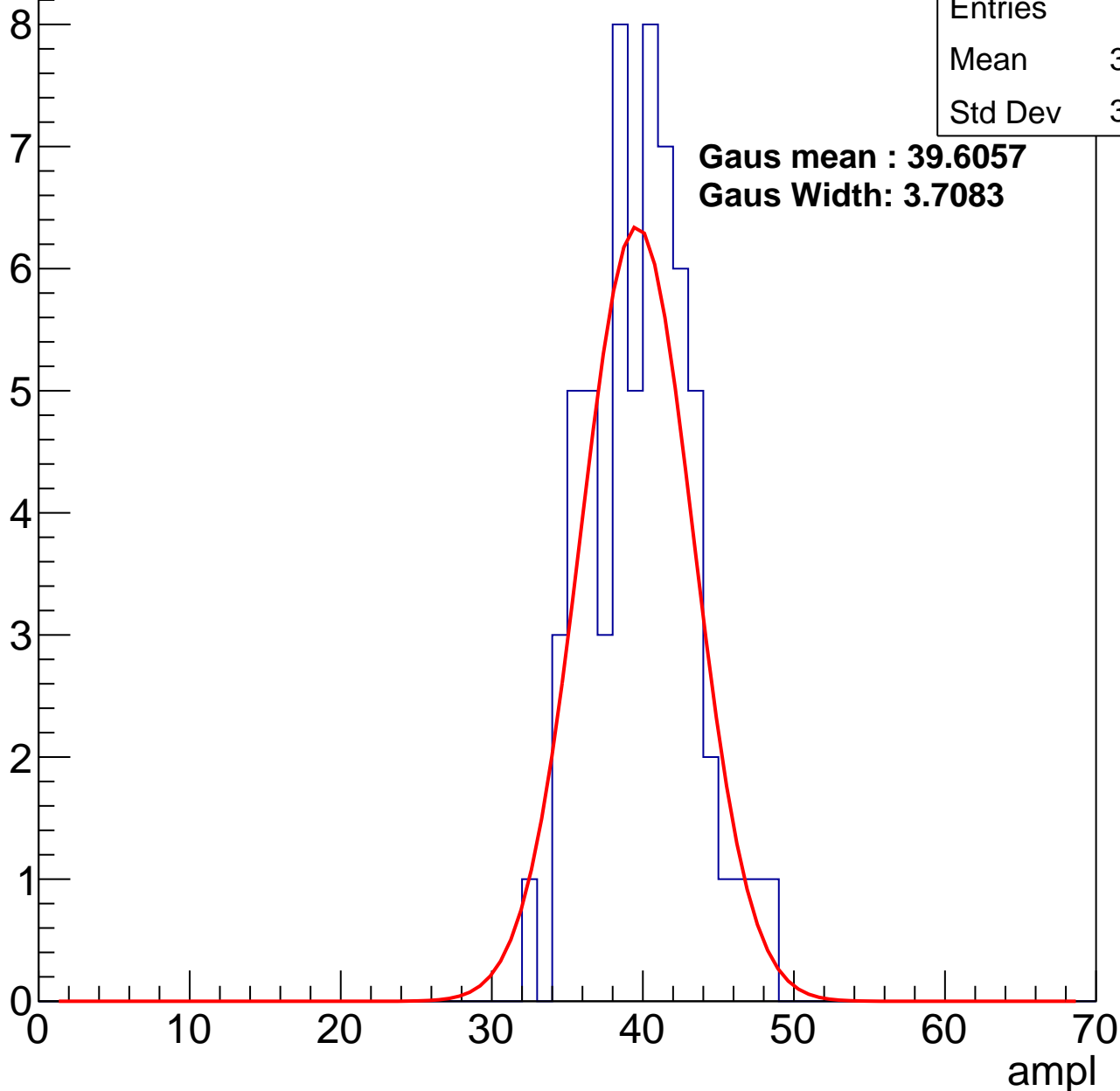
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	39.47
Std Dev	3.373

**Gaus mean : 39.6057**

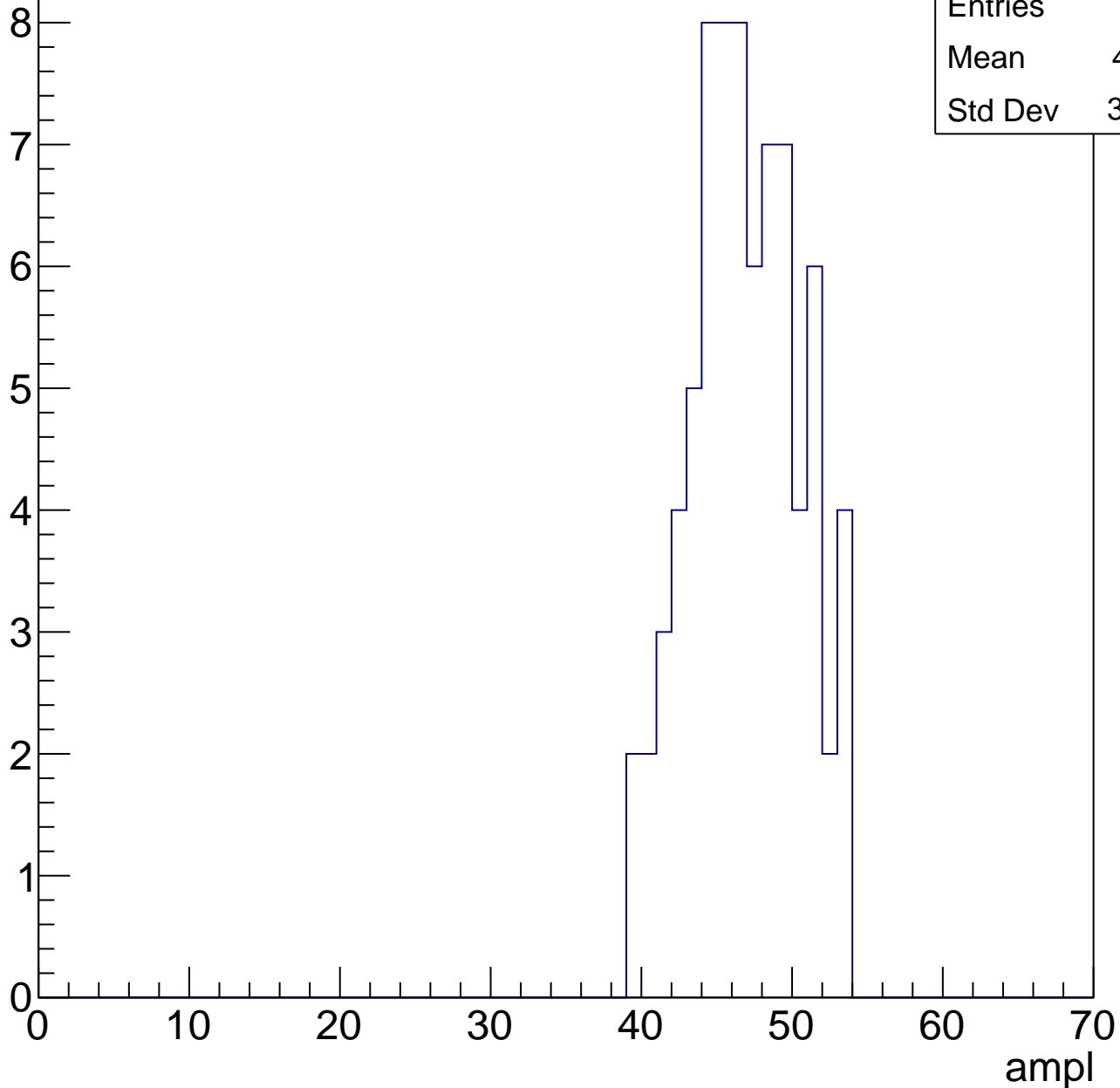
**Gaus Width: 3.7083**



# B0L000S, U18-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

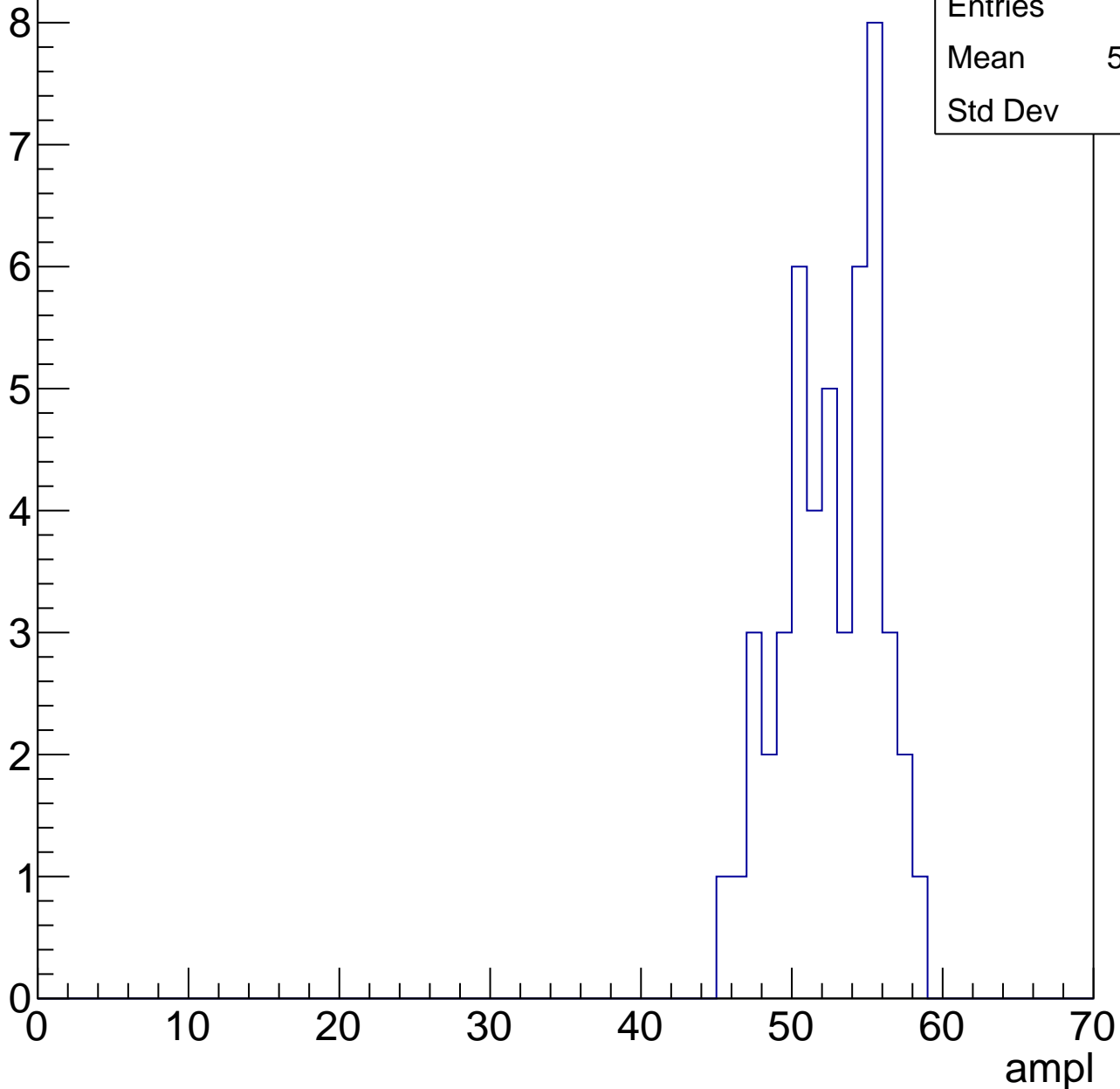


# B0L000S, U18-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	52.12
Std Dev	3.16

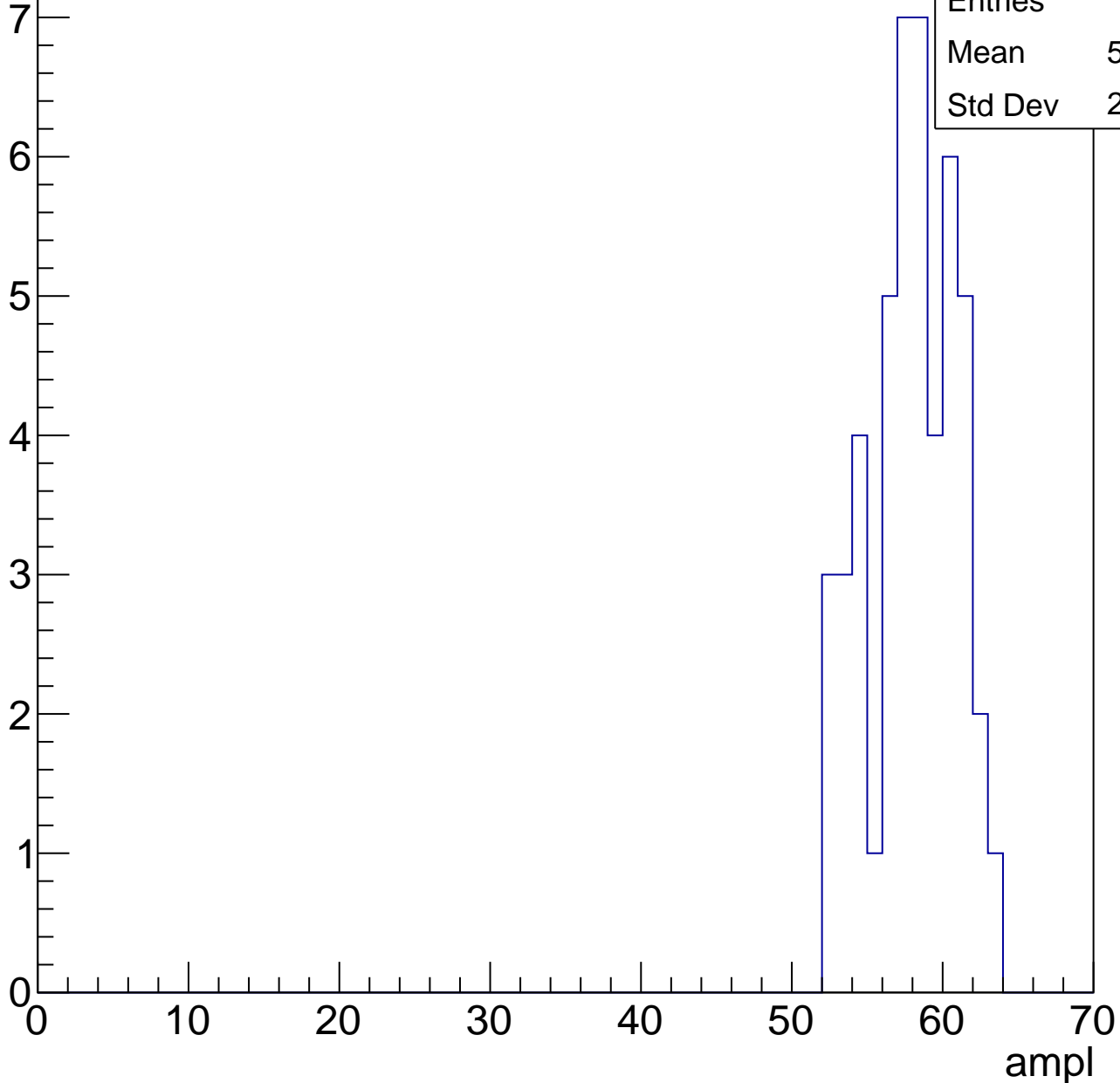


# B0L000S, U18-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	57.48
Std Dev	2.887

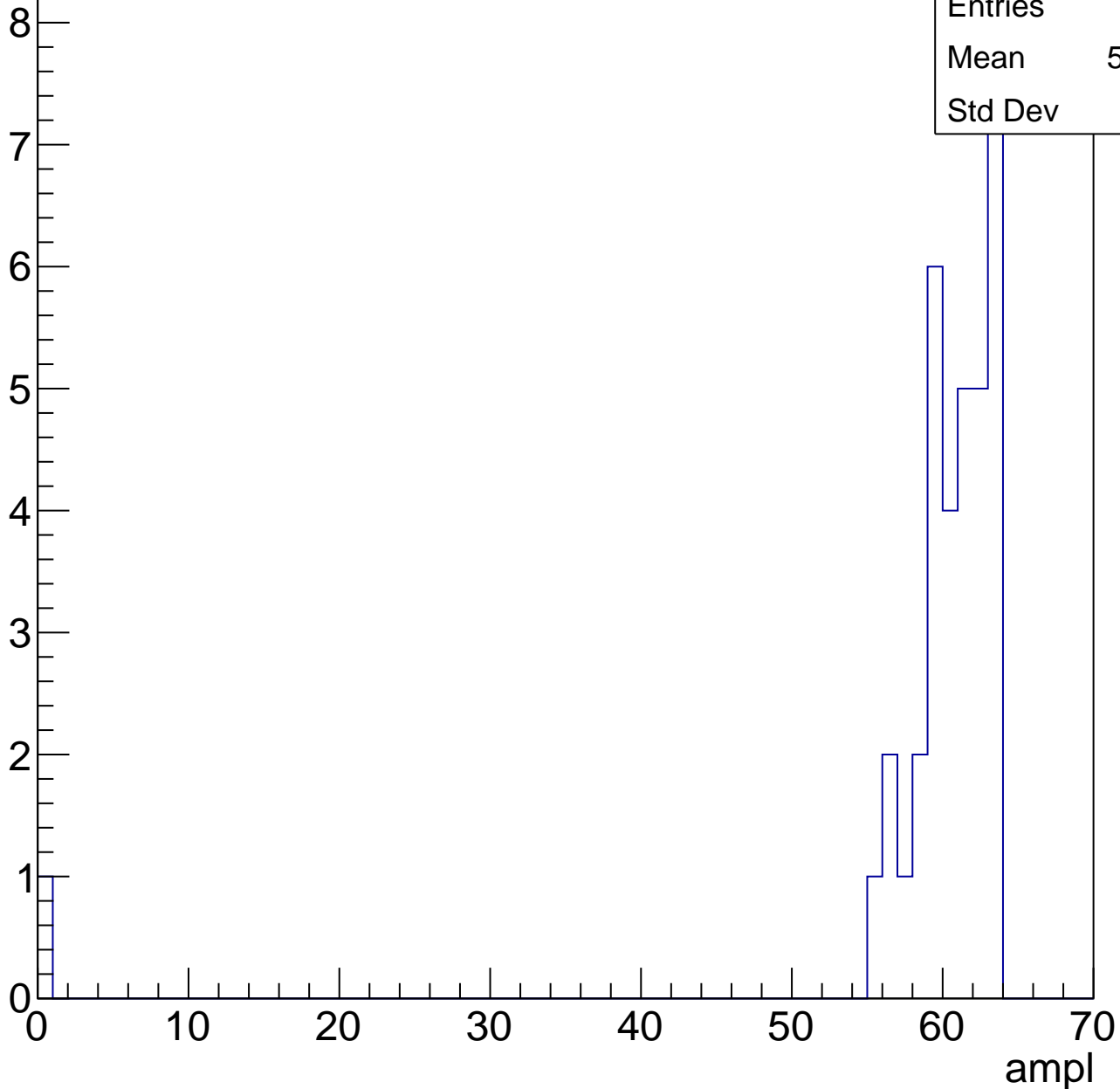


# B0L000S, U18-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.66
Std Dev	10.3

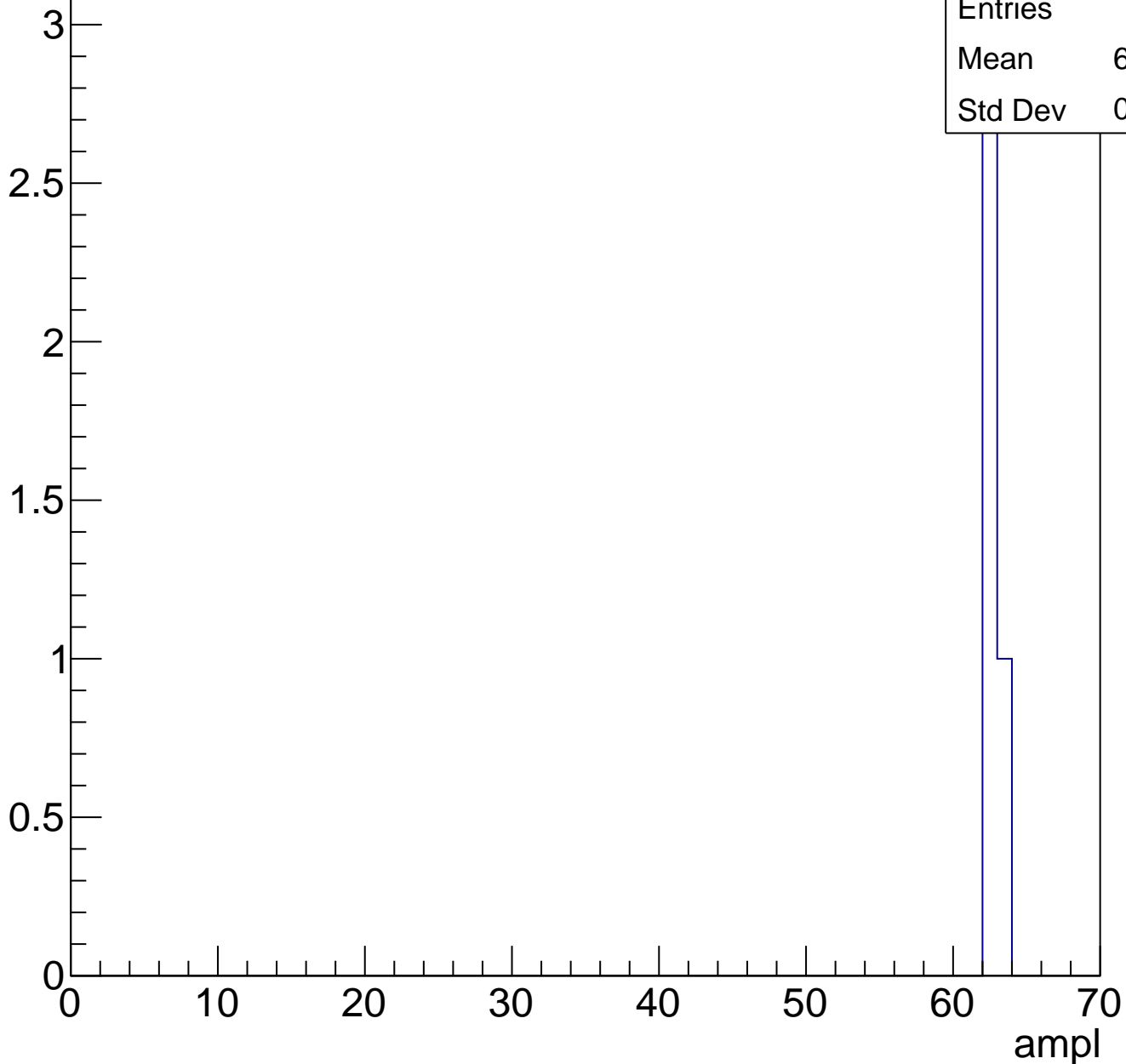




# B0L000S, U18-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch101, adc0

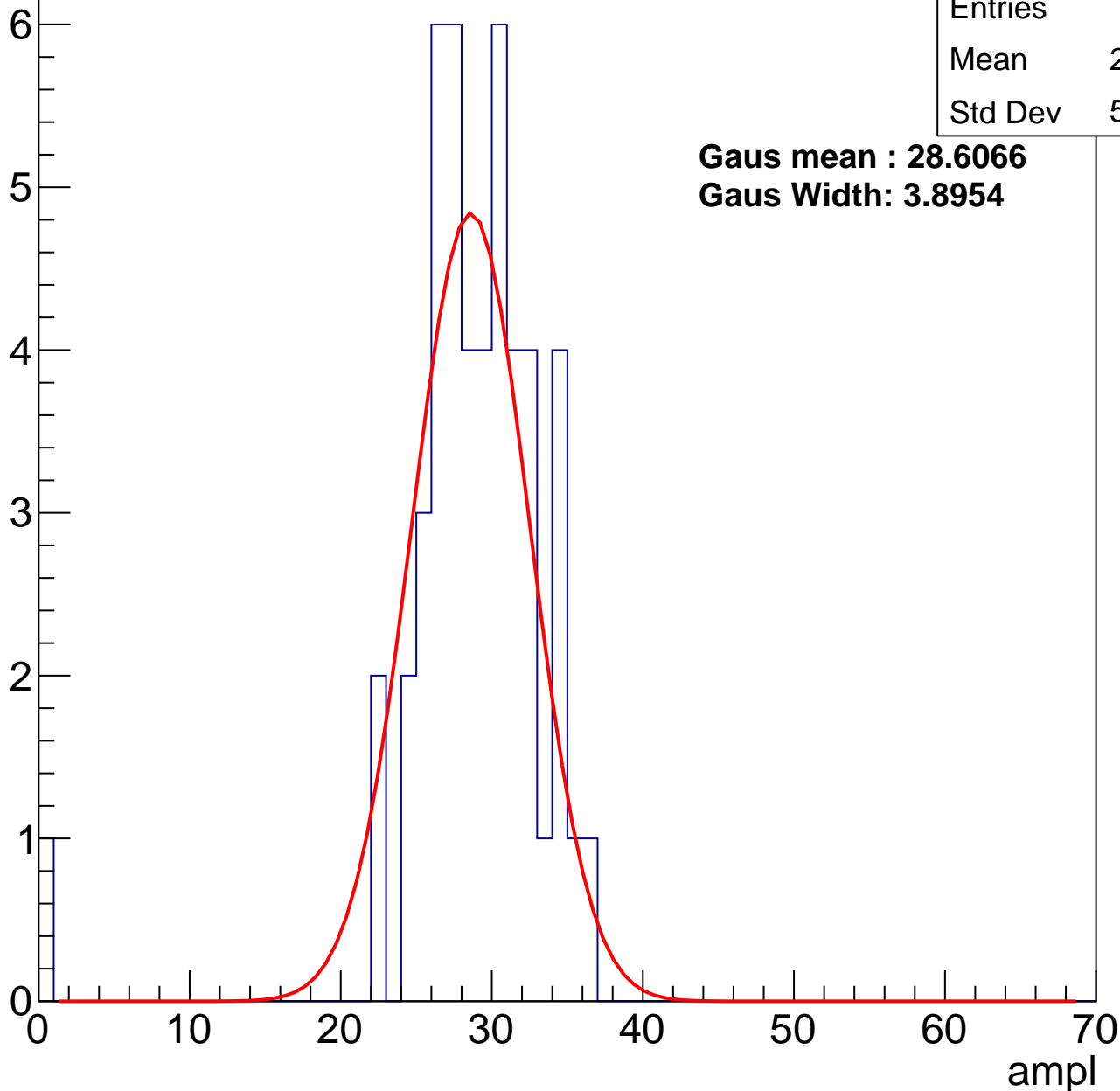
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	28.27
Std Dev	5.252

**Gaus mean : 28.6066**

**Gaus Width: 3.8954**



# B0L000S, U18-ch101, adc1

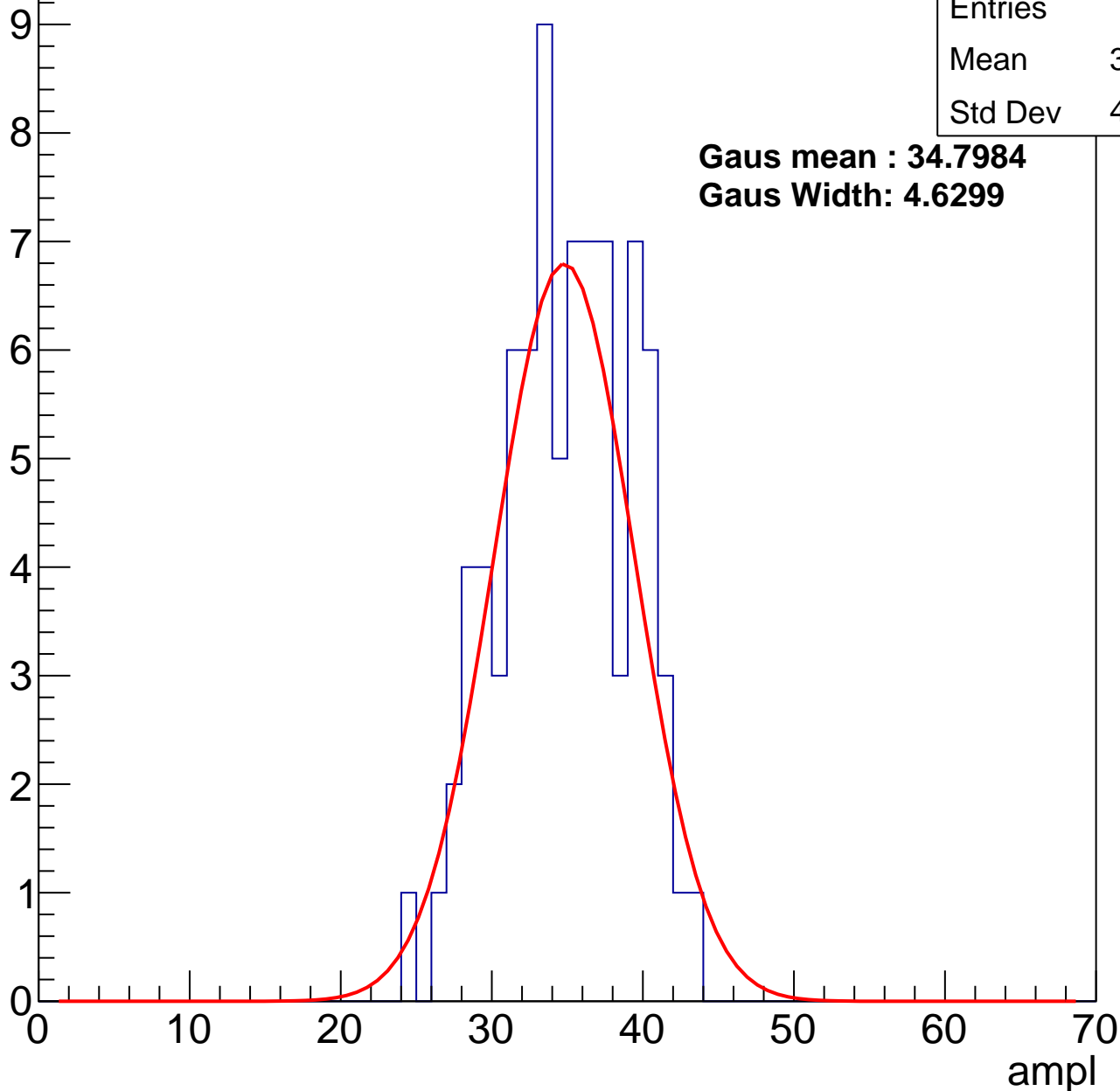
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	34.43
Std Dev	4.186

**Gaus mean : 34.7984**

**Gaus Width: 4.6299**

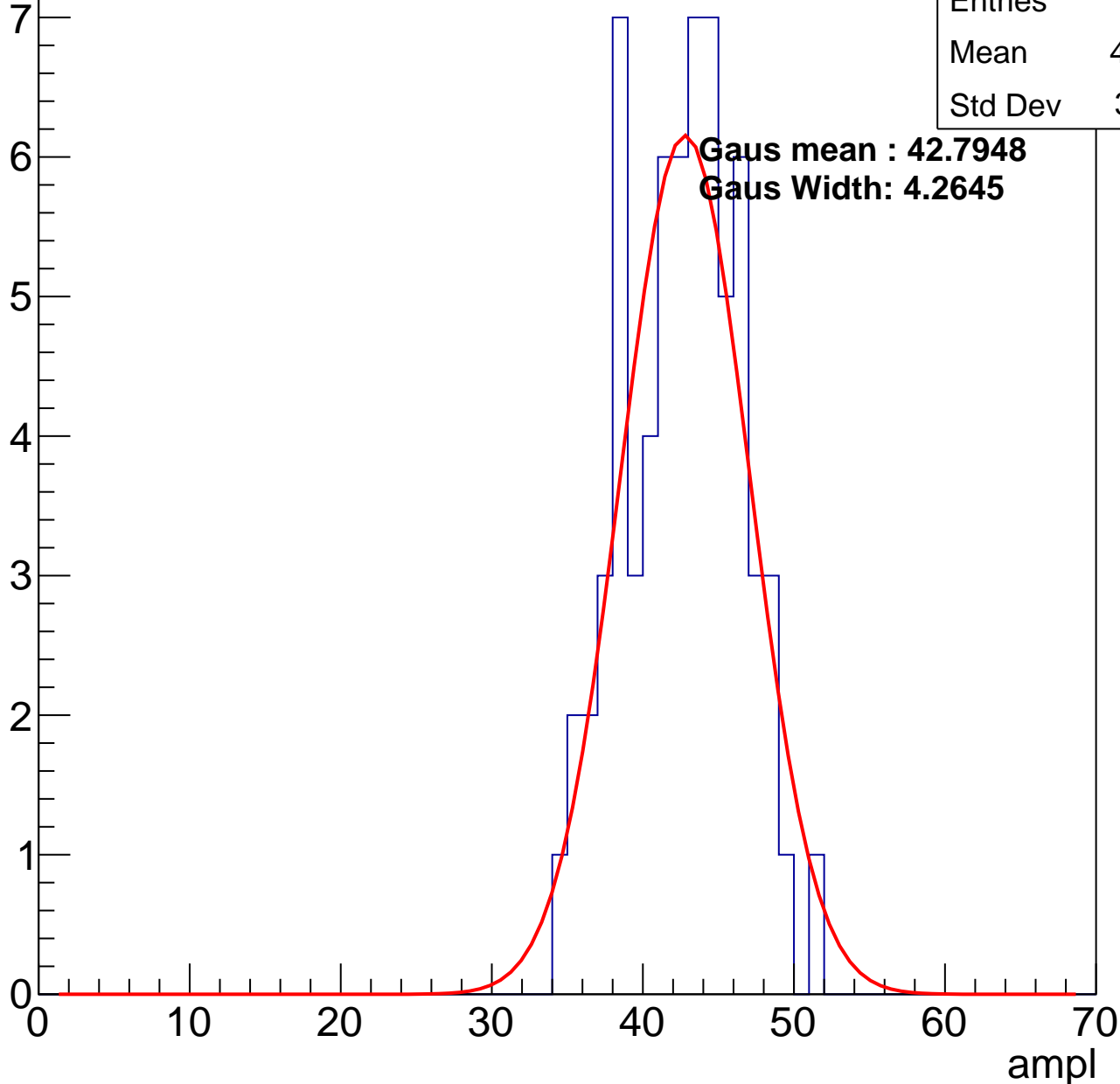


# B0L000S, U18-ch101, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	42.13
Std Dev	3.781

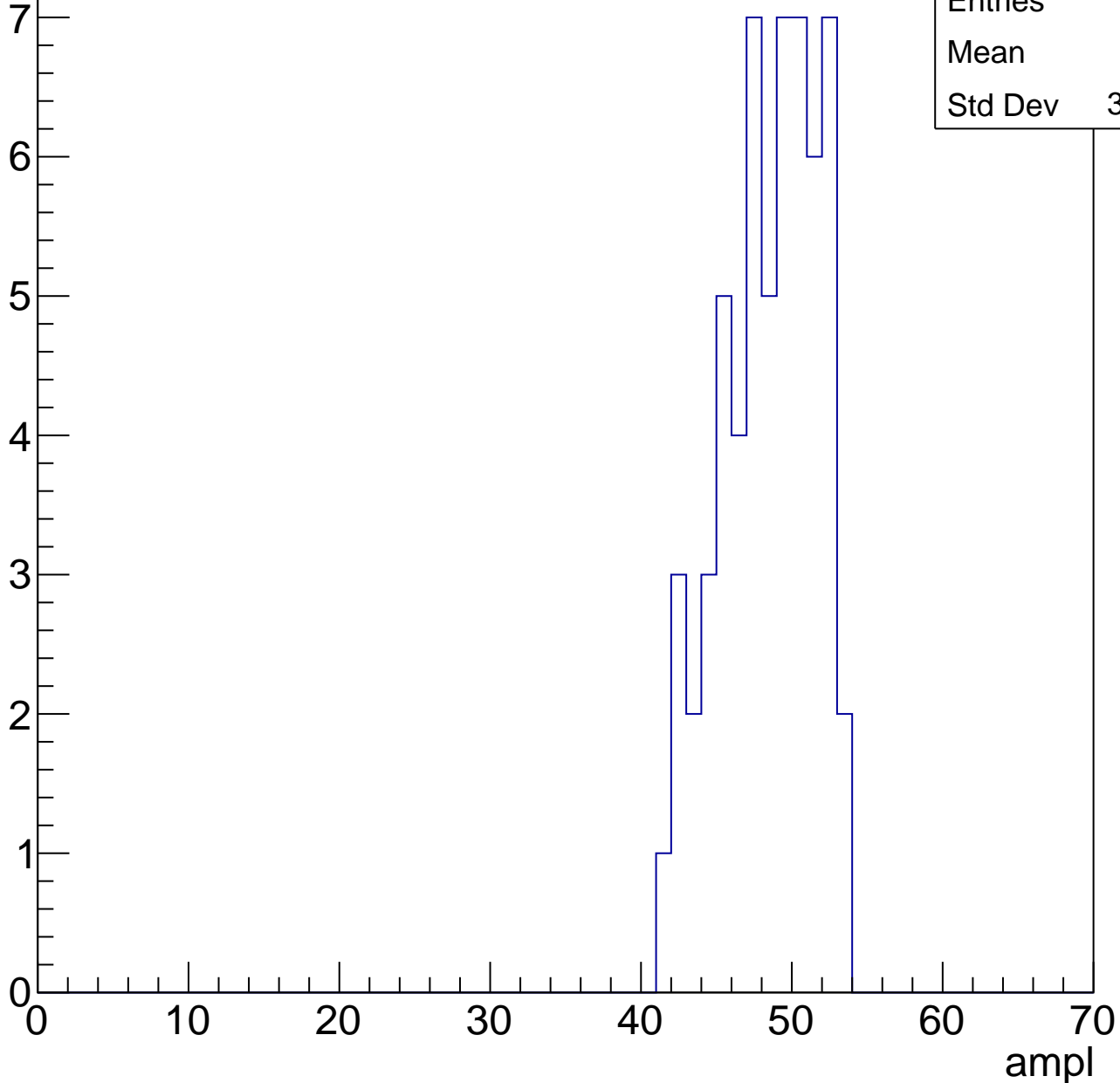


# B0L000S, U18-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	48
Std Dev	3.119

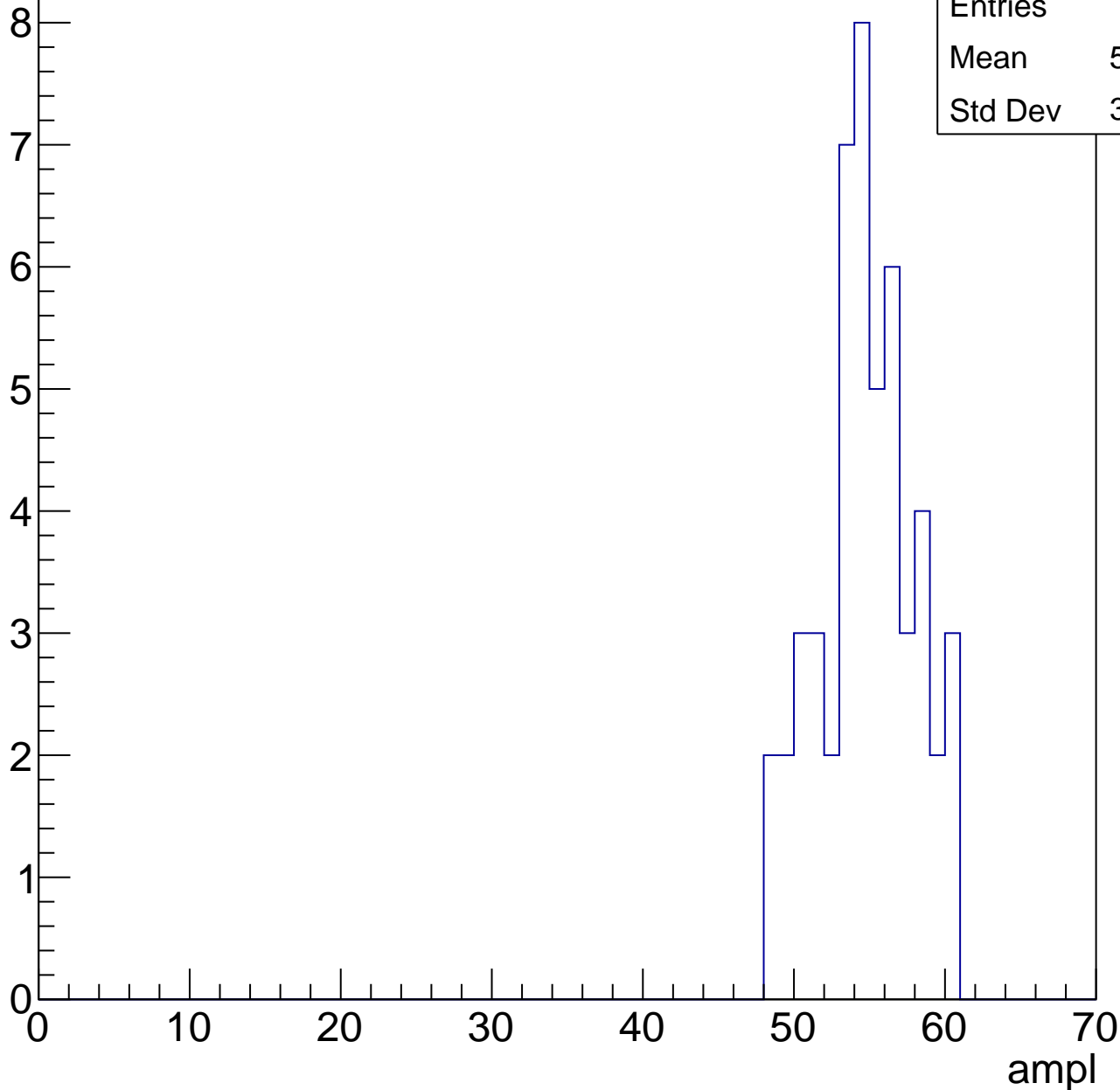


# B0L000S, U18-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

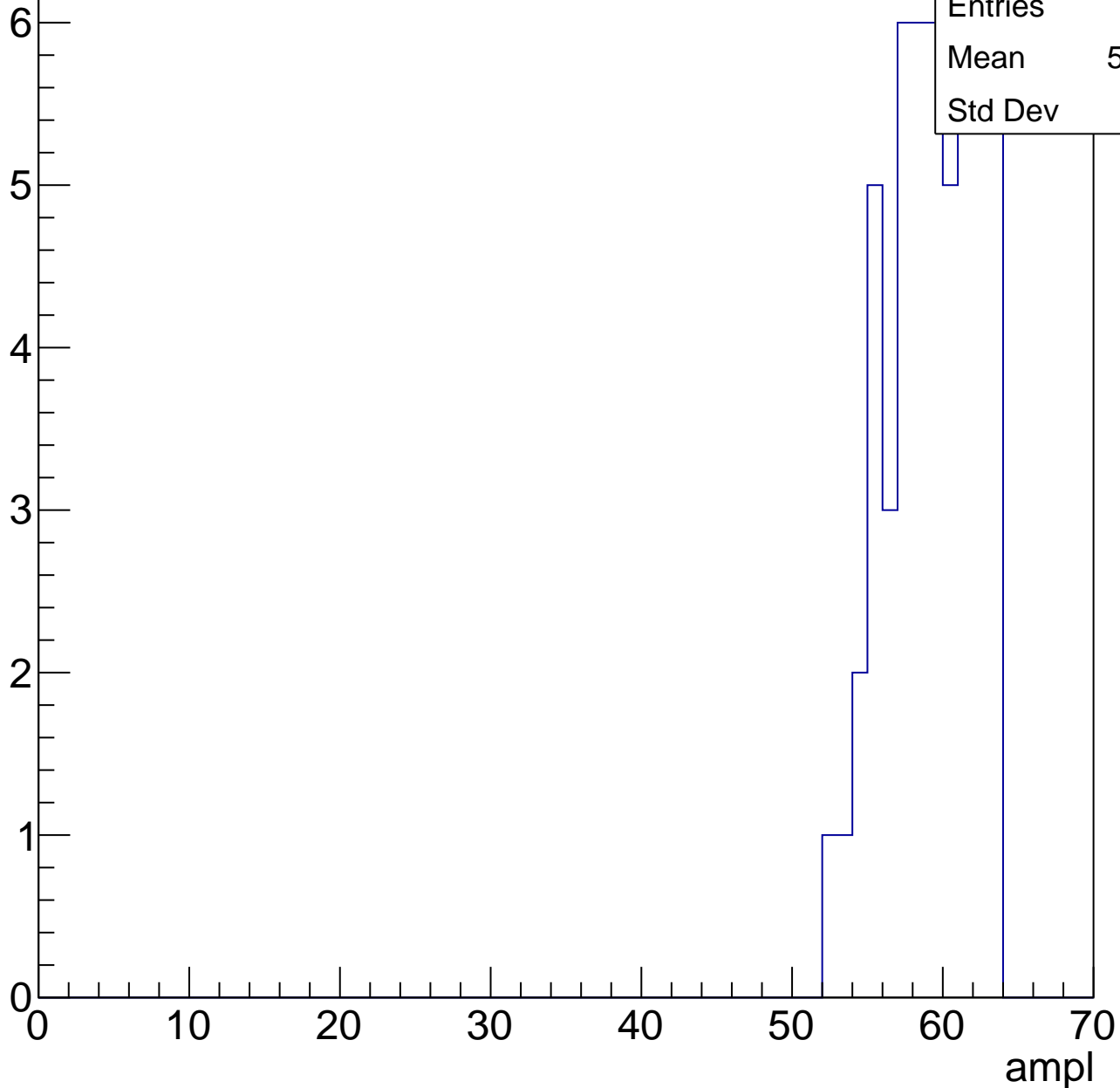
Entries	50
Mean	54.32
Std Dev	3.114



# B0L000S, U18-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

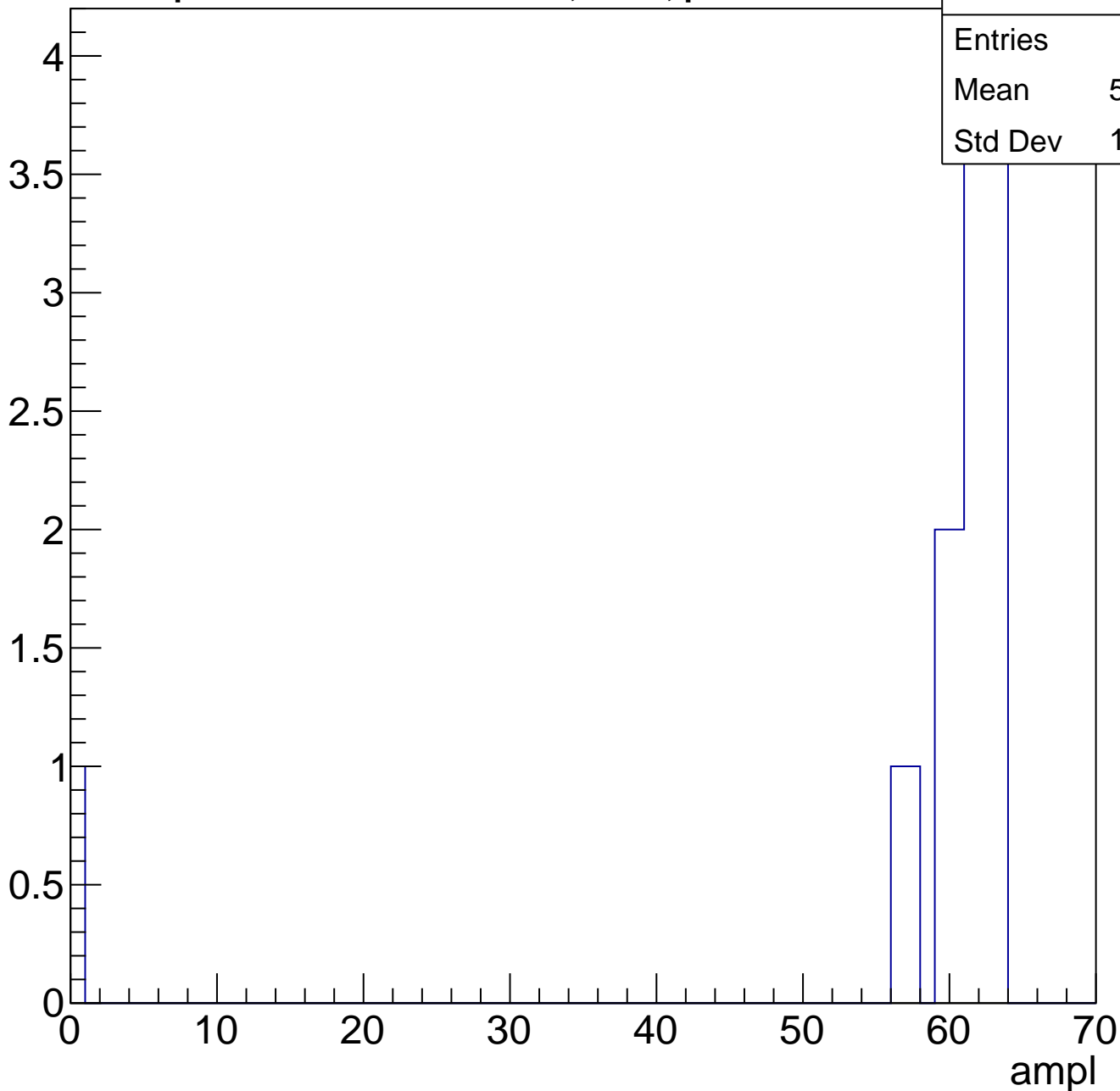
Entry



# B0L000S, U18-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	27.91
Std Dev	5.664

**Gaus mean : 29.0940**

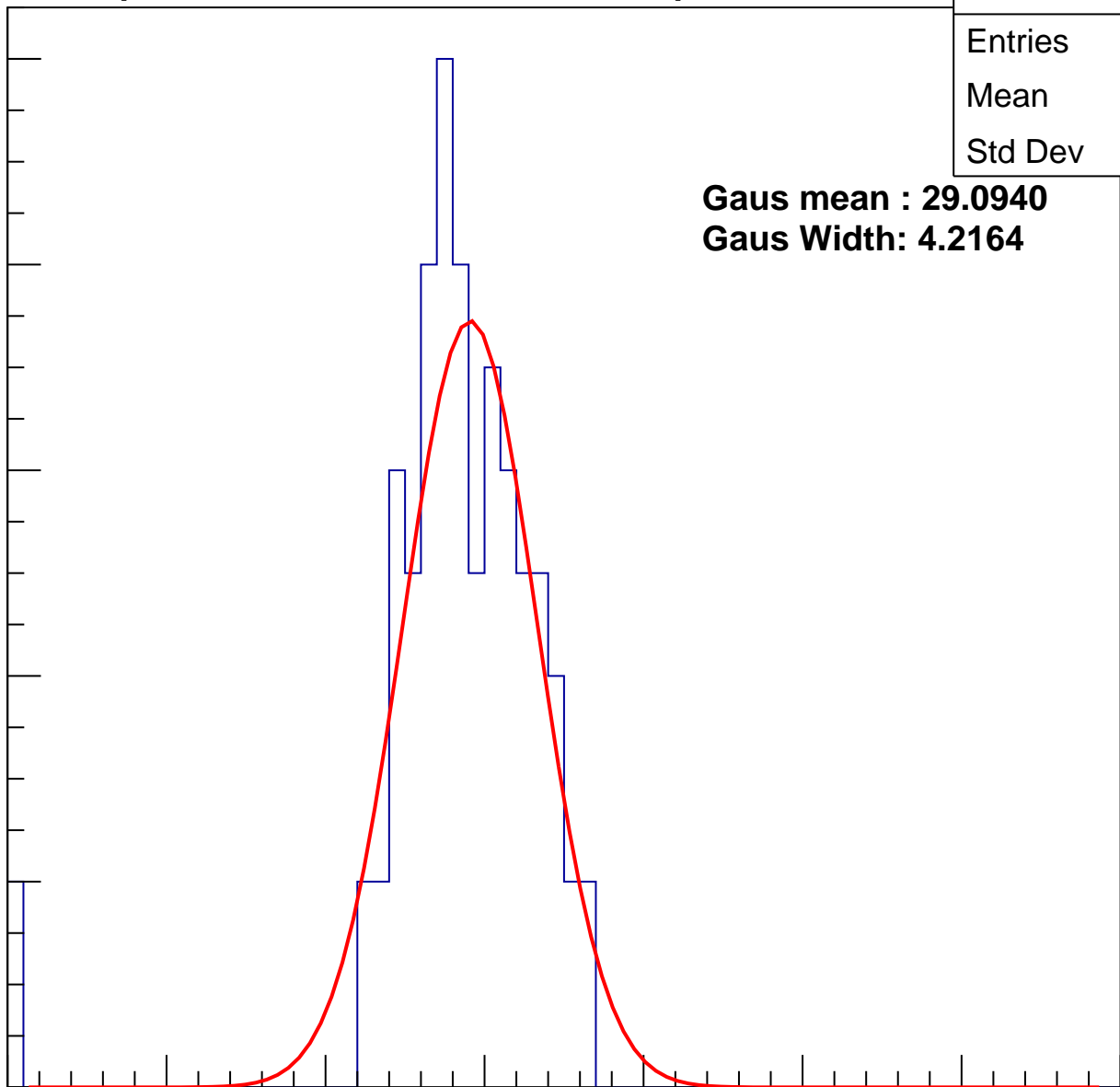
**Gaus Width: 4.2164**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch102, adc1

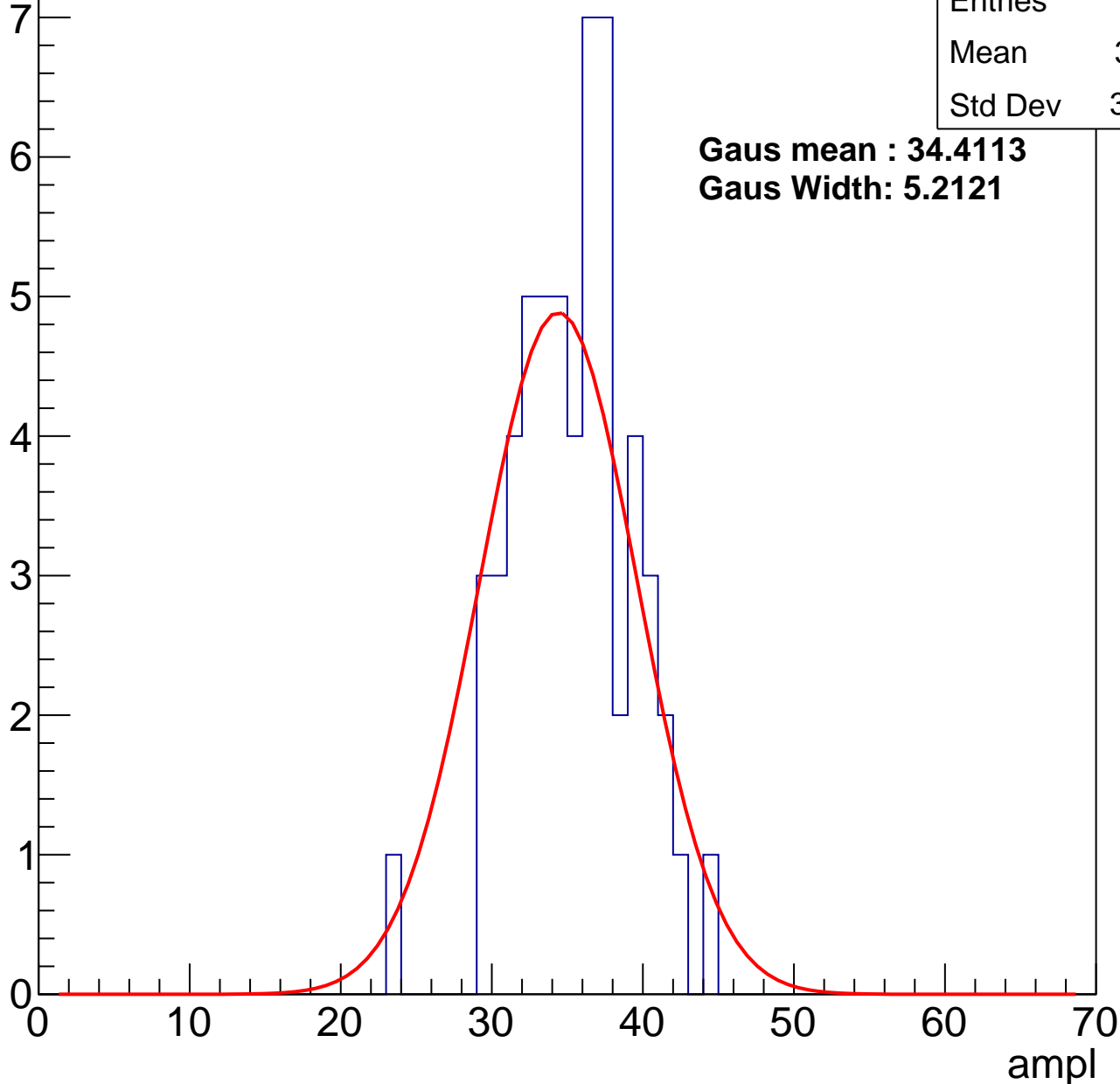
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	34.91
Std Dev	3.872

**Gaus mean : 34.4113**

**Gaus Width: 5.2121**



# B0L000S, U18-ch102, adc2

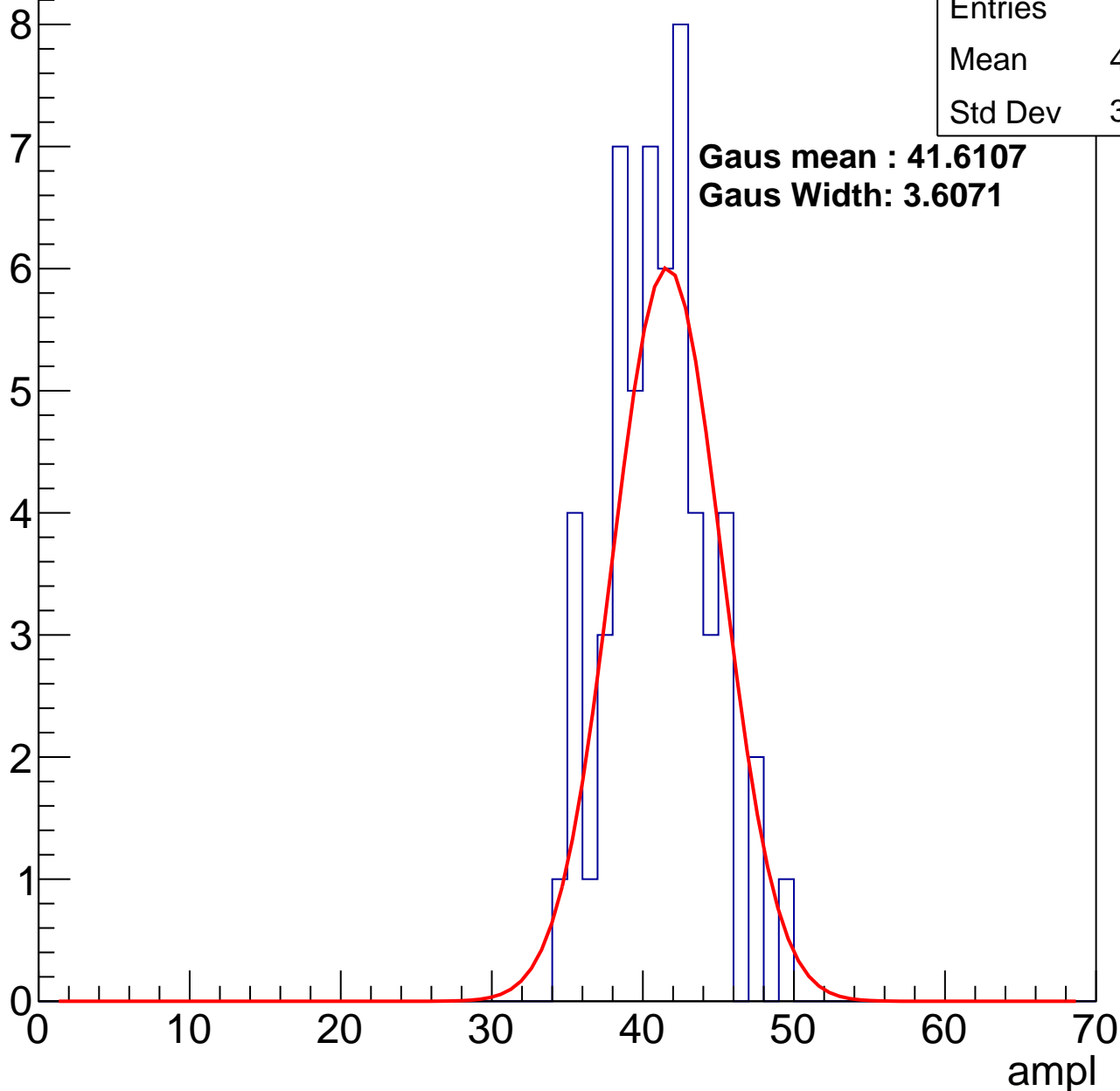
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	40.55
Std Dev	3.262

**Gaus mean : 41.6107**

**Gaus Width: 3.6071**

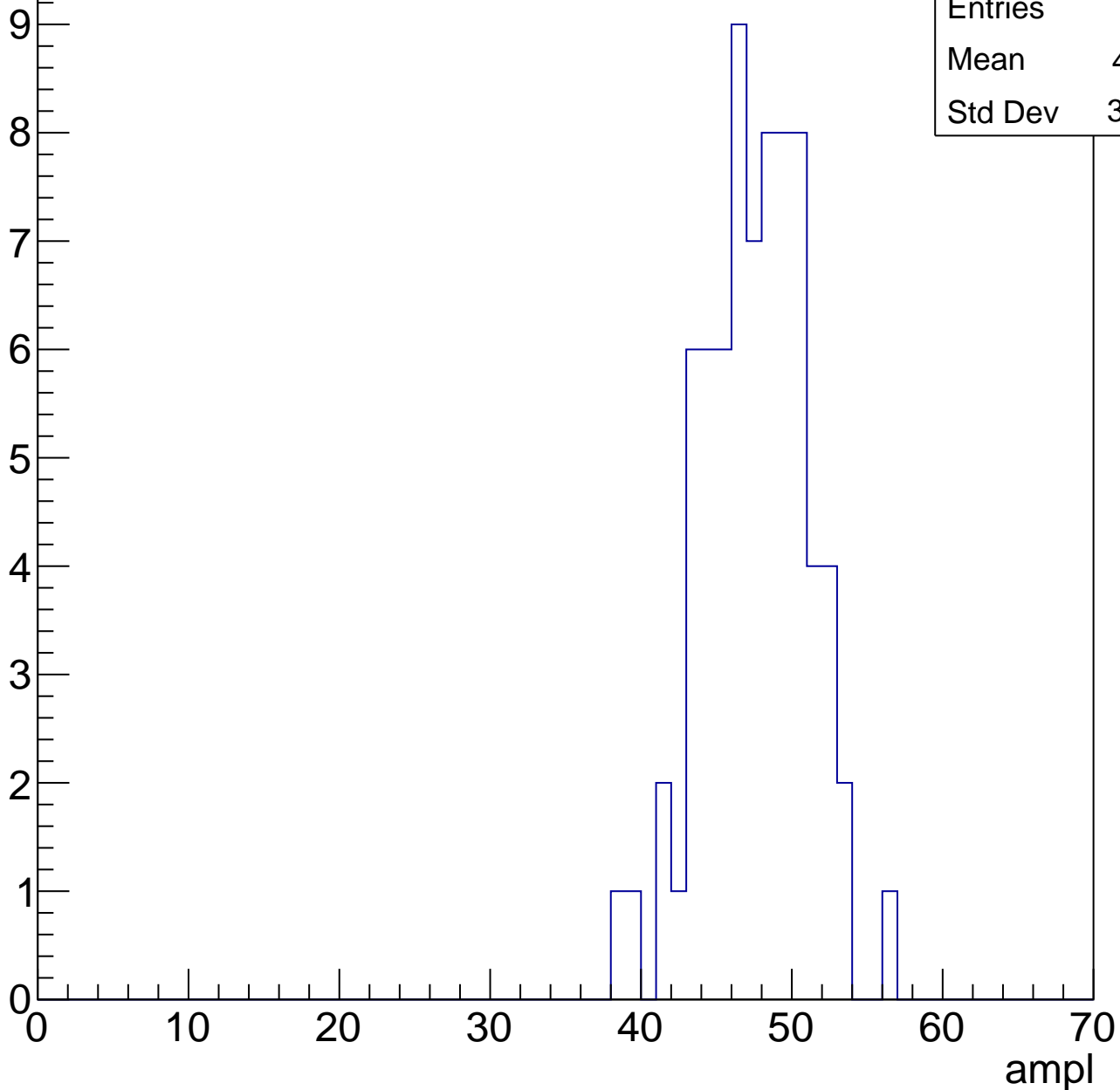


# B0L000S, U18-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	47.11
Std Dev	3.395

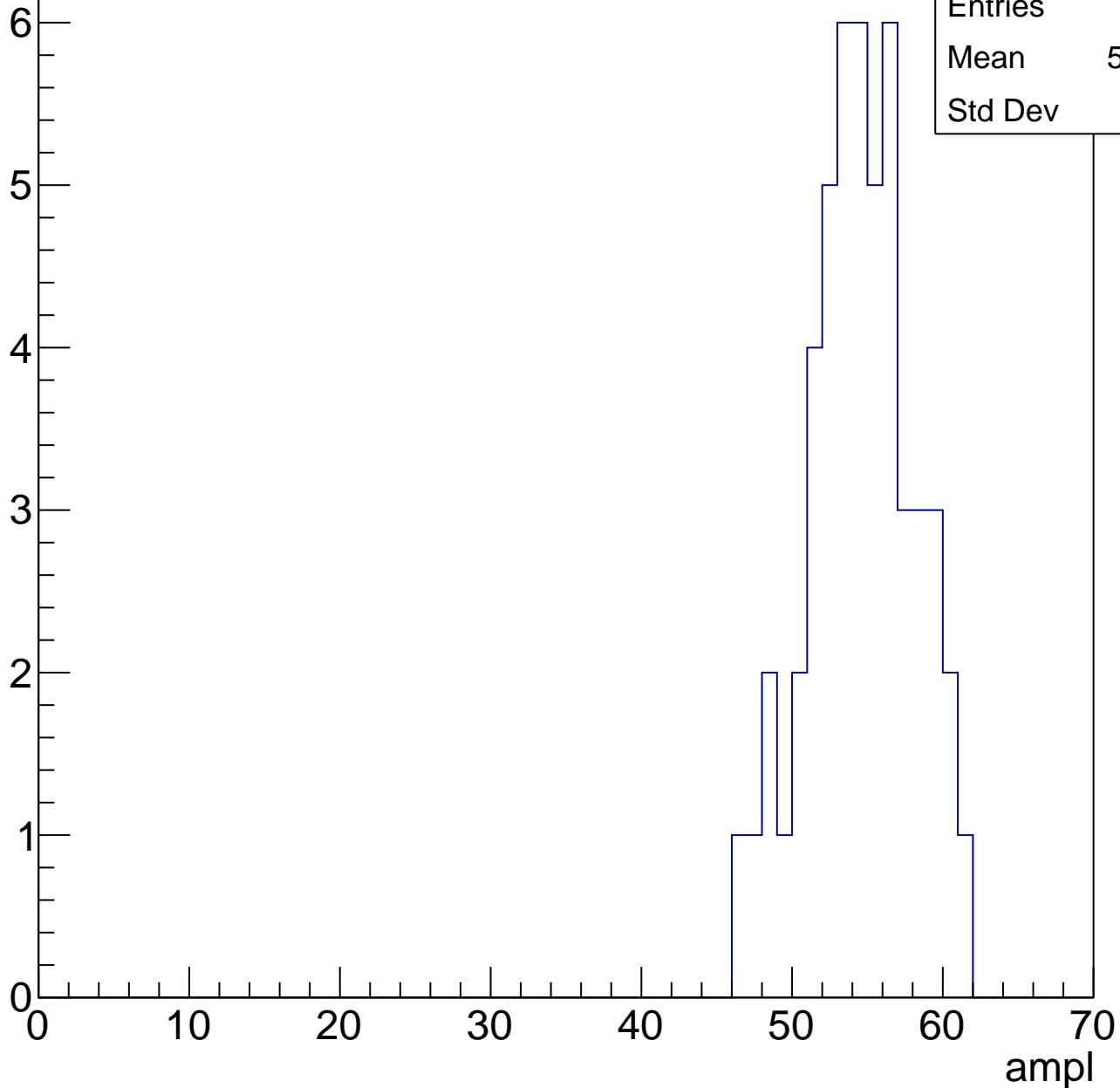


# B0L000S, U18-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.08
Std Dev	3.44

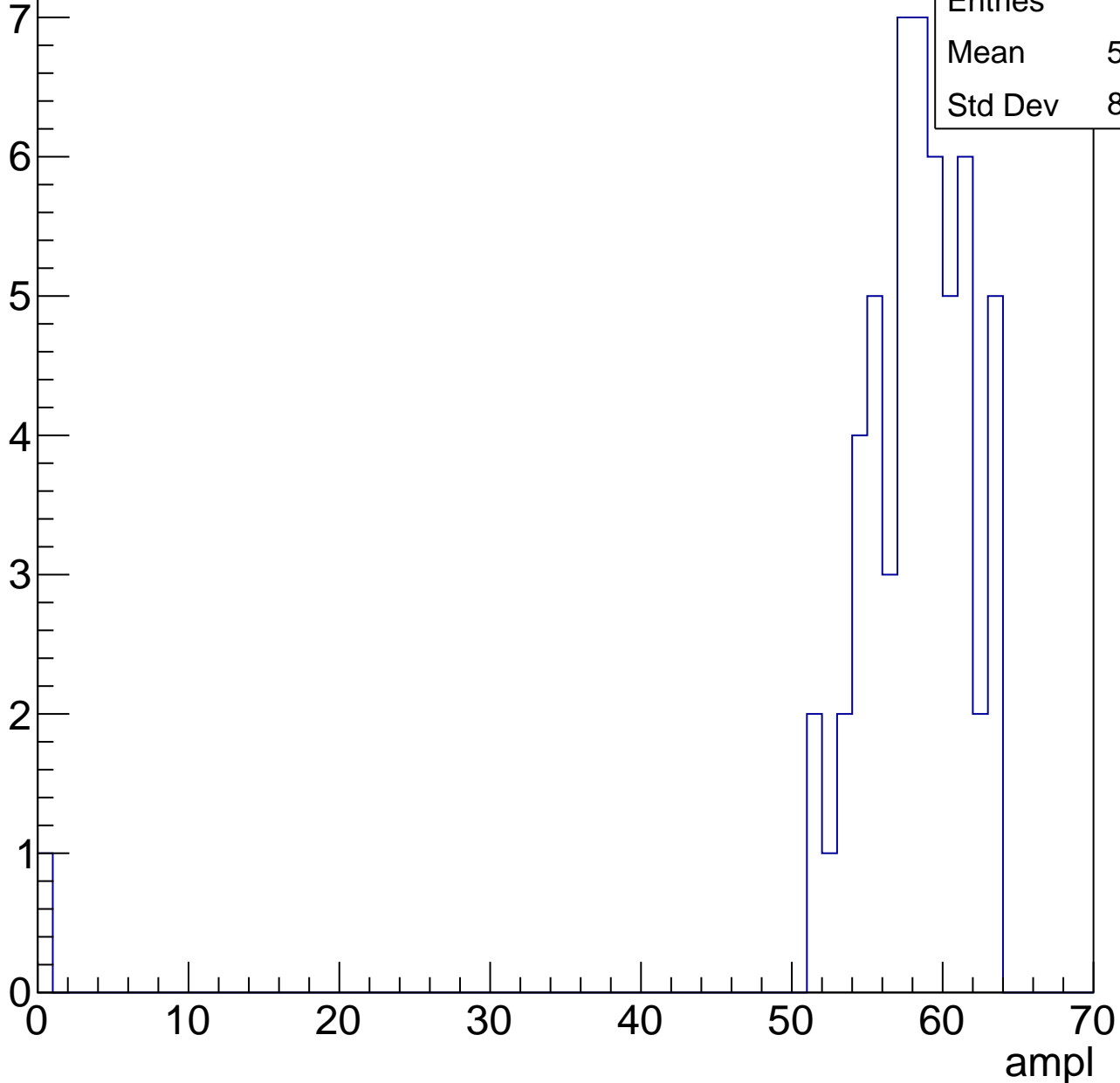


# B0L000S, U18-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	56.84
Std Dev	8.278

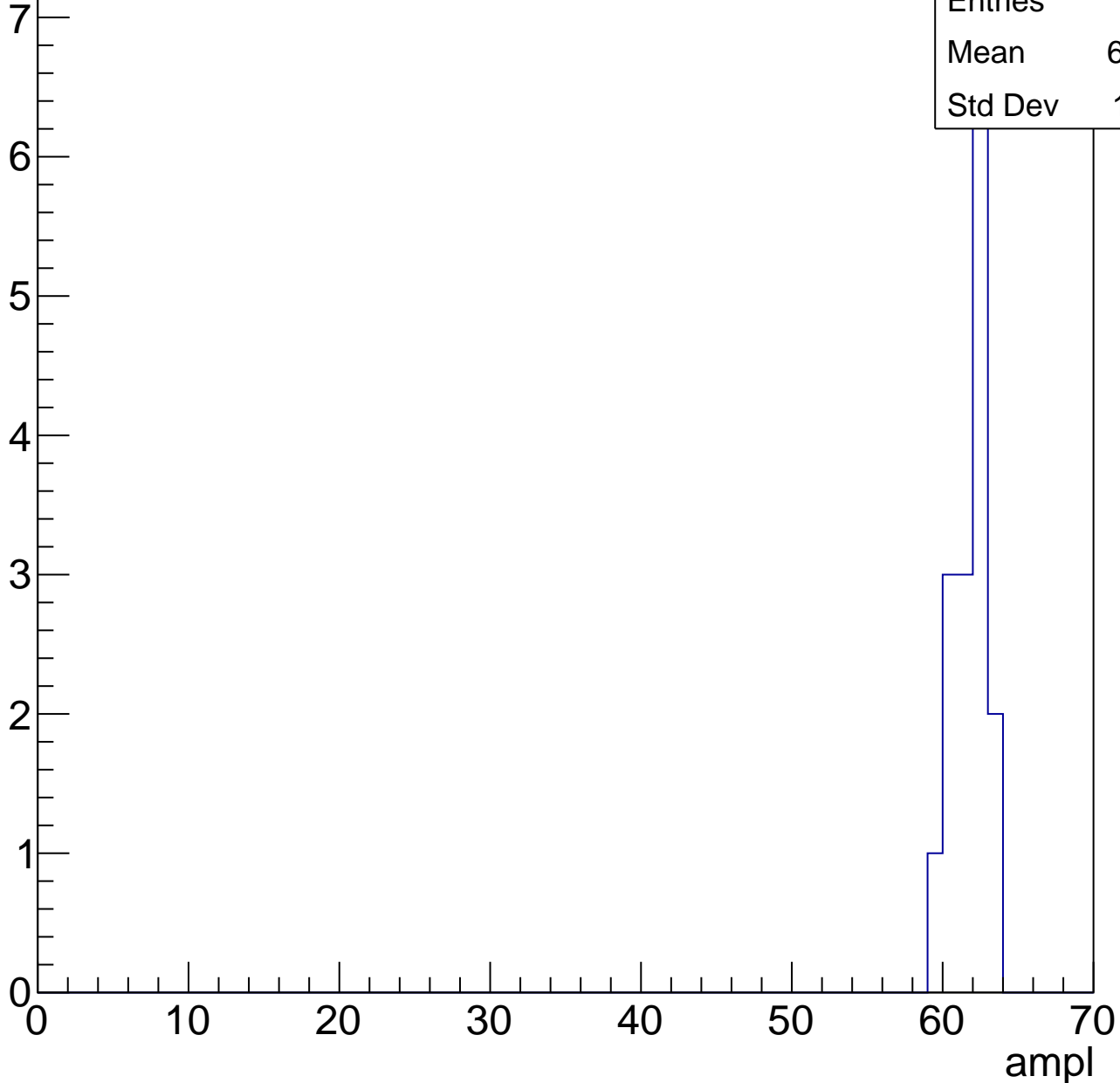


# B0L000S, U18-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.38
Std Dev	1.111

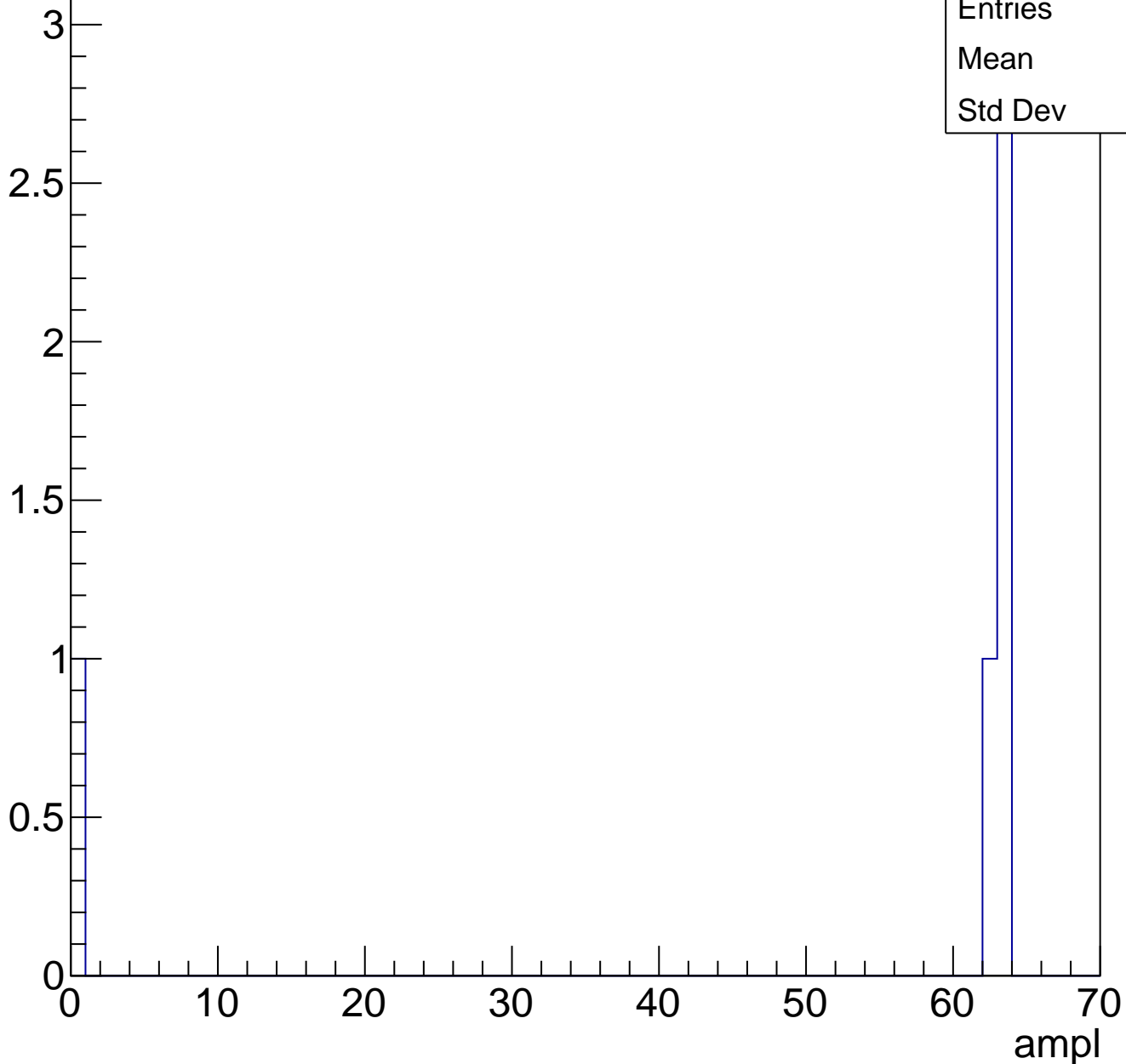




# B0L000S, U18-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch103, adc0

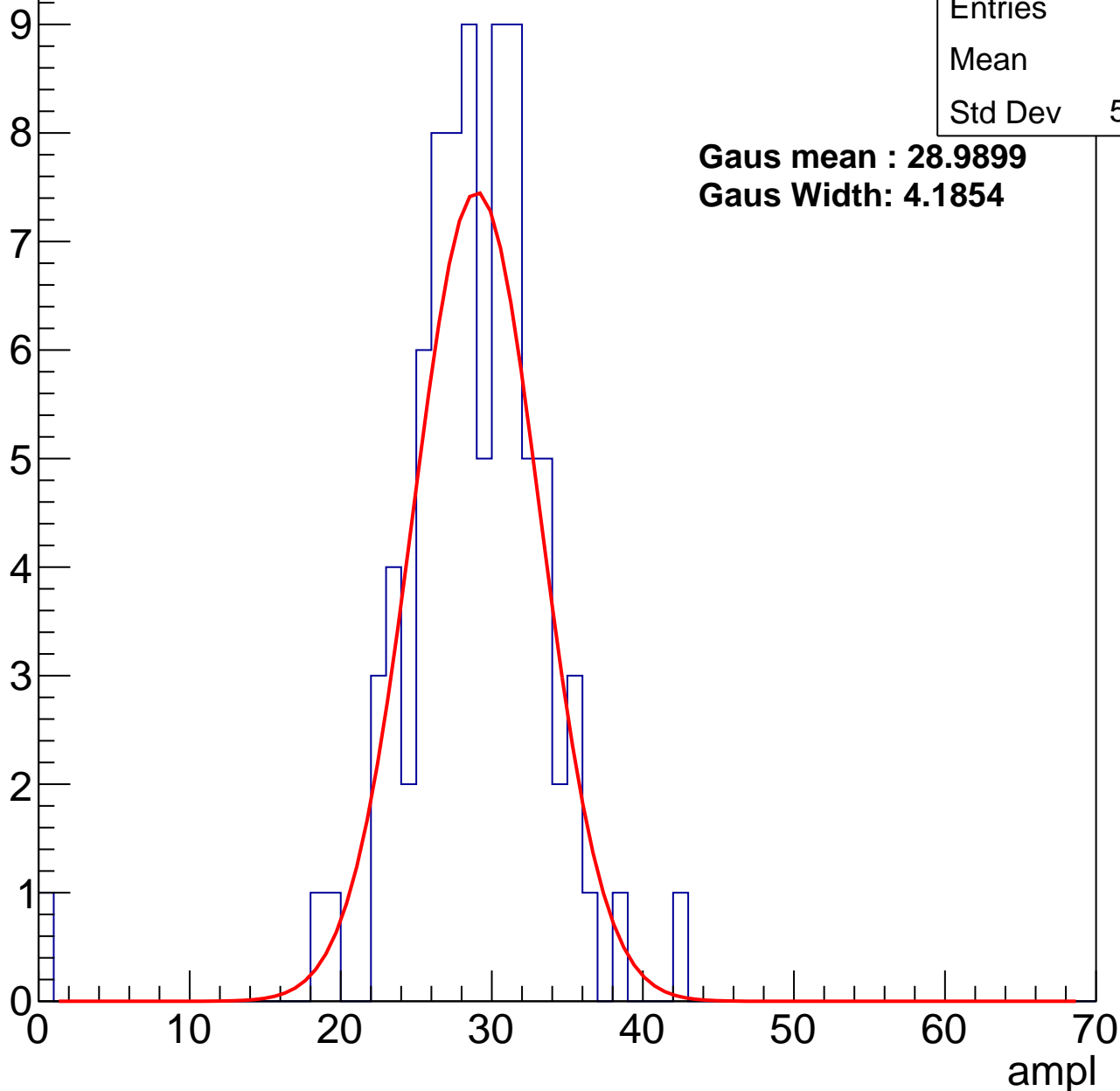
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	28.3
Std Dev	5.115

**Gaus mean : 28.9899**

**Gaus Width: 4.1854**



# B0L000S, U18-ch103, adc1

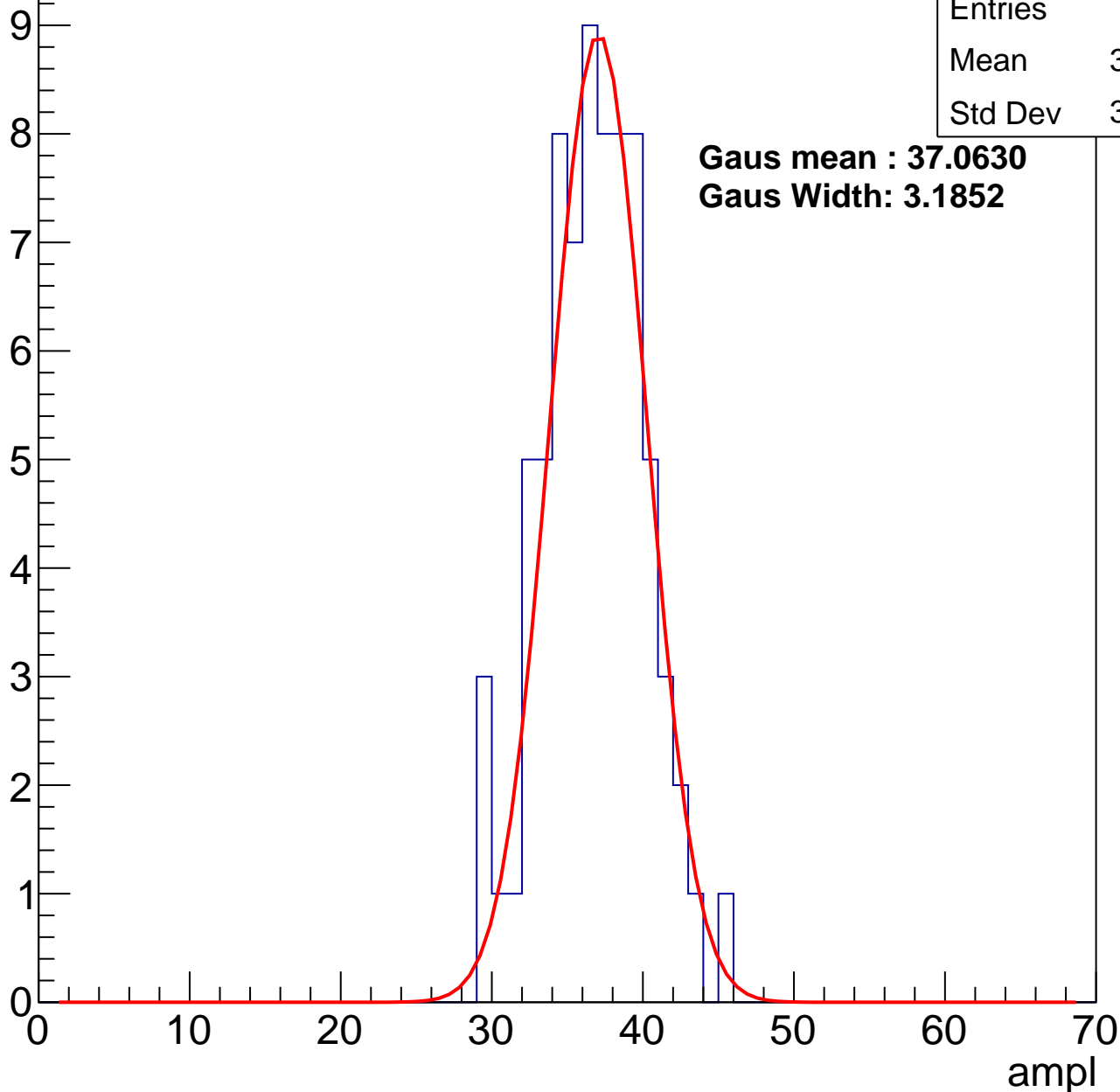
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	36.28
Std Dev	3.337

**Gaus mean : 37.0630**

**Gaus Width: 3.1852**



# B0L000S, U18-ch103, adc2

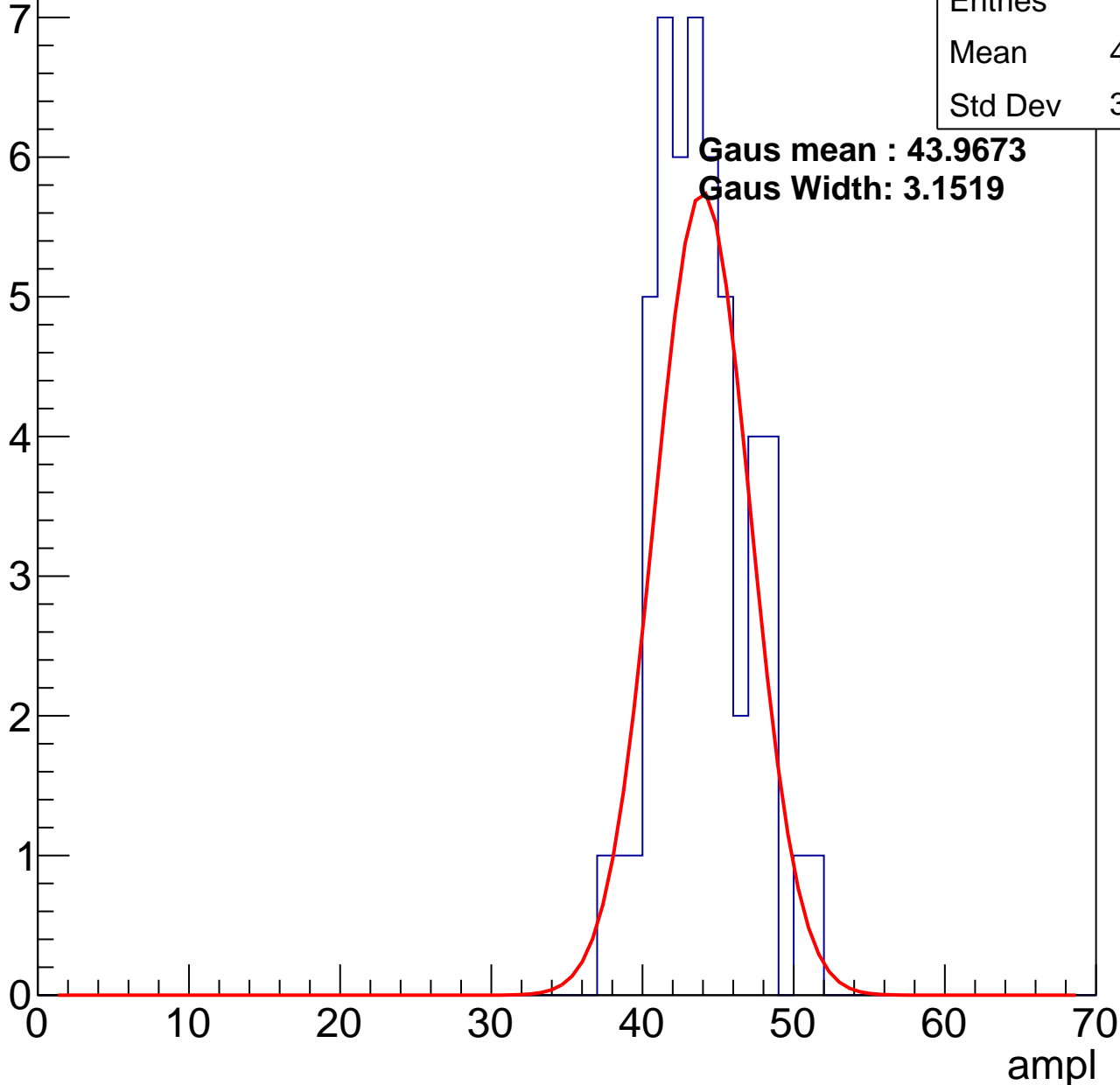
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	43.45
Std Dev	3.025

**Gaus mean : 43.9673**

**Gaus Width: 3.1519**

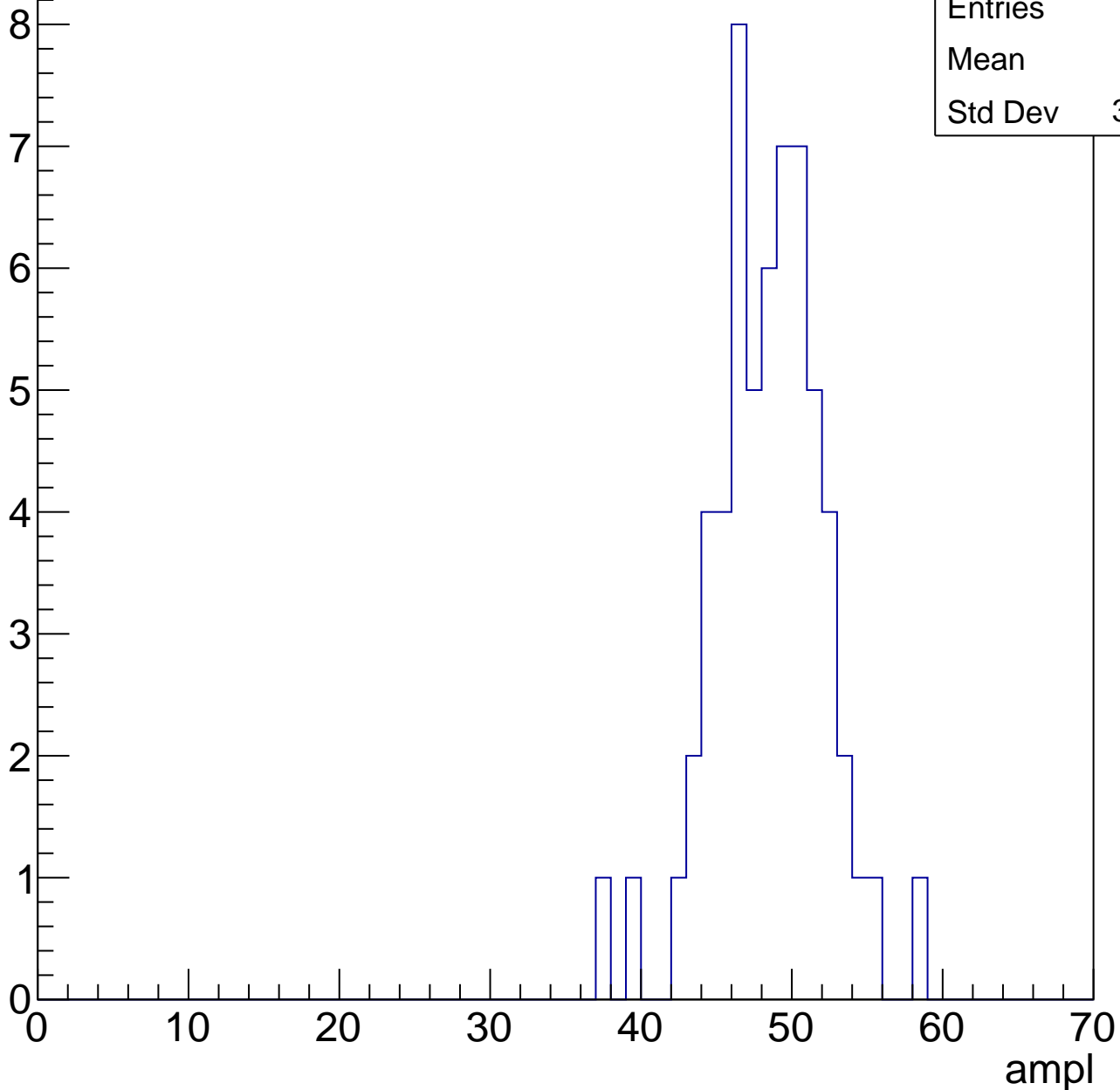


# B0L000S, U18-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	48
Std Dev	3.661

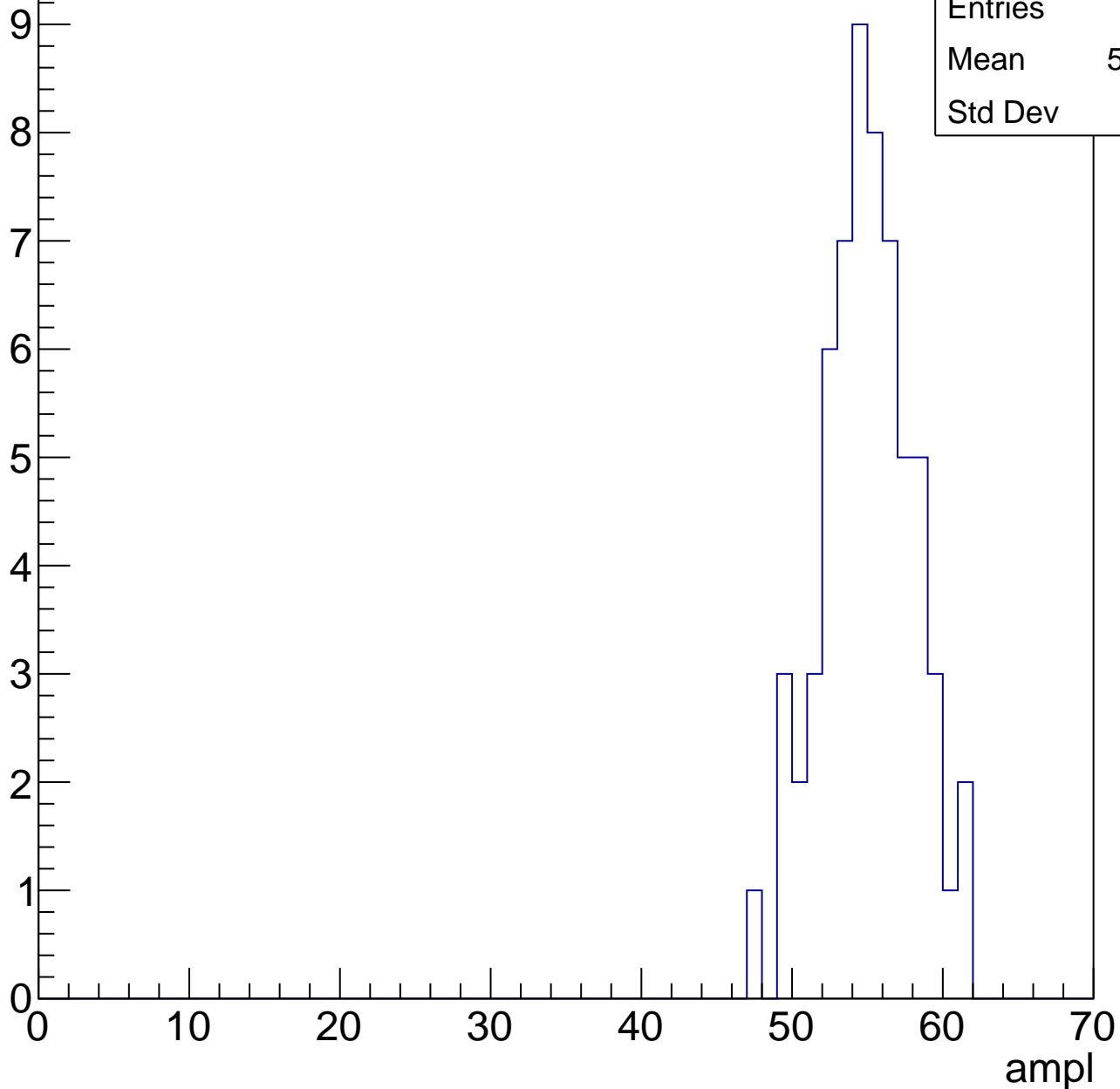


# B0L000S, U18-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	54.55
Std Dev	3.02

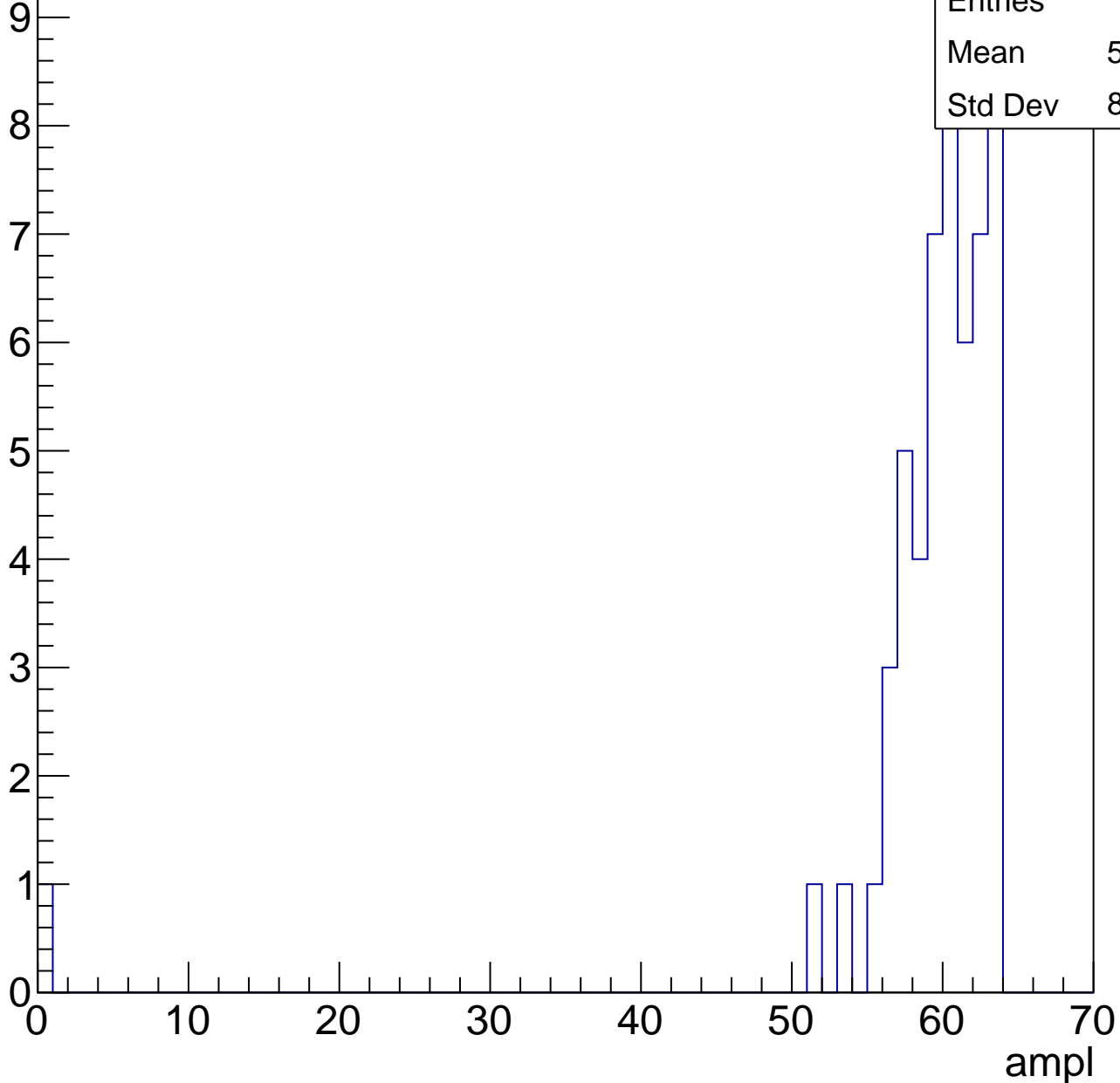


# B0L000S, U18-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

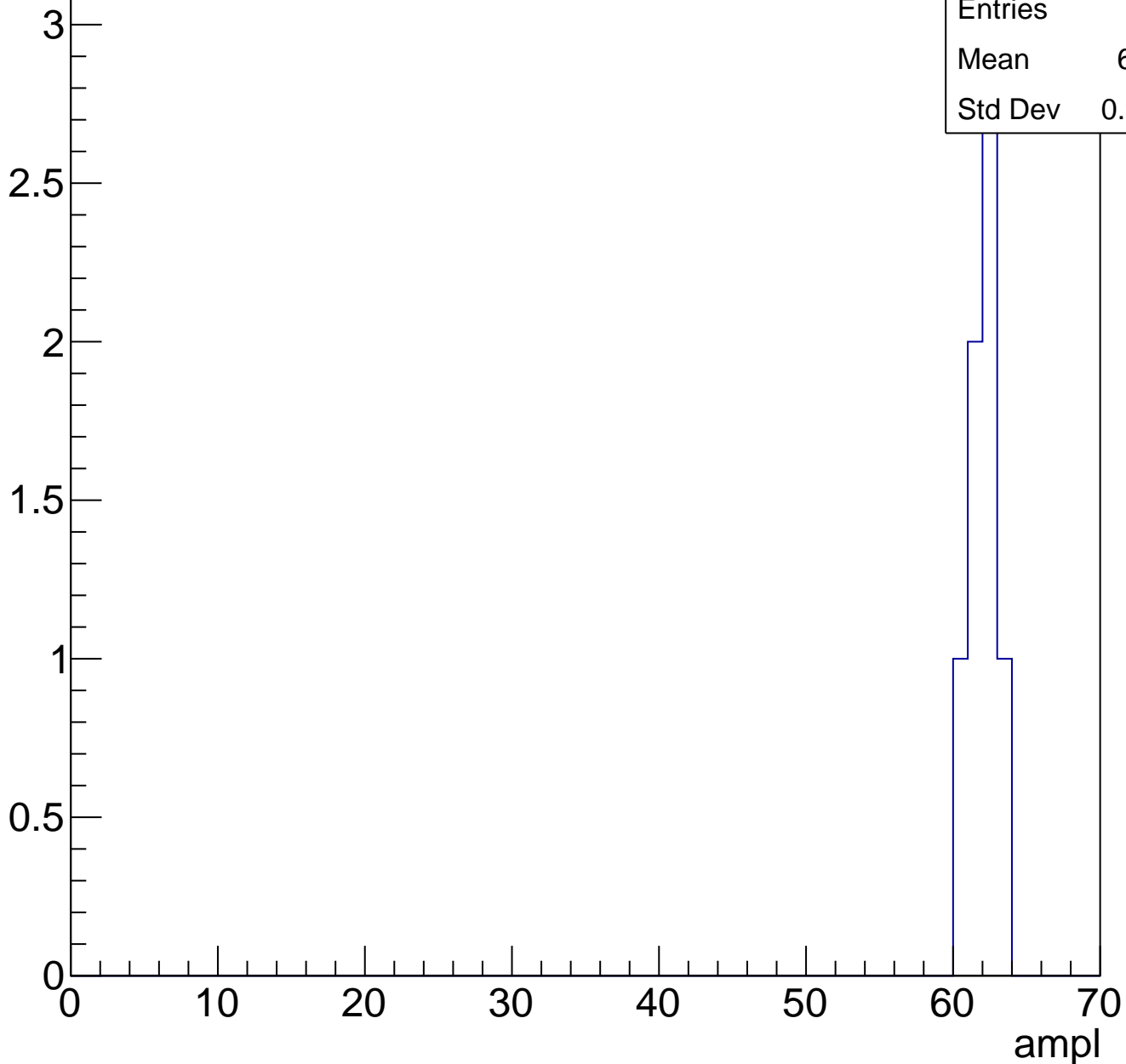
Entries	53
Mean	58.57
Std Dev	8.553



# B0L000S, U18-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch104, adc0

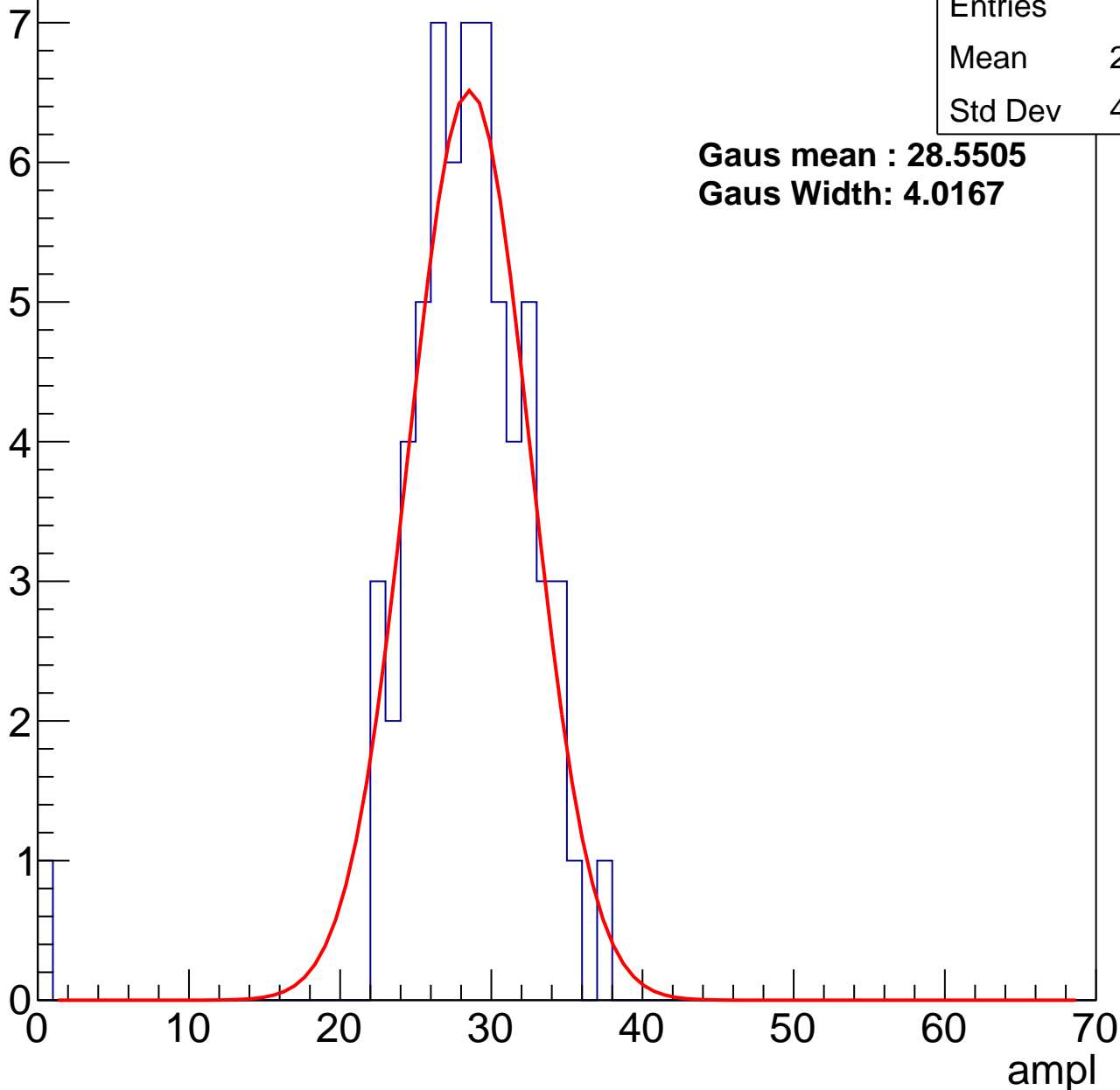
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	27.86
Std Dev	4.905

**Gaus mean : 28.5505**

**Gaus Width: 4.0167**



# B0L000S, U18-ch104, adc1

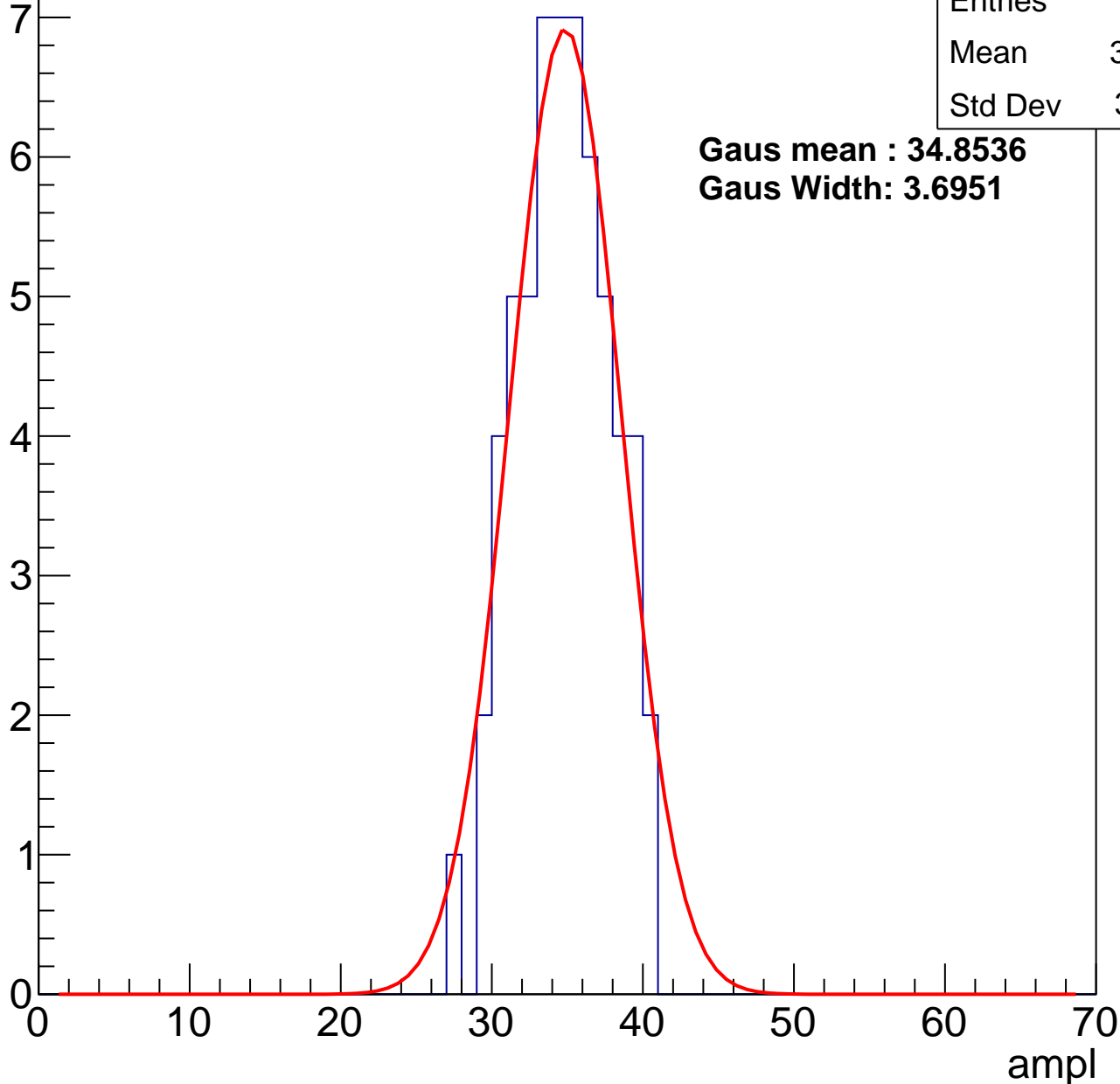
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	34.29
Std Dev	3.031

**Gaus mean : 34.8536**

**Gaus Width: 3.6951**



# B0L000S, U18-ch104, adc2

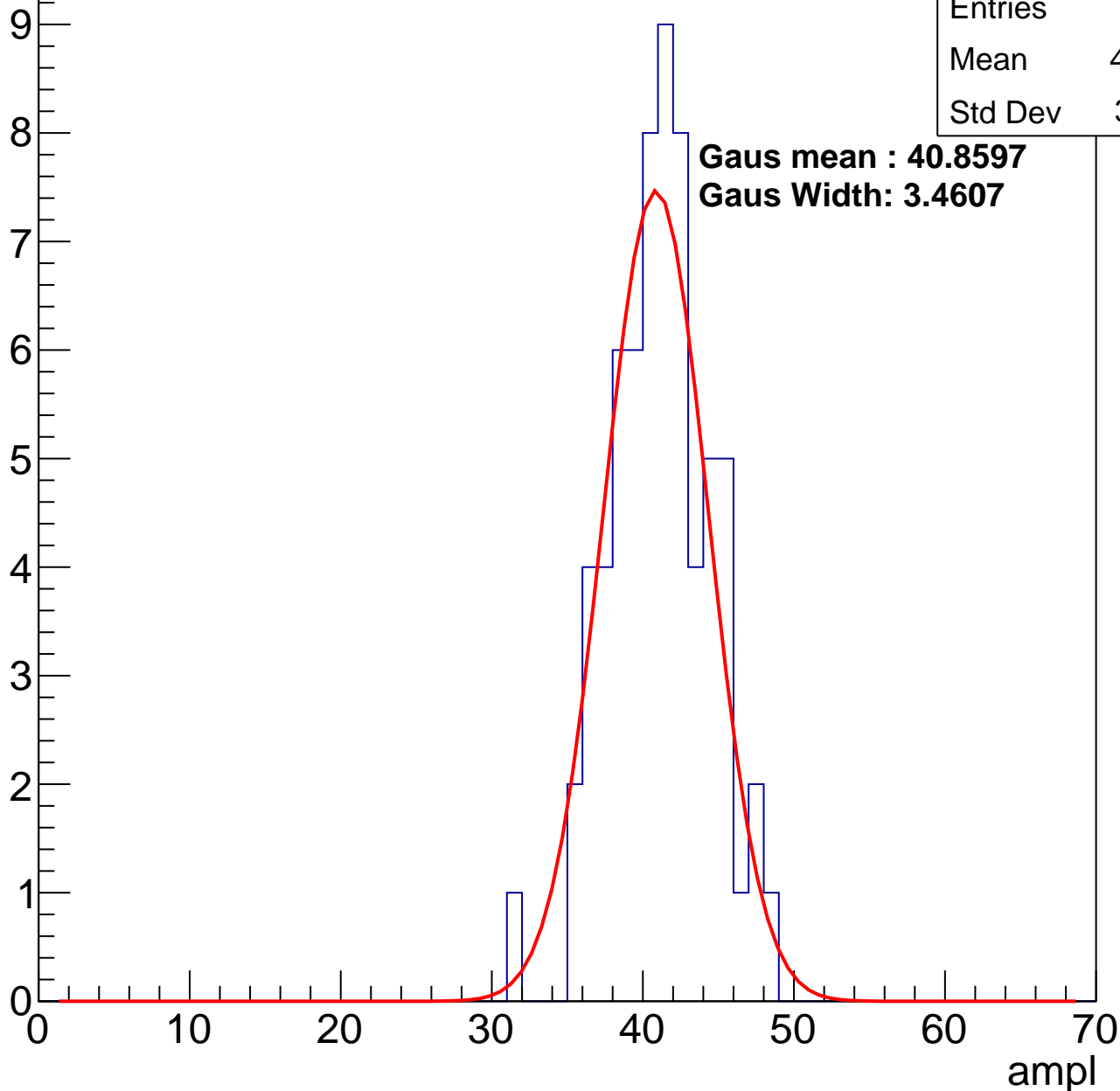
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	40.68
Std Dev	3.281

**Gaus mean : 40.8597**

**Gaus Width: 3.4607**

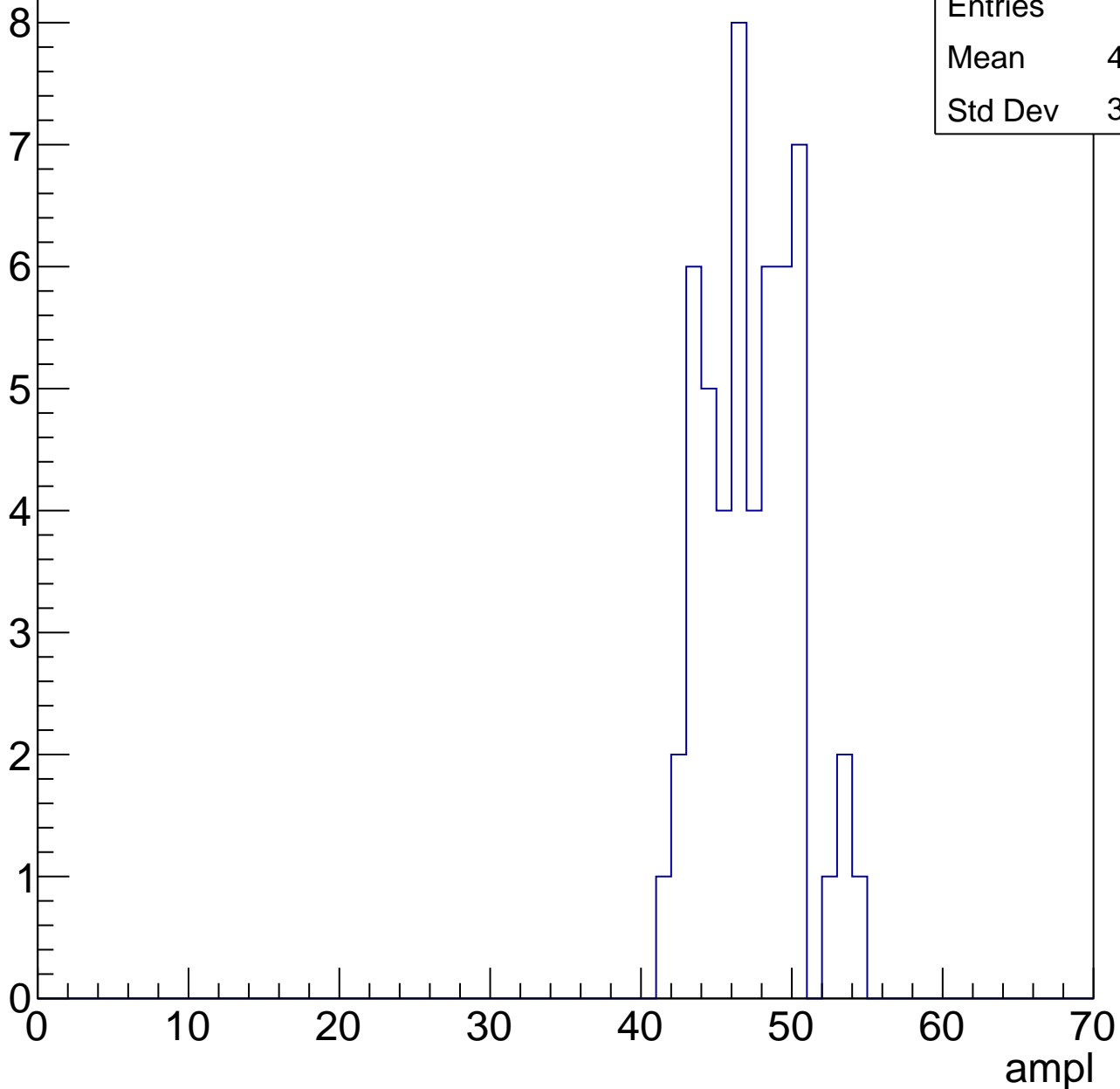


# B0L000S, U18-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	46.85
Std Dev	3.037

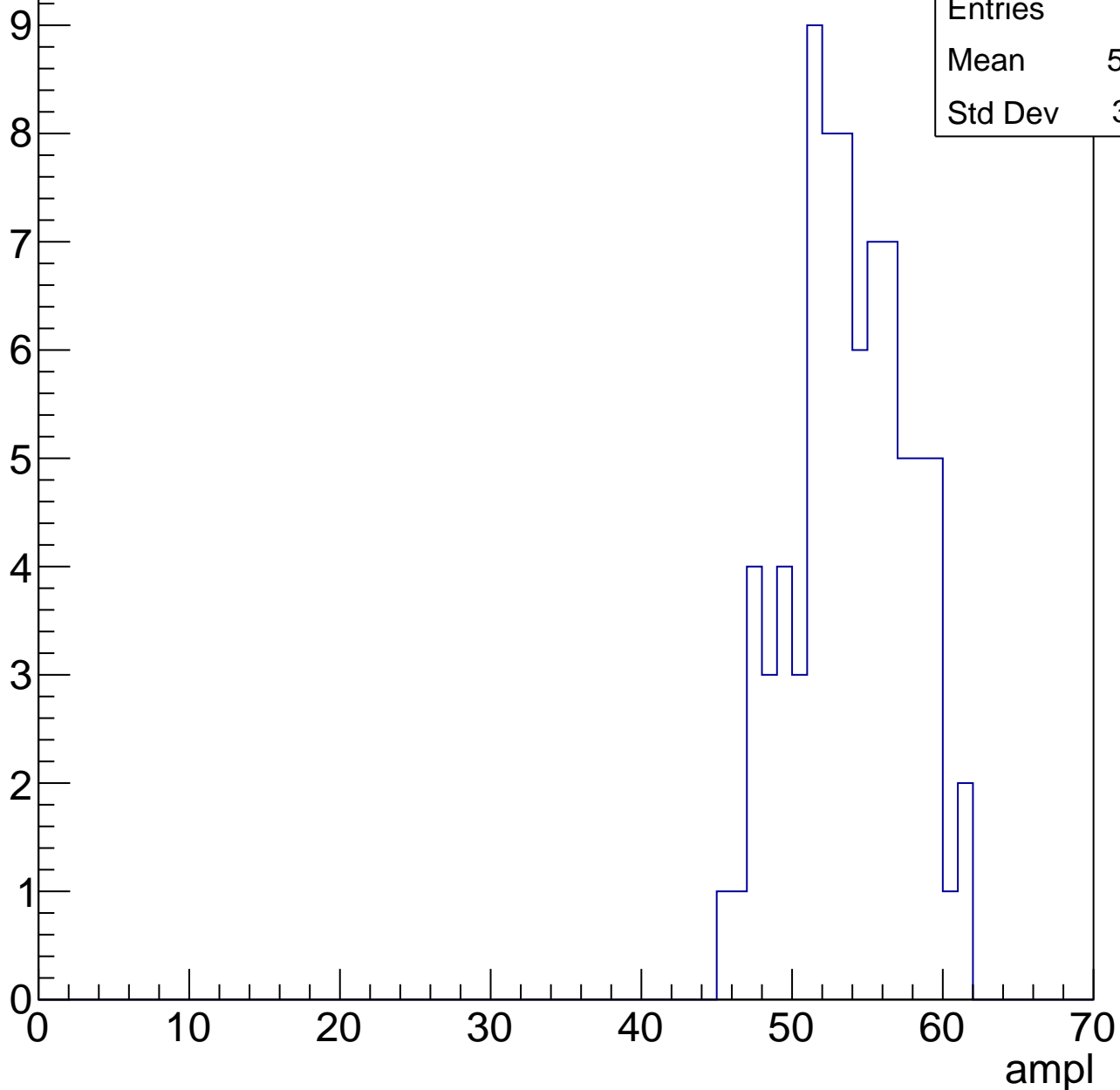


# B0L000S, U18-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	53.43
Std Dev	3.751

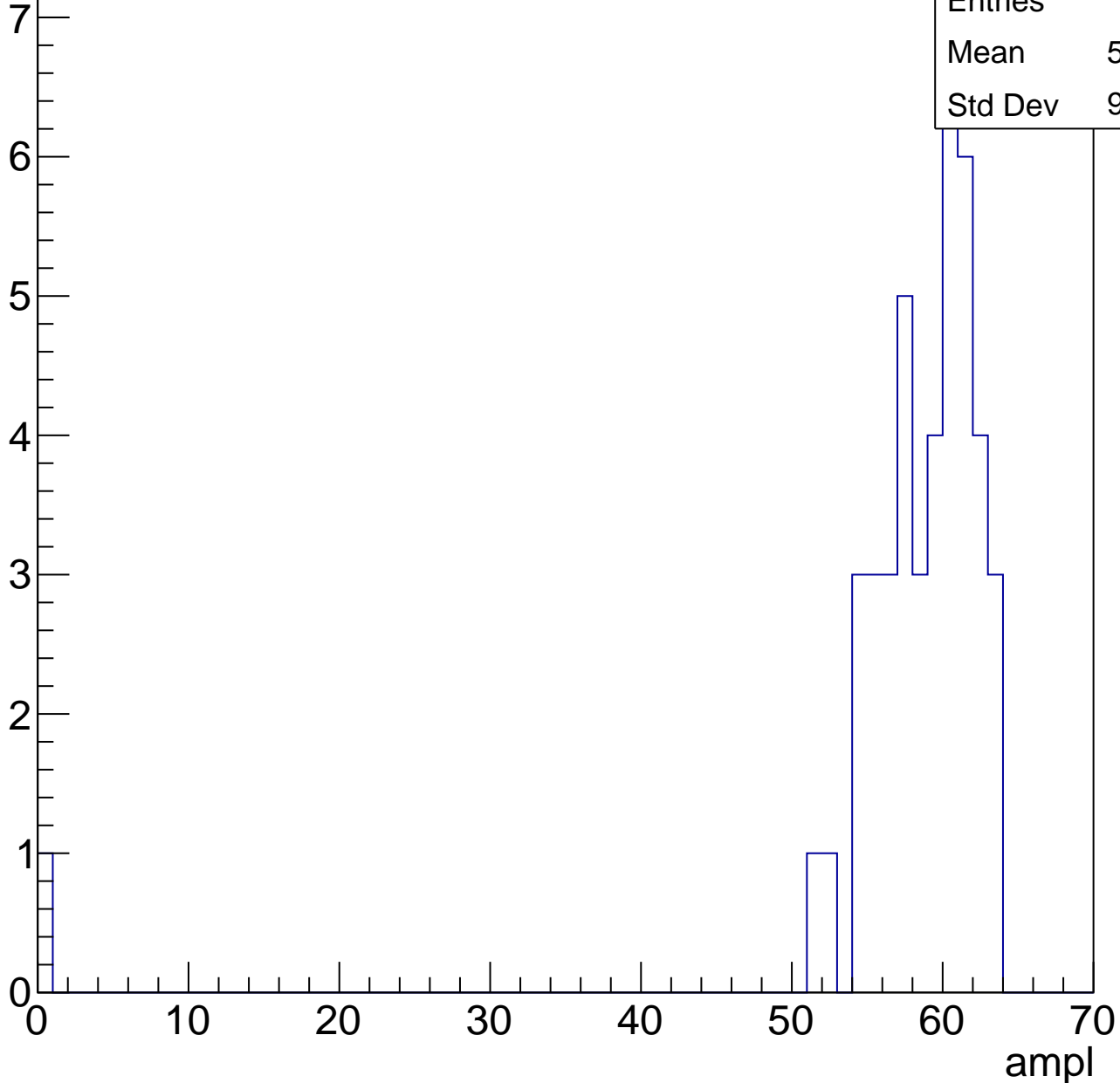


# B0L000S, U18-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	57.18
Std Dev	9.215

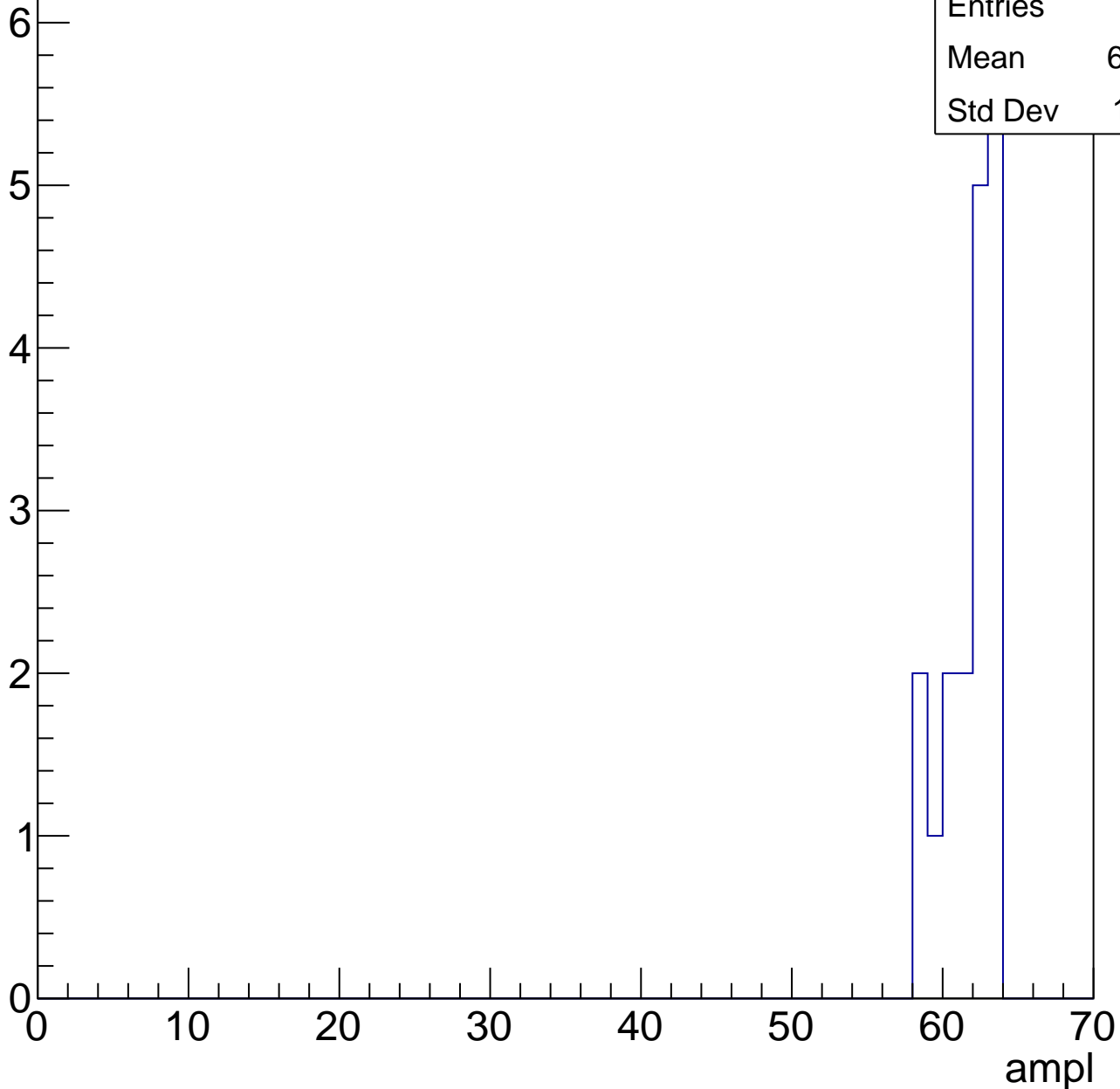


# B0L000S, U18-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	61.39
Std Dev	1.671





# B0L000S, U18-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	48.67
Std Dev	19.57

ampl

# B0L000S, U18-ch105, adc0

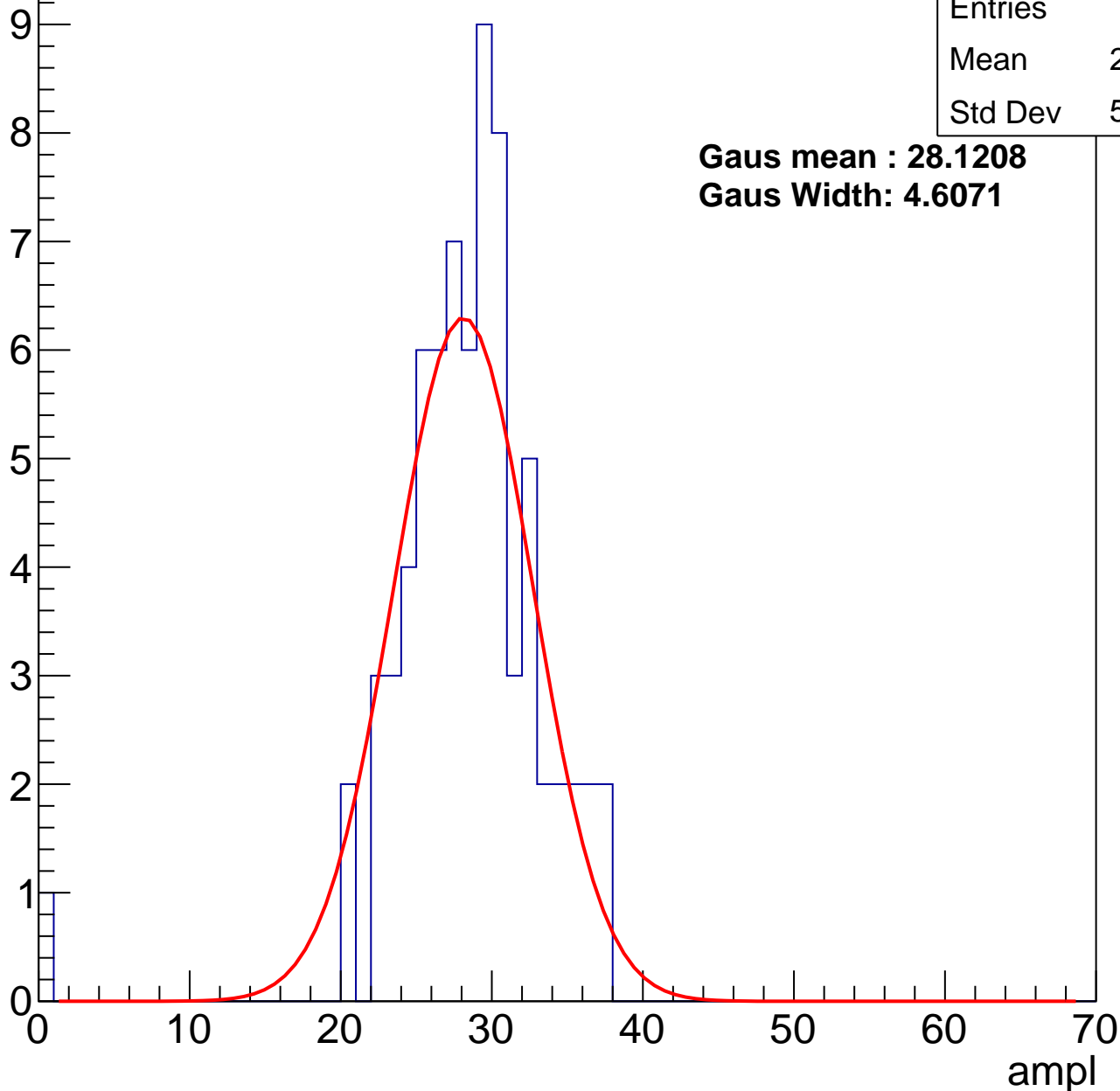
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	27.92
Std Dev	5.104

**Gaus mean : 28.1208**

**Gaus Width: 4.6071**



# B0L000S, U18-ch105, adc1

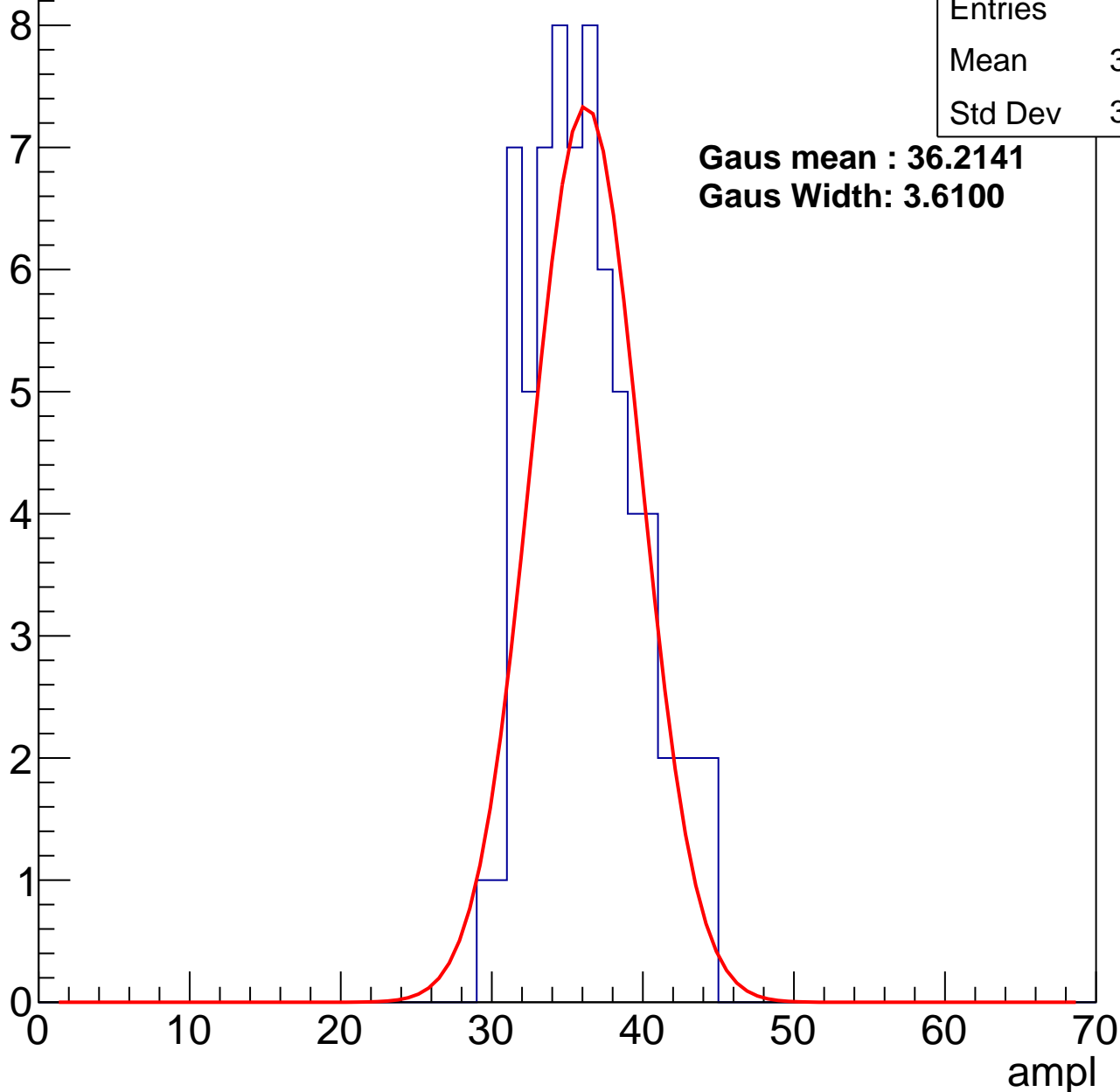
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	35.77
Std Dev	3.585

**Gaus mean : 36.2141**

**Gaus Width: 3.6100**



# B0L000S, U18-ch105, adc2

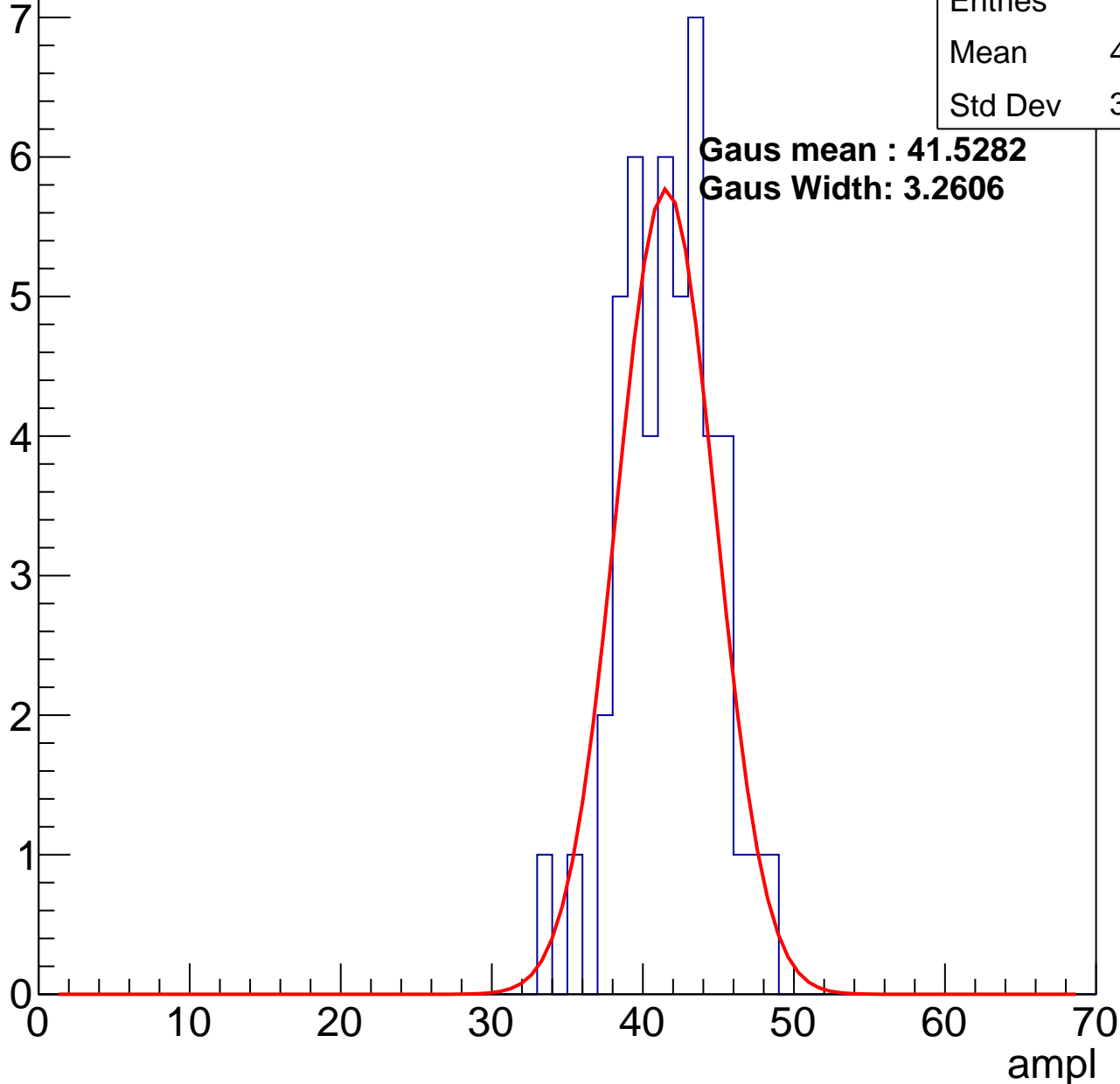
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	41.25
Std Dev	3.045

**Gaus mean : 41.5282**

**Gaus Width: 3.2606**

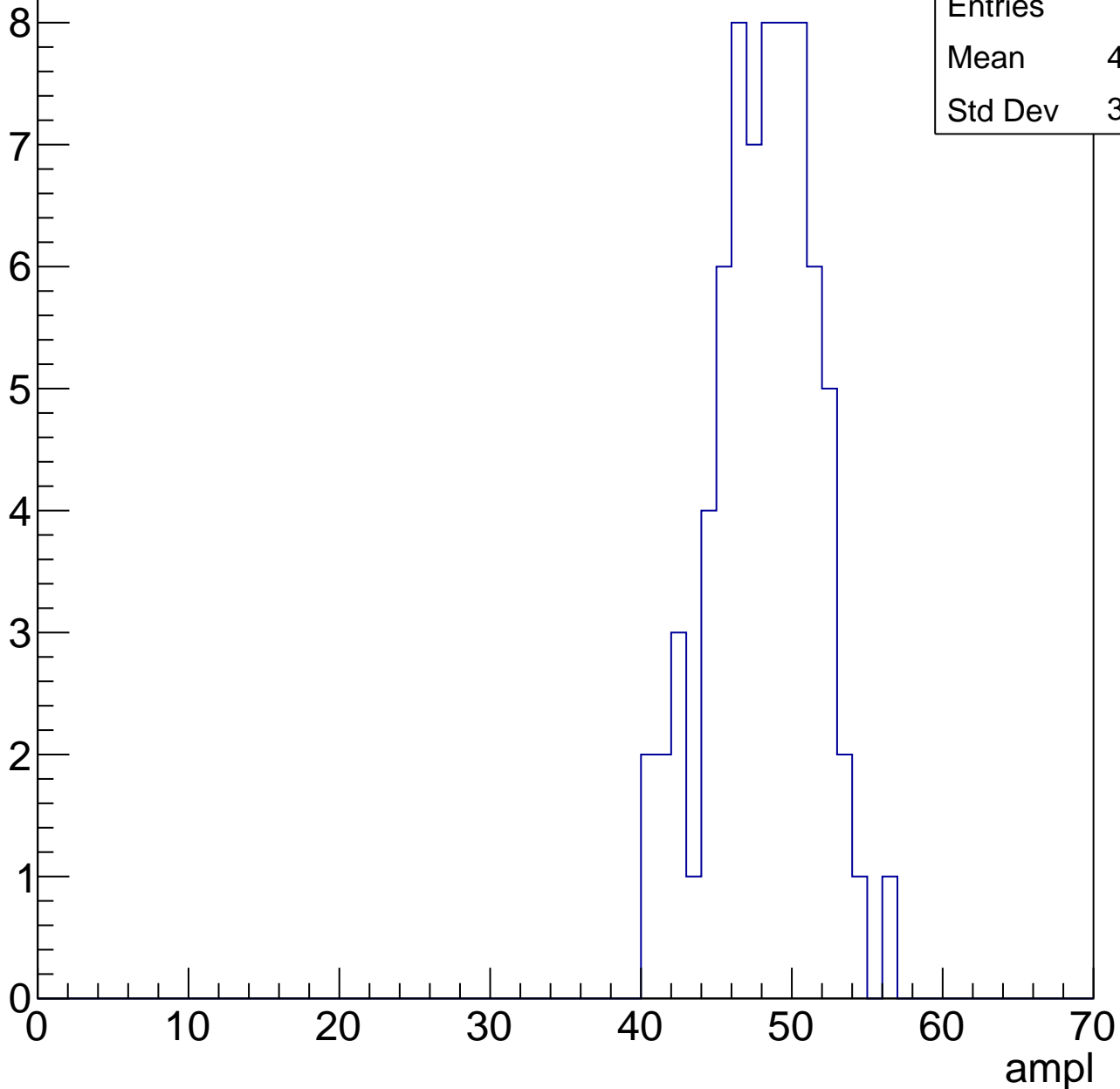


# B0L000S, U18-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	47.67
Std Dev	3.399

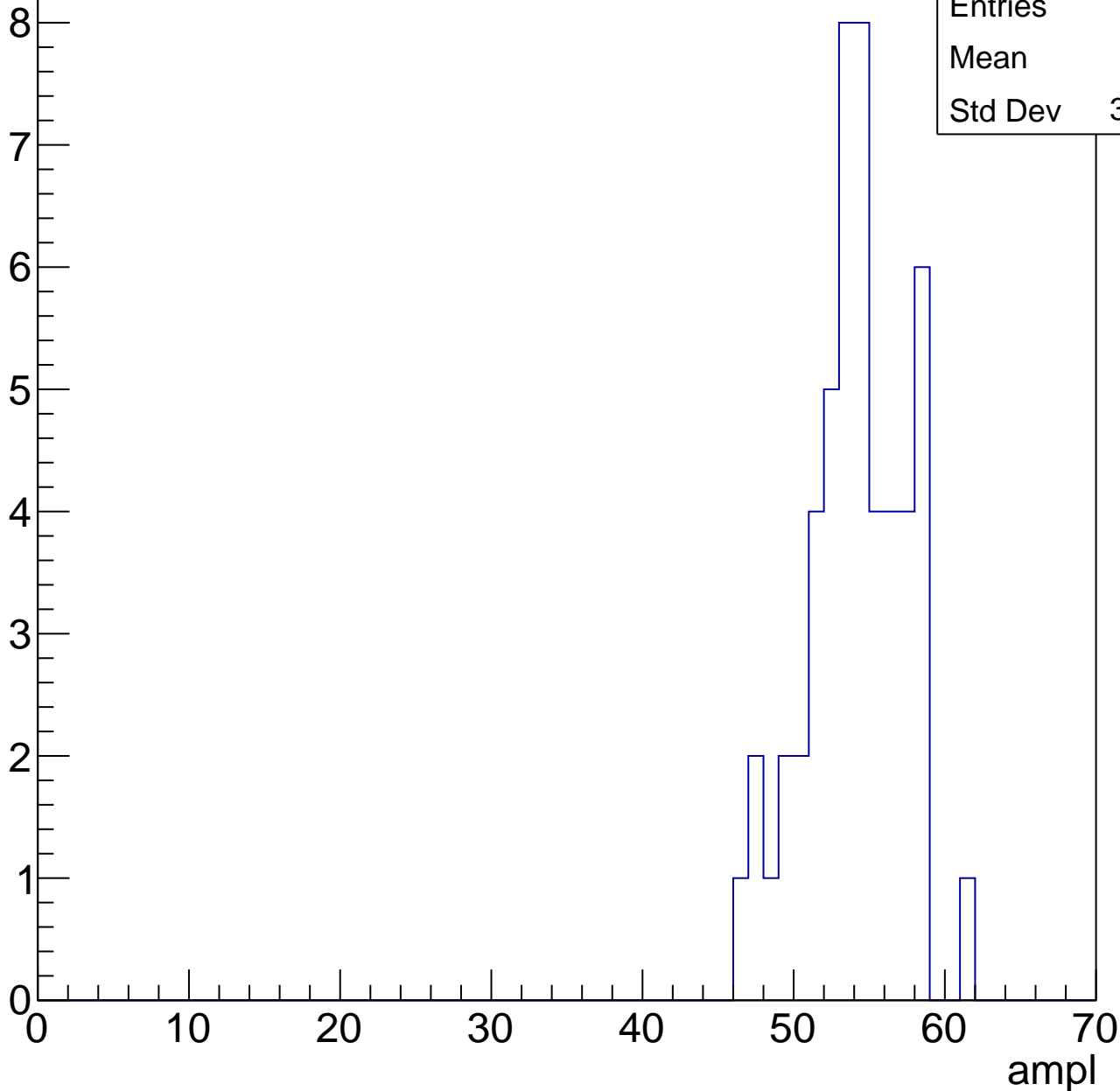


# B0L000S, U18-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	53.6
Std Dev	3.212

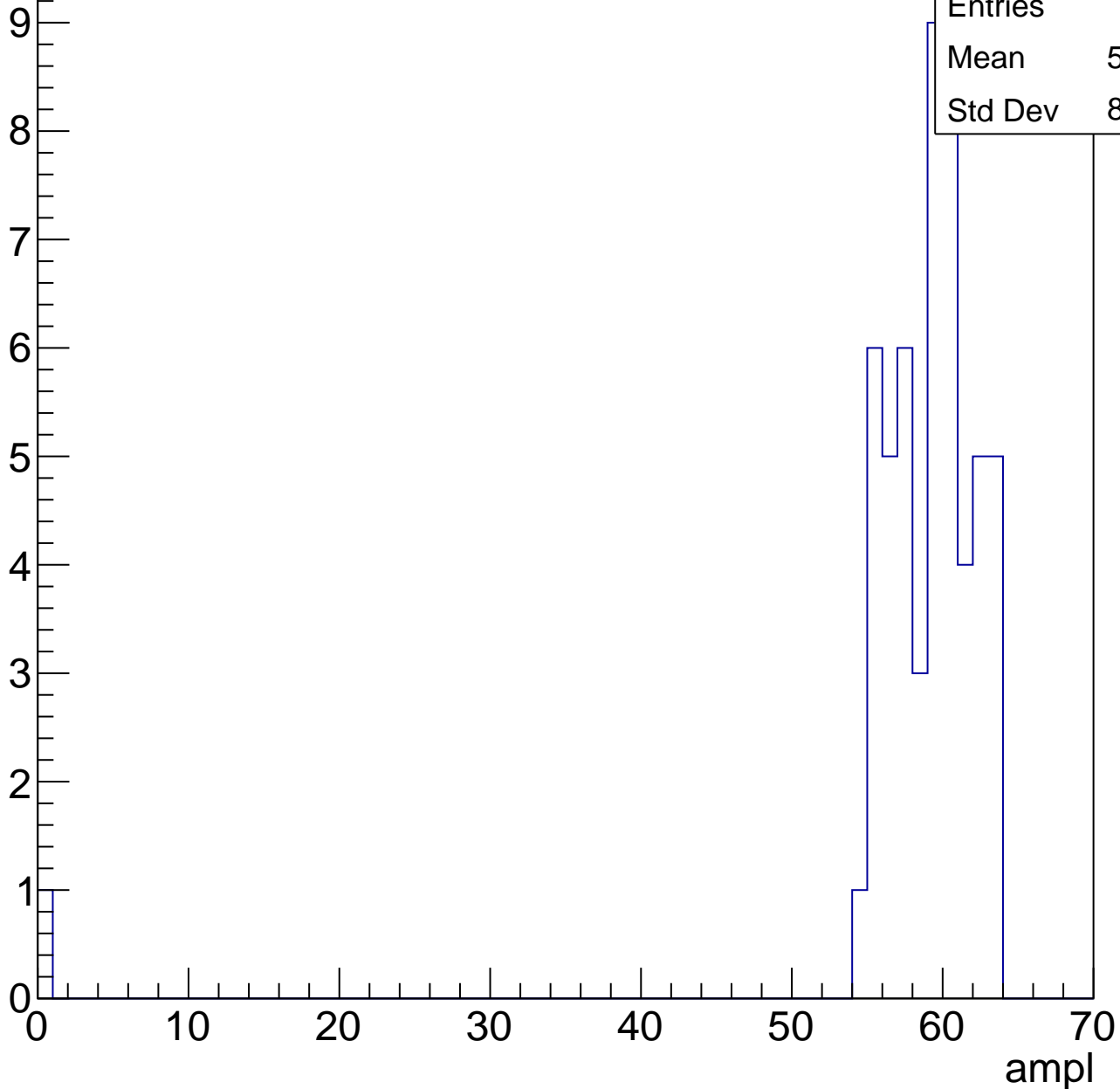


# B0L000S, U18-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	57.78
Std Dev	8.326

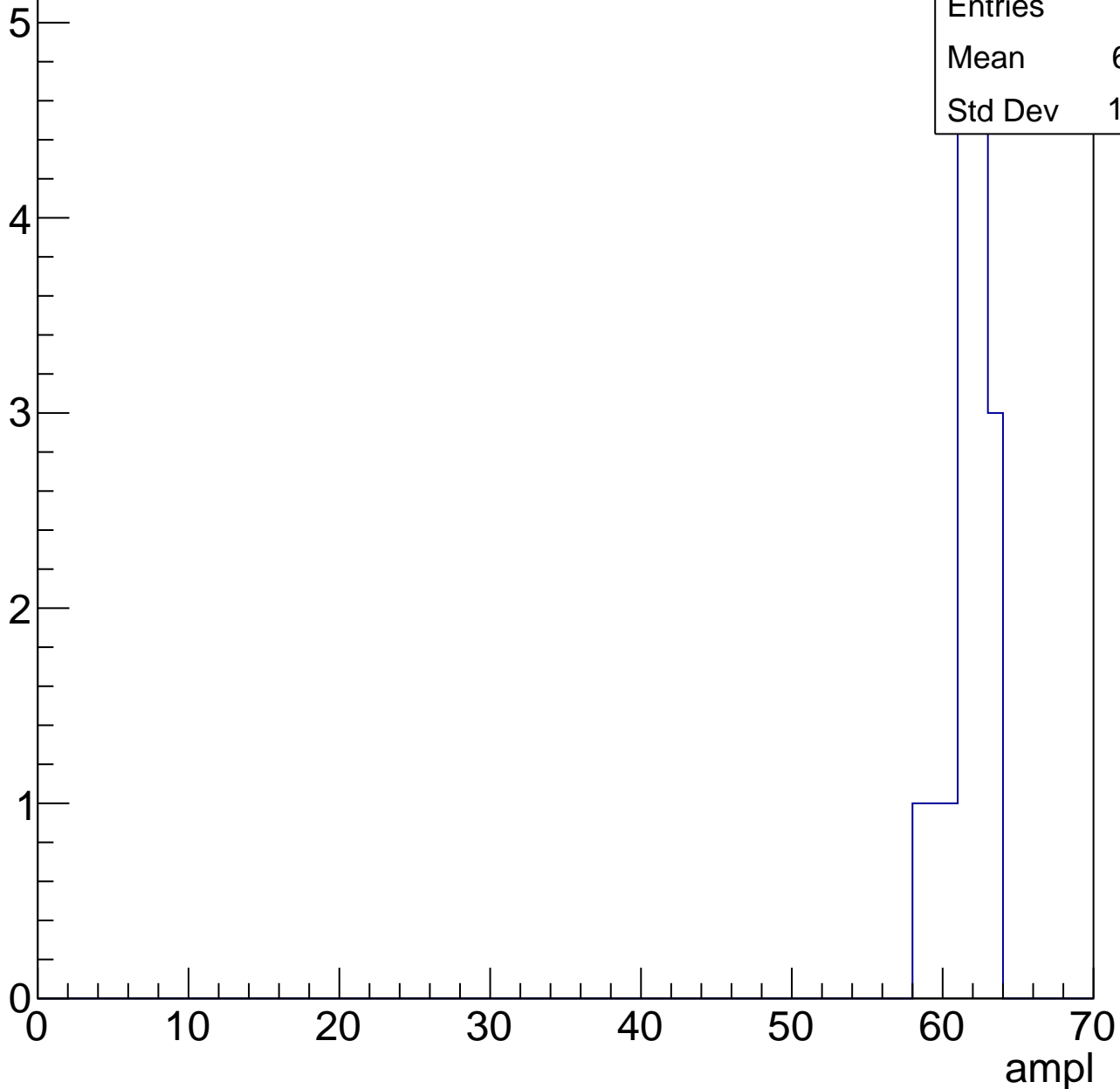


# B0L000S, U18-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.31
Std Dev	1.356

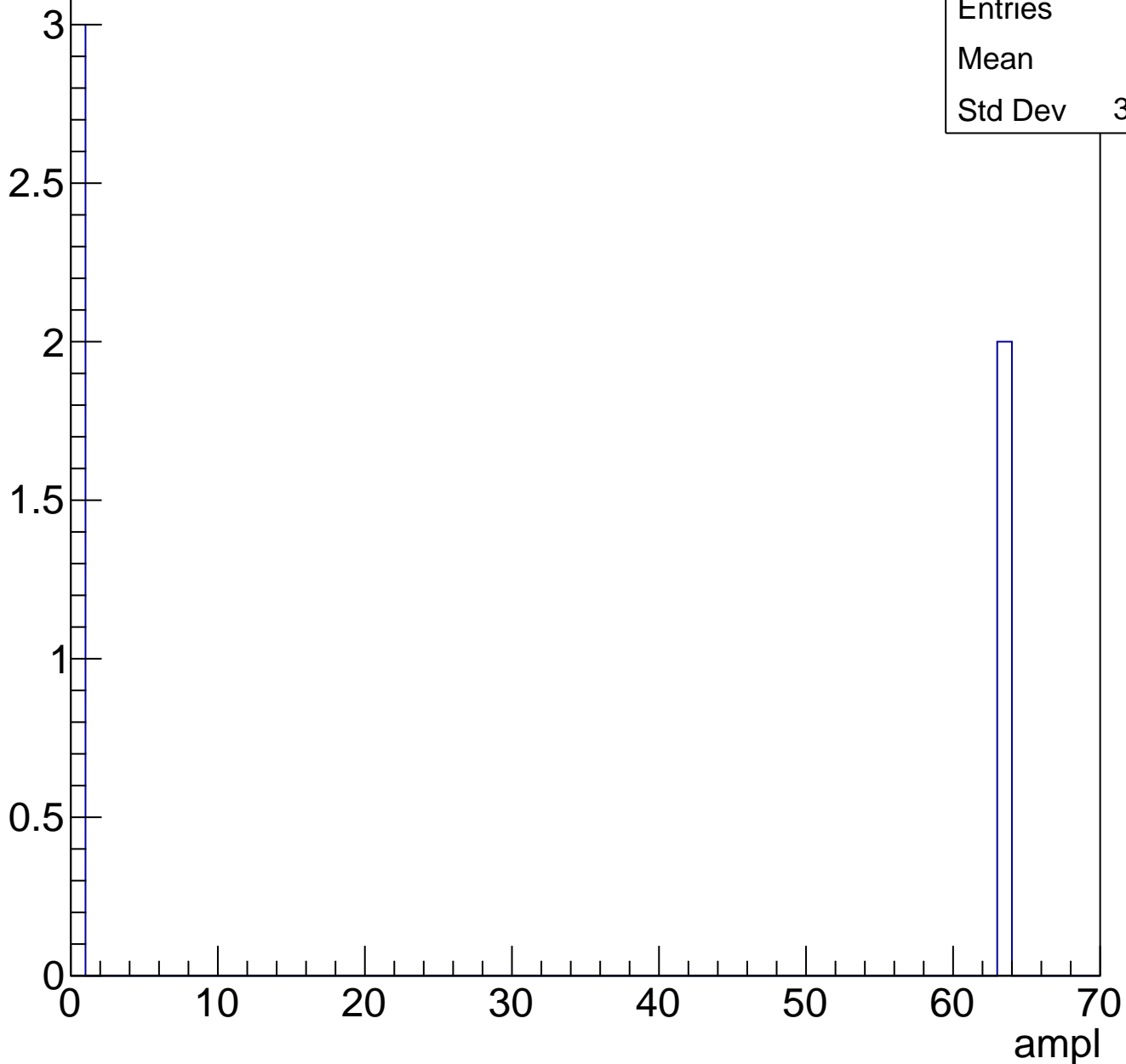




# B0L000S, U18-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch106, adc0

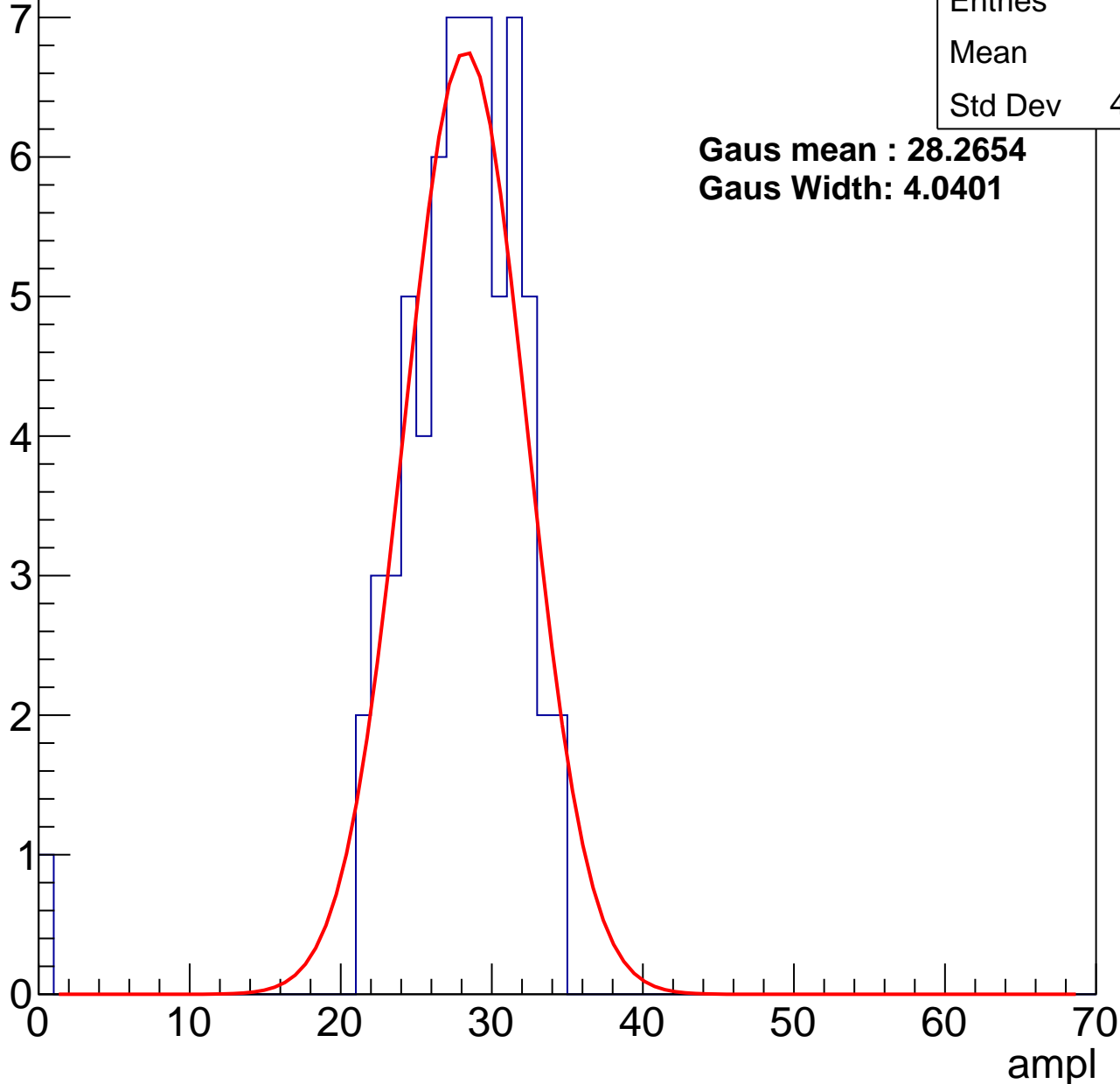
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	27.3
Std Dev	4.723

**Gaus mean : 28.2654**

**Gaus Width: 4.0401**



# B0L000S, U18-ch106, adc1

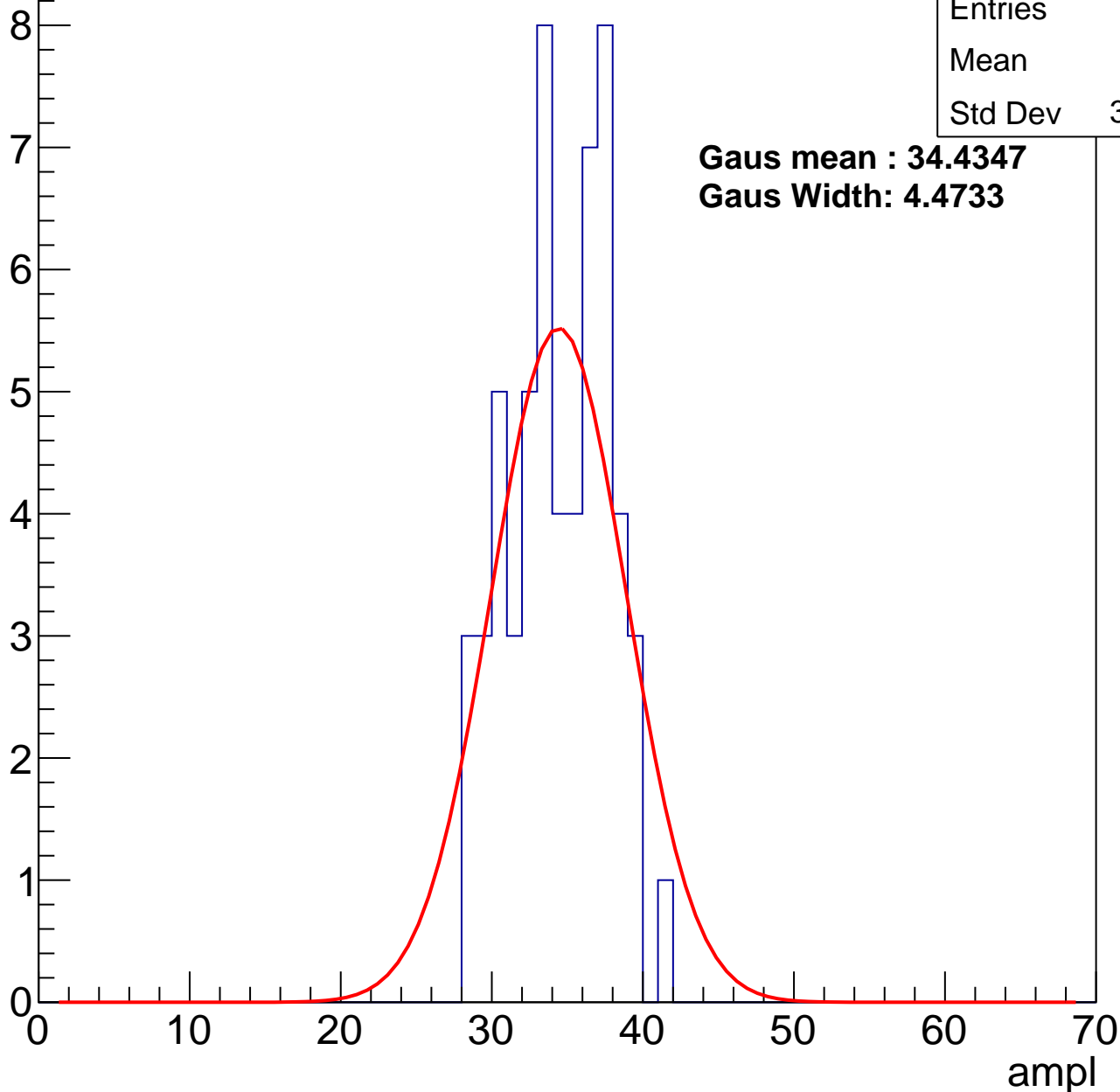
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	34
Std Dev	3.243

**Gaus mean : 34.4347**

**Gaus Width: 4.4733**



# B0L000S, U18-ch106, adc2

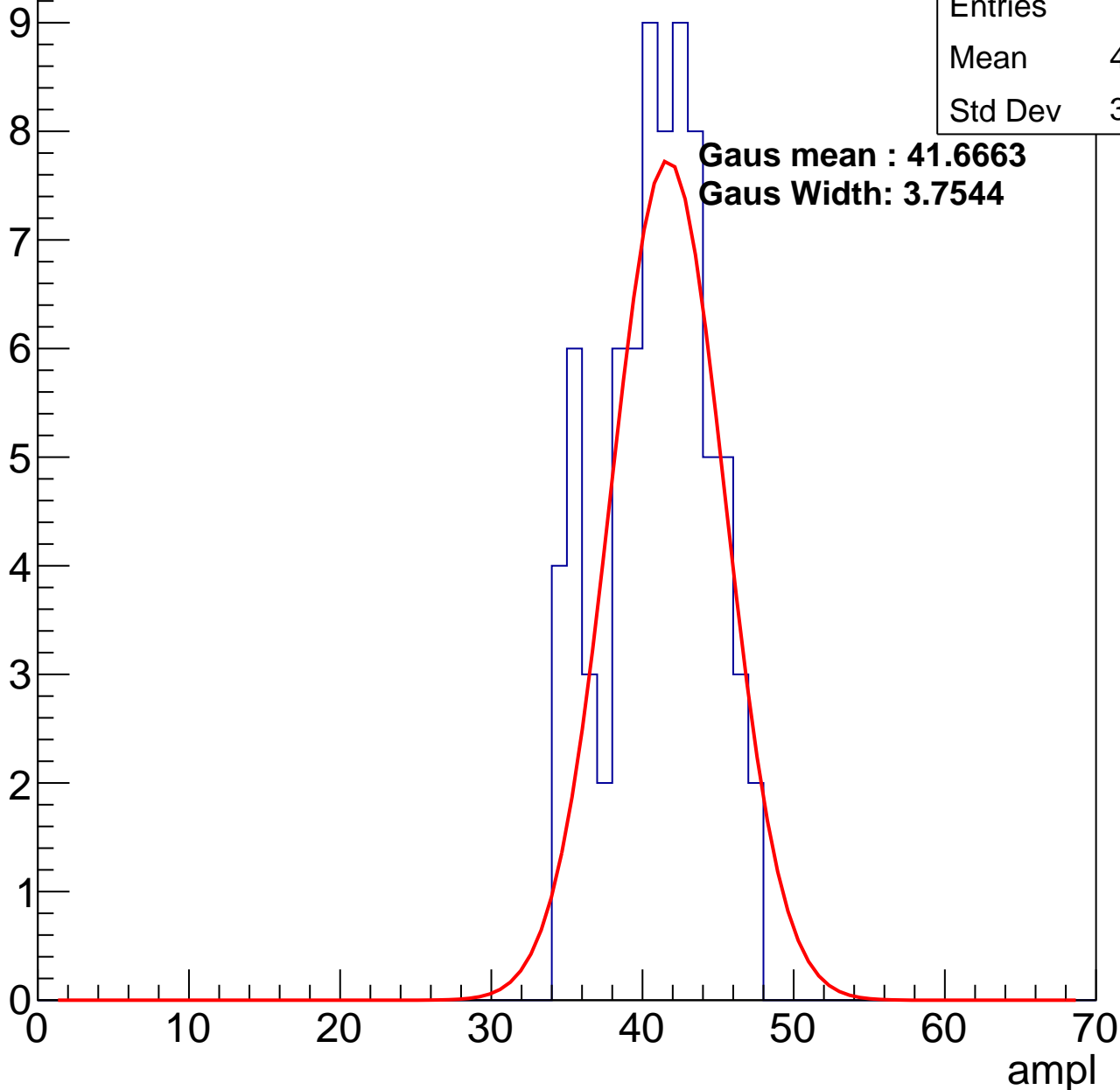
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	40.49
Std Dev	3.439

**Gaus mean : 41.6663**

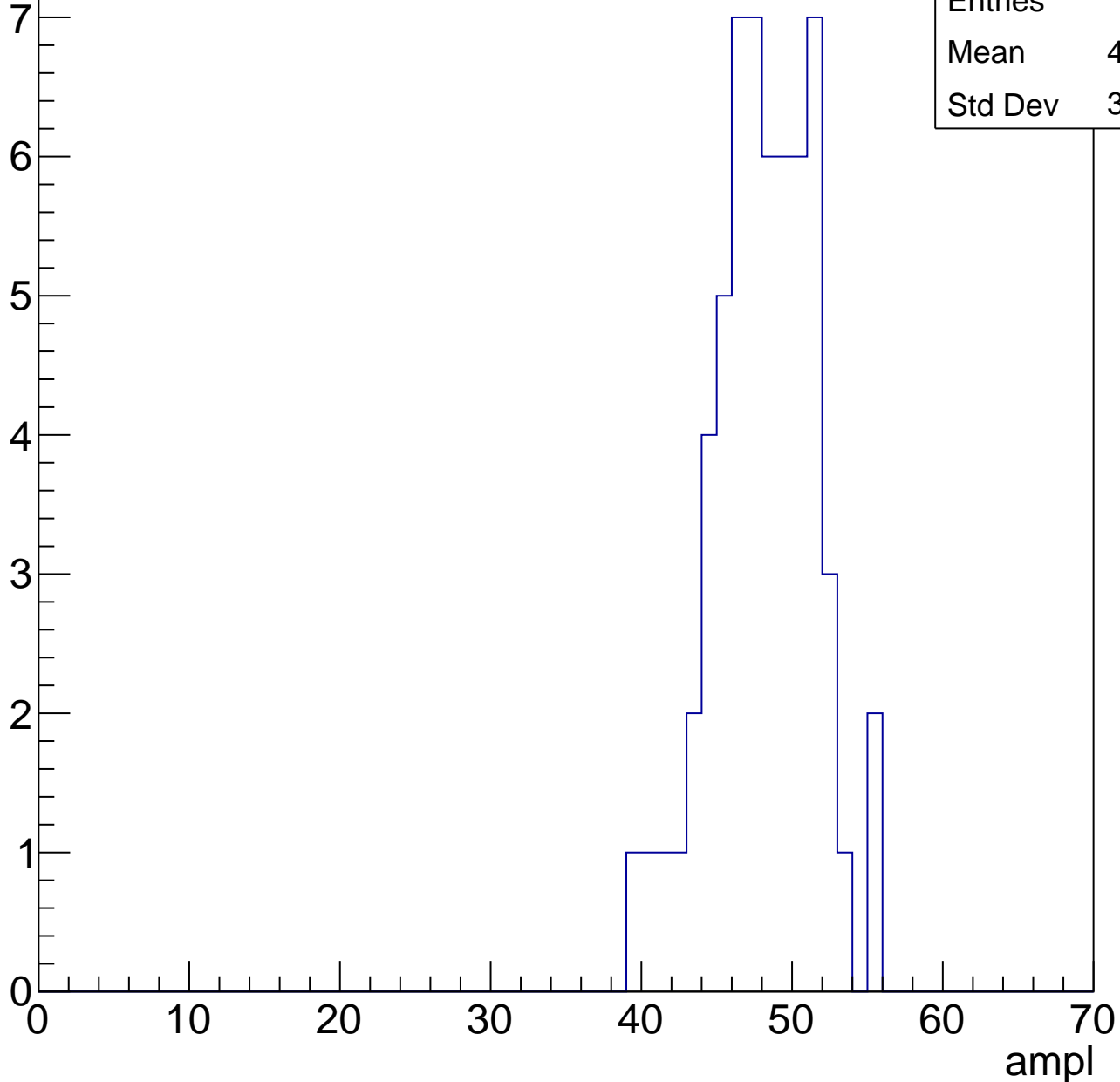
**Gaus Width: 3.7544**



# B0L000S, U18-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



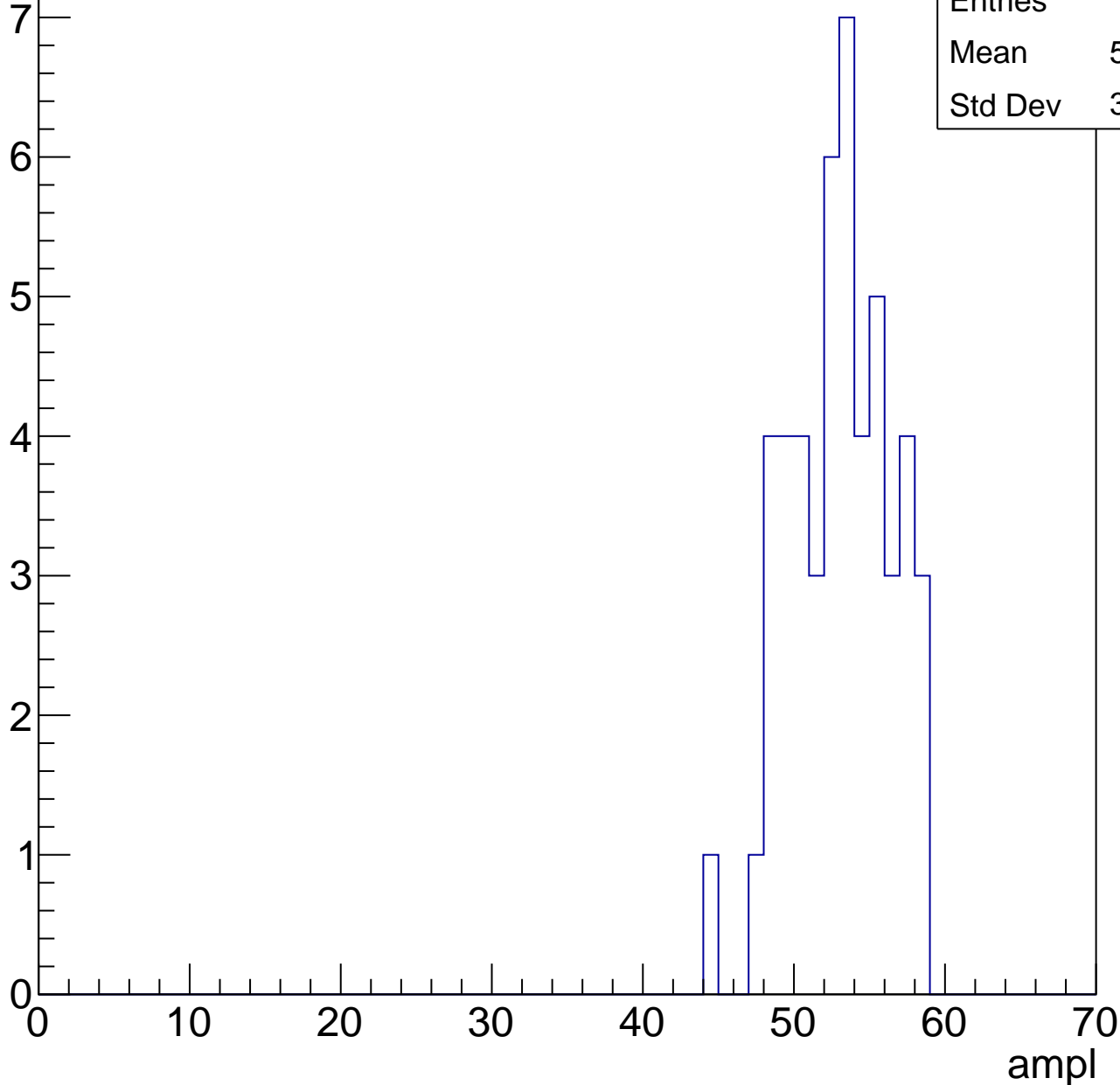
Entries	60
Mean	47.63
Std Dev	3.366

# B0L000S, U18-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	52.57
Std Dev	3.245

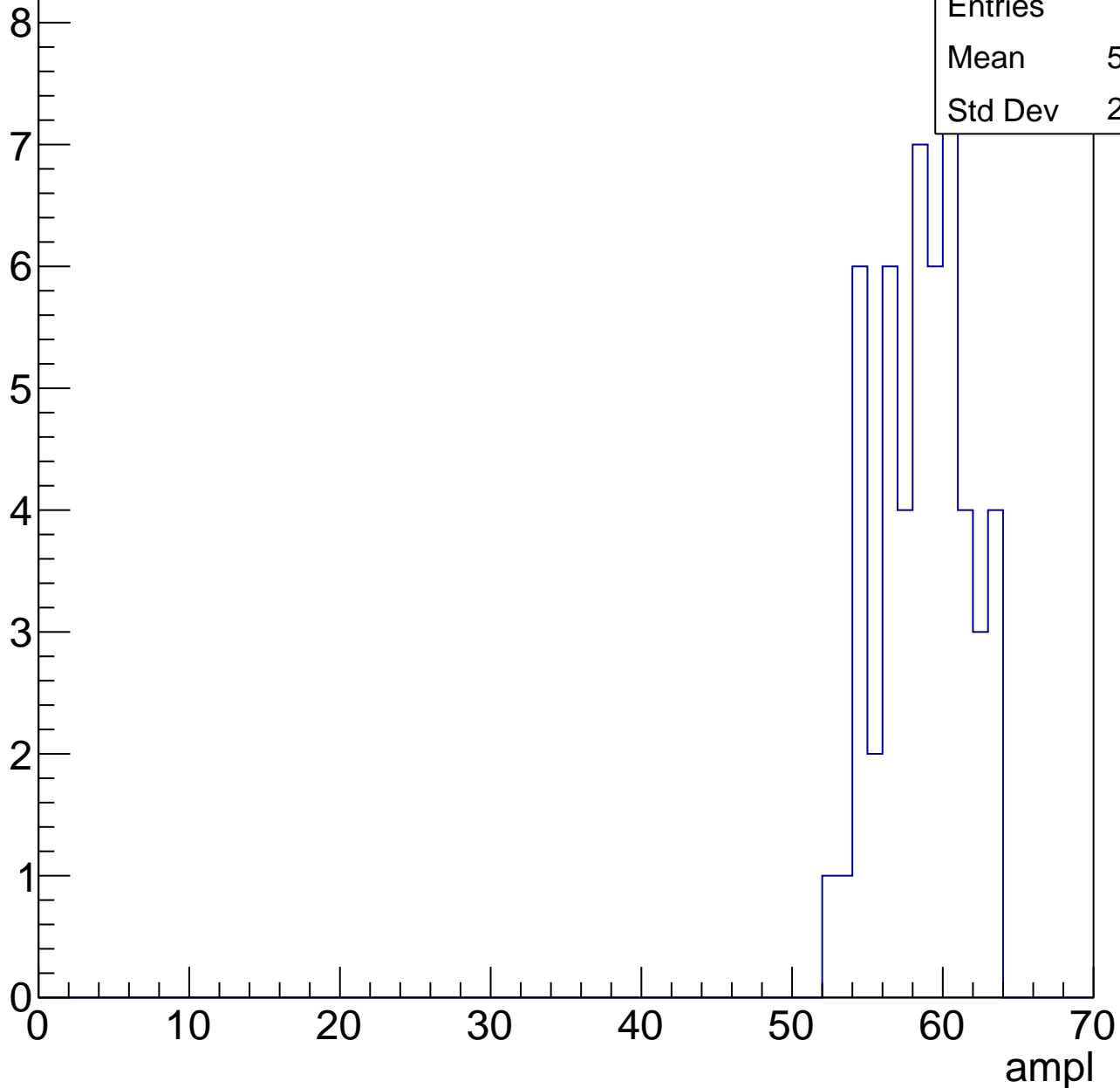


# B0L000S, U18-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.17
Std Dev	2.854

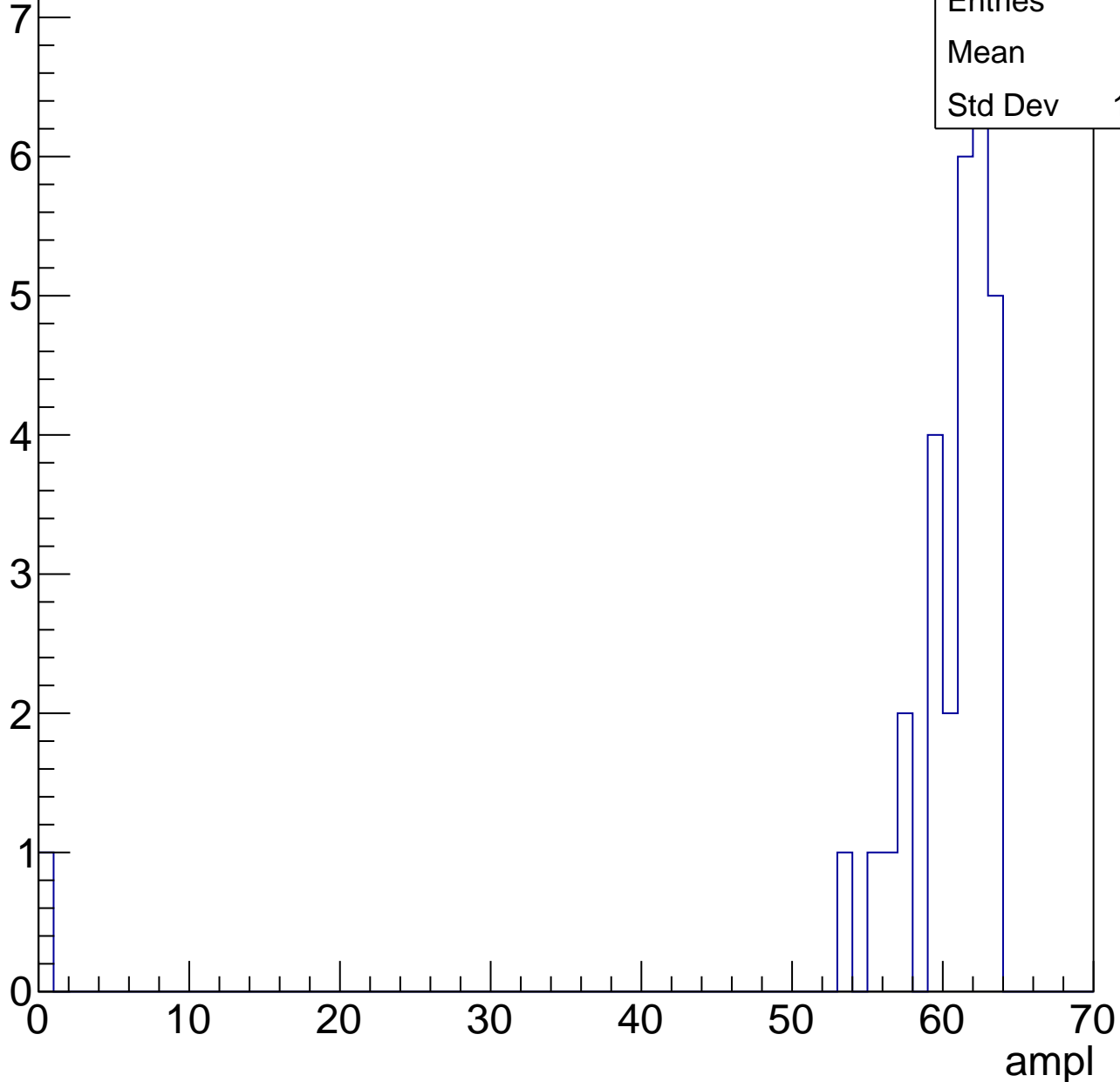


# B0L000S, U18-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	30
Mean	58.3
Std Dev	11.11

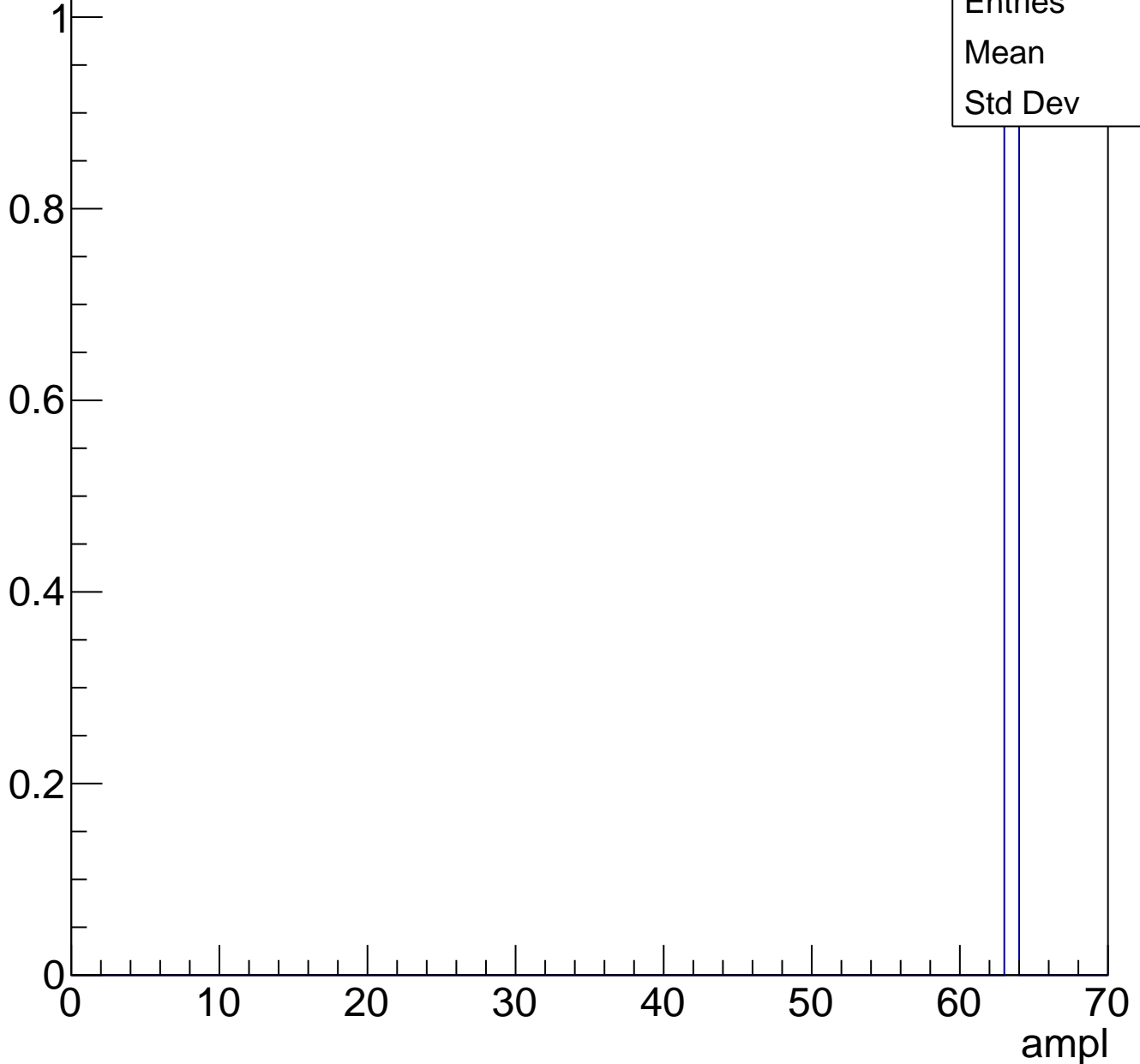




# B0L000S, U18-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



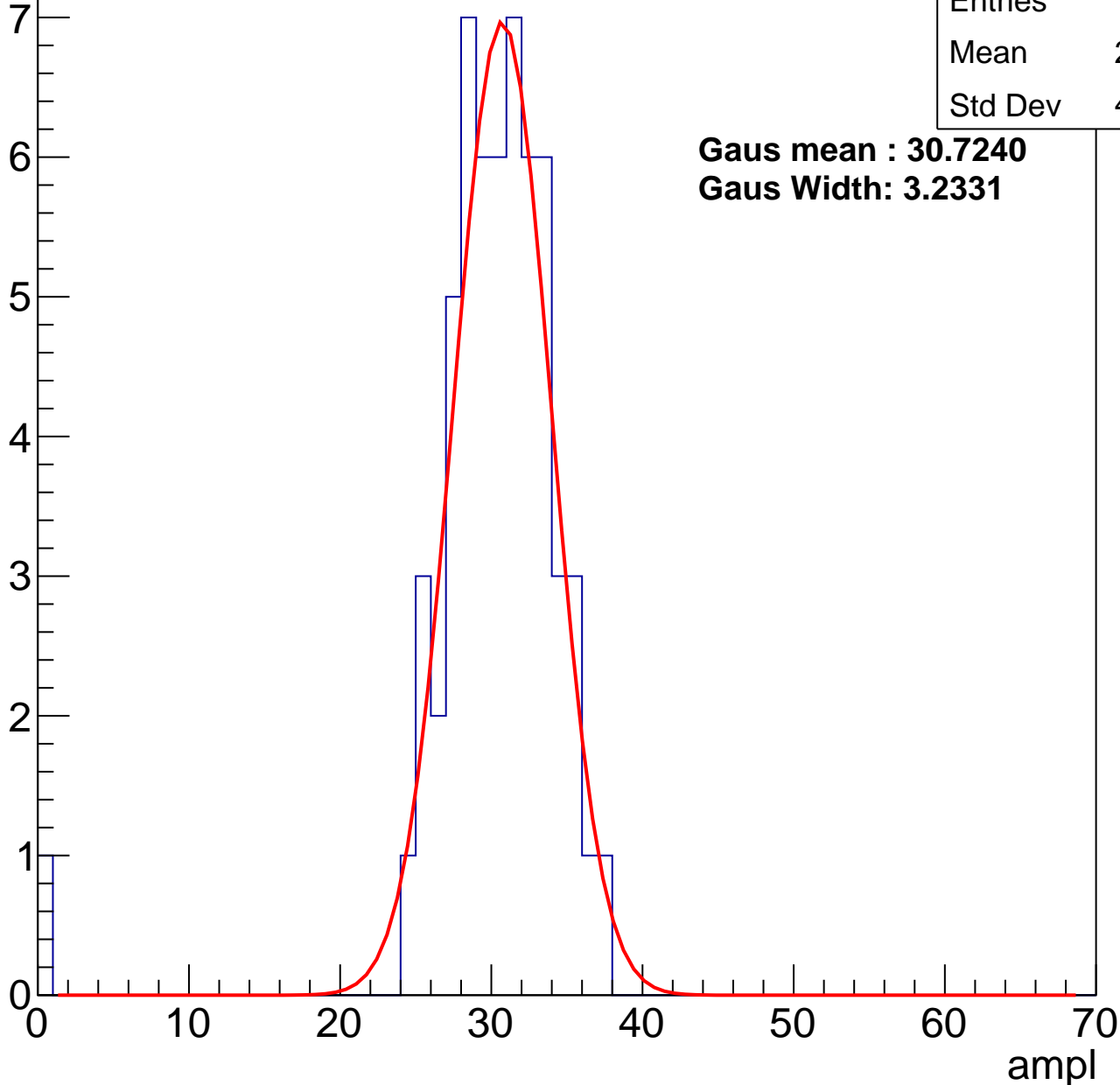
# B0L000S, U18-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	29.71
Std Dev	4.931

**Gaus mean : 30.7240**  
**Gaus Width: 3.2331**



# B0L000S, U18-ch107, adc1

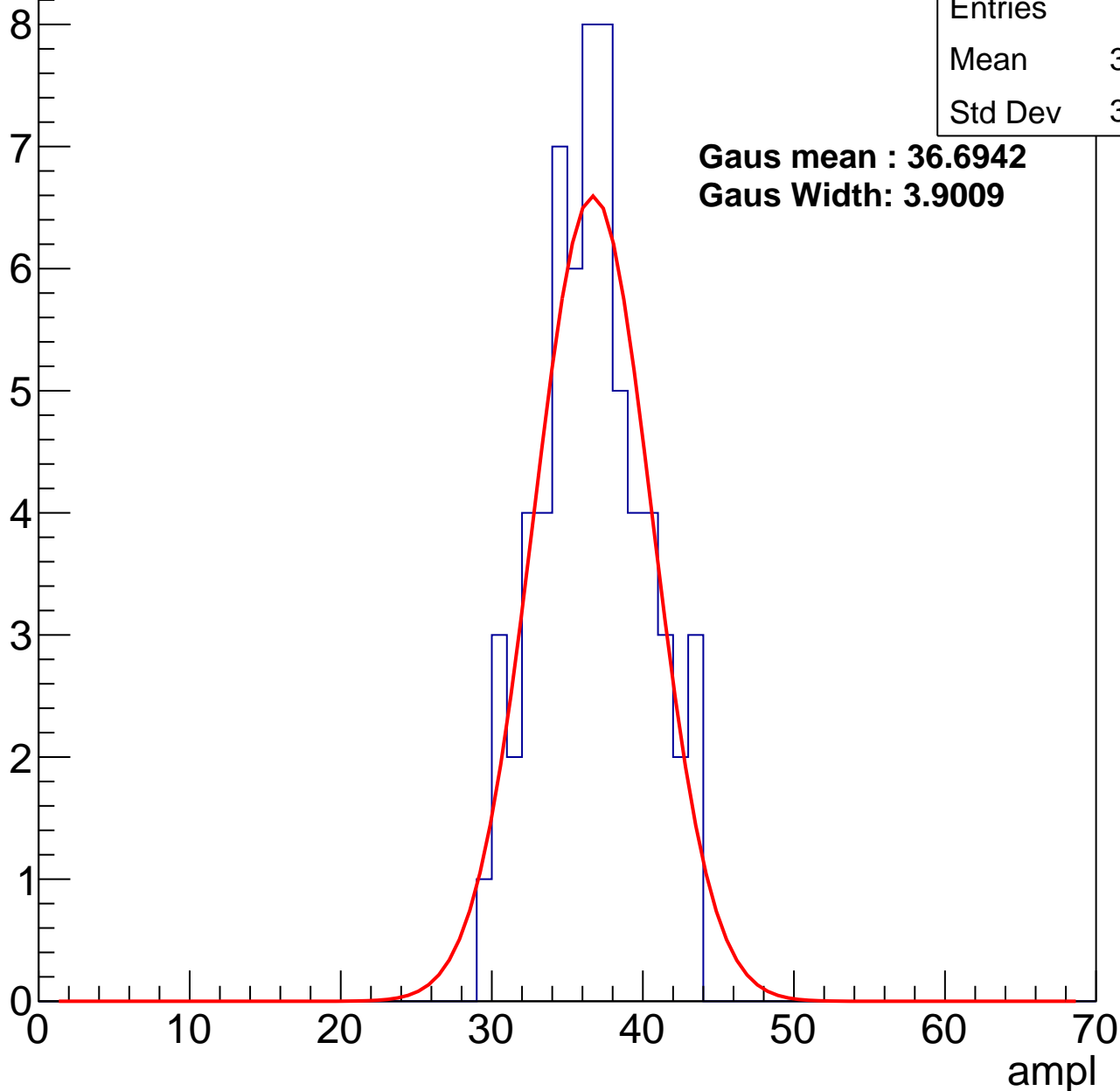
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.17
Std Dev	3.449

**Gaus mean : 36.6942**

**Gaus Width: 3.9009**



# B0L000S, U18-ch107, adc2

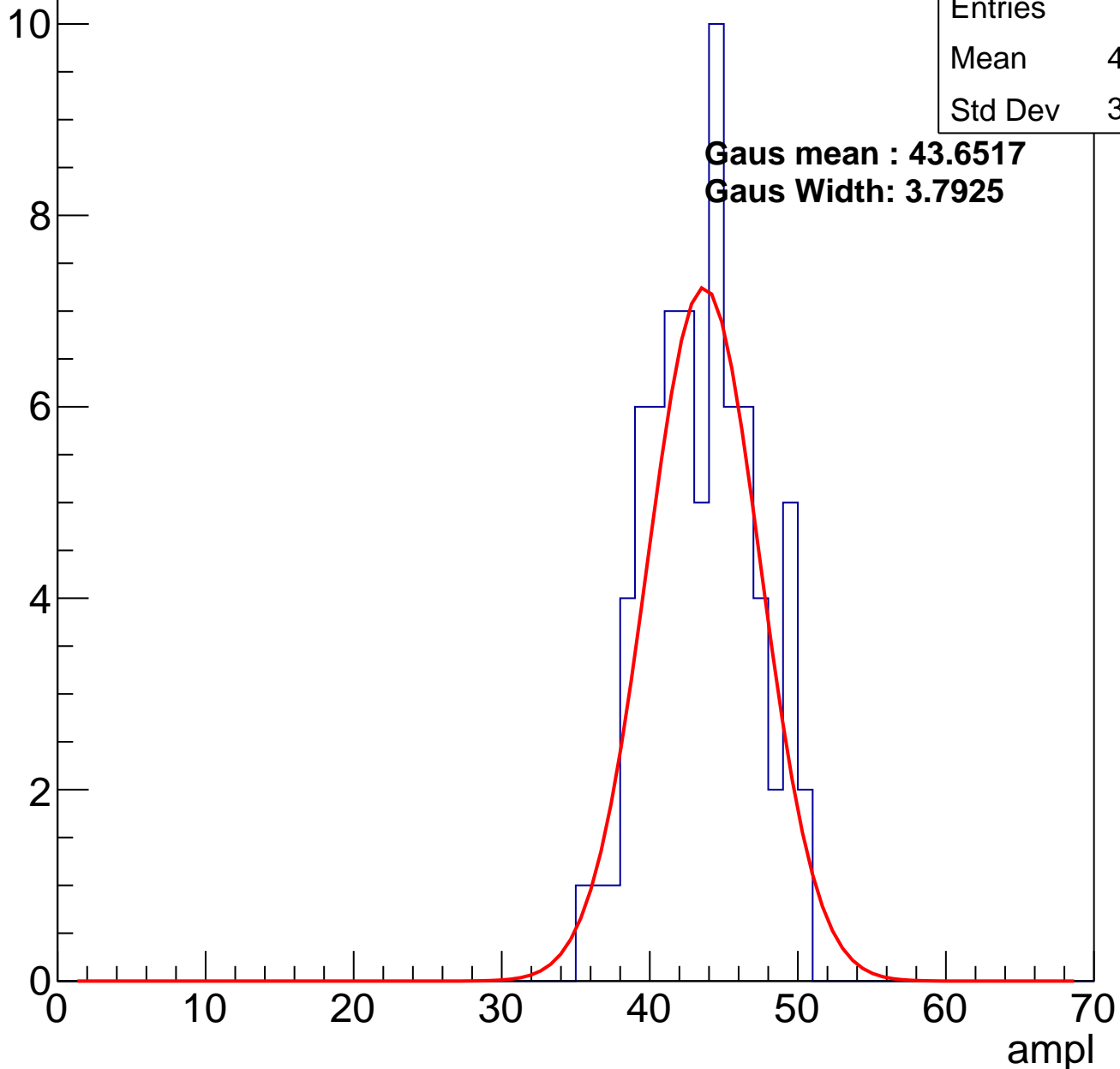
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	43.08
Std Dev	3.537

**Gaus mean : 43.6517**

**Gaus Width: 3.7925**

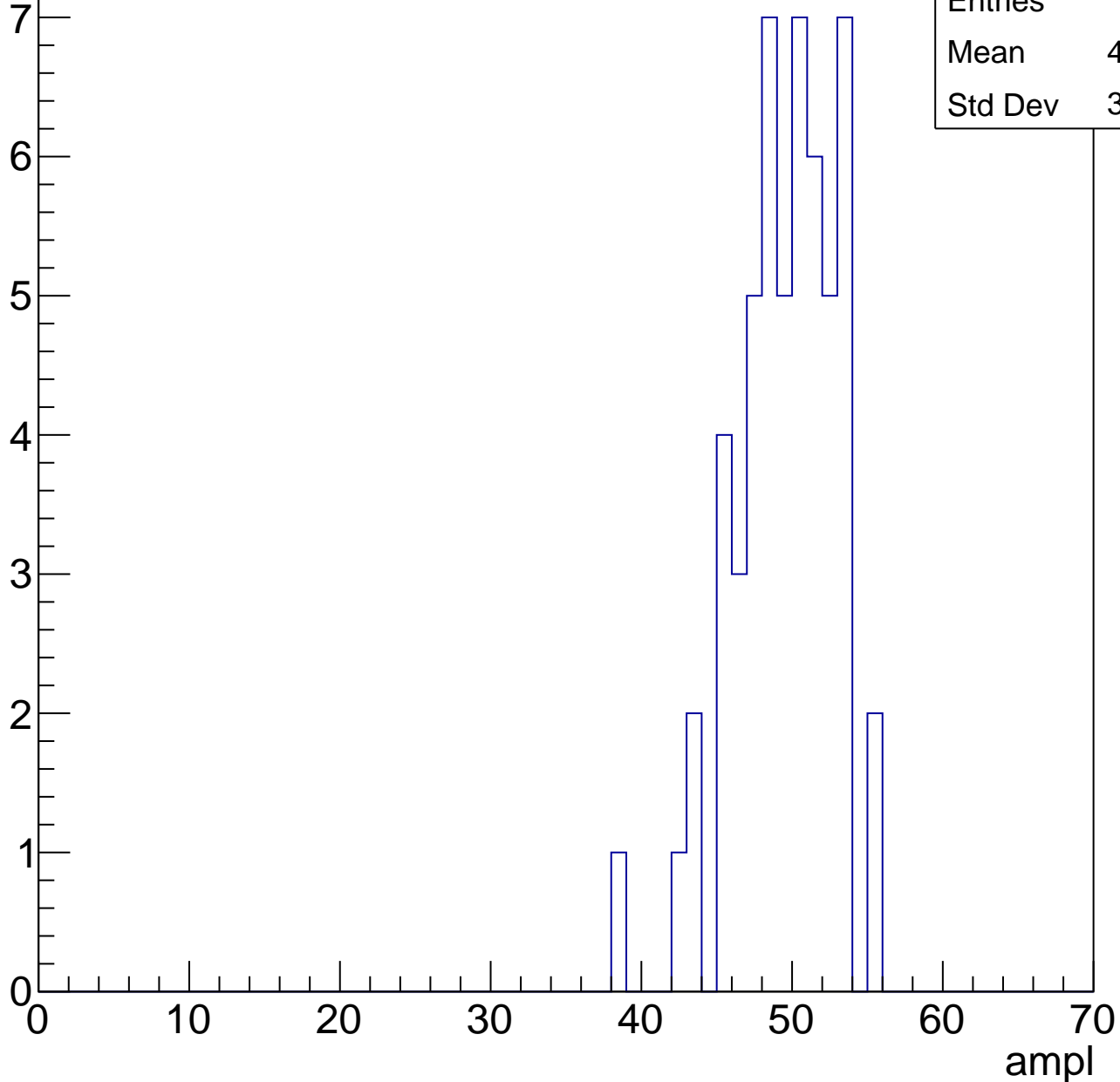
Entry



# B0L000S, U18-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



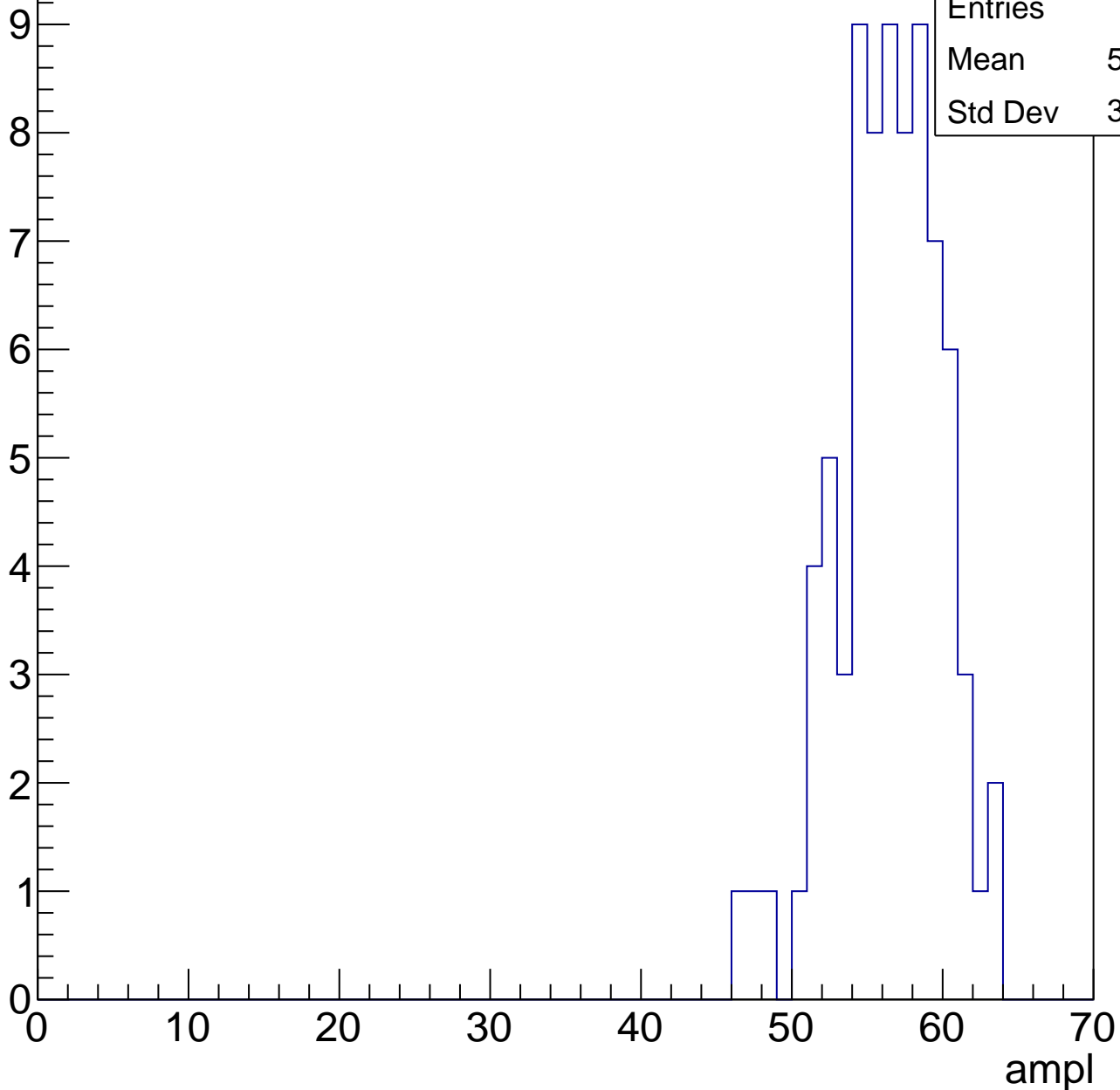
Entries	55
Mean	49.04
Std Dev	3.357

# B0L000S, U18-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	55.97
Std Dev	3.479

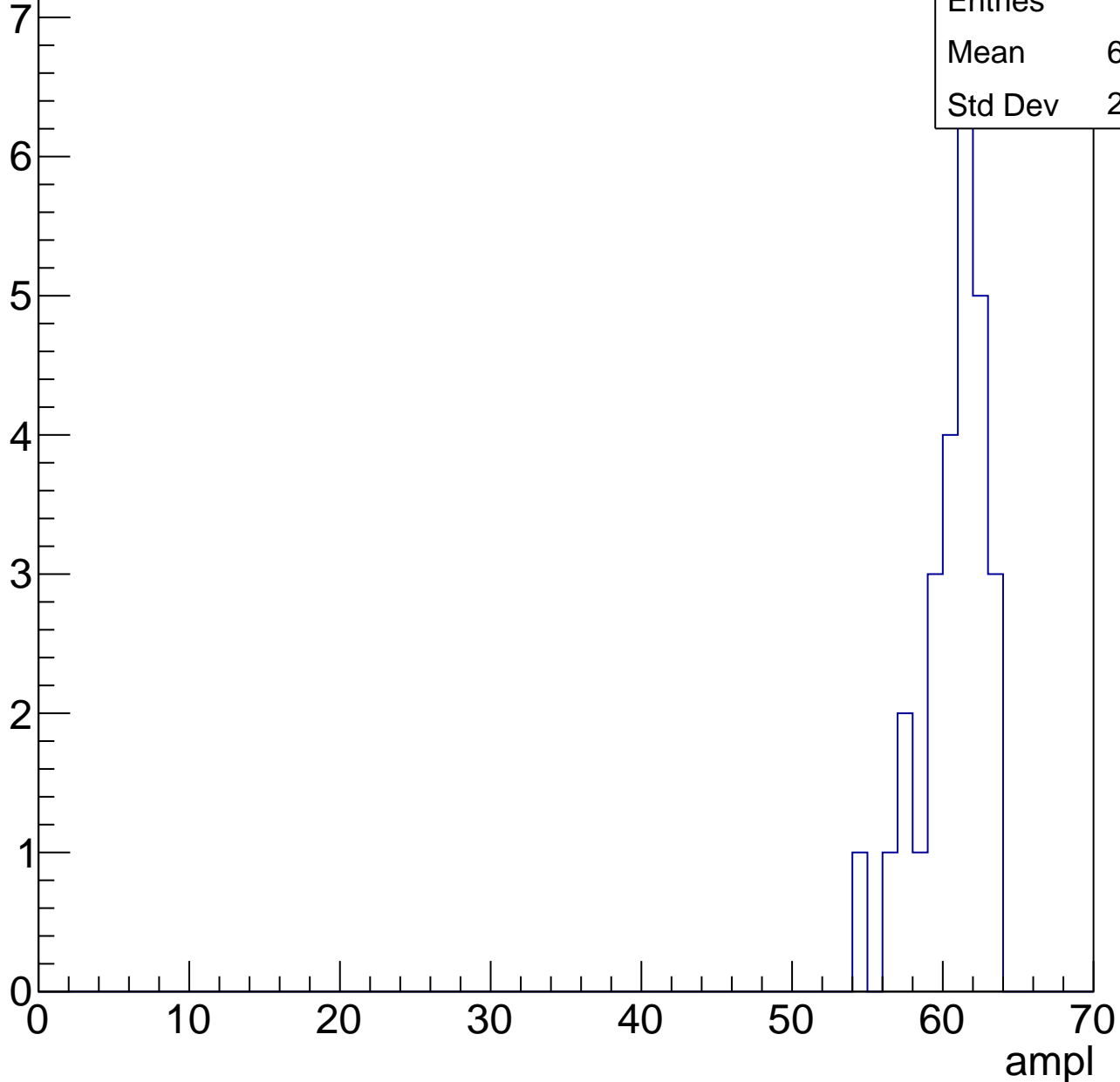


# B0L000S, U18-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

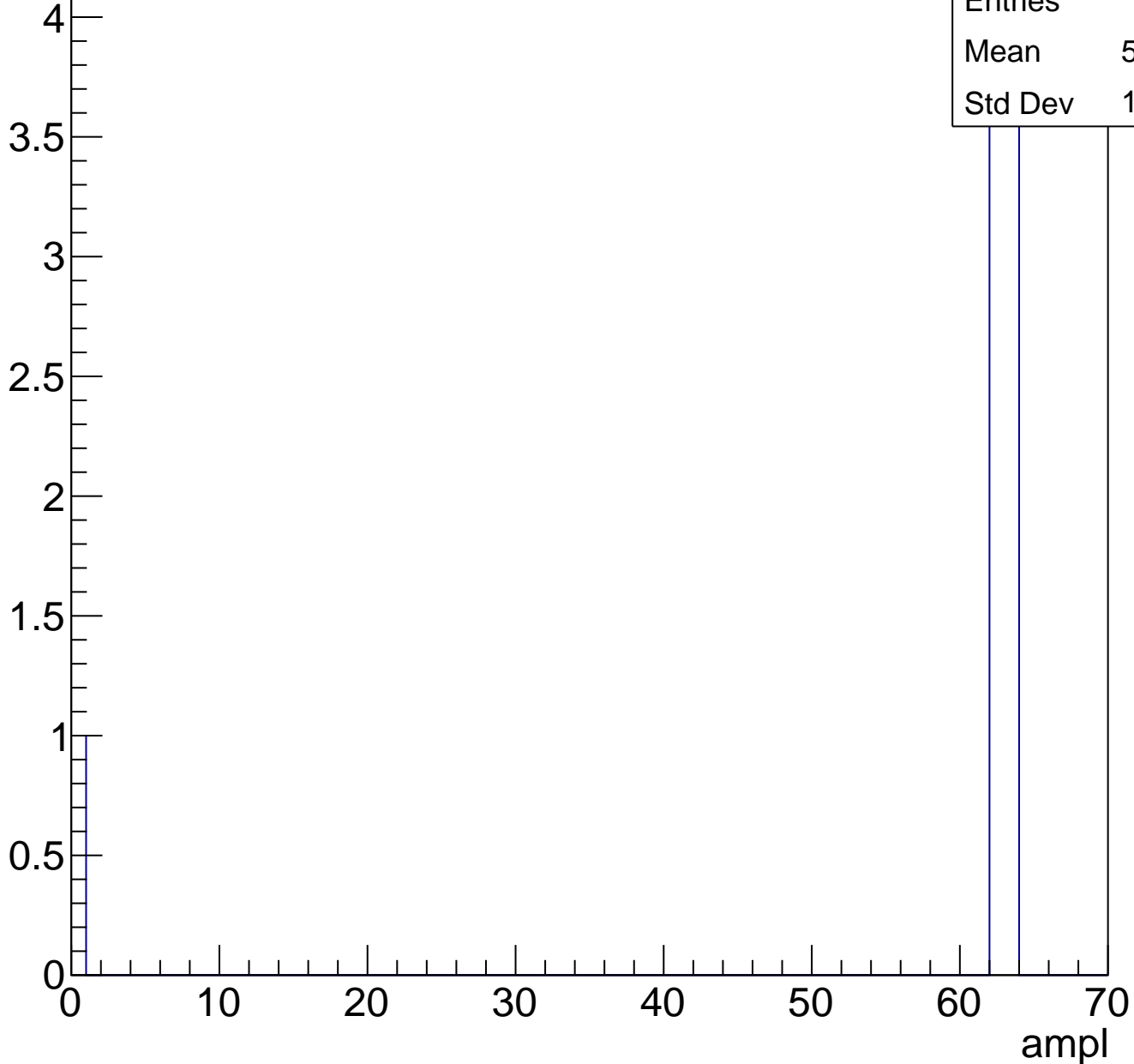
Entries	27
Mean	60.19
Std Dev	2.195



# B0L000S, U18-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

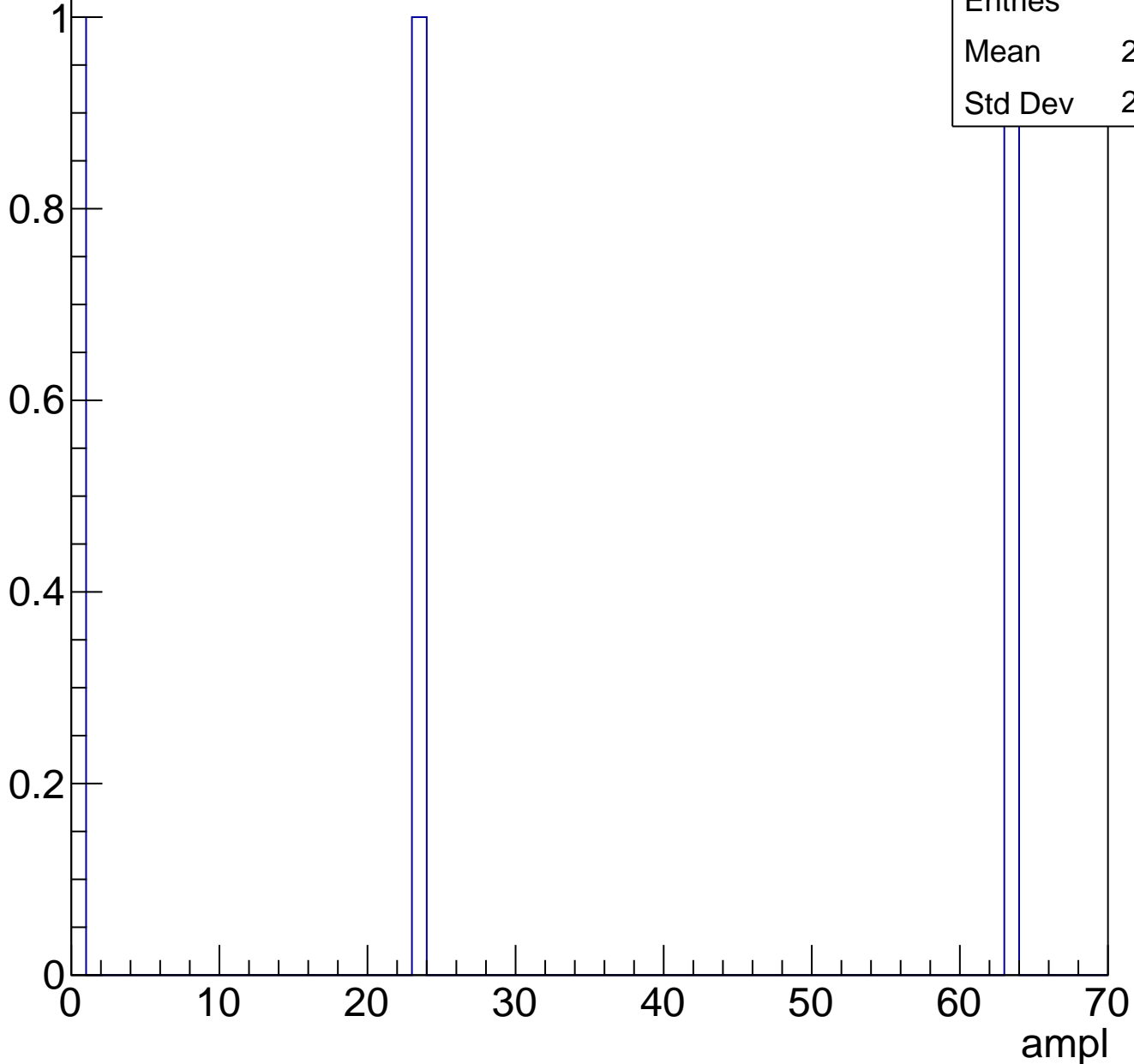




# B0L000S, U18-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	28.67
Std Dev	26.03

# B0L000S, U18-ch108, adc0

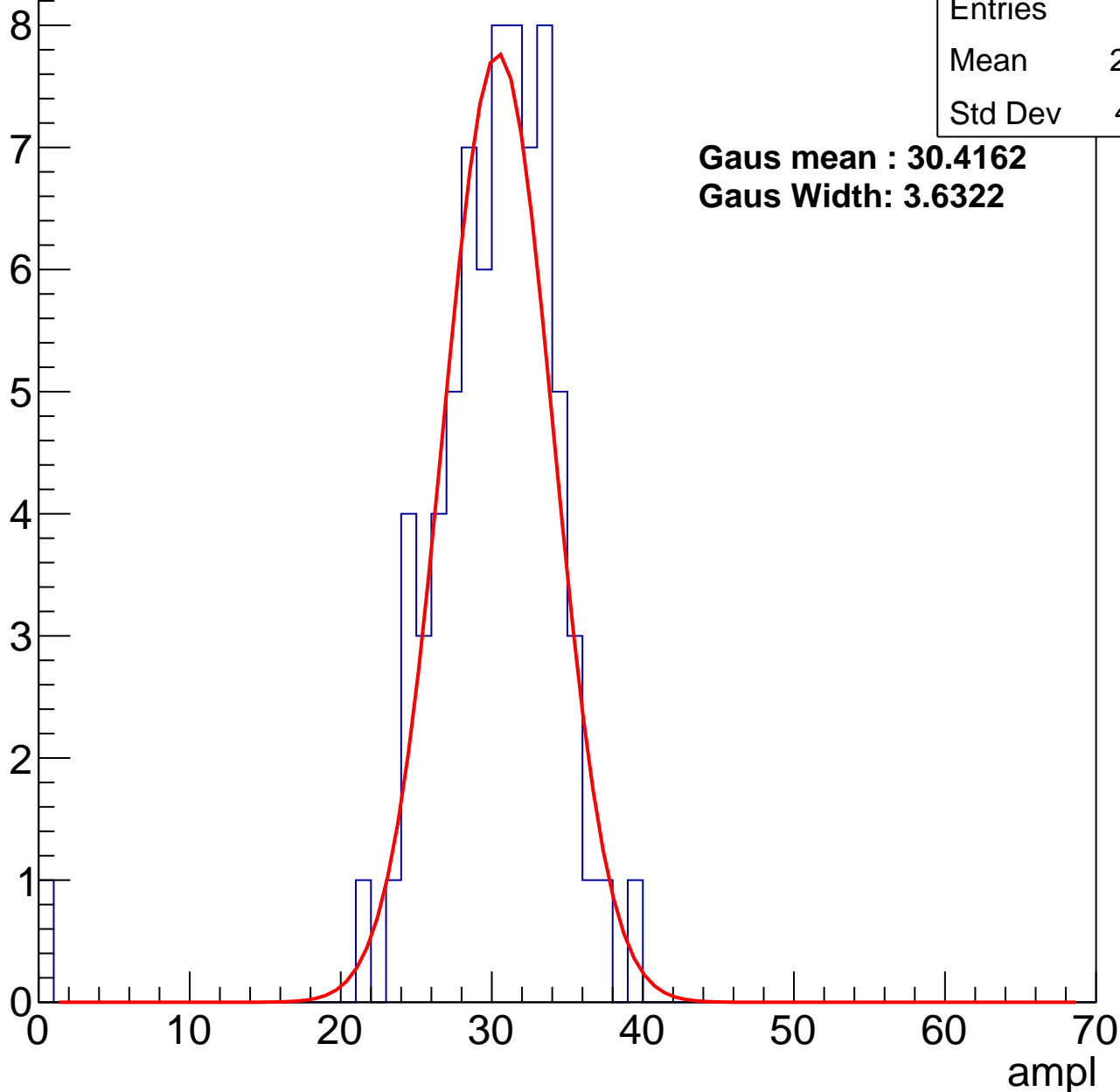
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.55
Std Dev	4.941

**Gaus mean : 30.4162**

**Gaus Width: 3.6322**



# B0L000S, U18-ch108, adc1

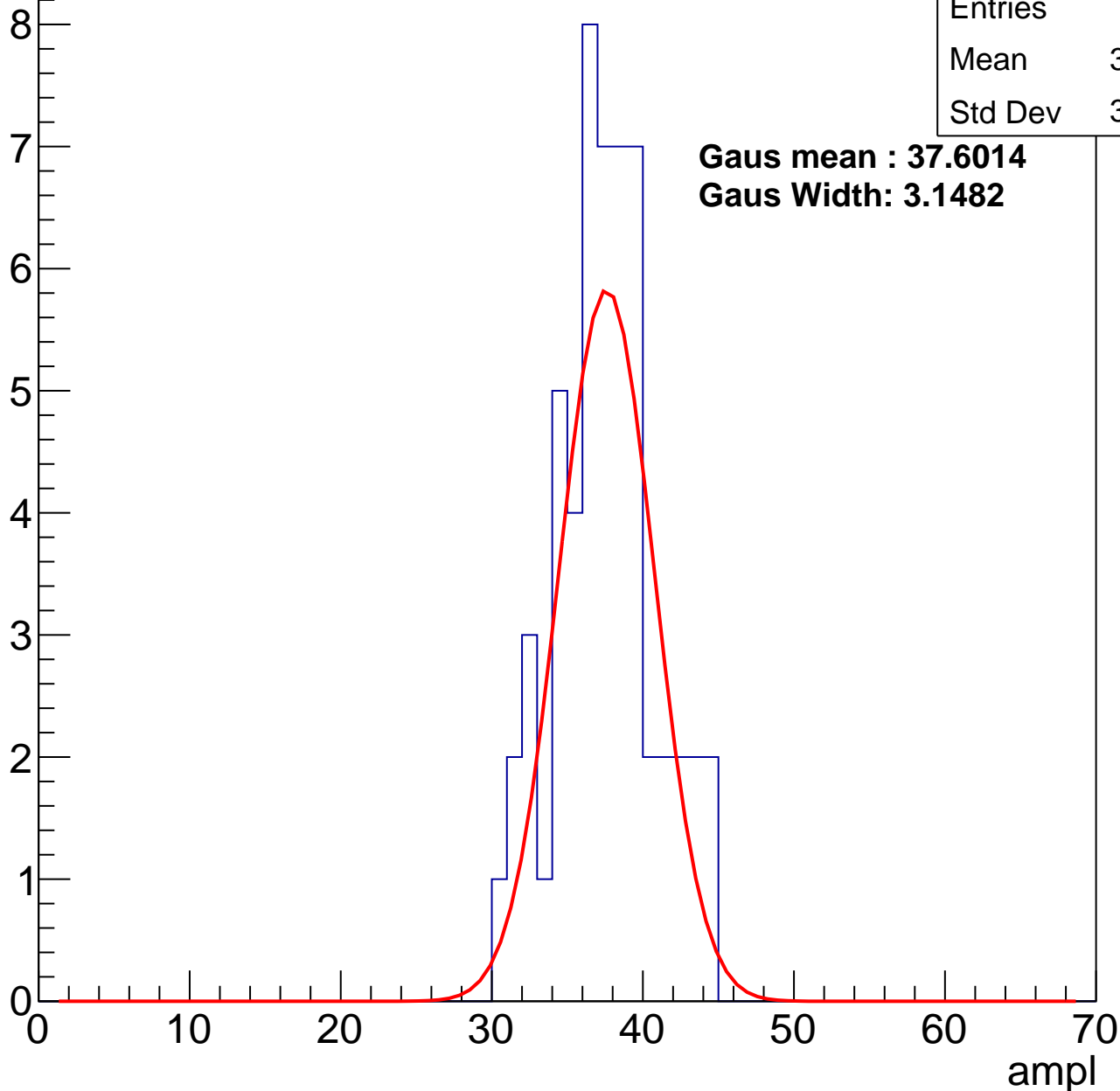
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	37.04
Std Dev	3.264

**Gaus mean : 37.6014**

**Gaus Width: 3.1482**



# B0L000S, U18-ch108, adc2

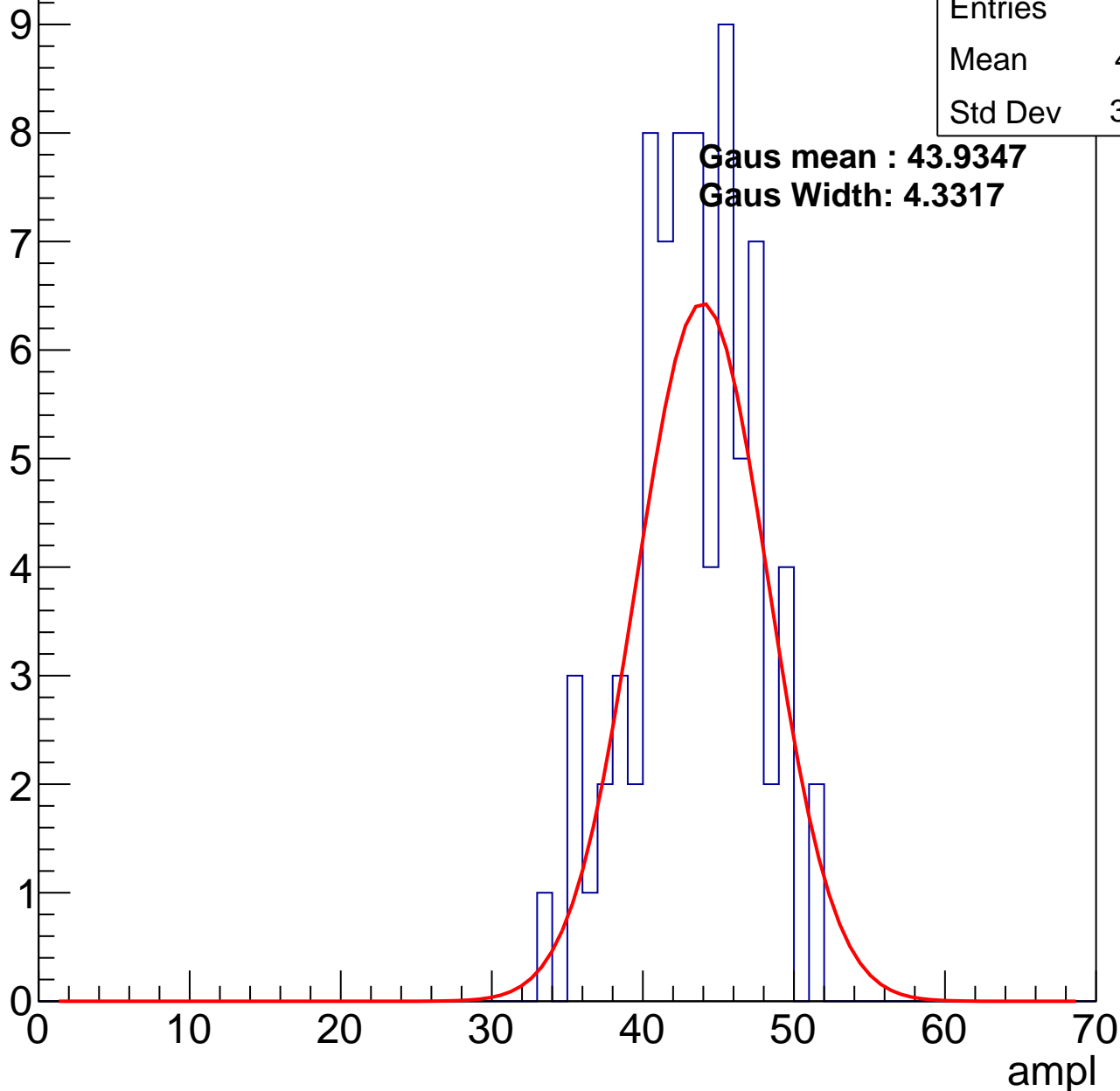
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.91
Std Dev	3.884

**Gaus mean : 43.9347**

**Gaus Width: 4.3317**

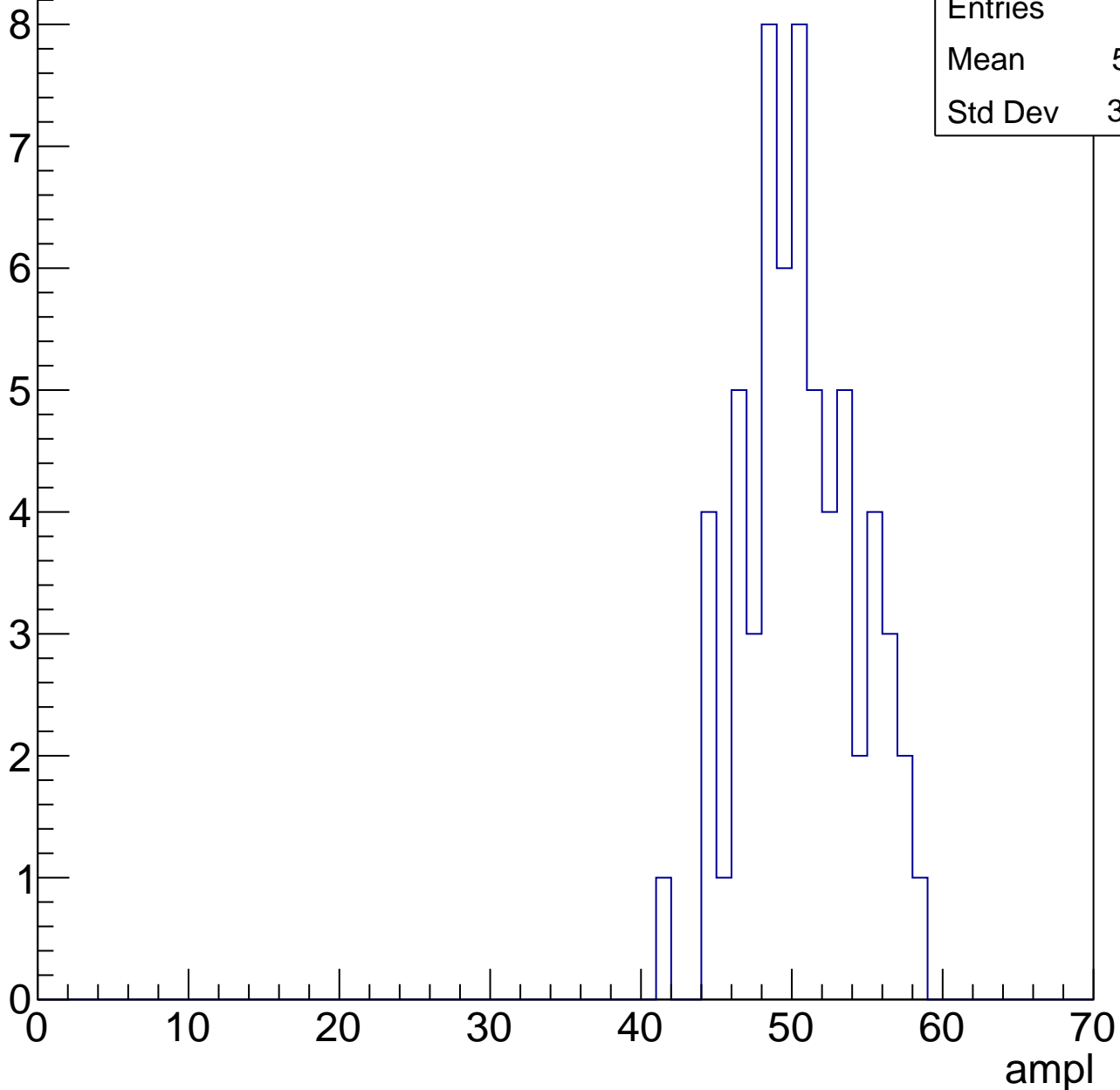


# B0L000S, U18-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.11
Std Dev	3.729

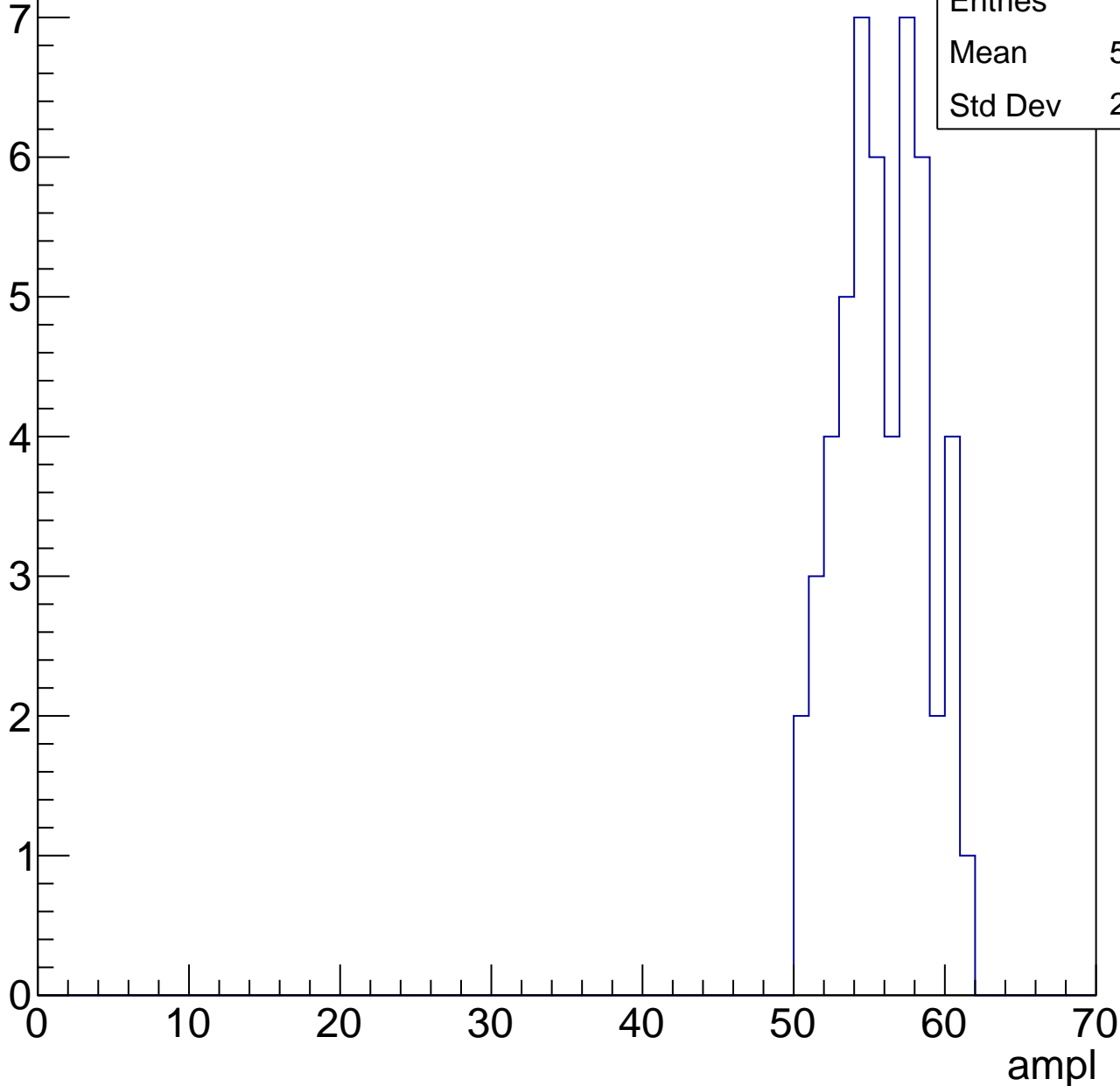


# B0L000S, U18-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	55.37
Std Dev	2.828

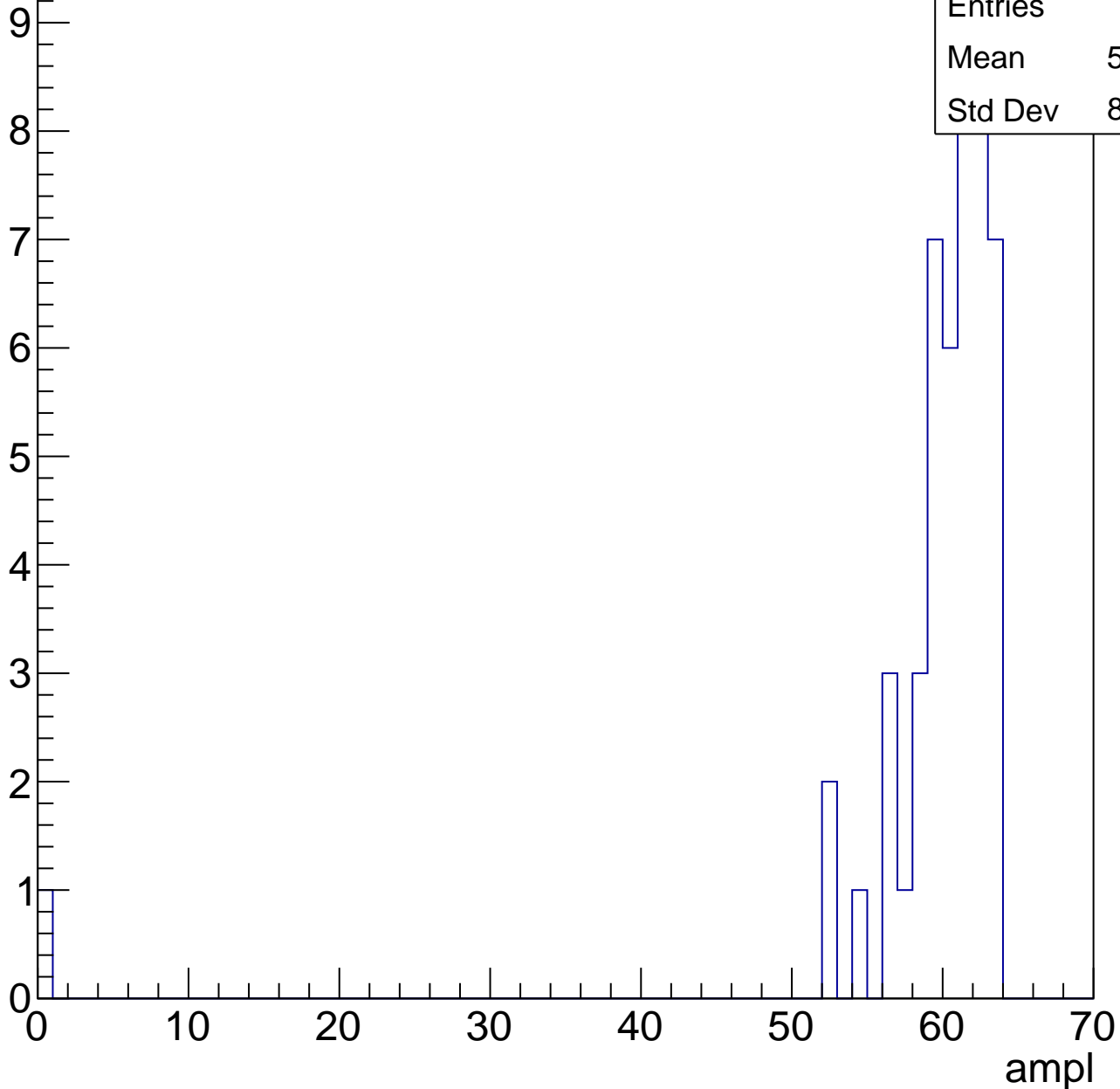


# B0L000S, U18-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	58.73
Std Dev	8.887



# B0L000S, U18-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch109, adc0

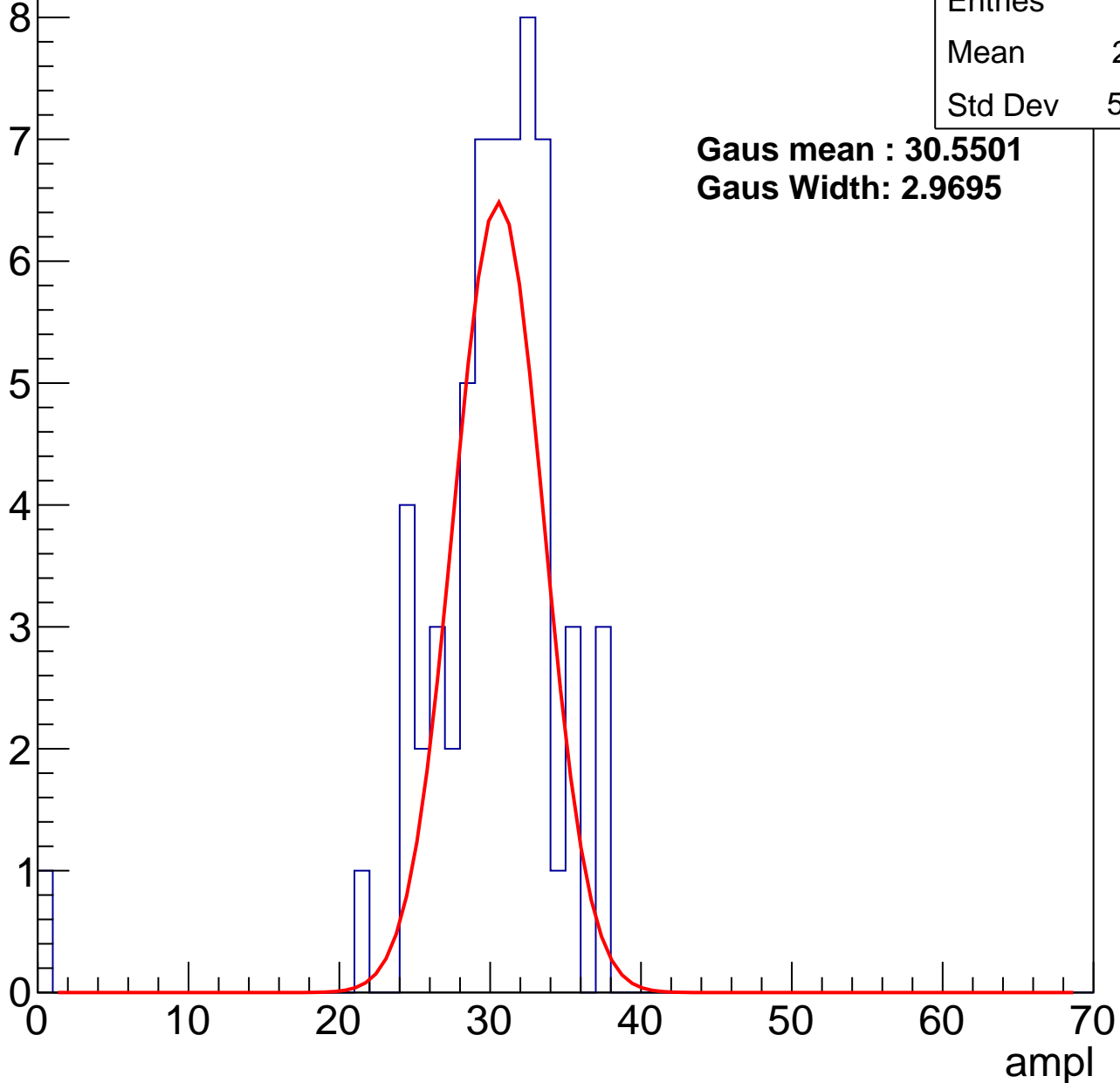
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.61
Std Dev	5.119

**Gaus mean : 30.5501**

**Gaus Width: 2.9695**



# B0L000S, U18-ch109, adc1

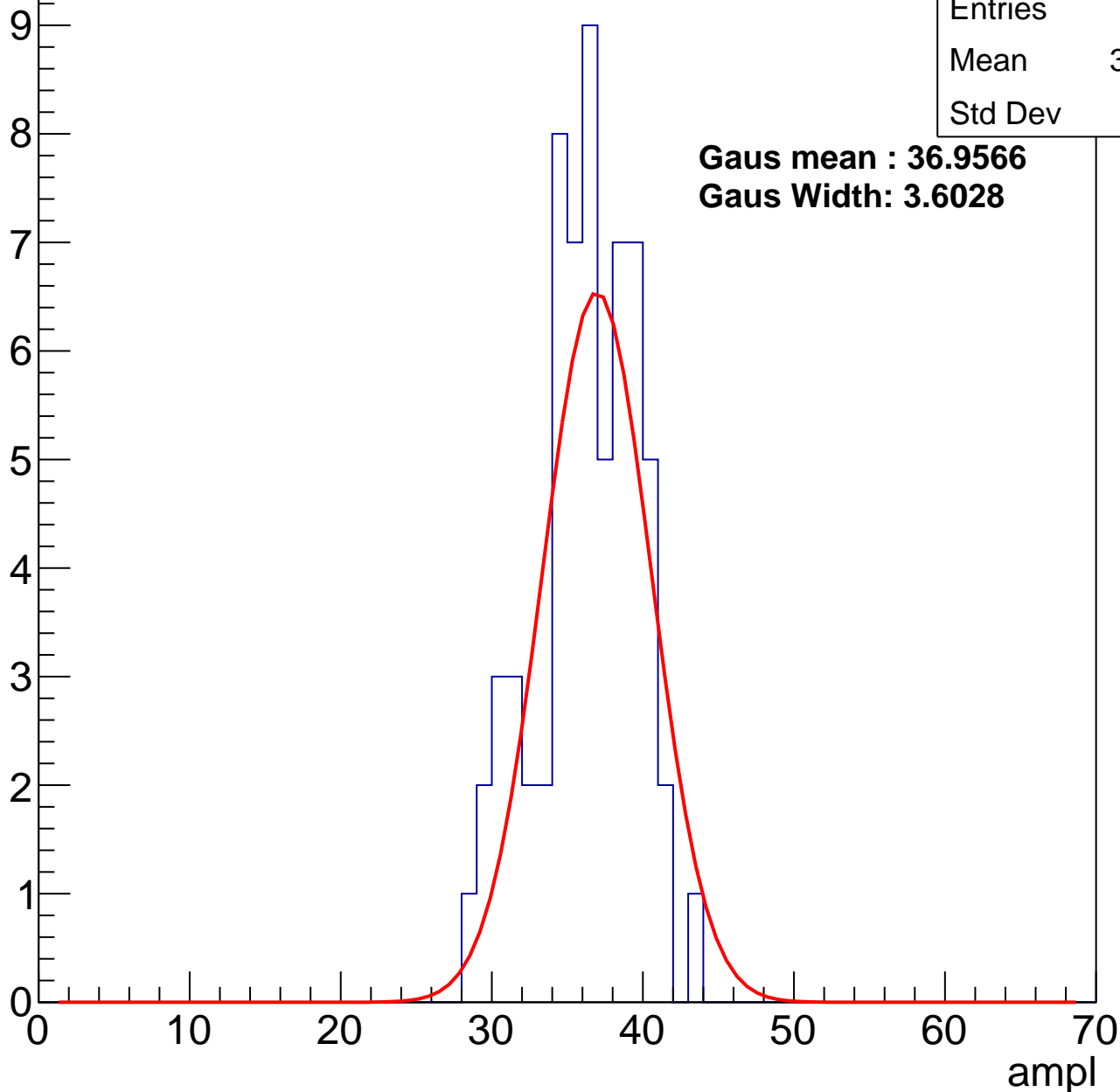
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	35.77
Std Dev	3.32

**Gaus mean : 36.9566**

**Gaus Width: 3.6028**



# B0L000S, U18-ch109, adc2

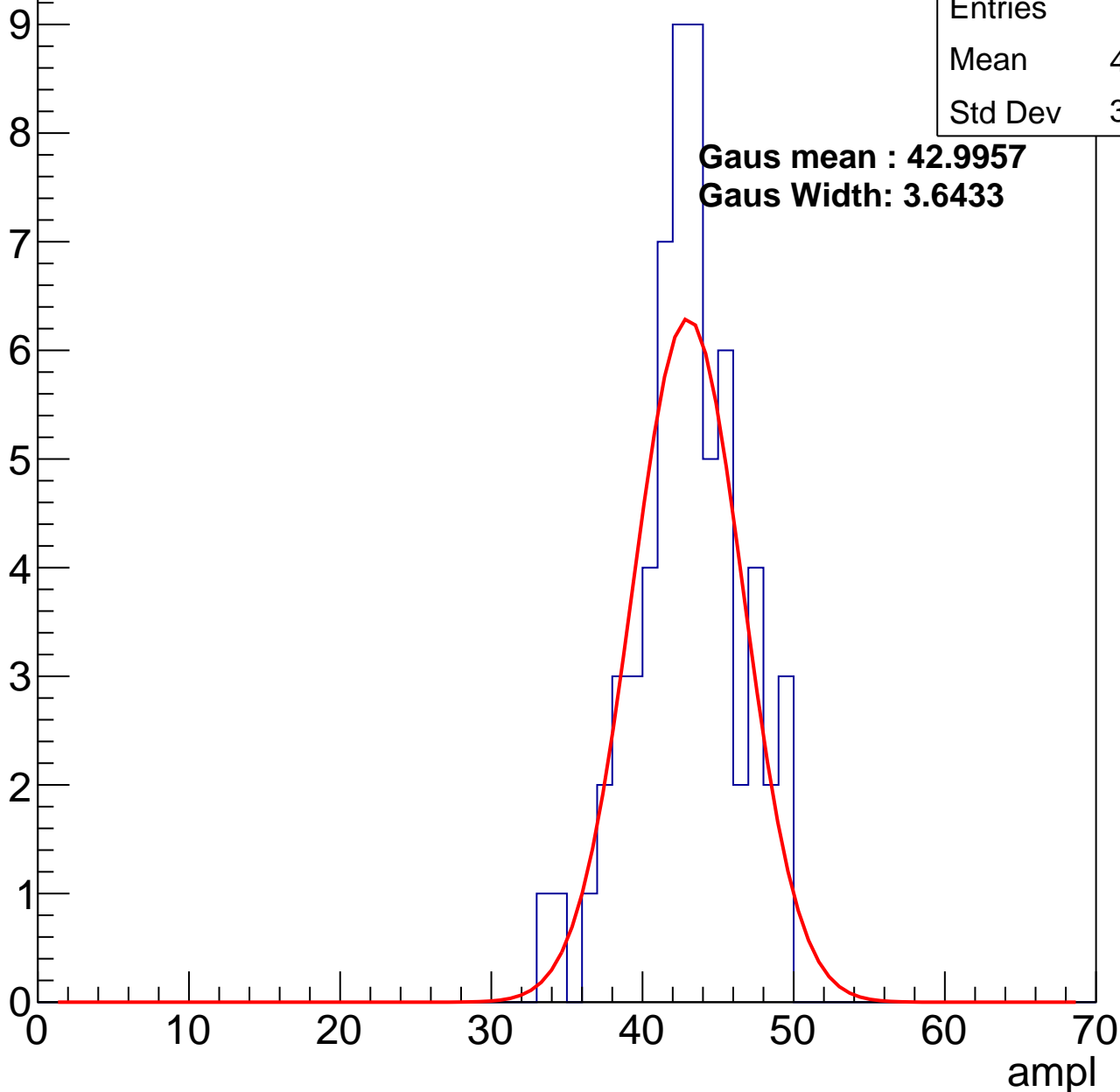
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.47
Std Dev	3.477

**Gaus mean : 42.9957**

**Gaus Width: 3.6433**



# B0L000S, U18-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

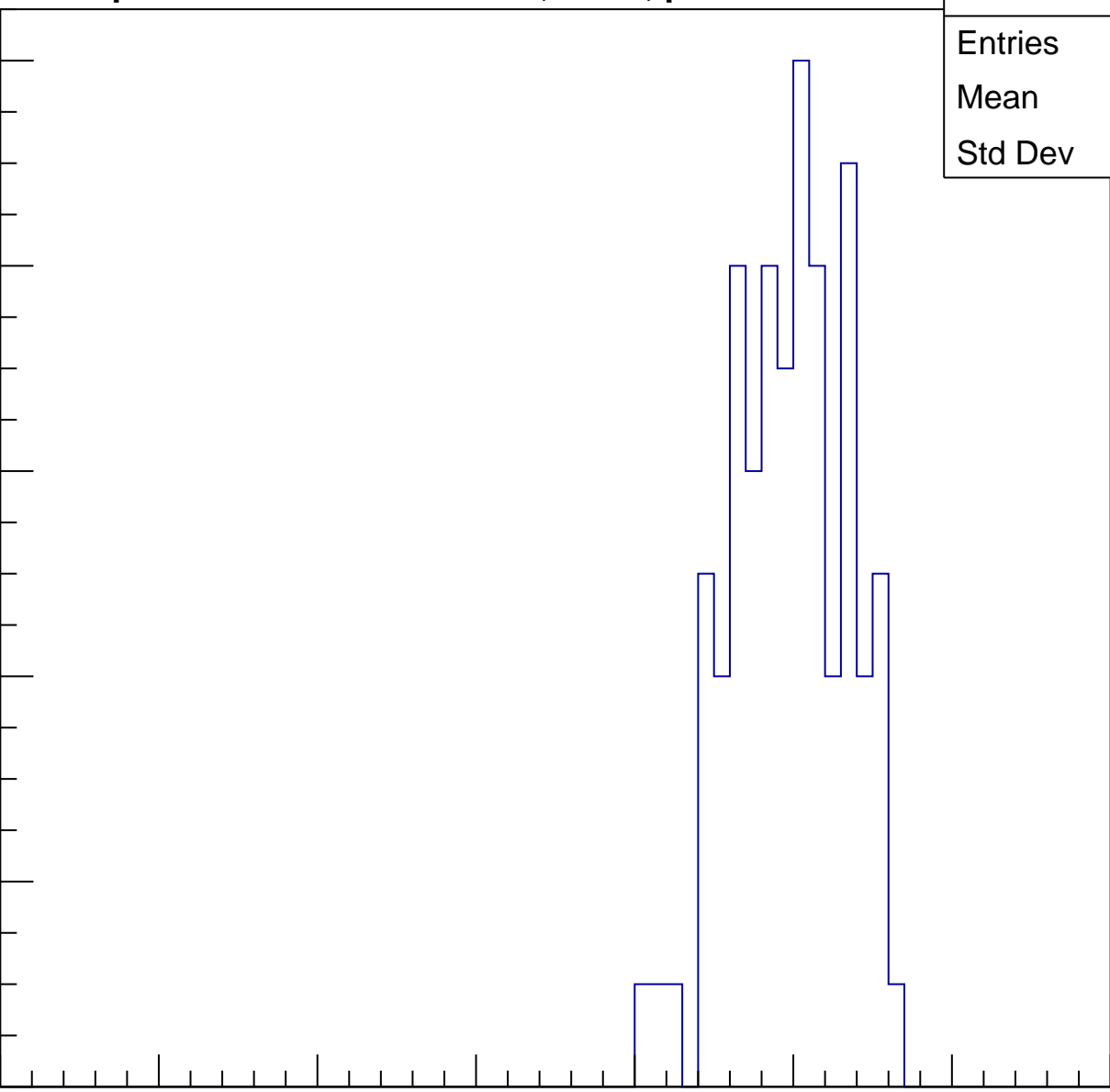
Entries	82
Mean	49.27
Std Dev	3.548

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

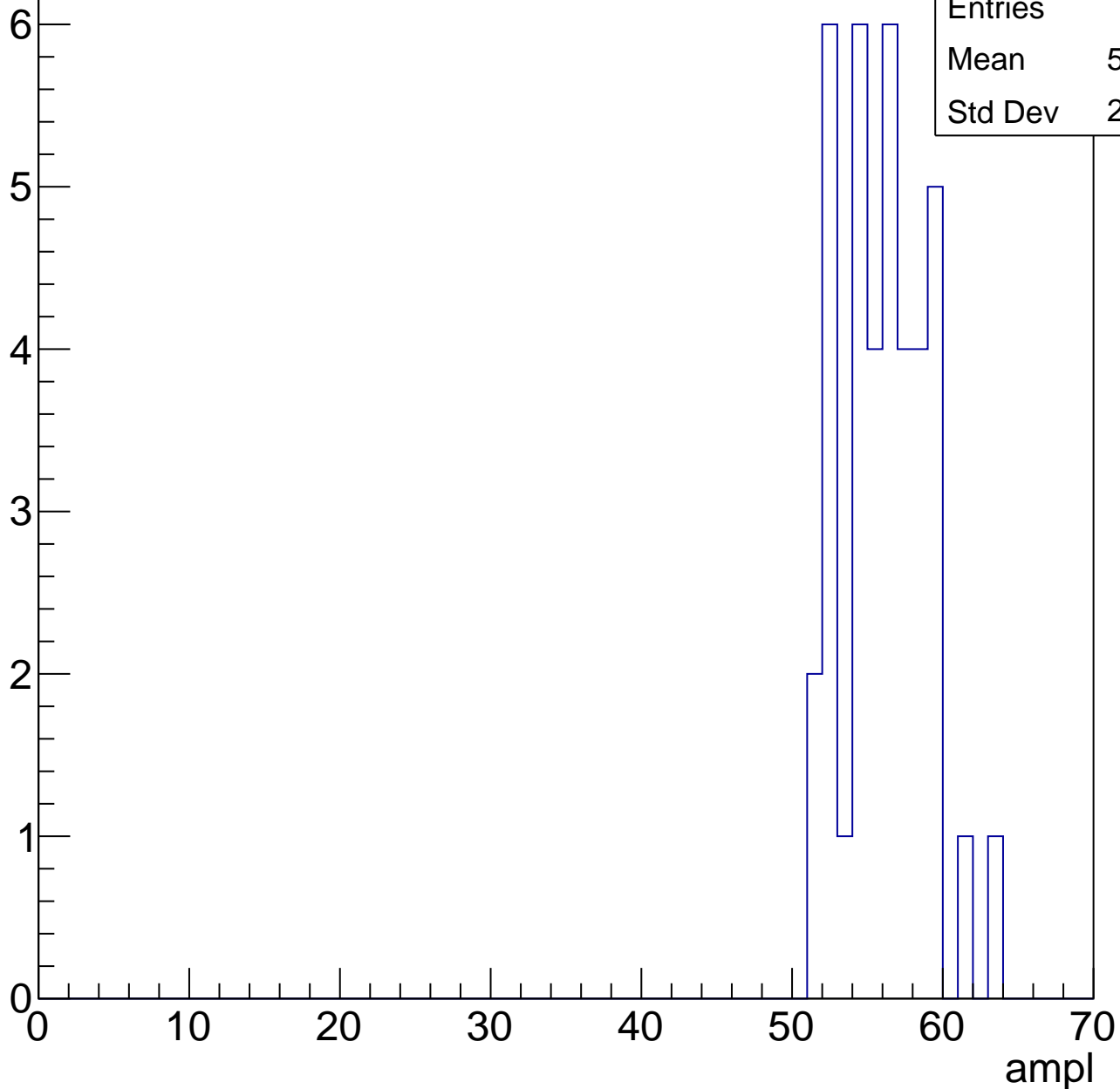


# B0L000S, U18-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	55.65
Std Dev	2.816

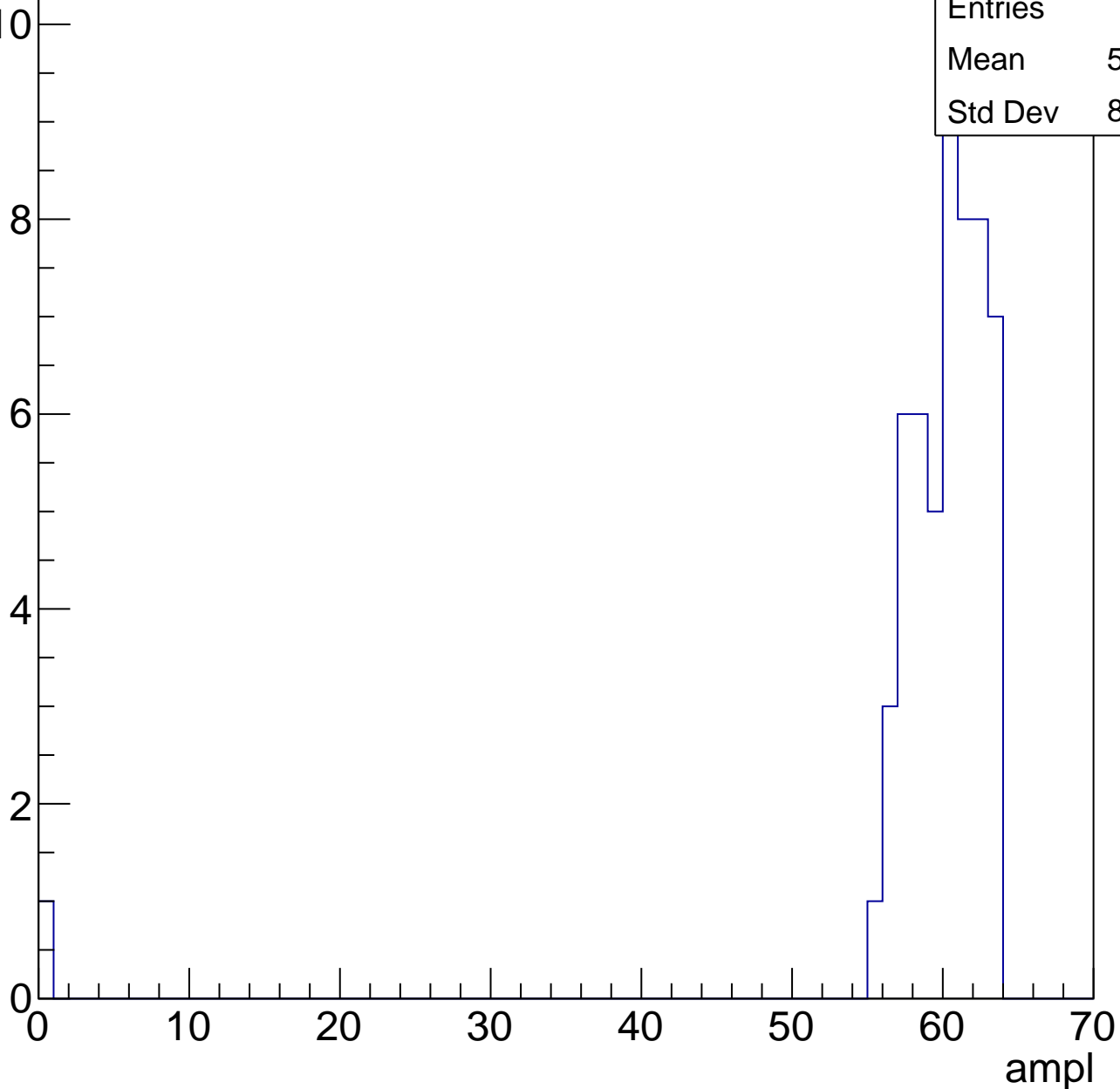


# B0L000S, U18-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.78
Std Dev	8.287



# B0L000S, U18-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch110, adc0

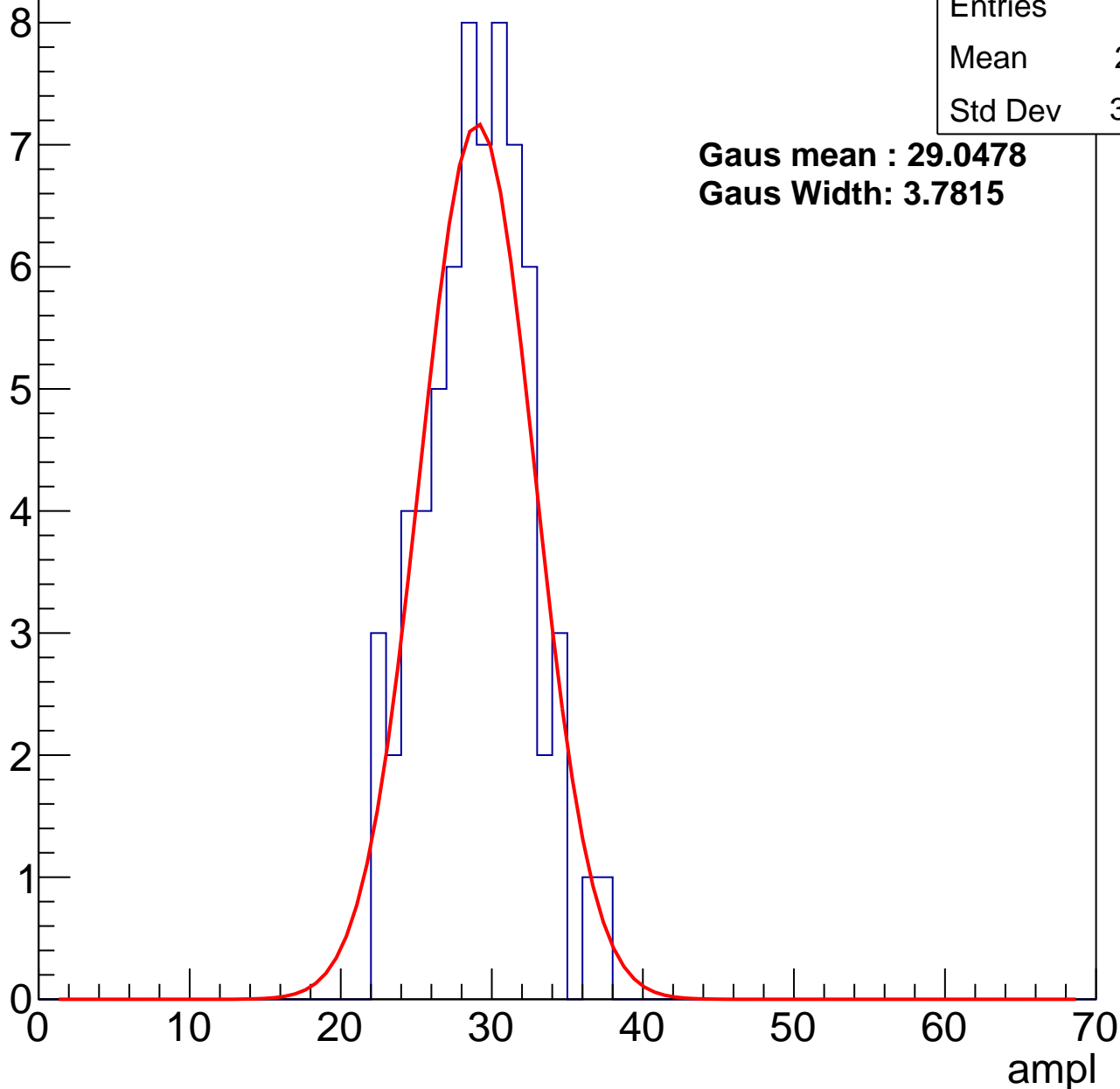
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.61
Std Dev	3.368

**Gaus mean : 29.0478**

**Gaus Width: 3.7815**



# B0L000S, U18-ch110, adc1

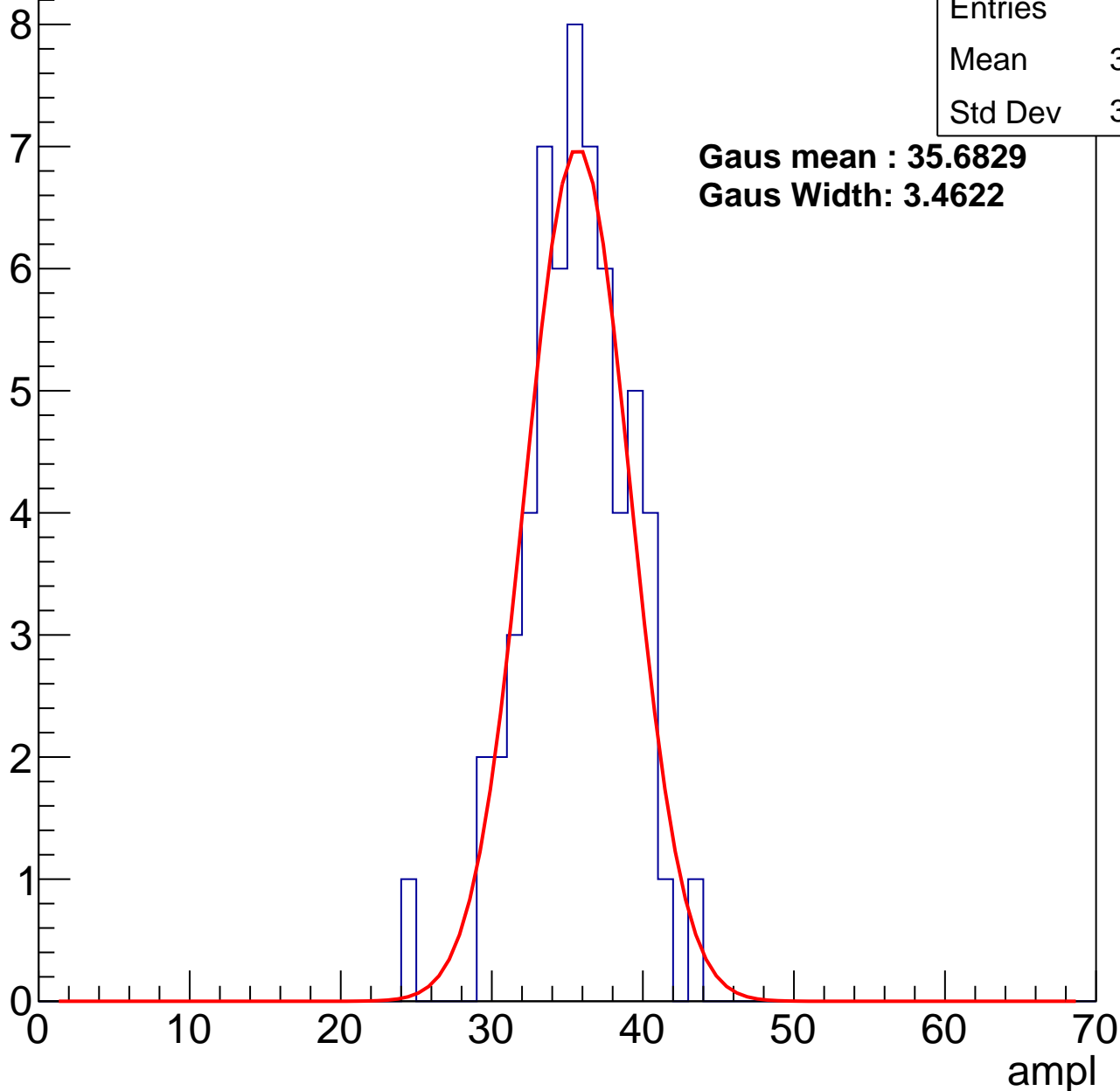
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.13
Std Dev	3.404

**Gaus mean : 35.6829**

**Gaus Width: 3.4622**



# B0L000S, U18-ch110, adc2

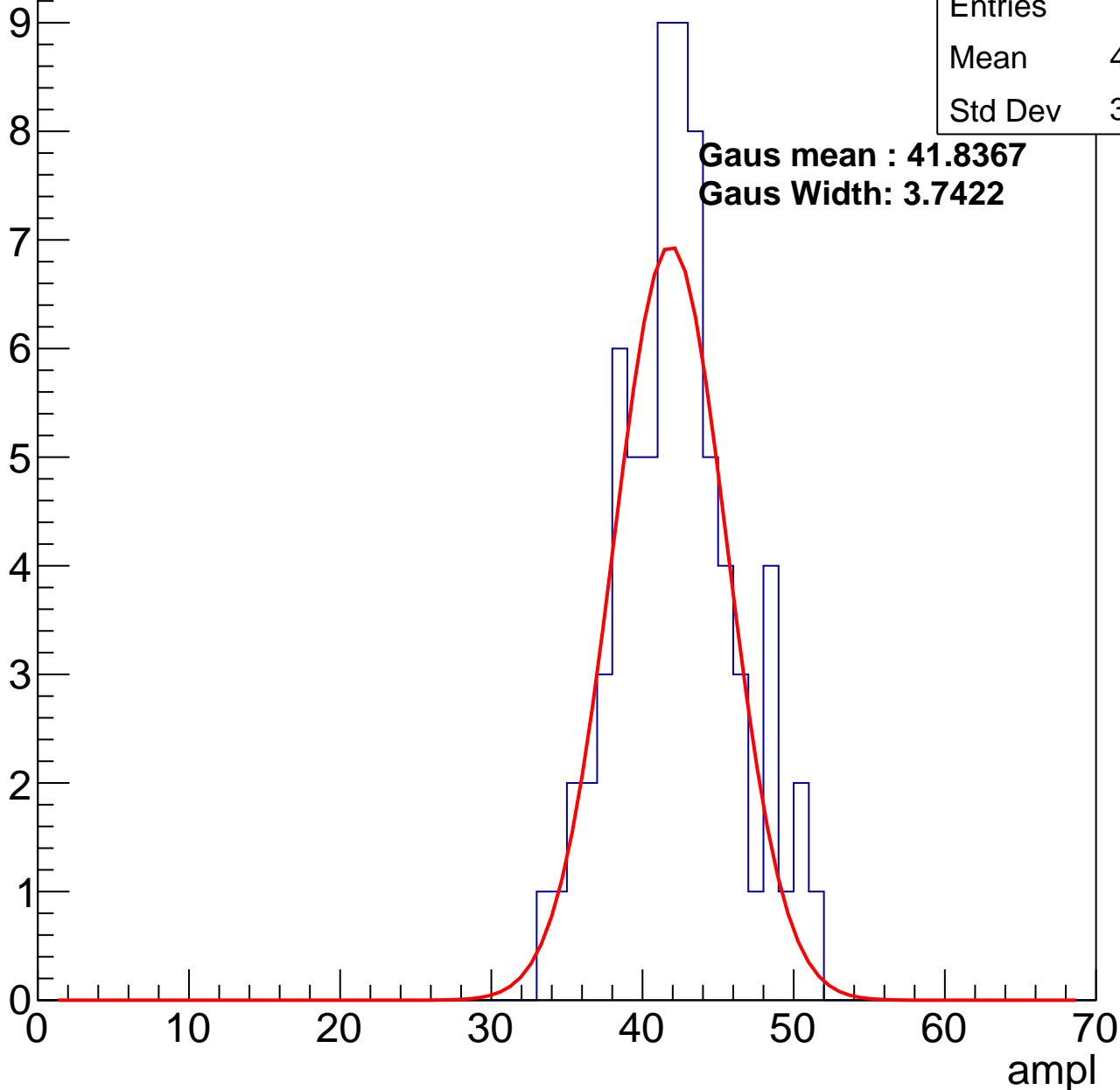
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	41.82
Std Dev	3.903

**Gaus mean : 41.8367**

**Gaus Width: 3.7422**

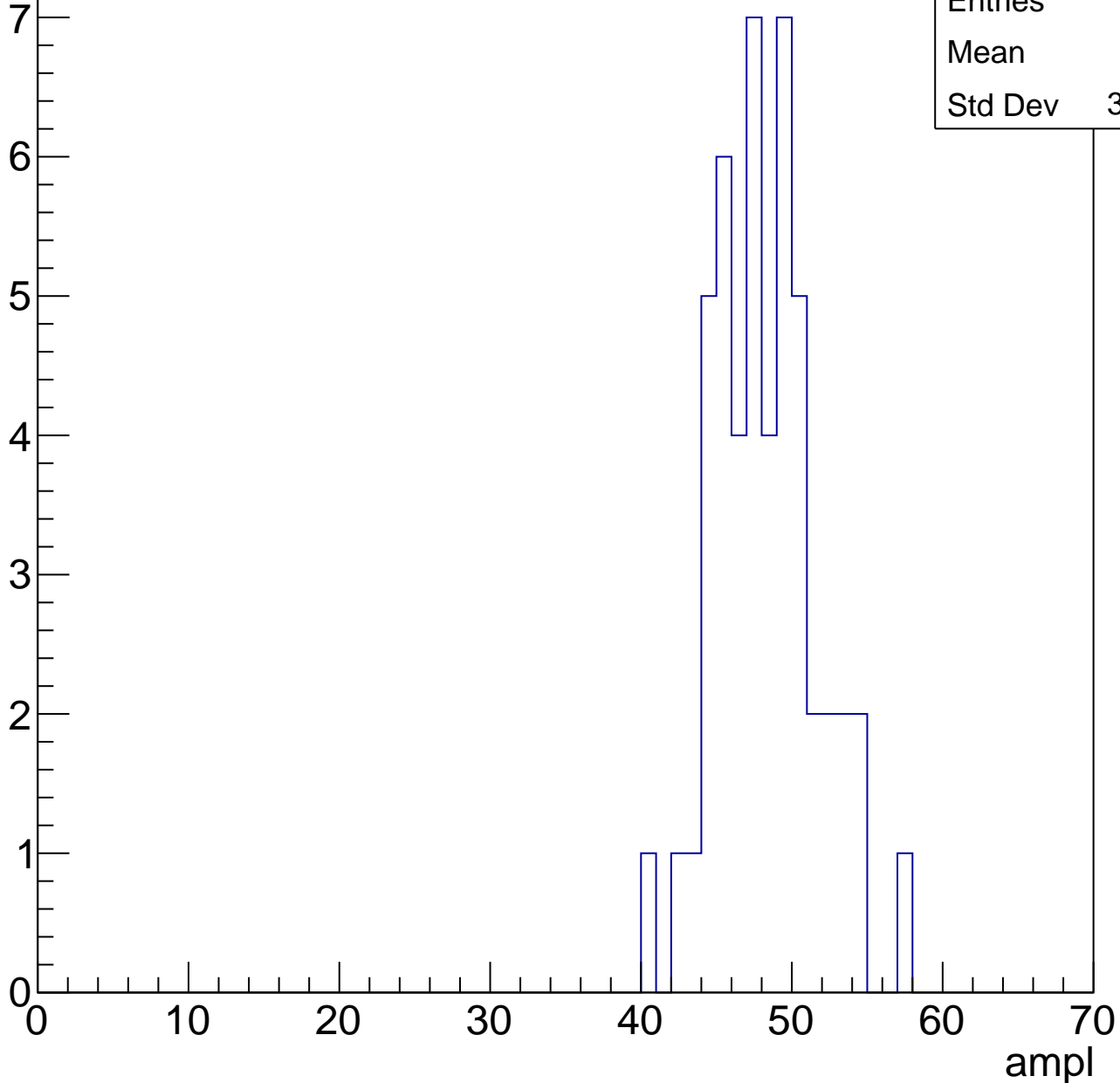


# B0L000S, U18-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

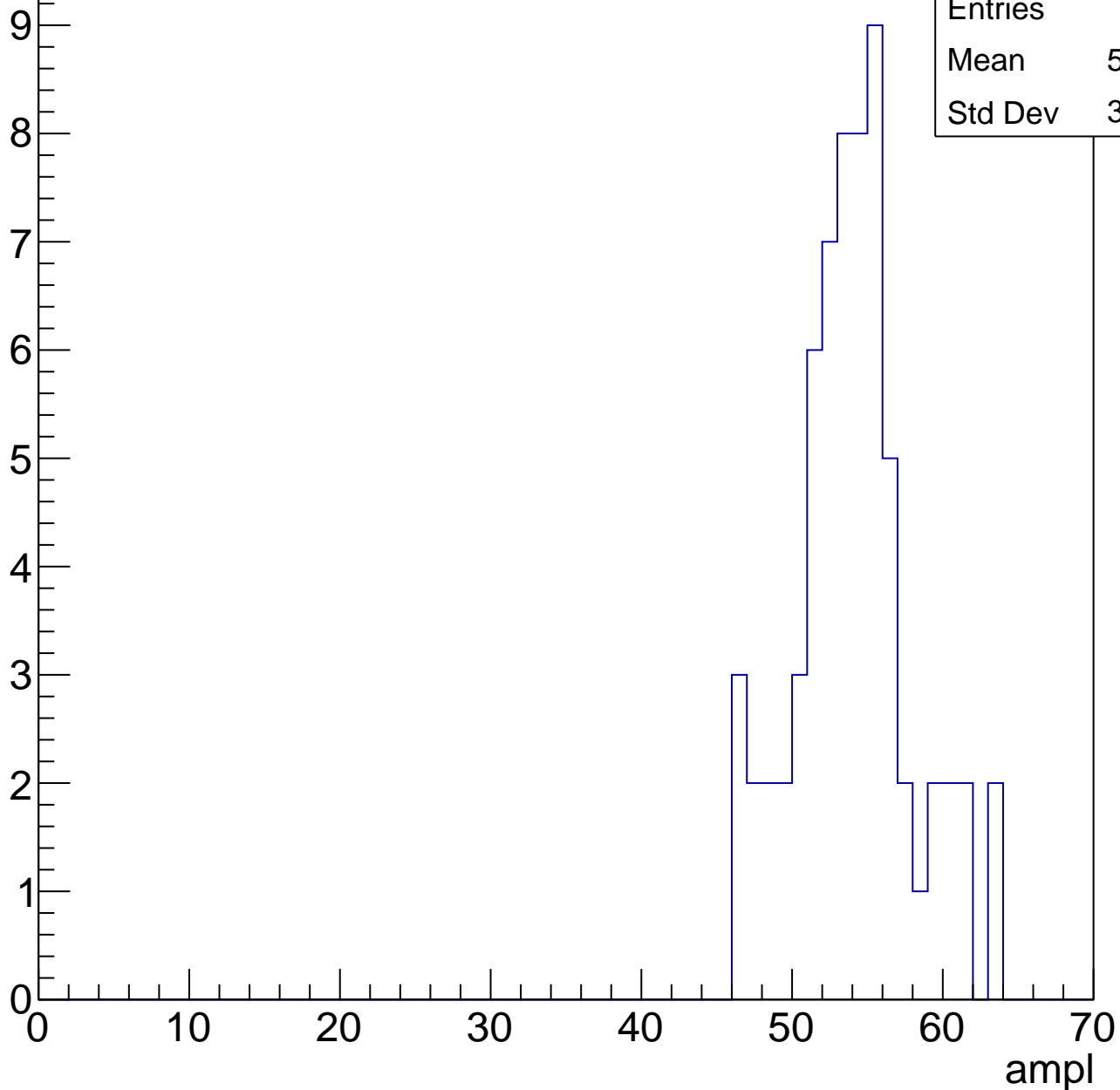
Entries	50
Mean	47.8
Std Dev	3.347



# B0L000S, U18-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

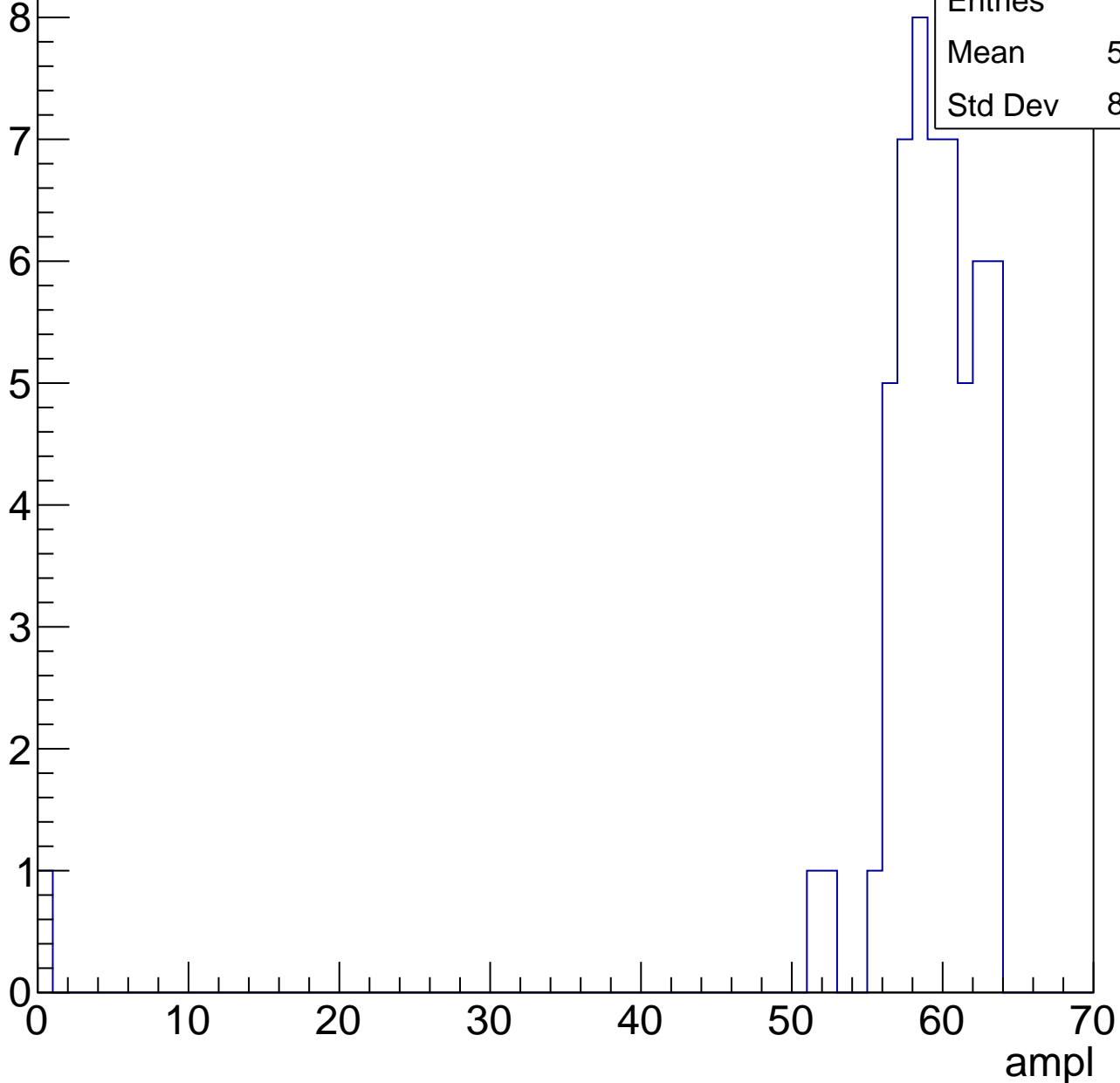


# B0L000S, U18-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

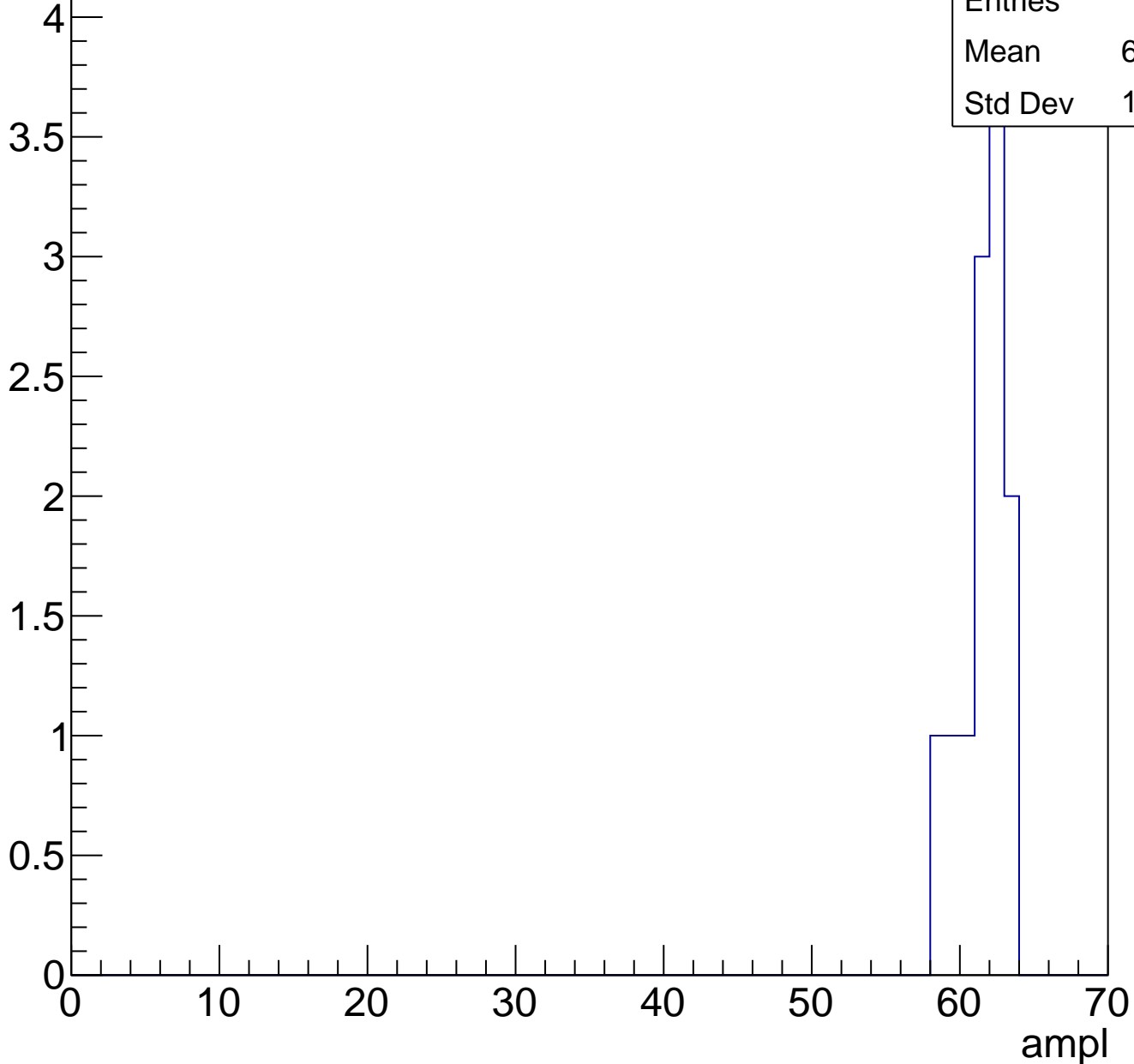
Entries	55
Mean	57.98
Std Dev	8.324



# B0L000S, U18-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch111, adc0

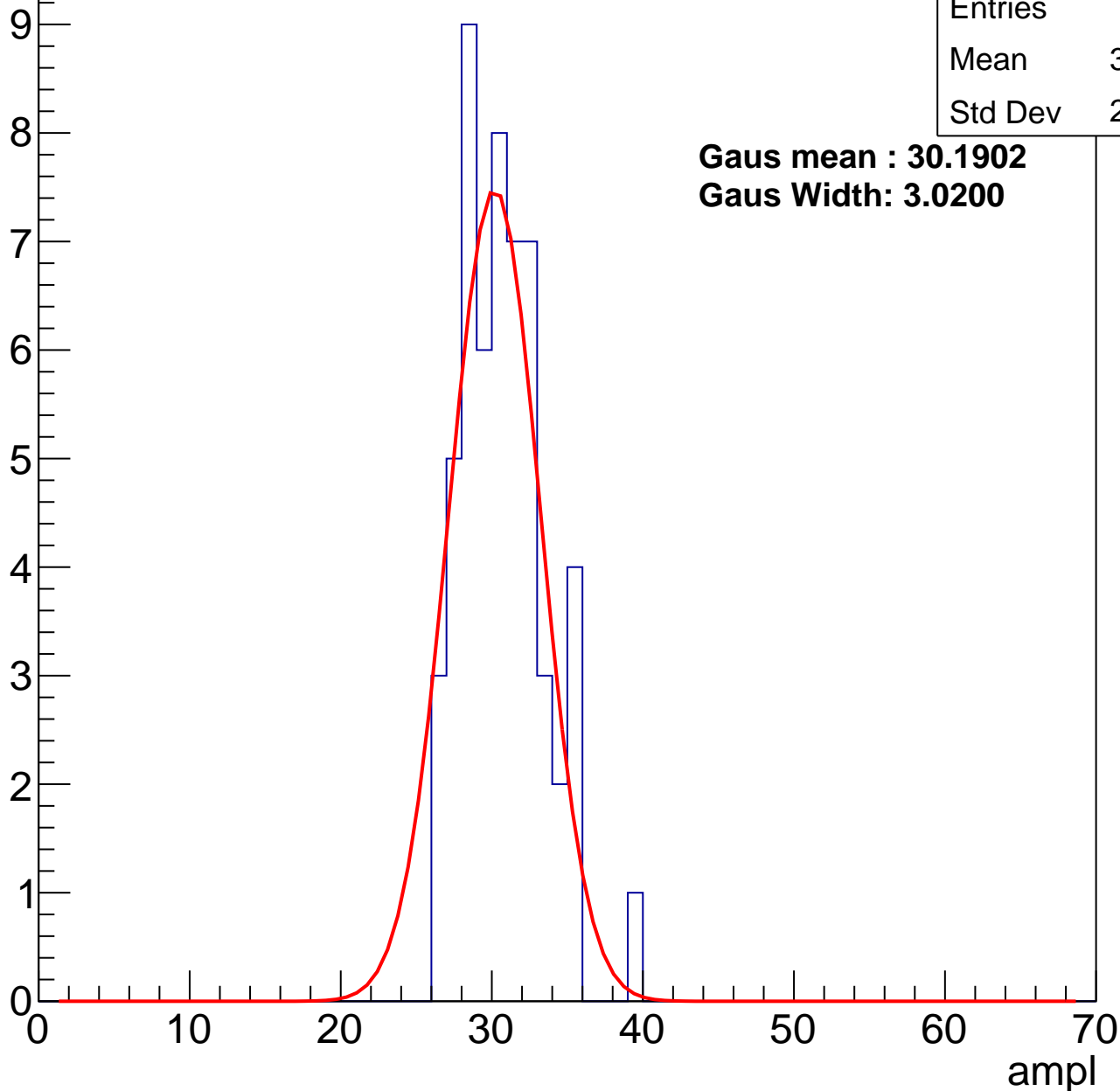
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	30.29
Std Dev	2.715

**Gaus mean : 30.1902**

**Gaus Width: 3.0200**



# B0L000S, U18-ch111, adc1

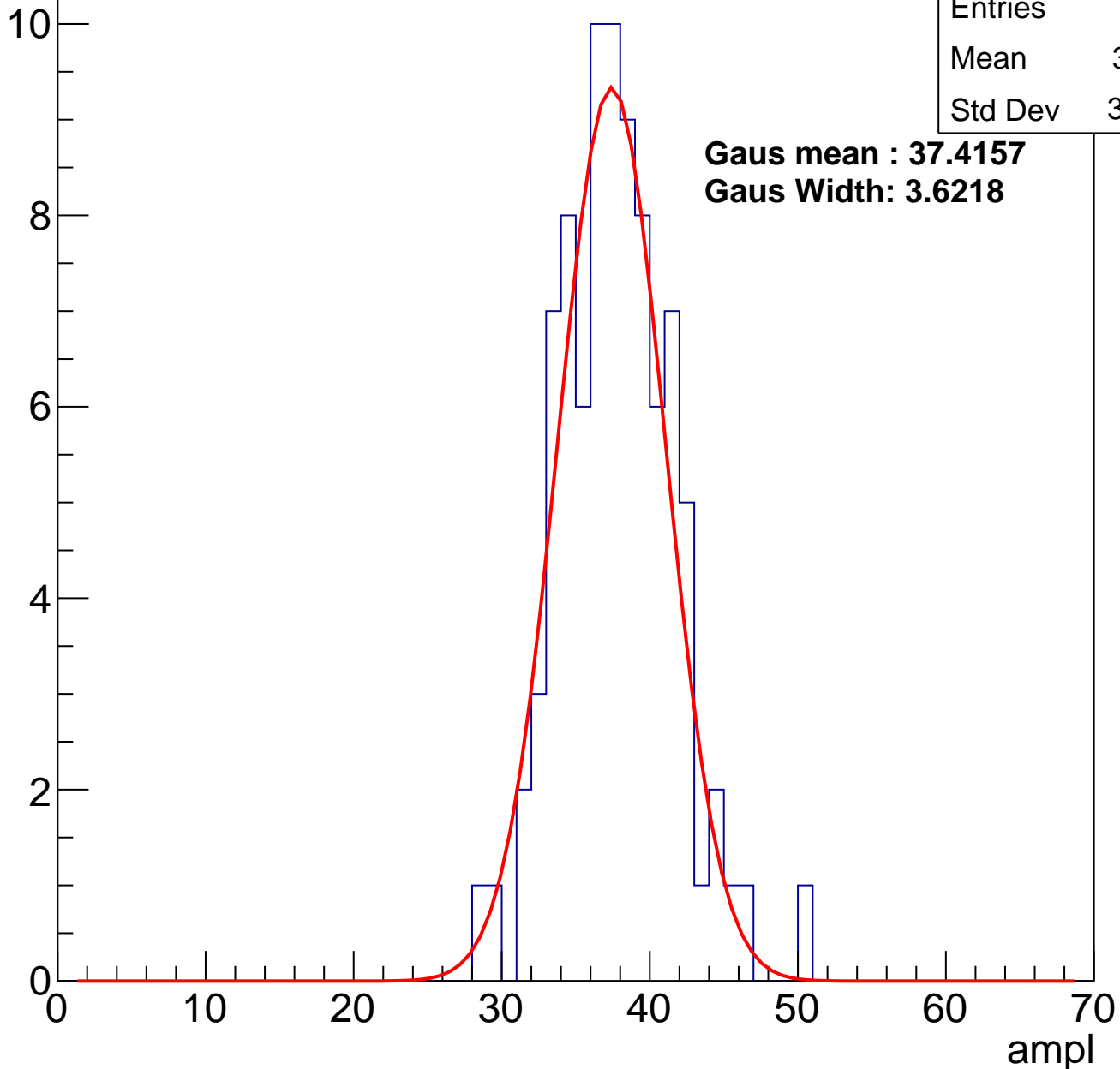
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	89
Mean	37.31
Std Dev	3.794

**Gaus mean : 37.4157**

**Gaus Width: 3.6218**

Entry



# B0L000S, U18-ch111, adc2

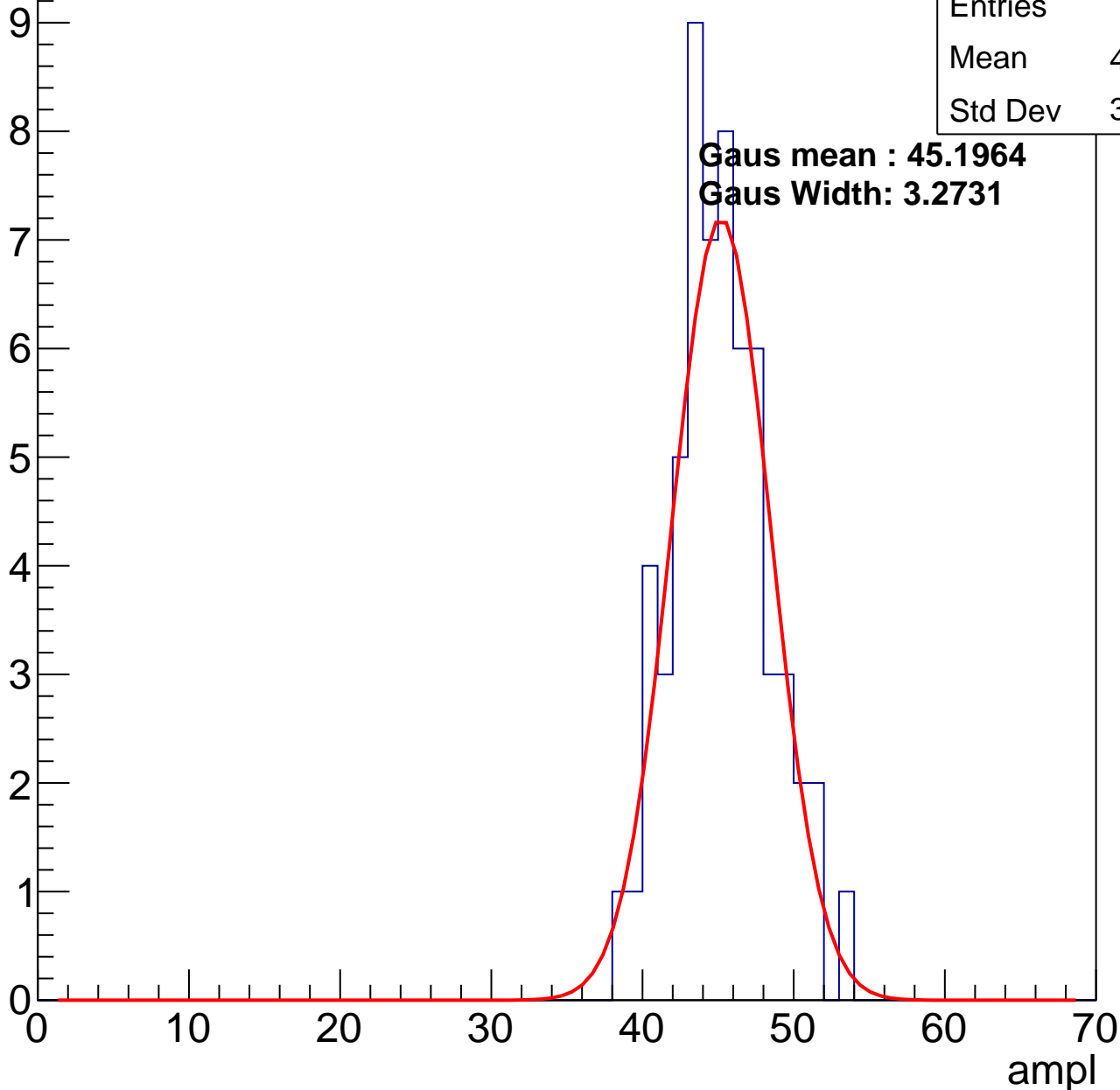
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.74
Std Dev	3.157

**Gaus mean : 45.1964**

**Gaus Width: 3.2731**

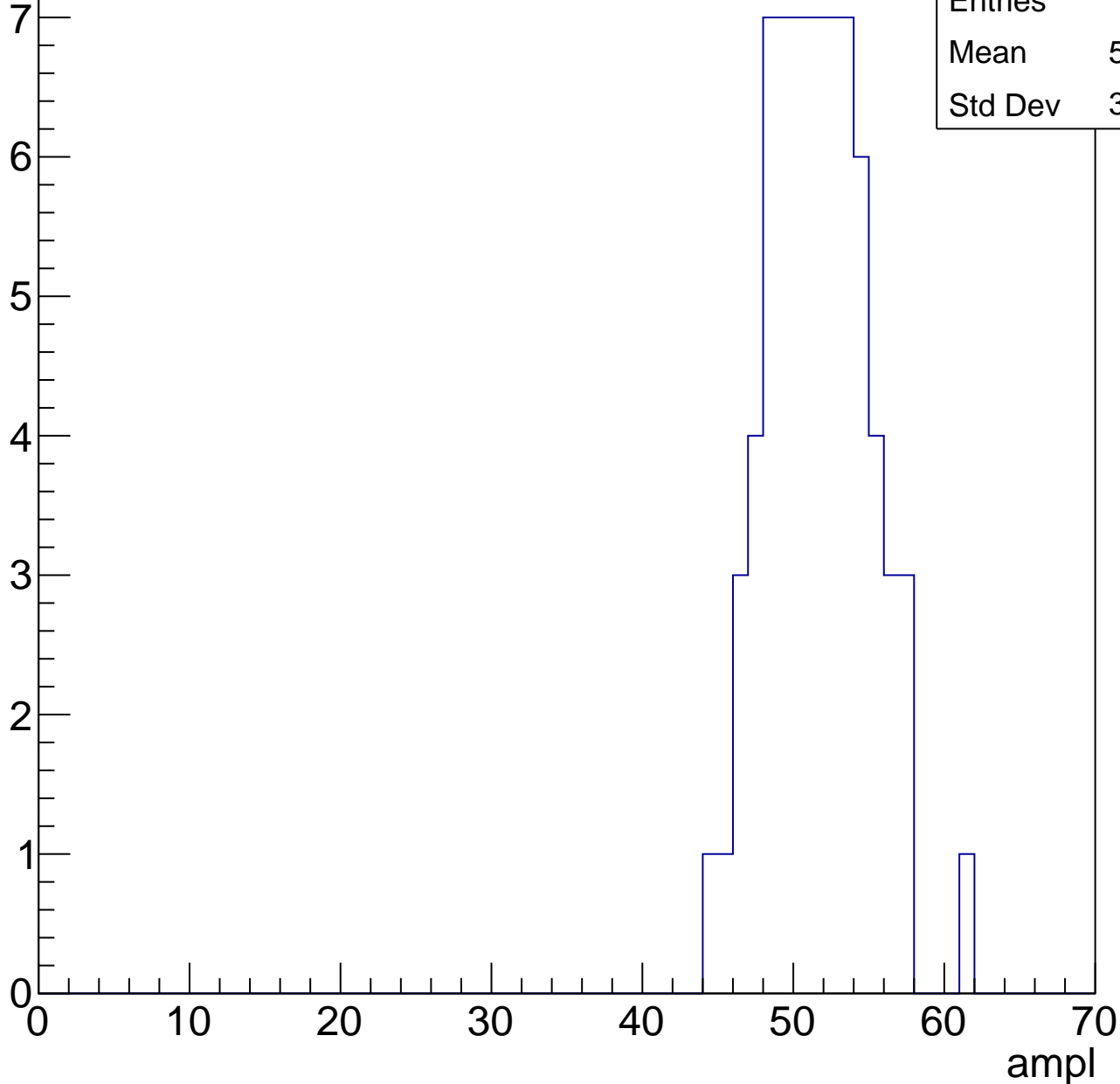


# B0L000S, U18-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	51.18
Std Dev	3.334

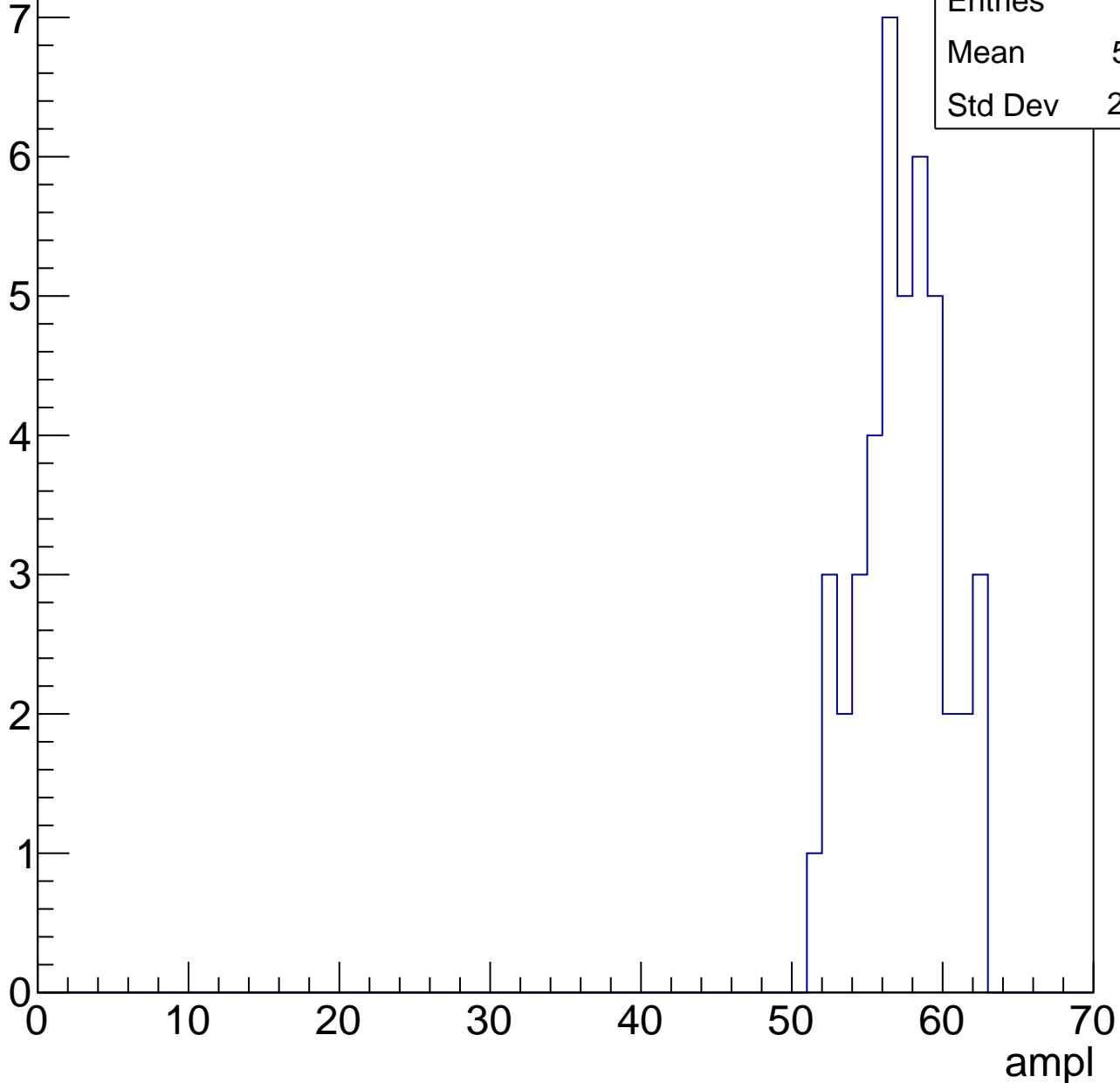


# B0L000S, U18-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

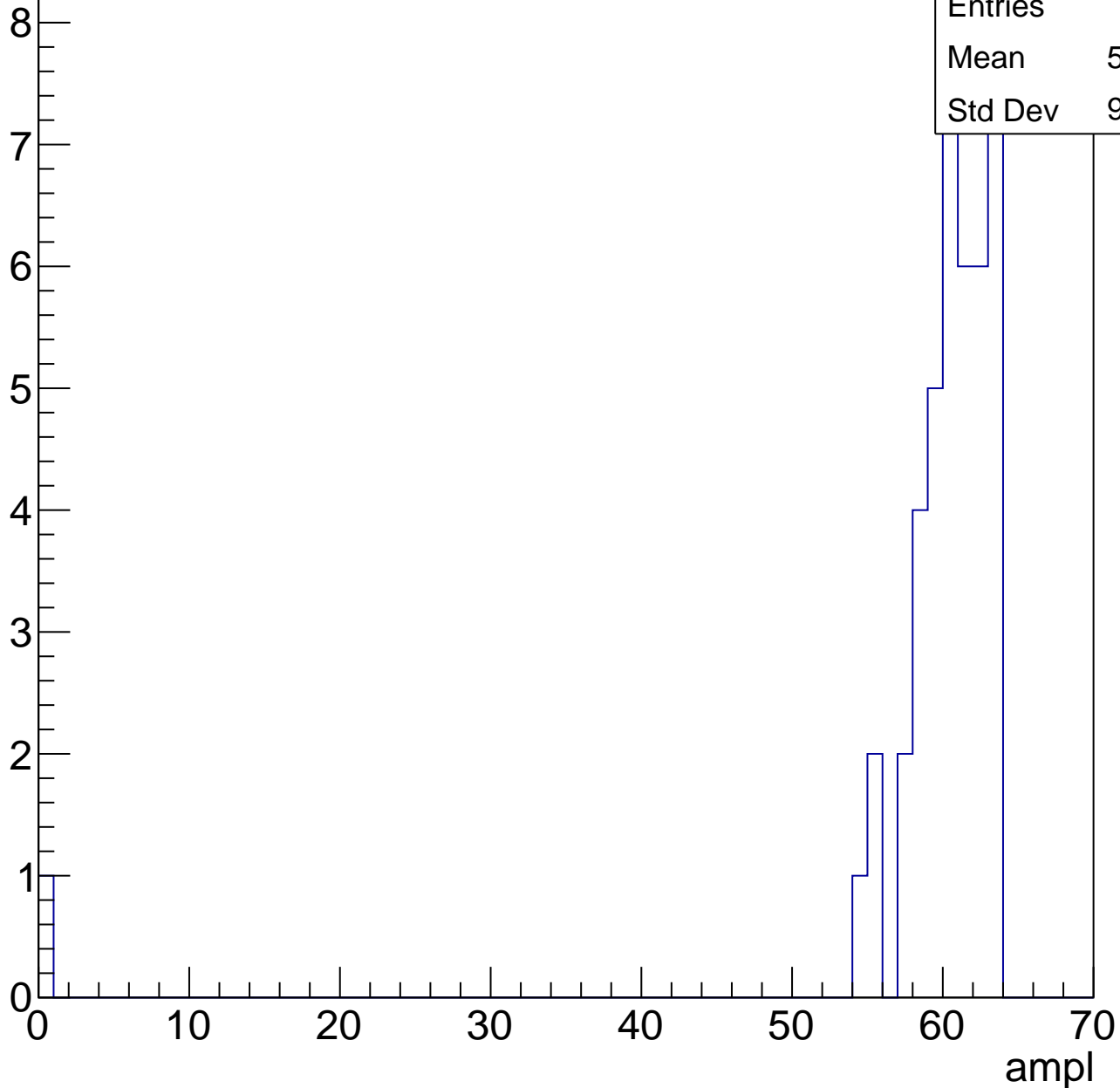
Entries	43
Mean	56.81
Std Dev	2.822



# B0L000S, U18-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



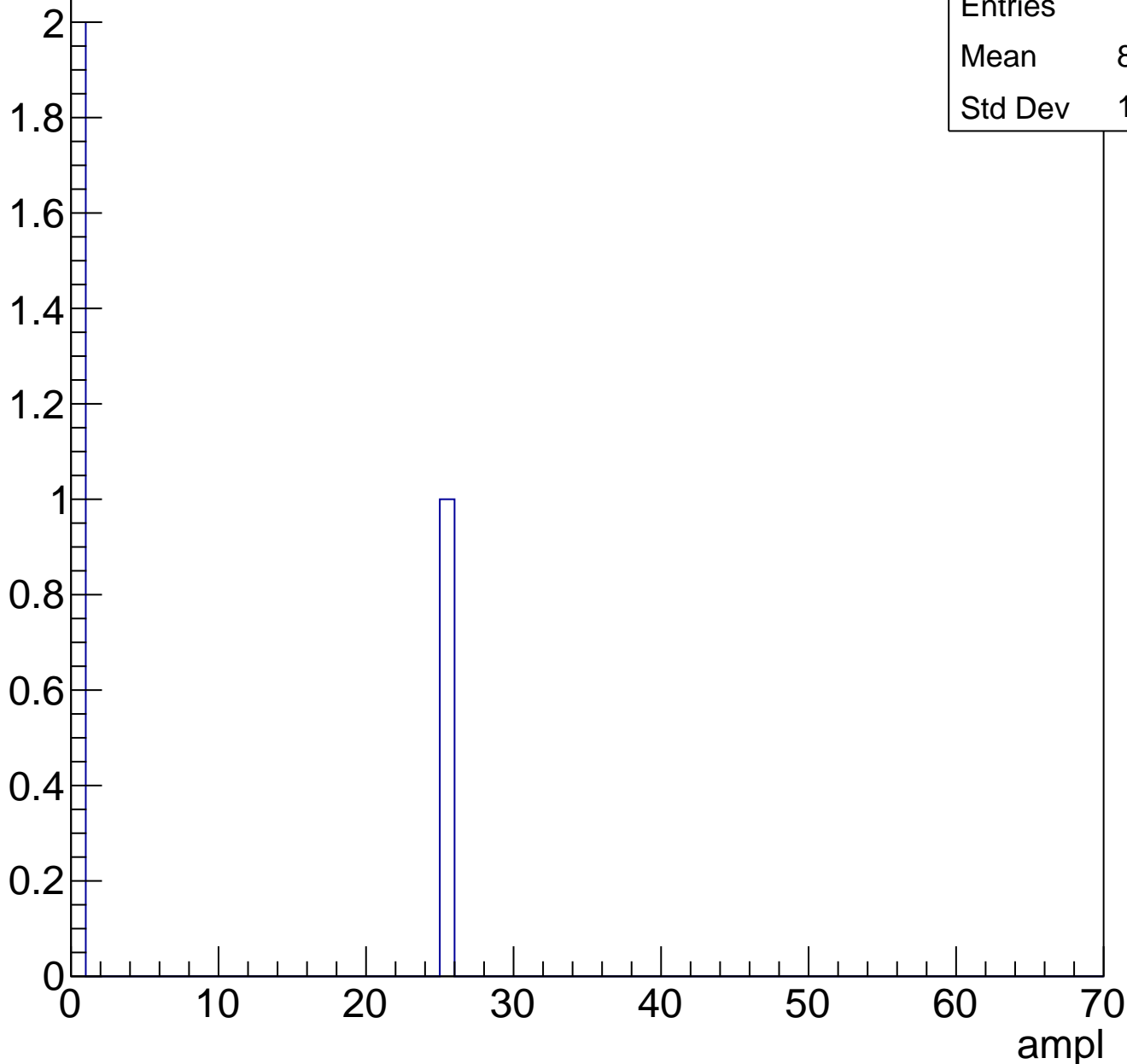
Entries	4
Mean	62.25
Std Dev	0.8292



# B0L000S, U18-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

# B0L000S, U18-ch112, adc0

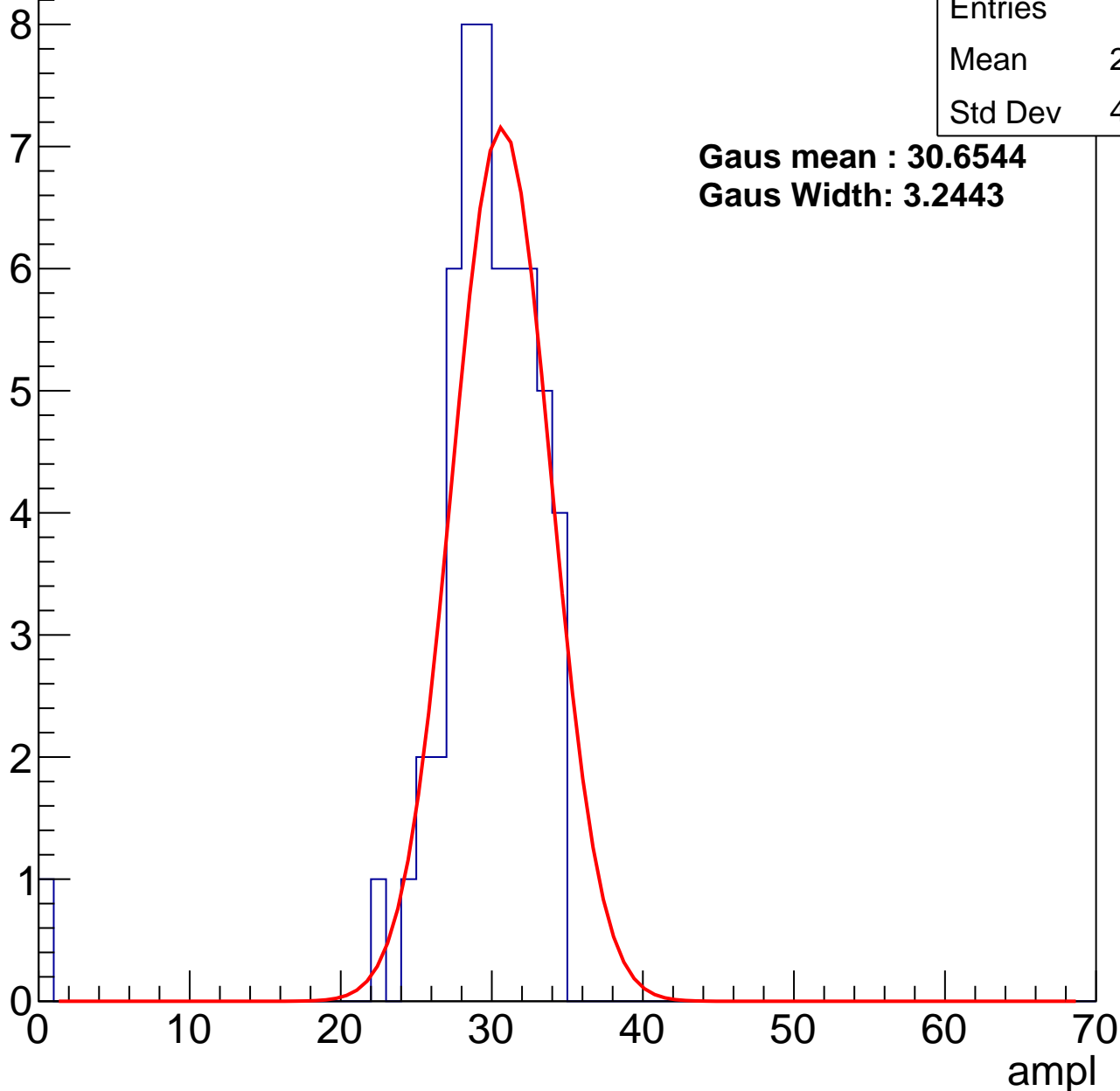
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.02
Std Dev	4.745

**Gaus mean : 30.6544**

**Gaus Width: 3.2443**



# B0L000S, U18-ch112, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	36.12
Std Dev	3.49

**Gaus mean : 36.7058**

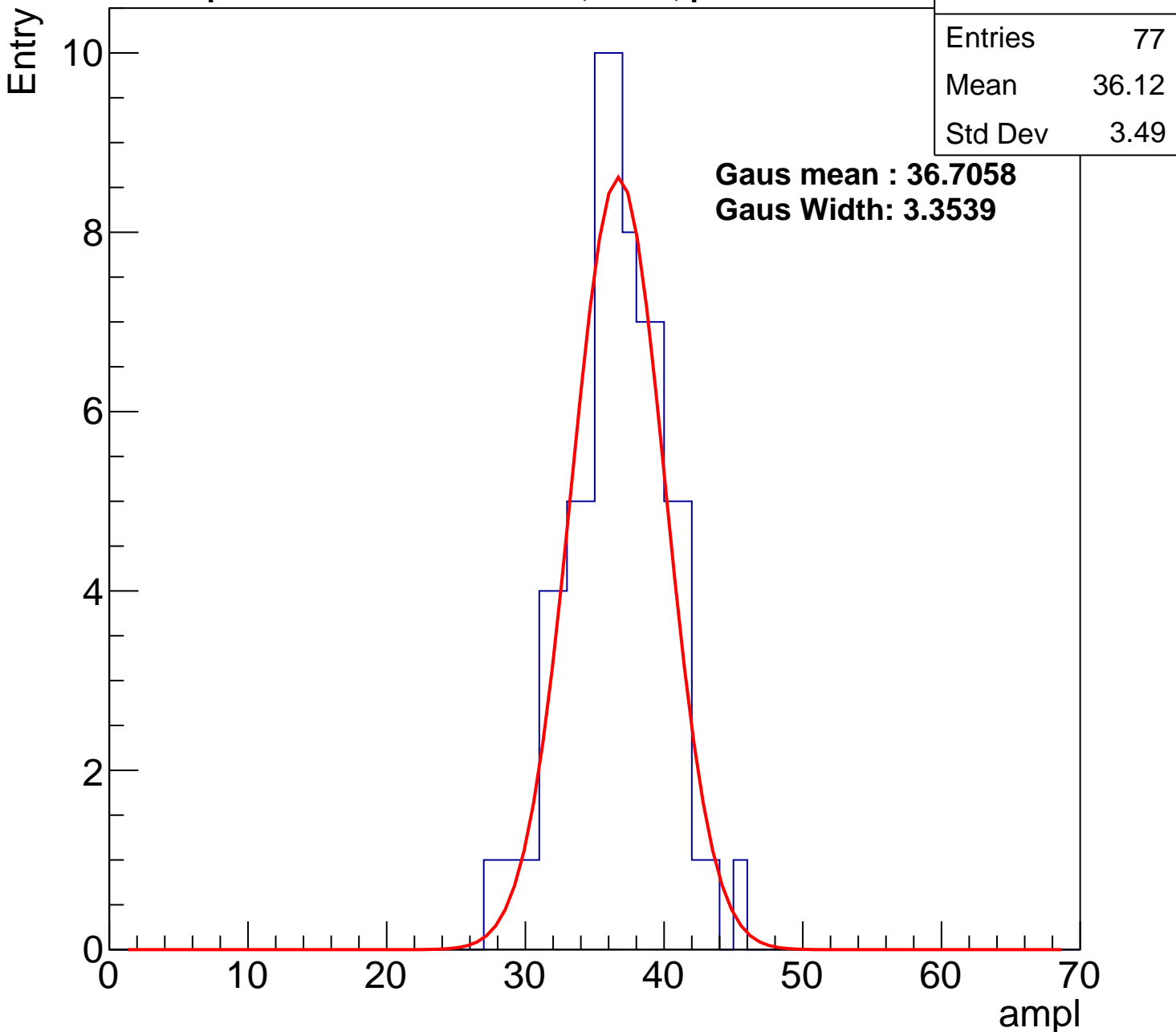
**Gaus Width: 3.3539**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch112, adc2

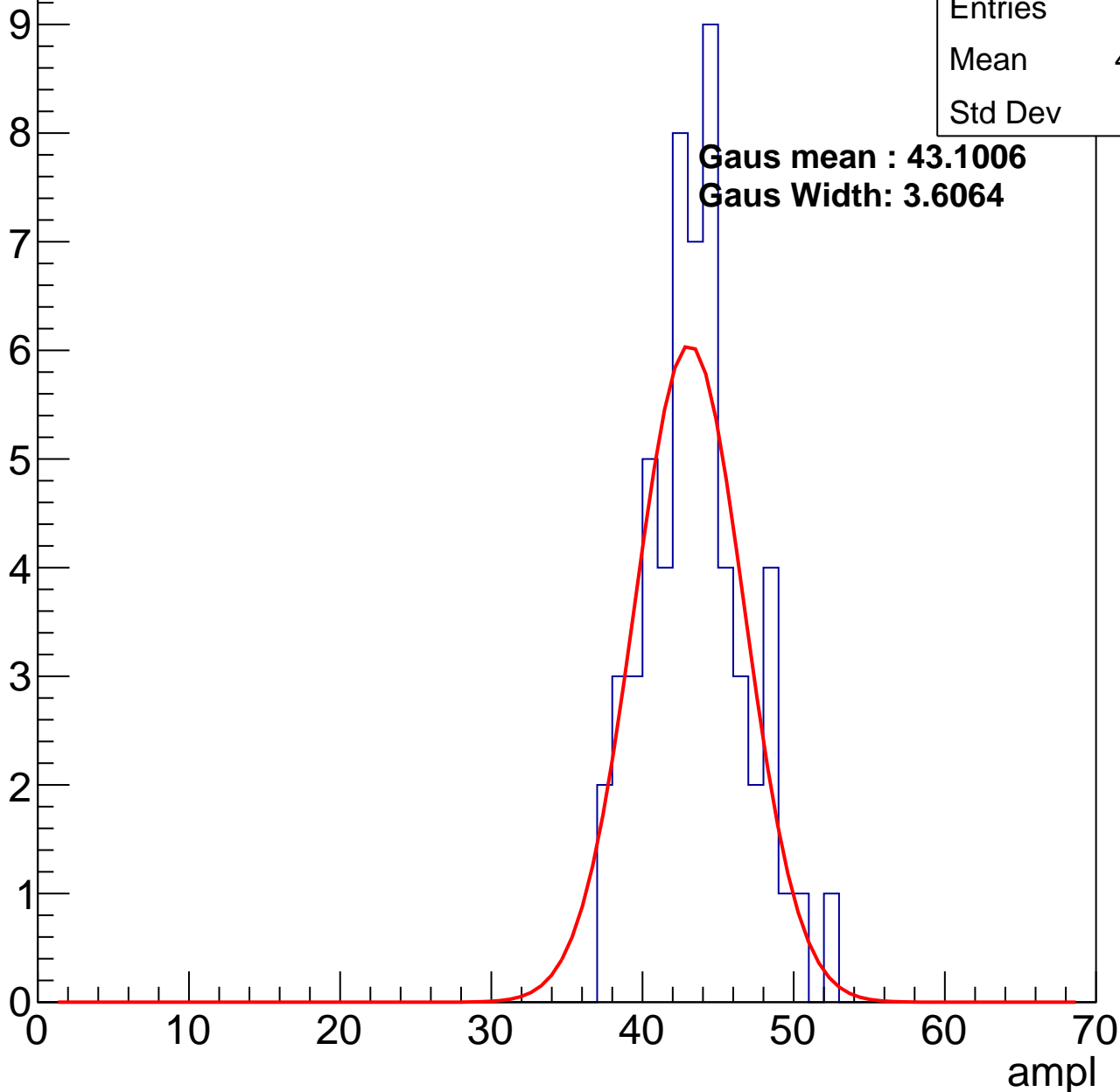
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.11
Std Dev	3.28

**Gaus mean : 43.1006**

**Gaus Width: 3.6064**

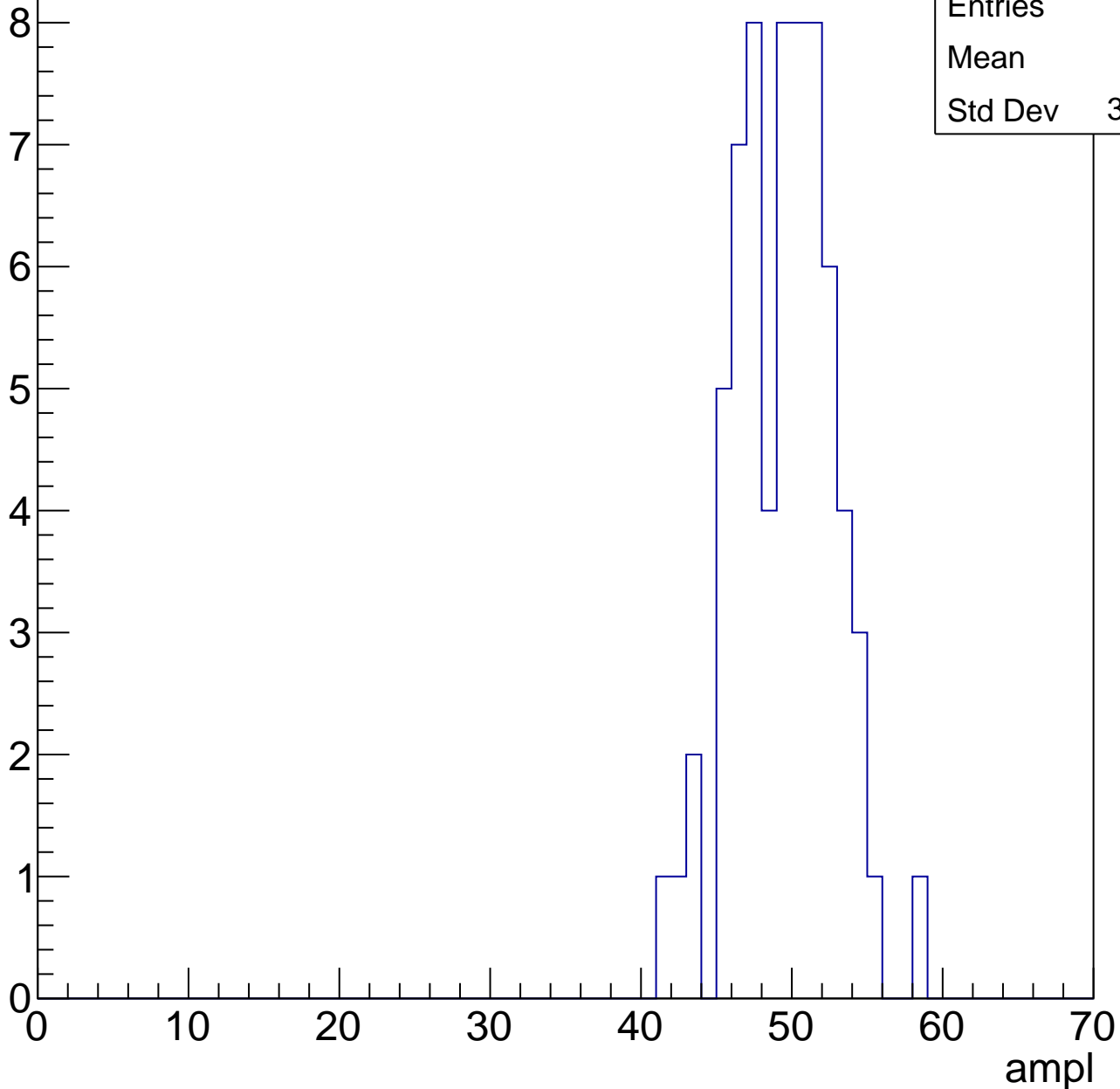


# B0L000S, U18-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49
Std Dev	3.264

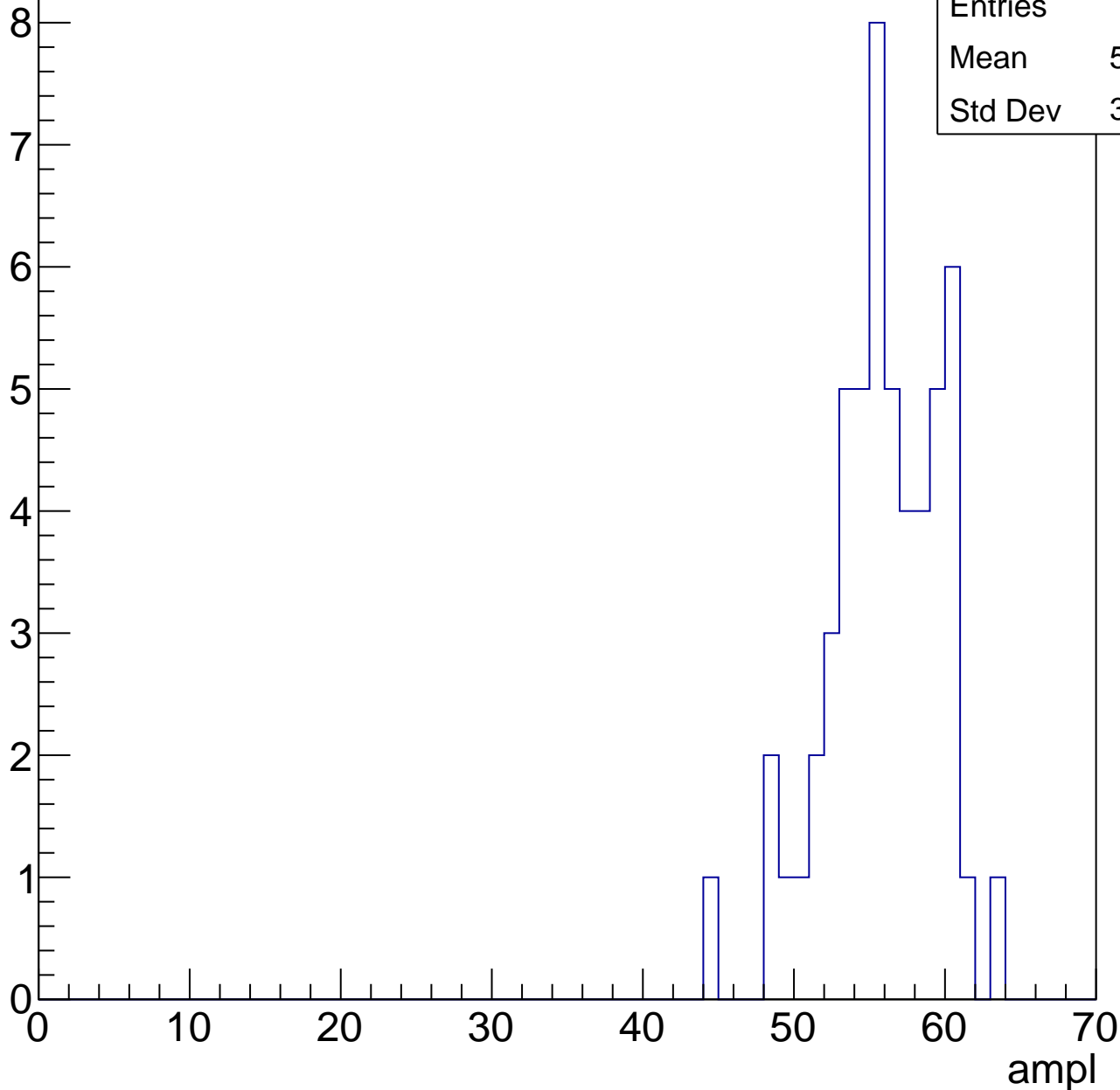


# B0L000S, U18-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.39
Std Dev	3.709

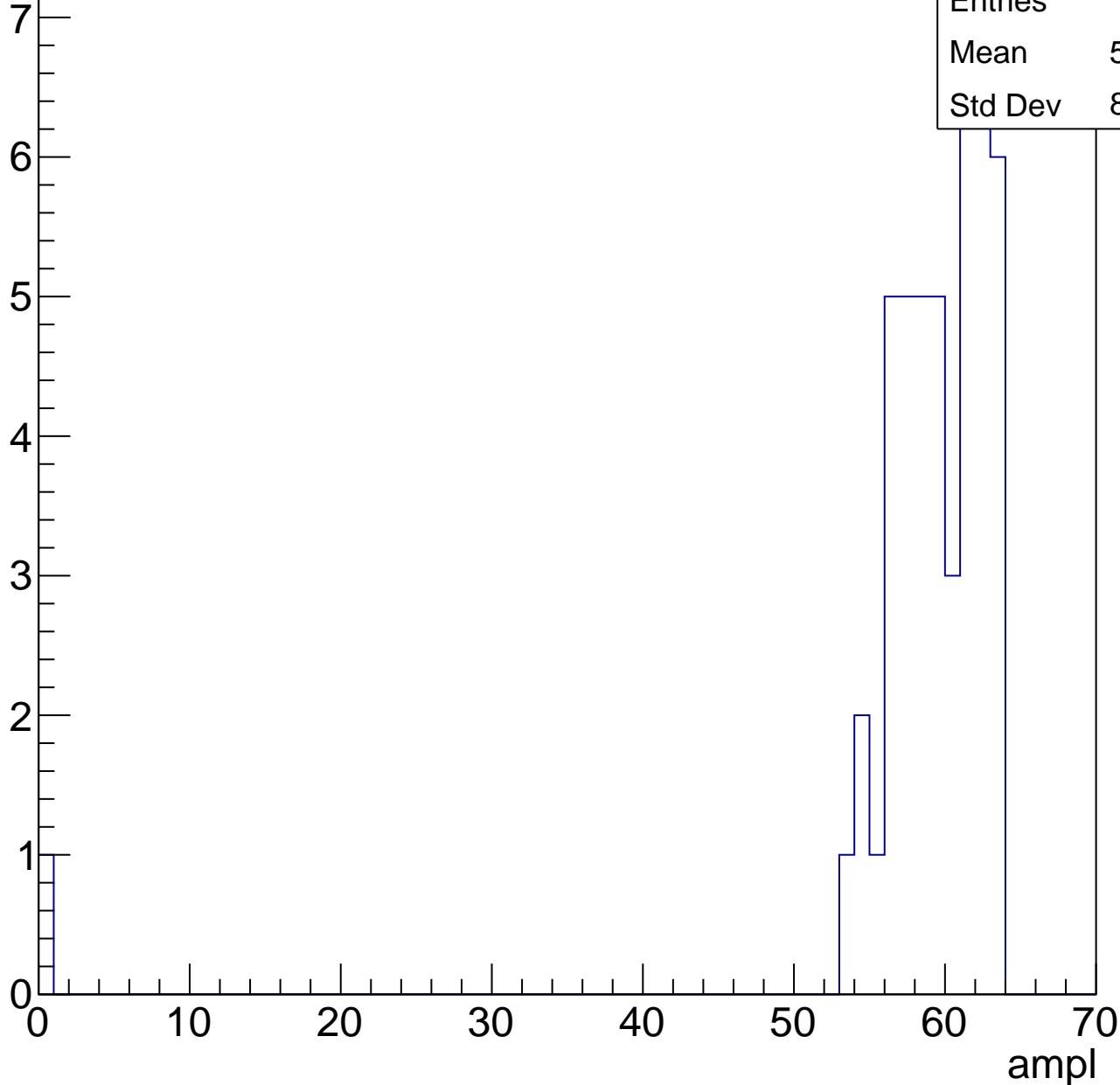


# B0L000S, U18-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

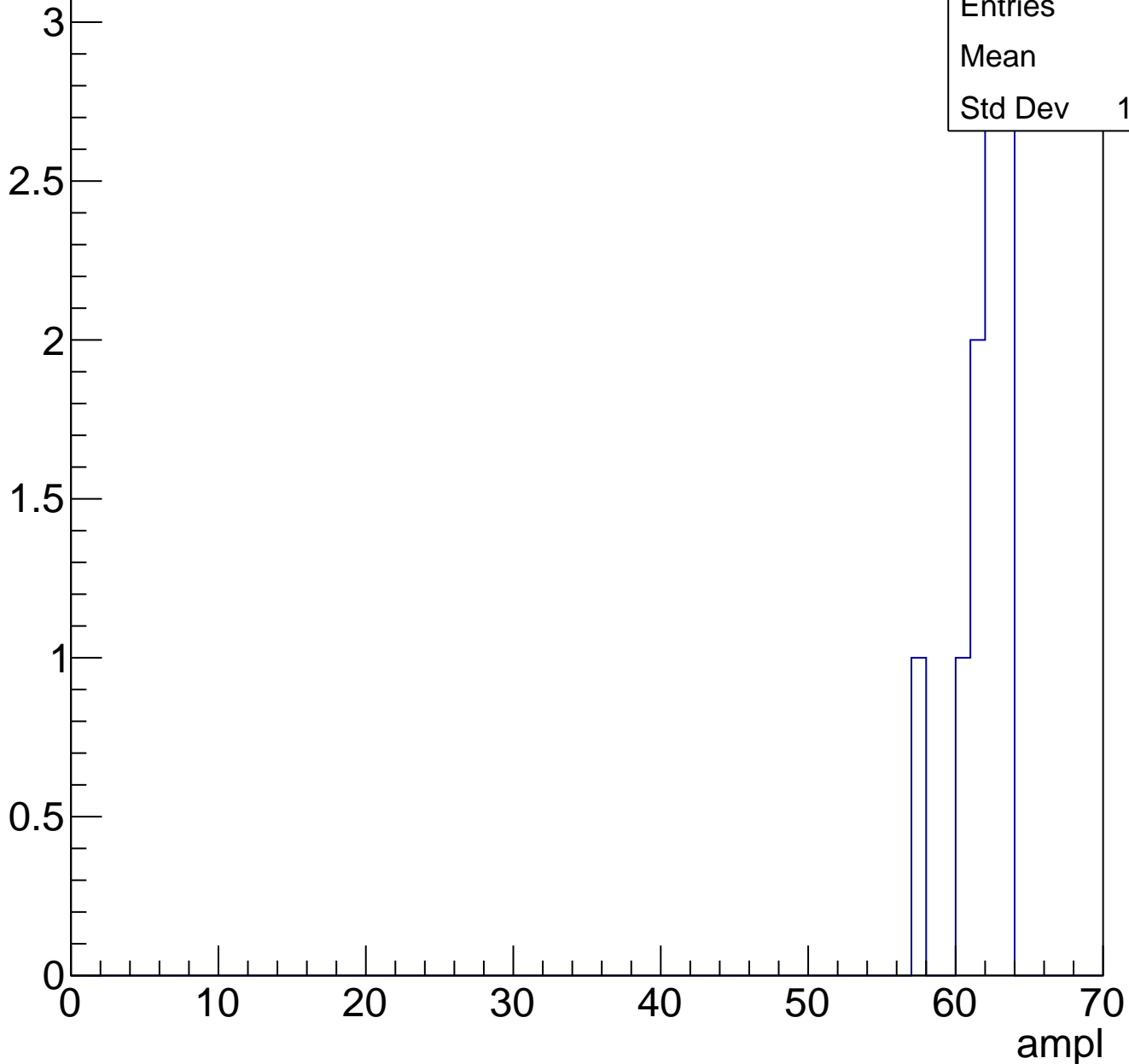
Entries	48
Mean	58.02
Std Dev	8.894



# B0L000S, U18-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch113, adc0

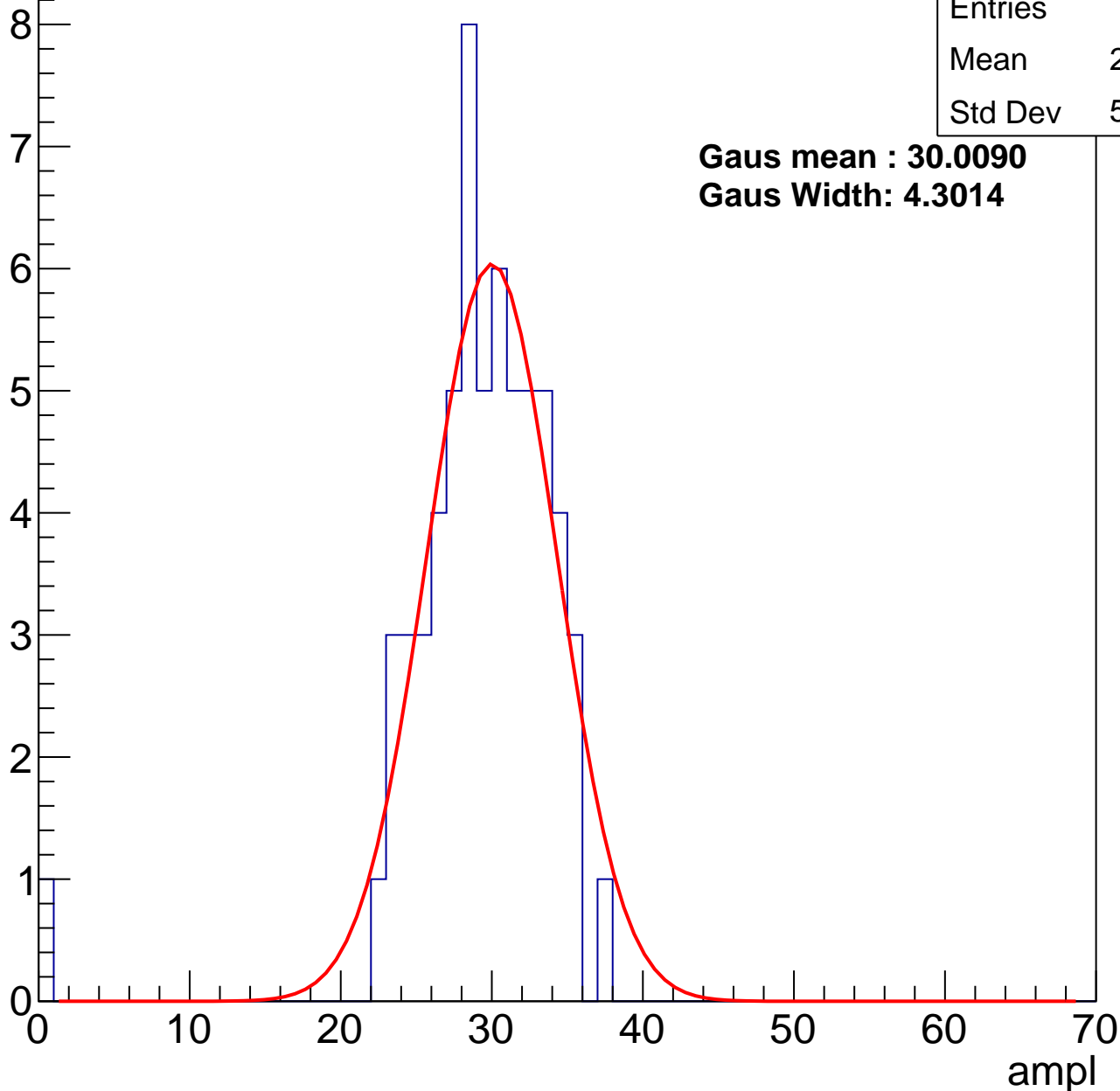
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	28.77
Std Dev	5.088

**Gaus mean : 30.0090**

**Gaus Width: 4.3014**



# B0L000S, U18-ch113, adc1

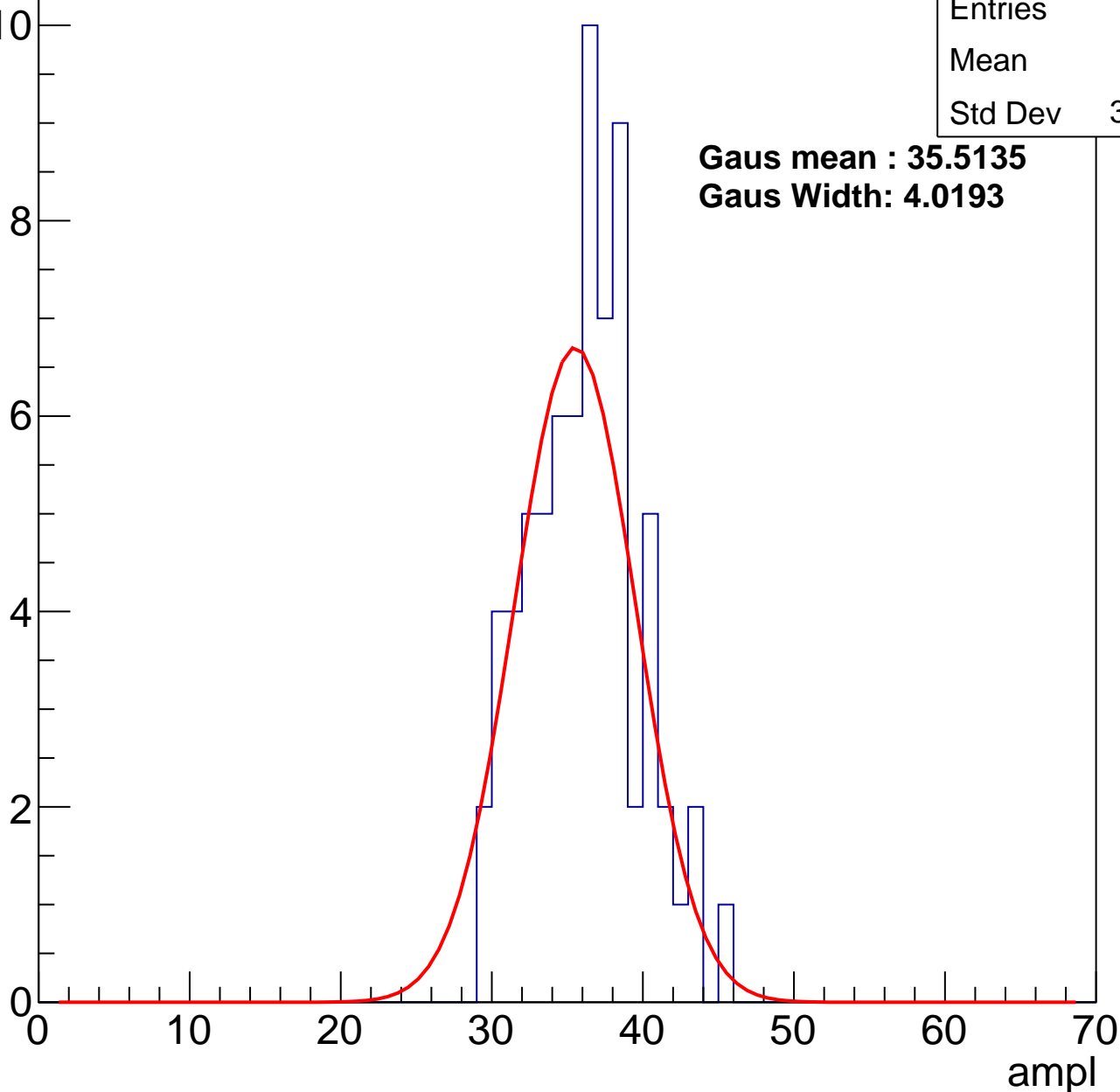
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	35.7
Std Dev	3.554

**Gaus mean : 35.5135**

**Gaus Width: 4.0193**



# B0L000S, U18-ch113, adc2

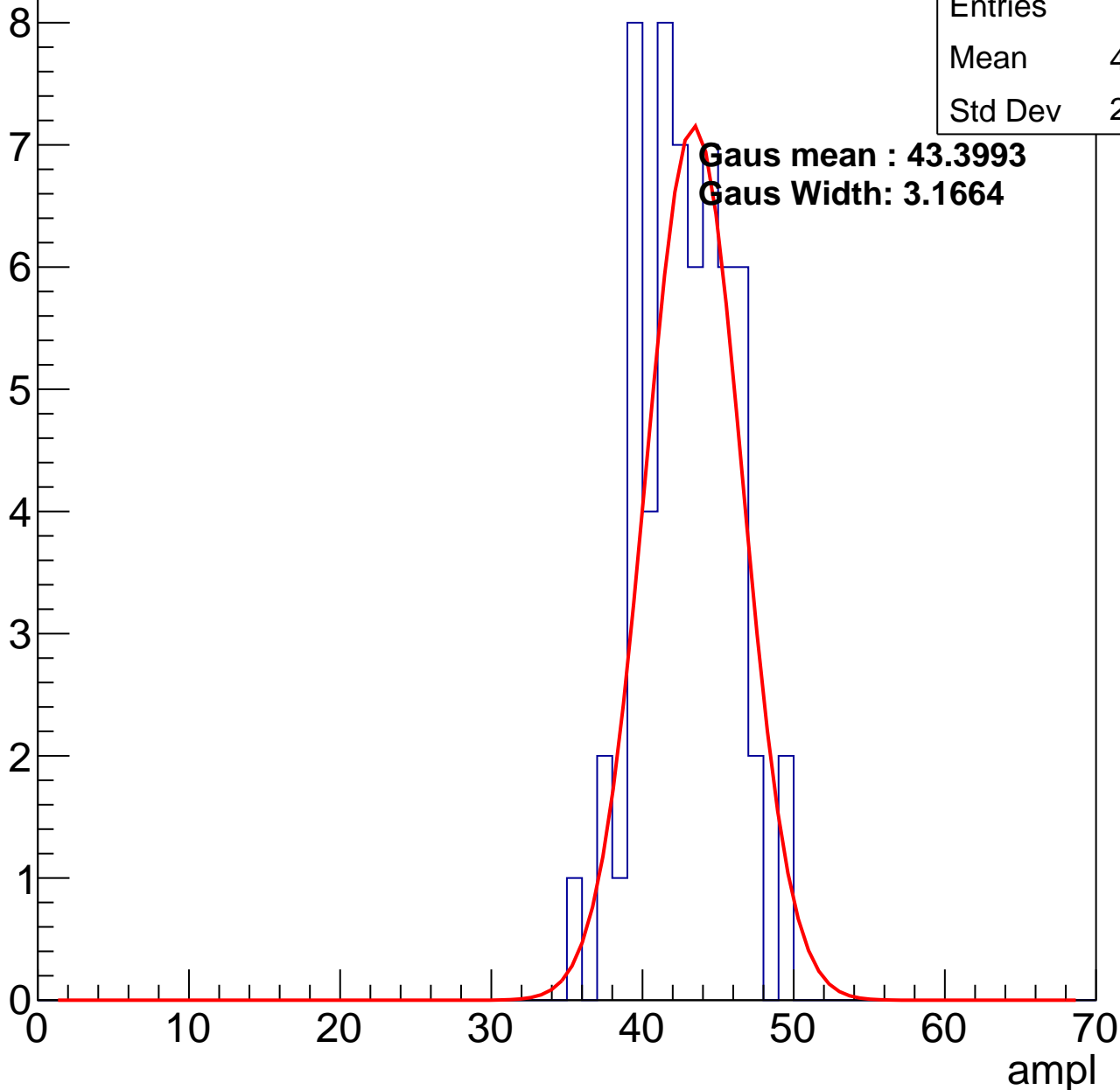
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.42
Std Dev	2.979

**Gaus mean : 43.3993**

**Gaus Width: 3.1664**



# B0L000S, U18-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

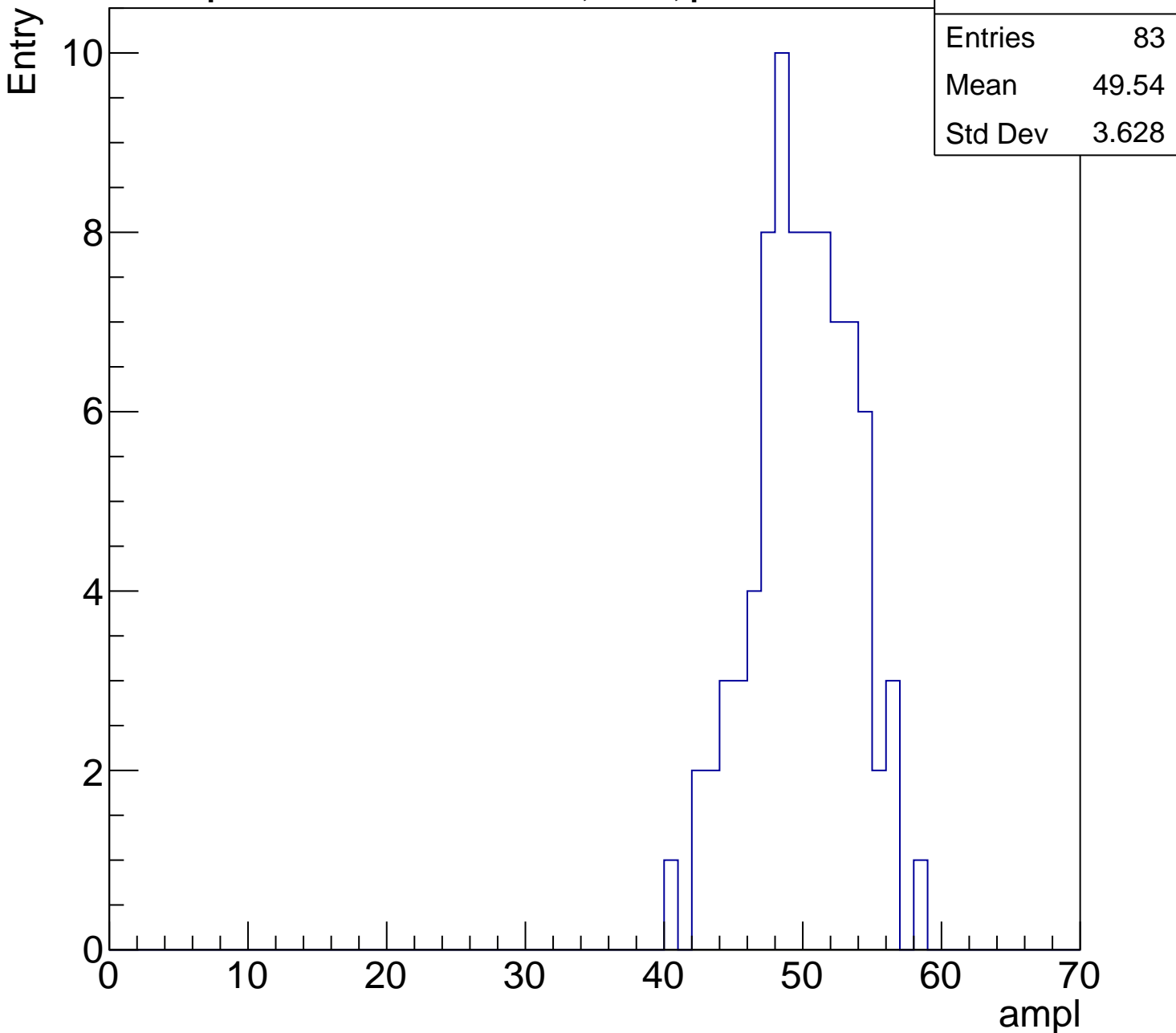
Entries	83
Mean	49.54
Std Dev	3.628

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

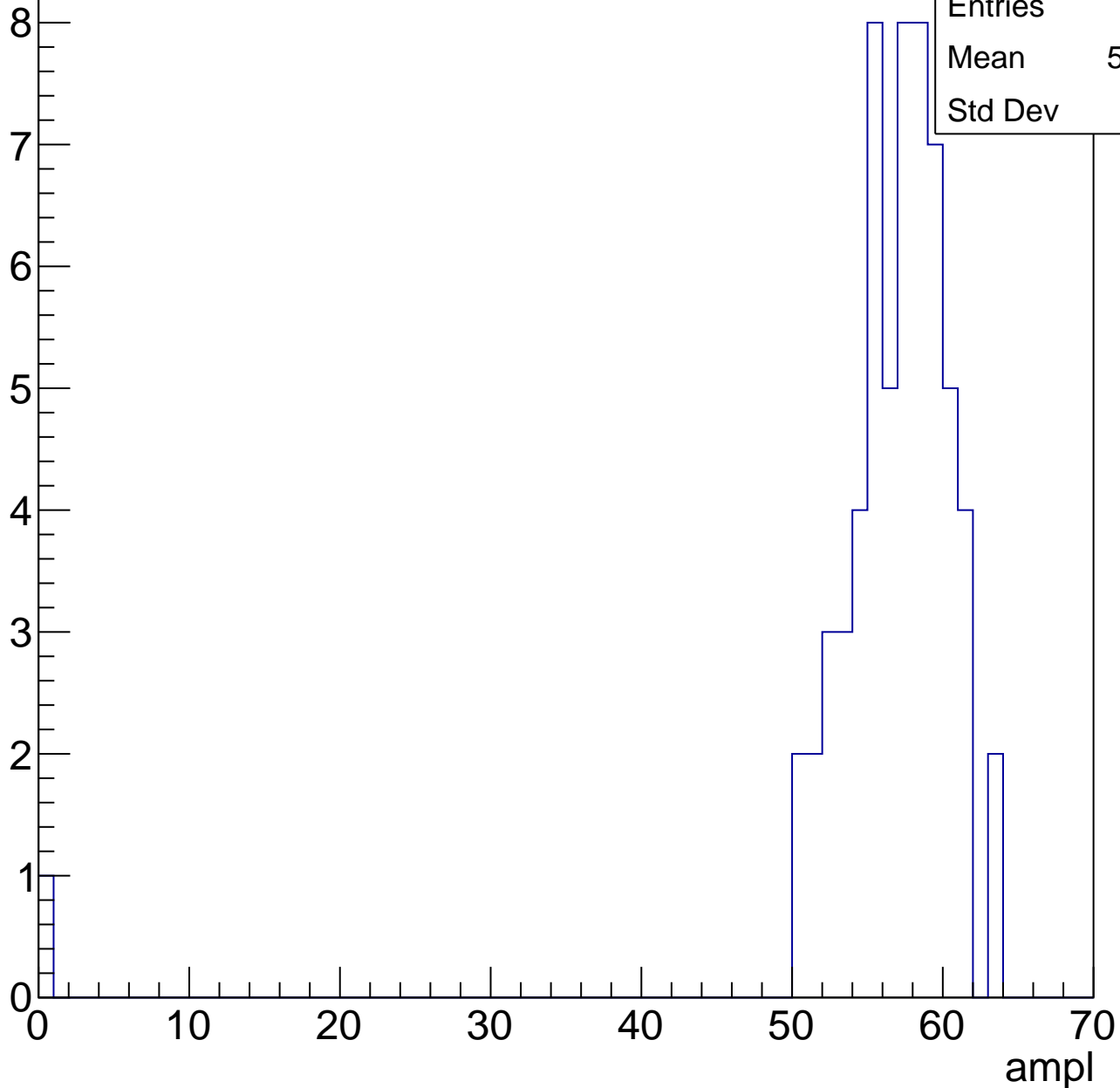


# B0L000S, U18-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.74
Std Dev	7.76

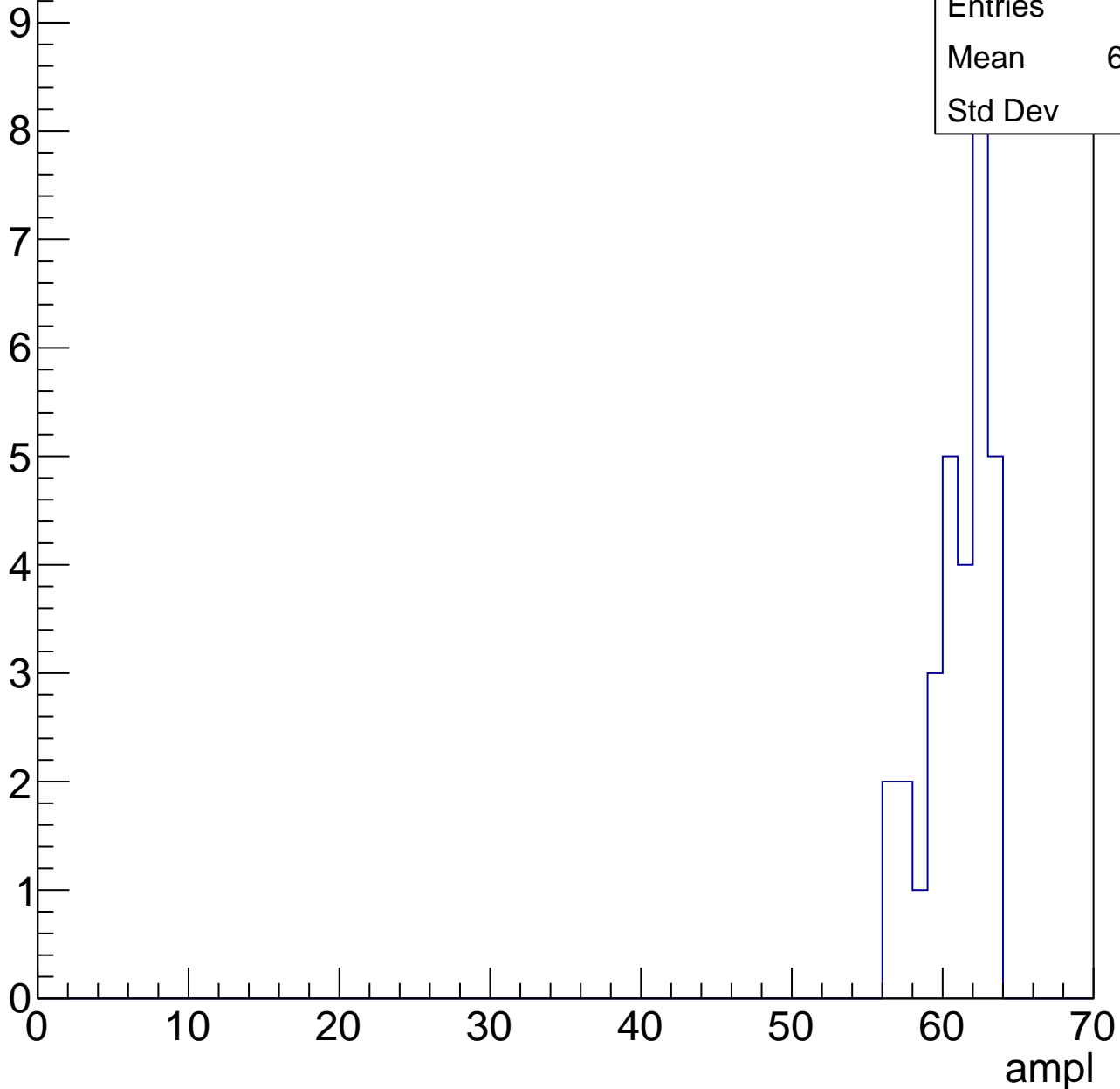


# B0L000S, U18-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	60.58
Std Dev	2.06



# B0L000S, U18-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	59
Mean	30.41
Std Dev	2.811

**Gaus mean : 30.2820**

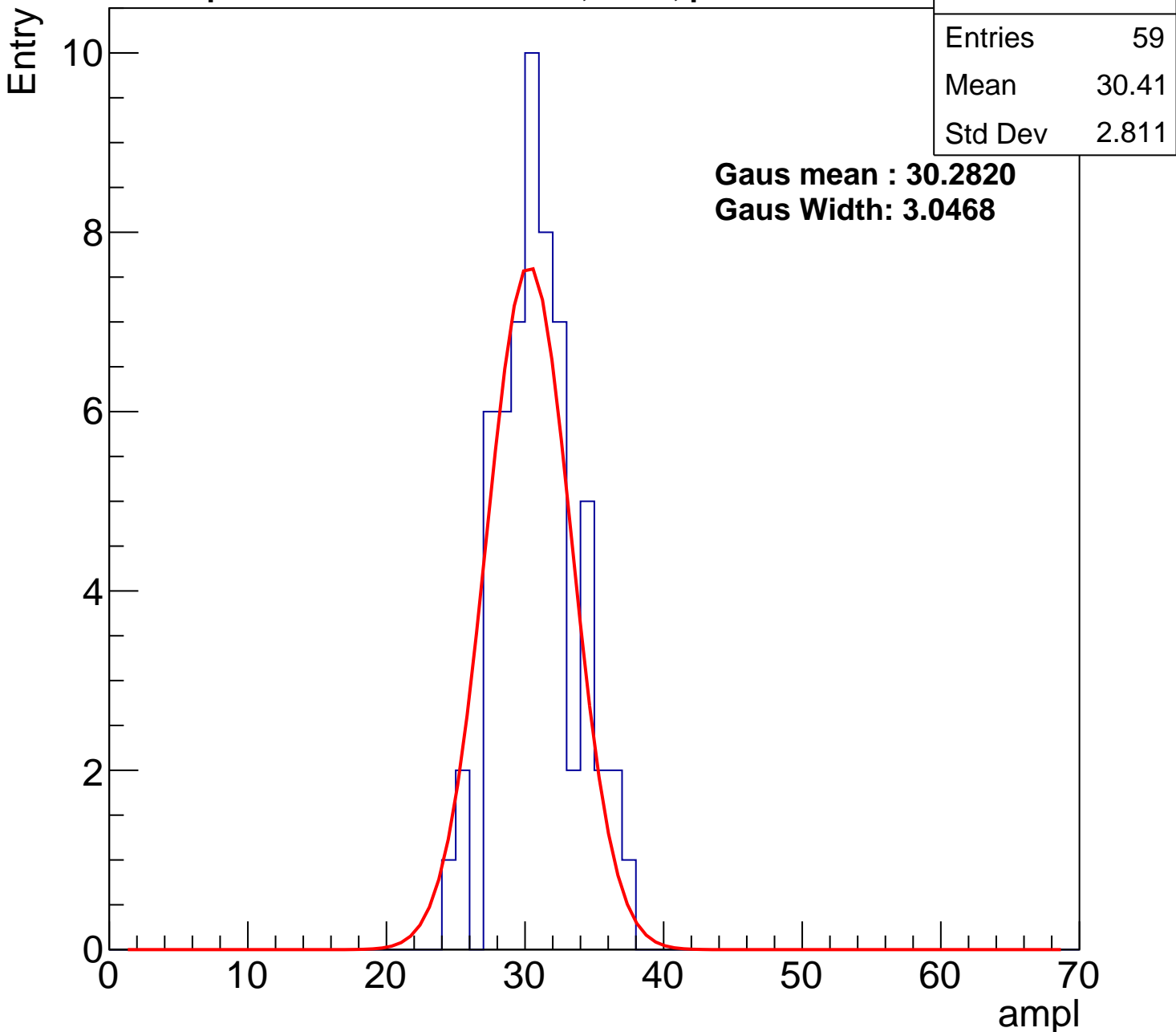
**Gaus Width: 3.0468**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch114, adc1

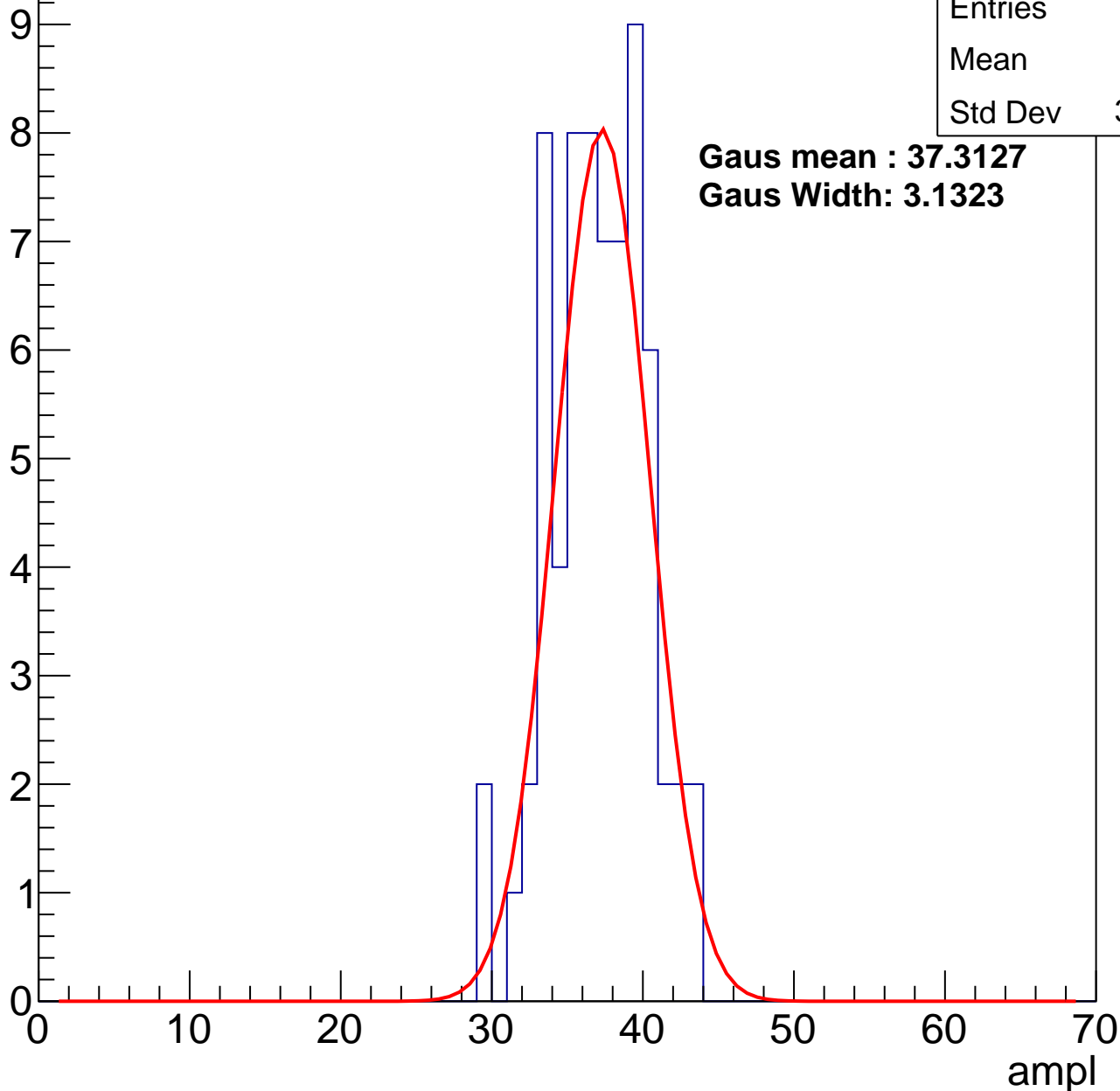
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.6
Std Dev	3.111

**Gaus mean : 37.3127**

**Gaus Width: 3.1323**



# B0L000S, U18-ch114, adc2

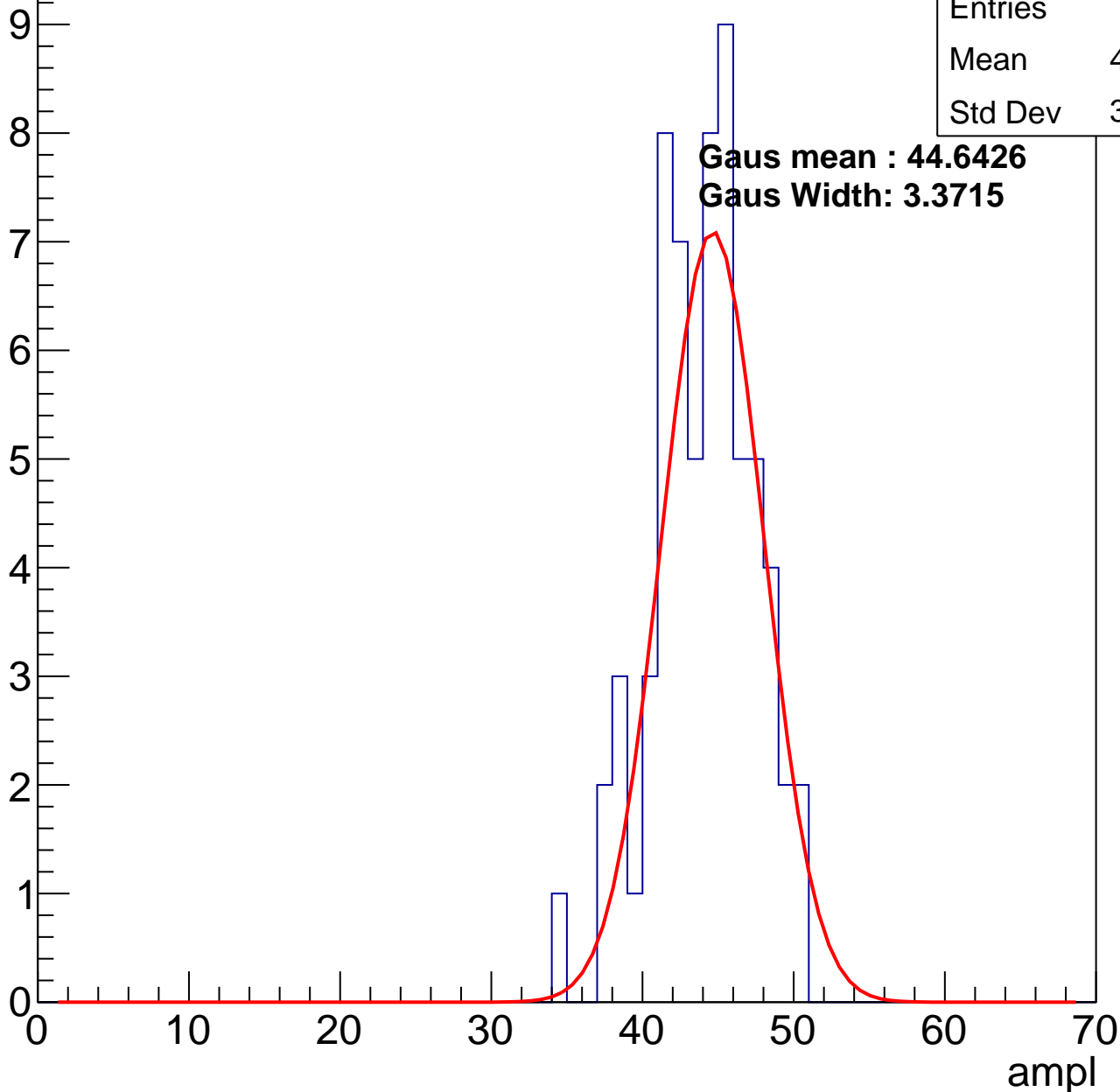
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.54
Std Dev	3.352

**Gaus mean : 44.6426**

**Gaus Width: 3.3715**

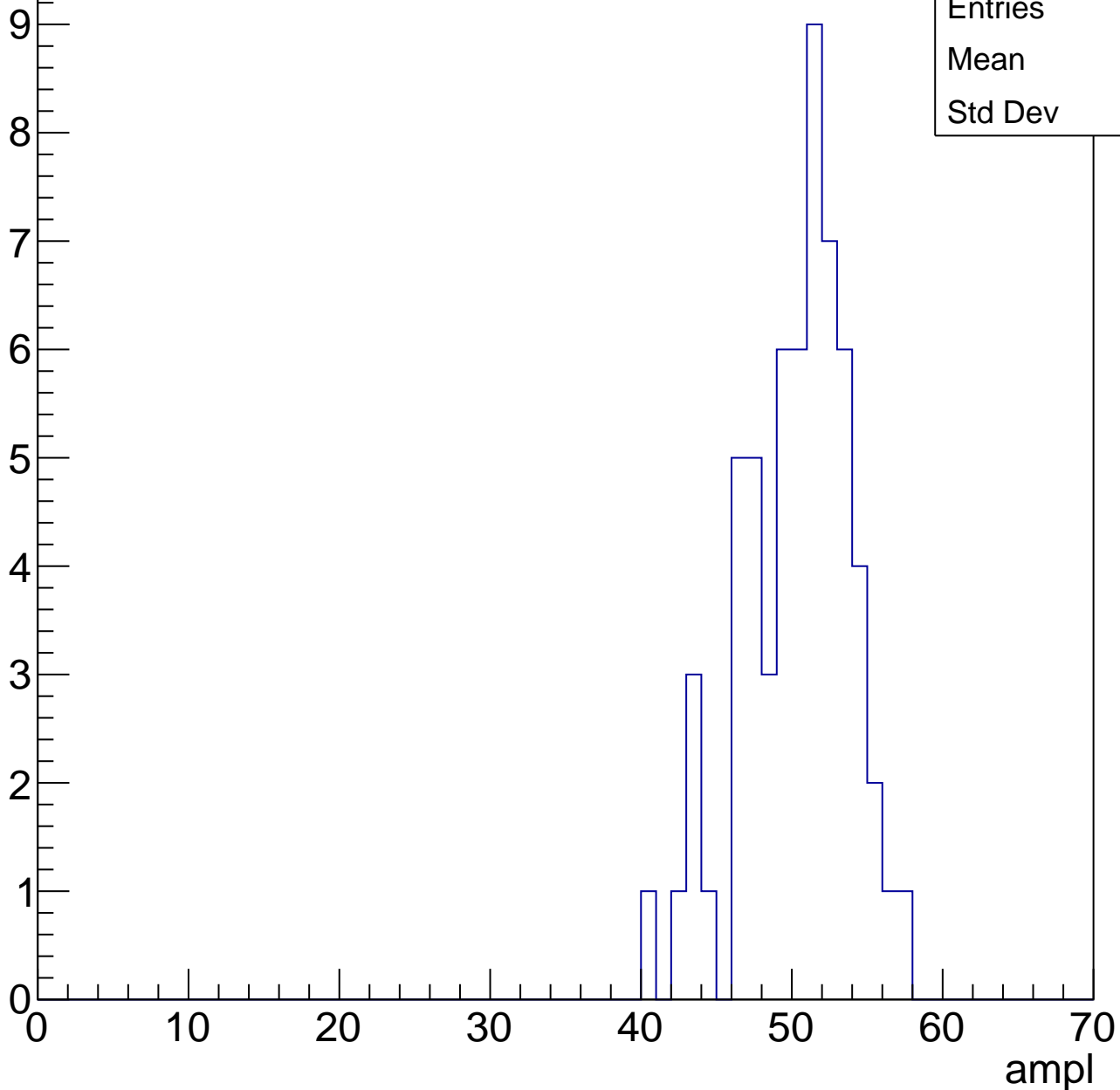


# B0L000S, U18-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	49.8
Std Dev	3.57

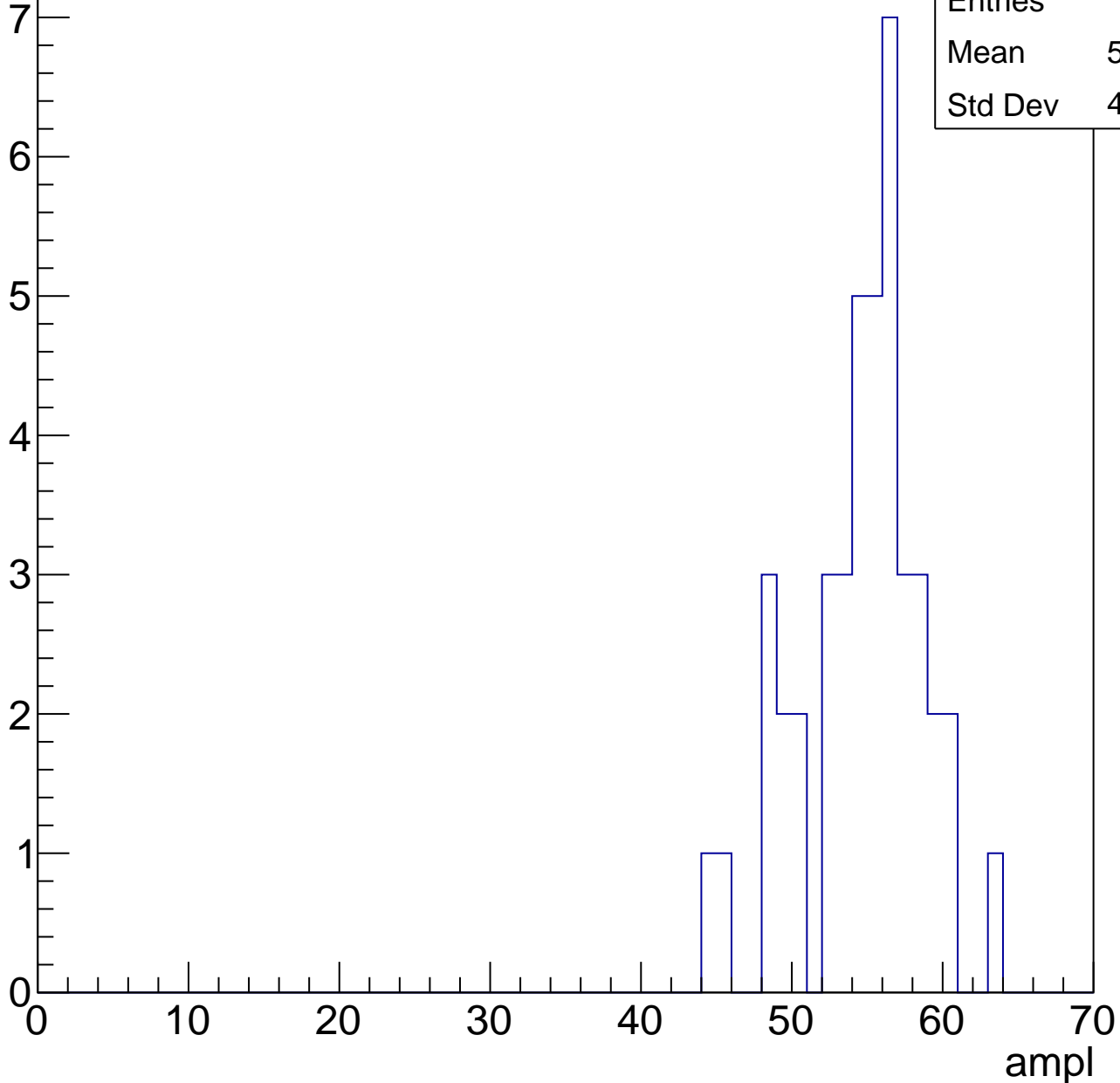


# B0L000S, U18-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	54.16
Std Dev	4.023

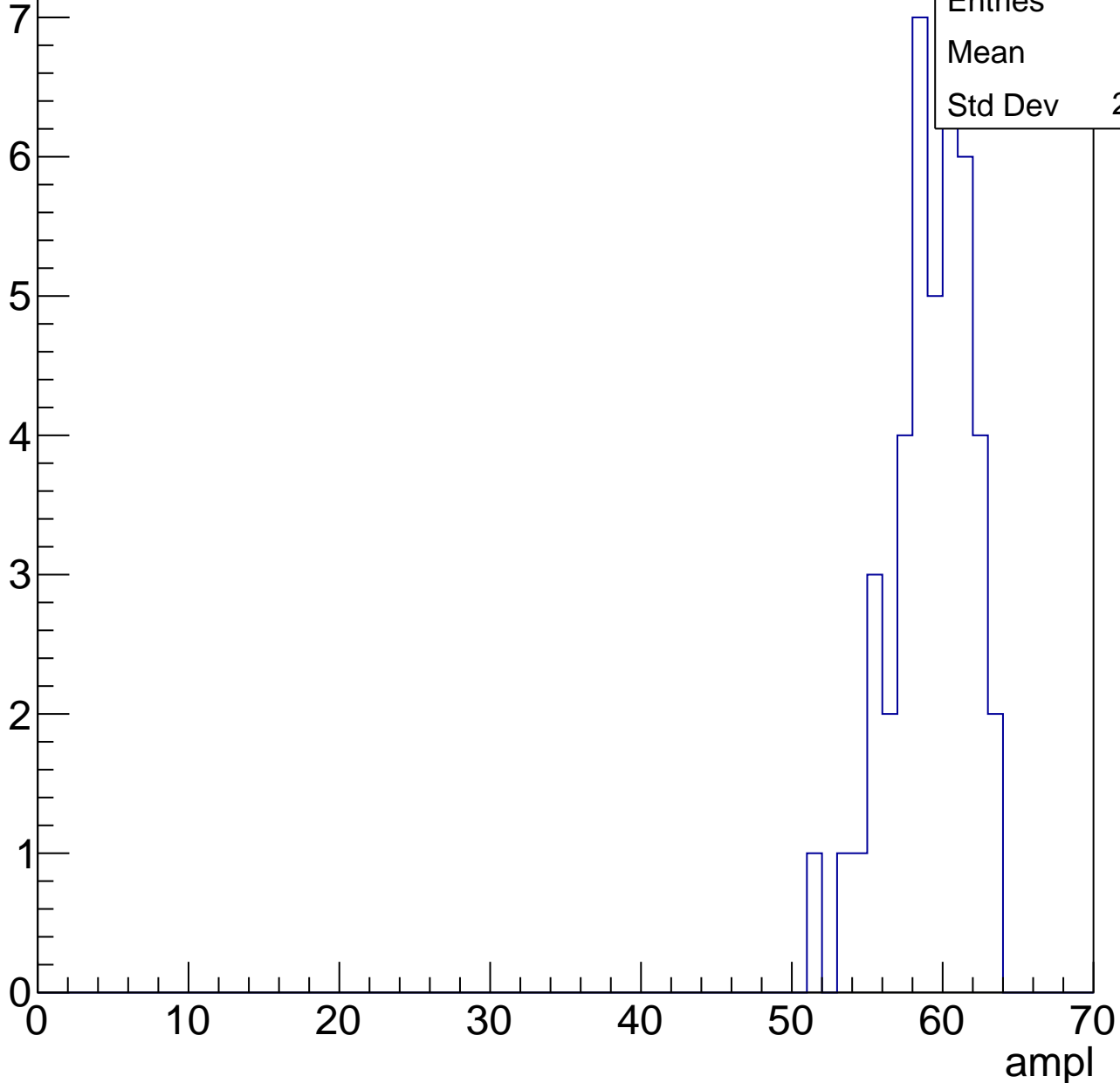


# B0L000S, U18-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	58.7
Std Dev	2.681

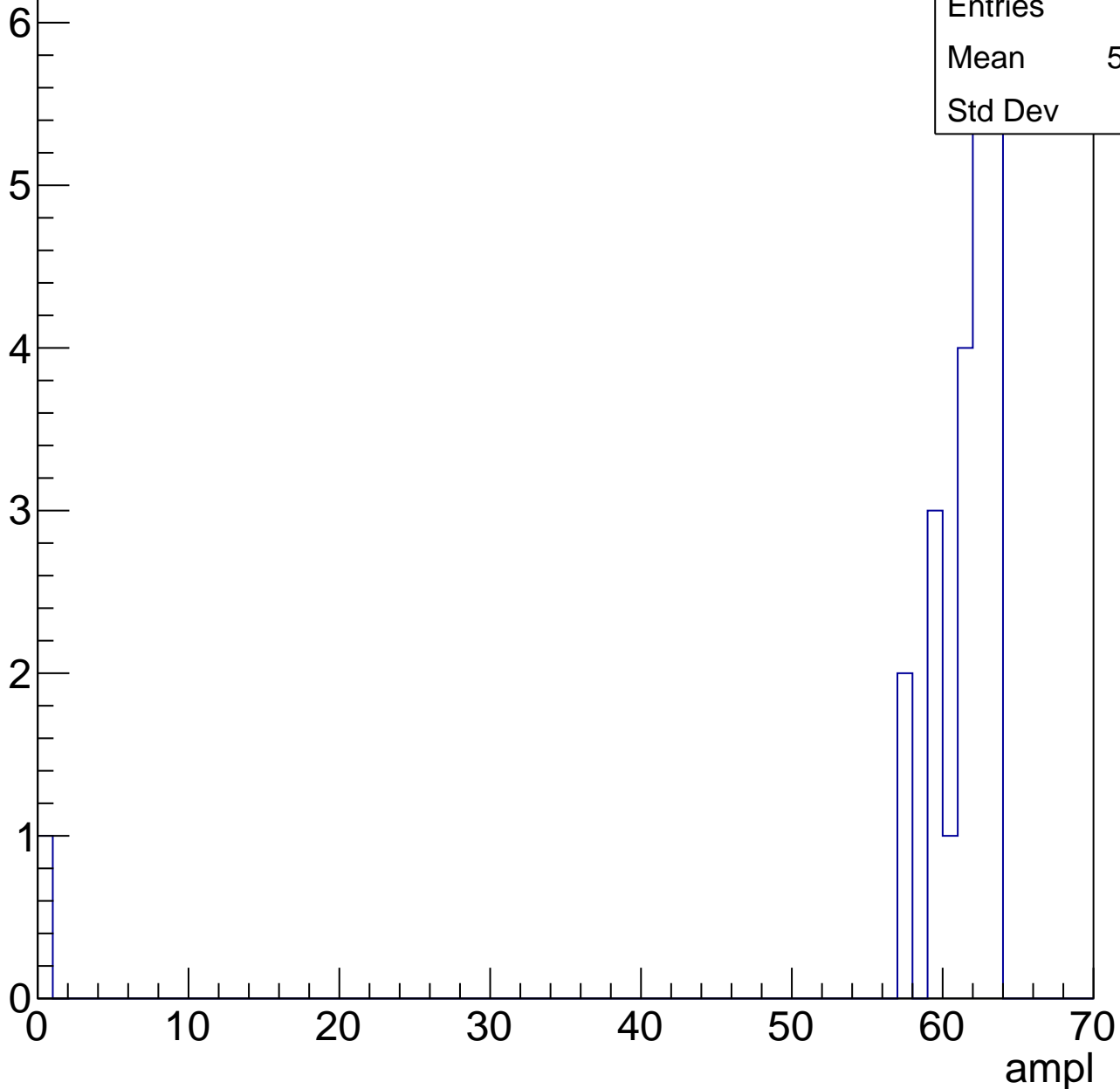


# B0L000S, U18-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.48
Std Dev	12.6

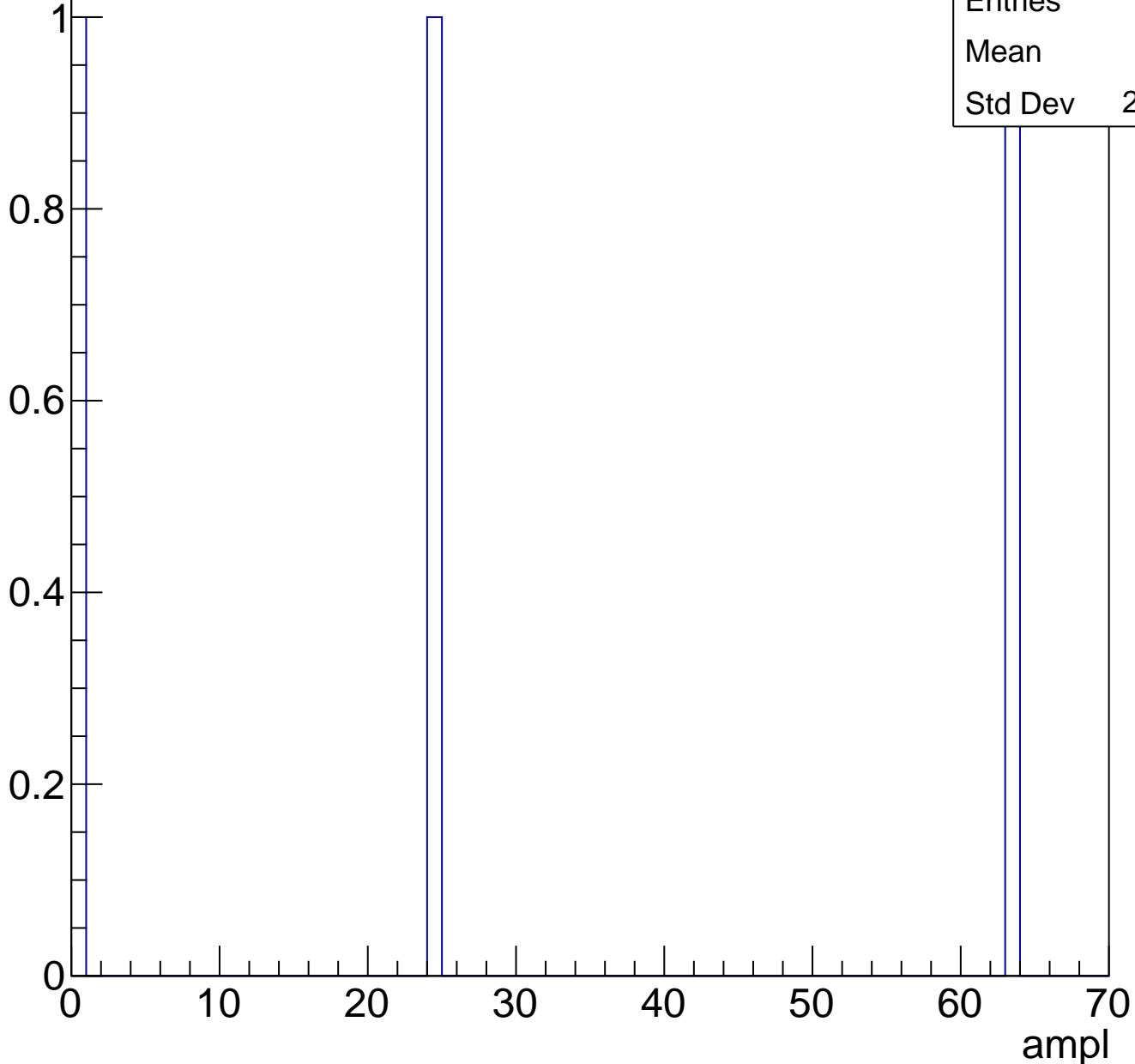




# B0L000S, U18-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	29
Std Dev	25.96

# B0L000S, U18-ch115, adc0

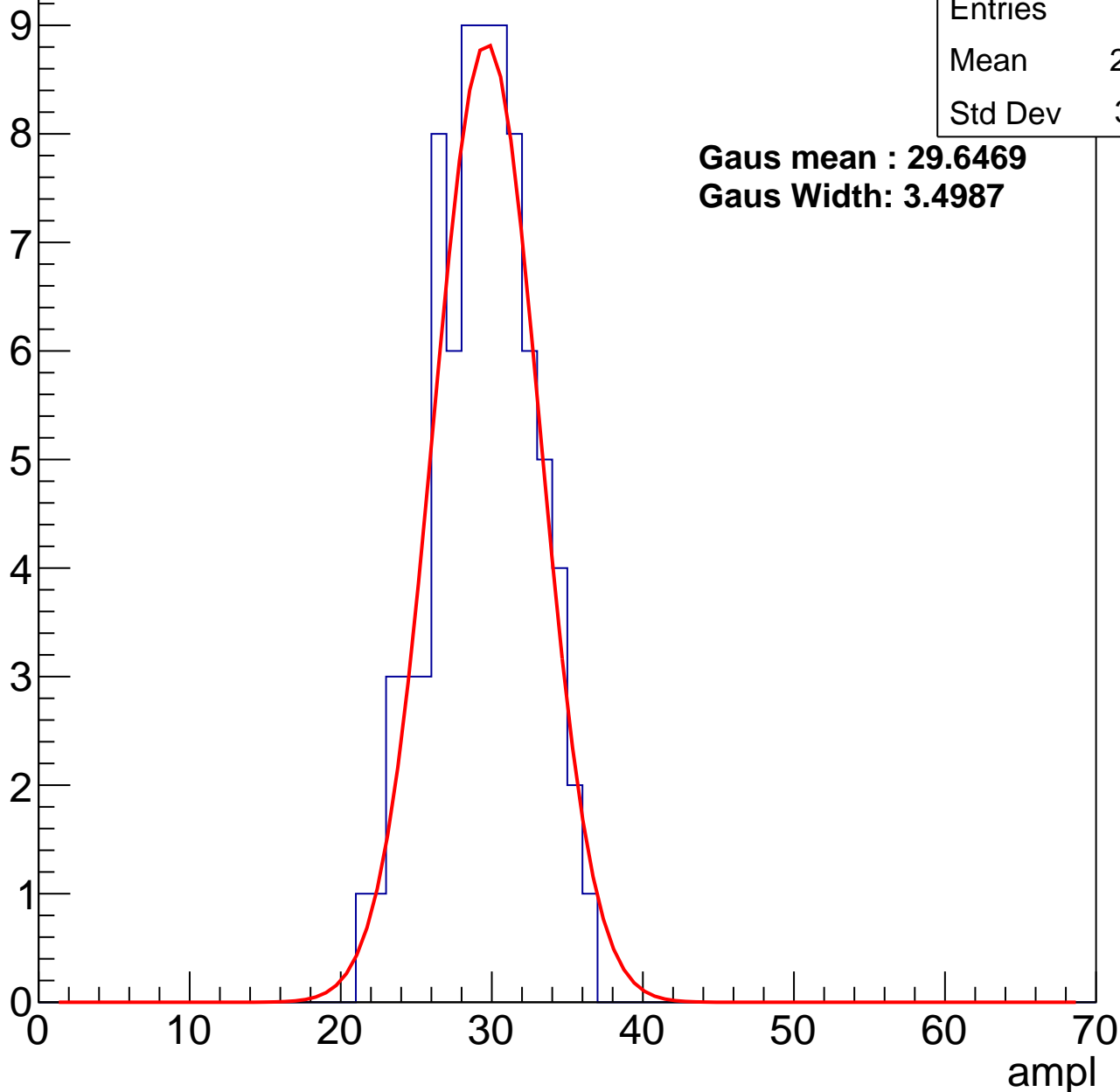
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.96
Std Dev	3.291

**Gaus mean : 29.6469**

**Gaus Width: 3.4987**



# B0L000S, U18-ch115, adc1

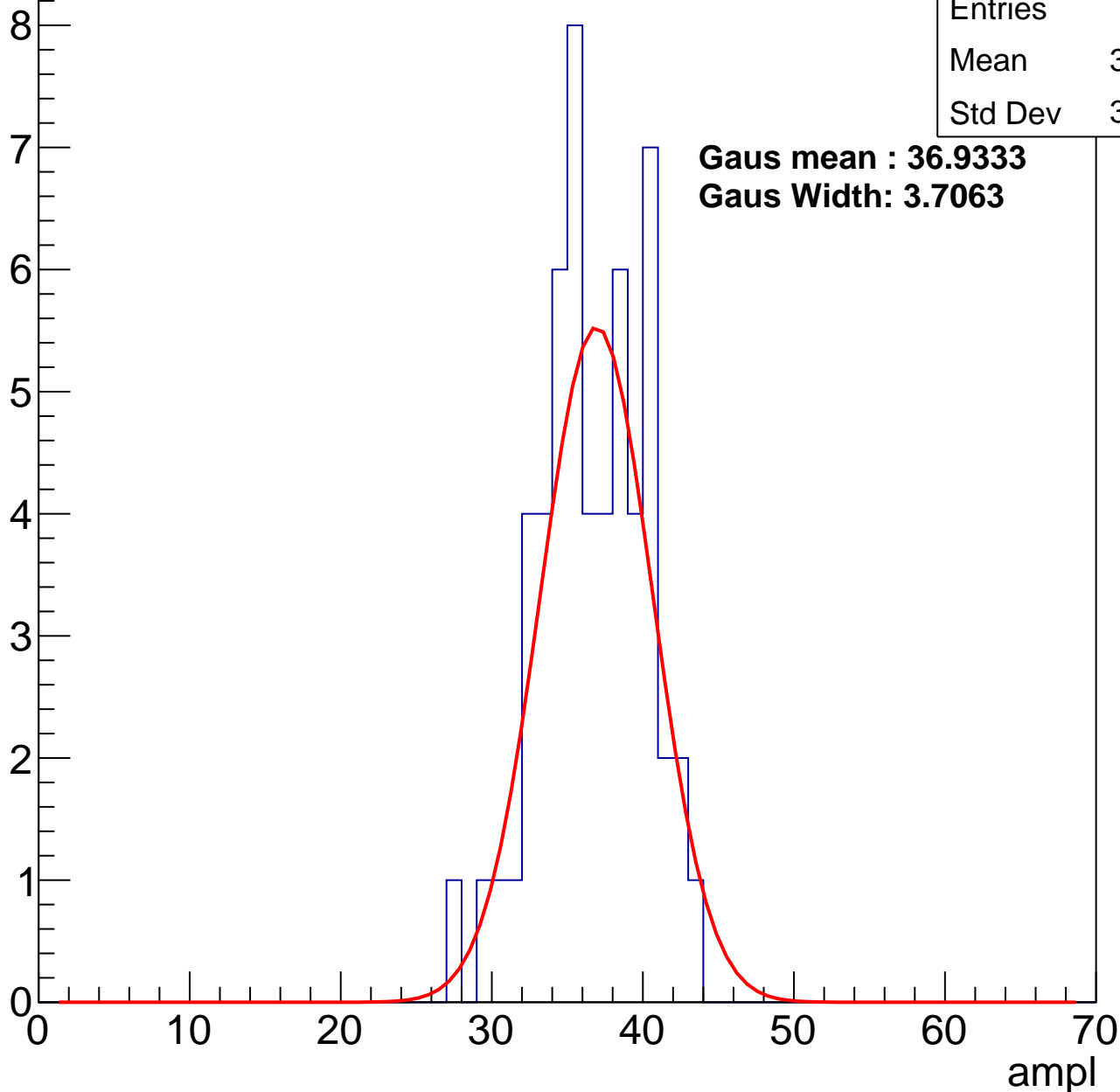
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.18
Std Dev	3.454

**Gaus mean : 36.9333**

**Gaus Width: 3.7063**



# B0L000S, U18-ch115, adc2

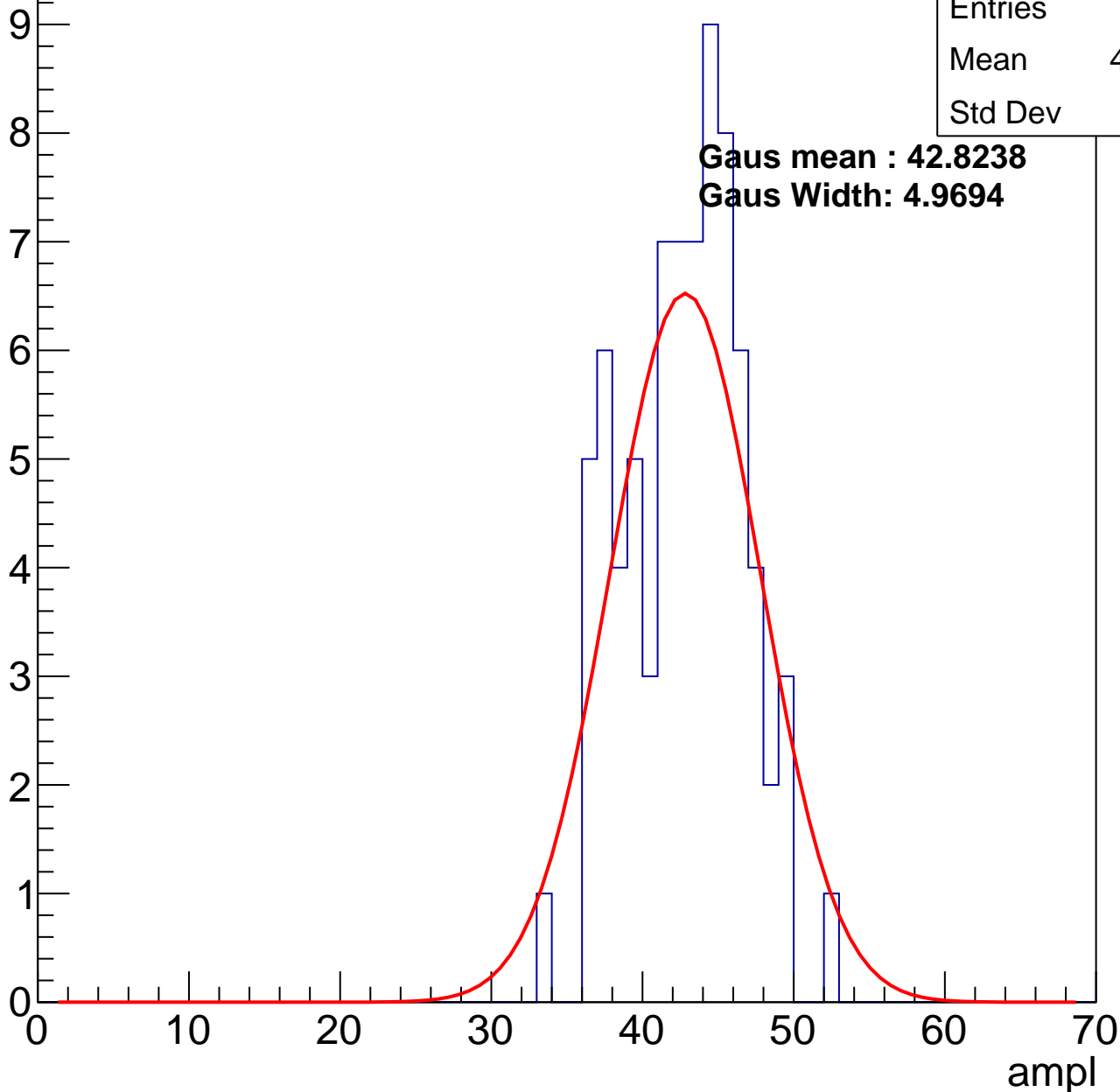
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	42.29
Std Dev	3.86

**Gaus mean : 42.8238**

**Gaus Width: 4.9694**

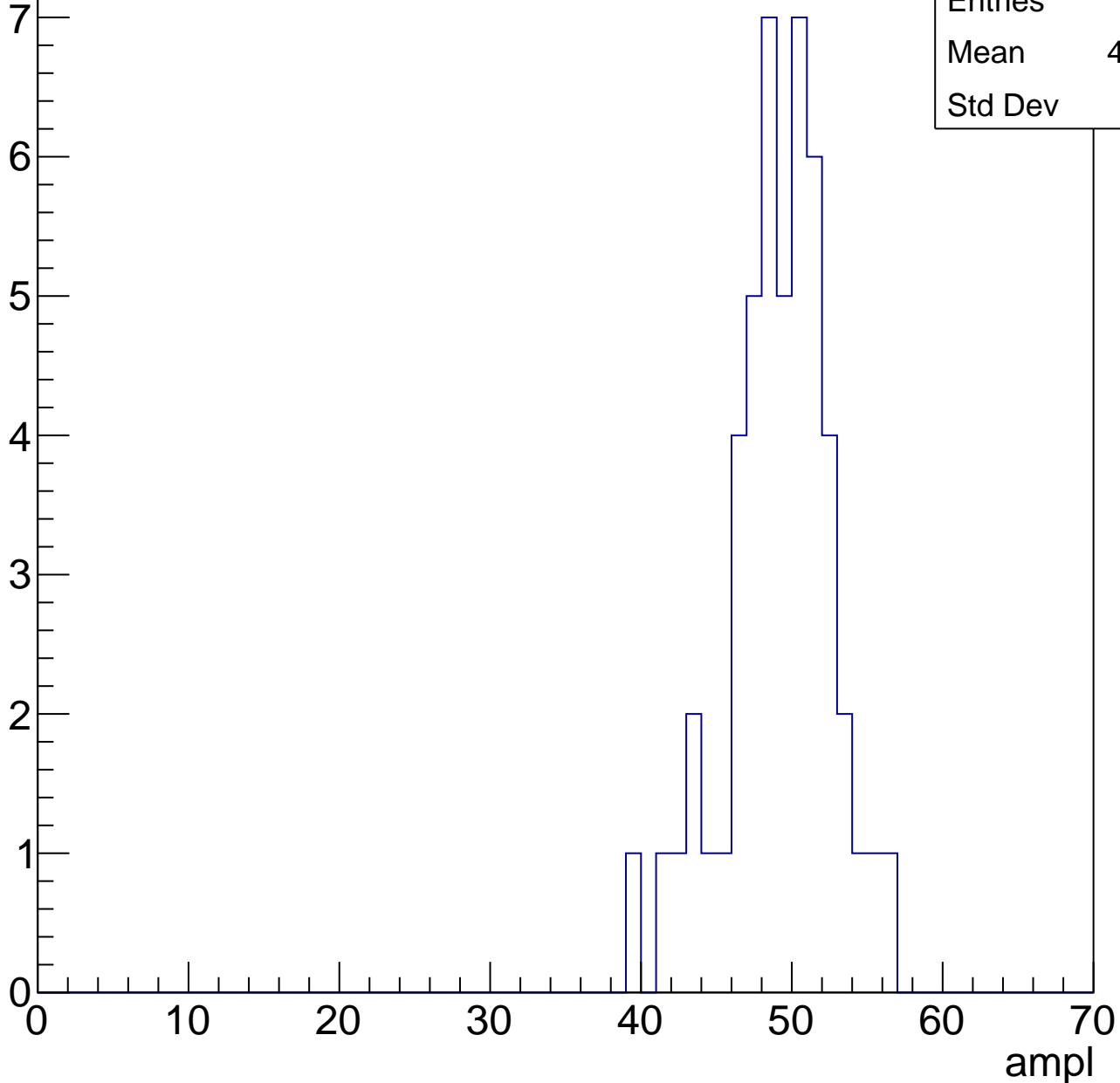


# B0L000S, U18-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	48.64
Std Dev	3.44

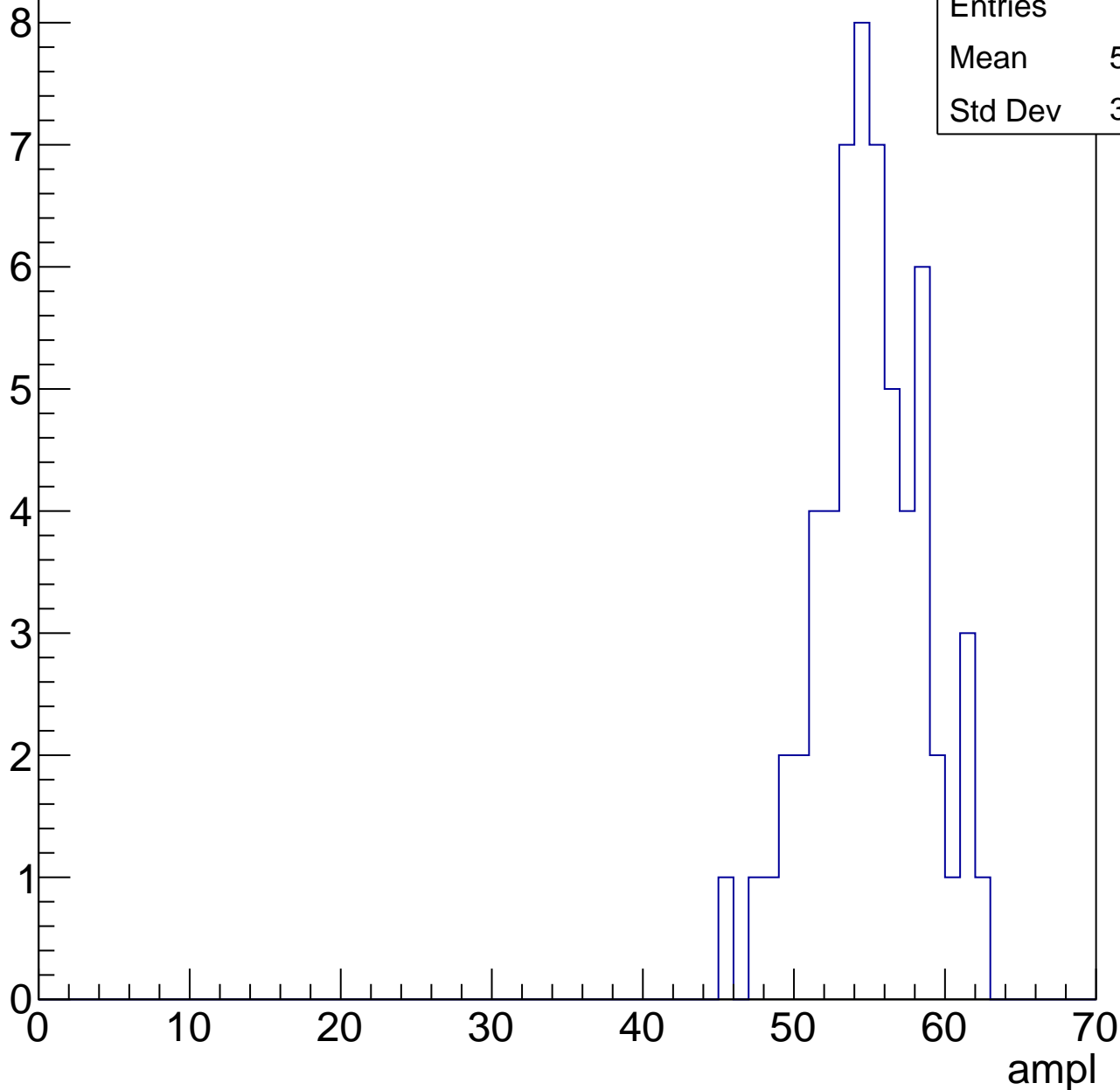


# B0L000S, U18-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	54.53
Std Dev	3.558

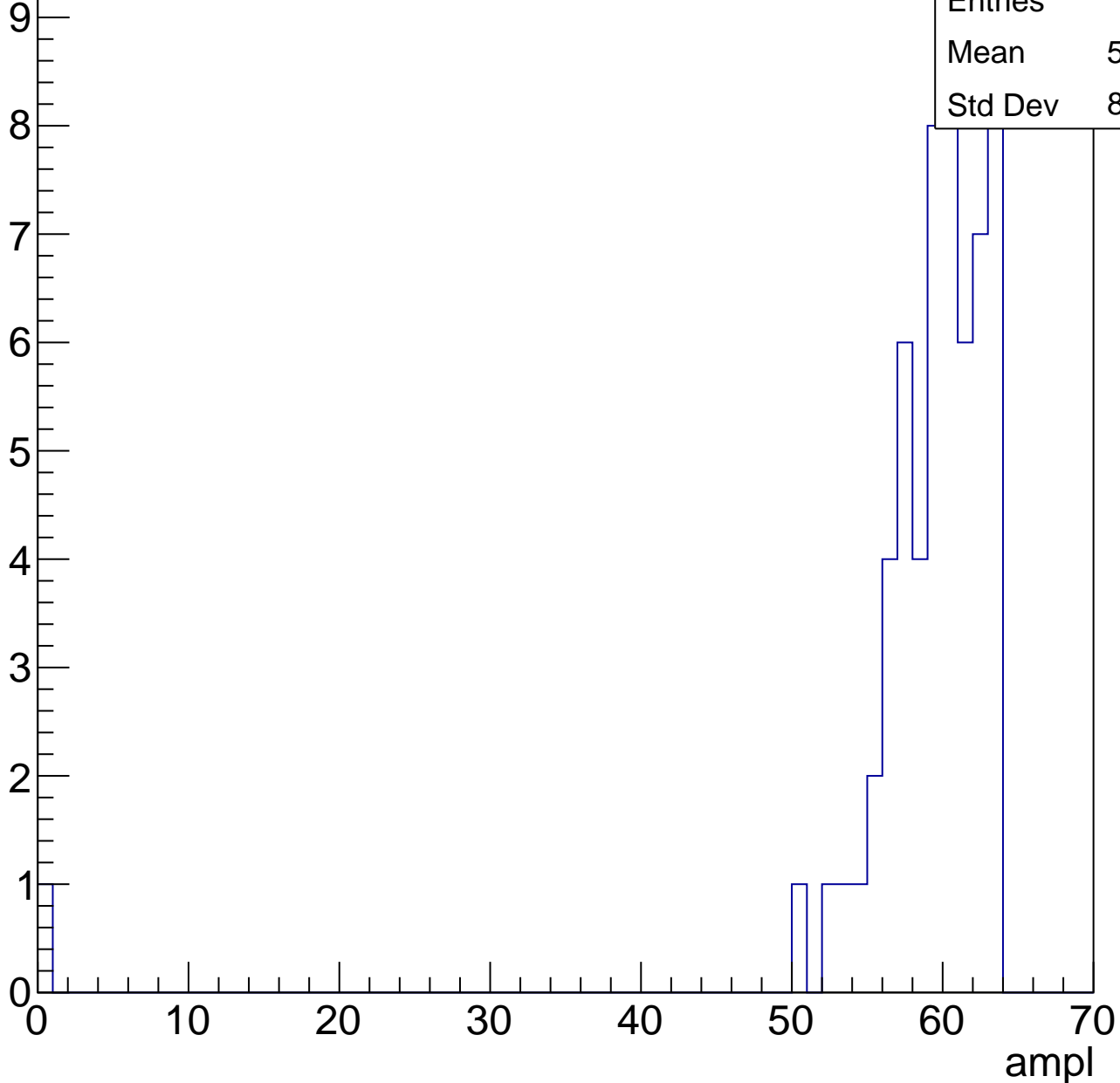


# B0L000S, U18-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

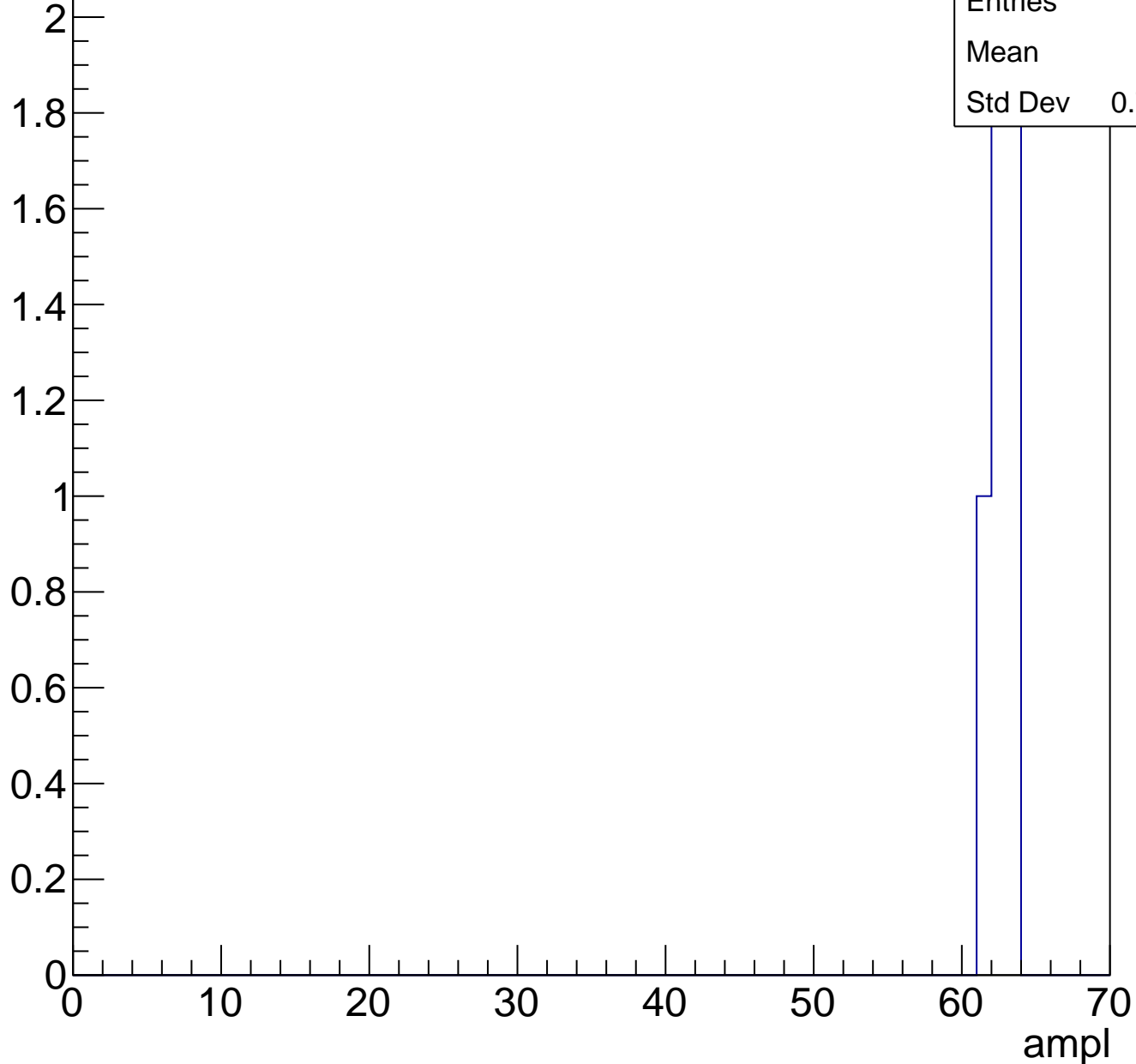
Entries	59
Mean	58.19
Std Dev	8.185



# B0L000S, U18-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch116, adc0

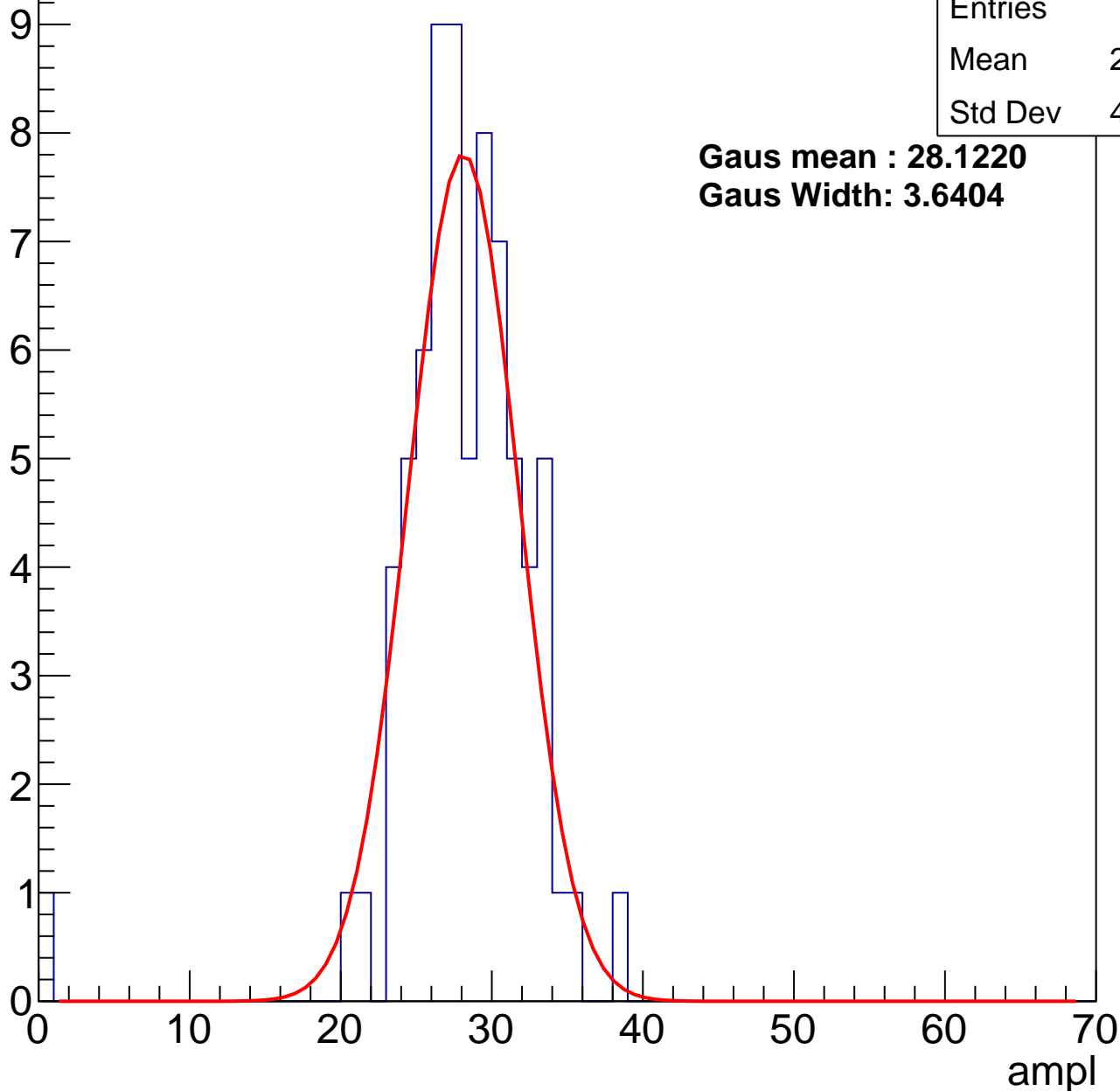
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	27.63
Std Dev	4.712

**Gaus mean : 28.1220**

**Gaus Width: 3.6404**



# B0L000S, U18-ch116, adc1

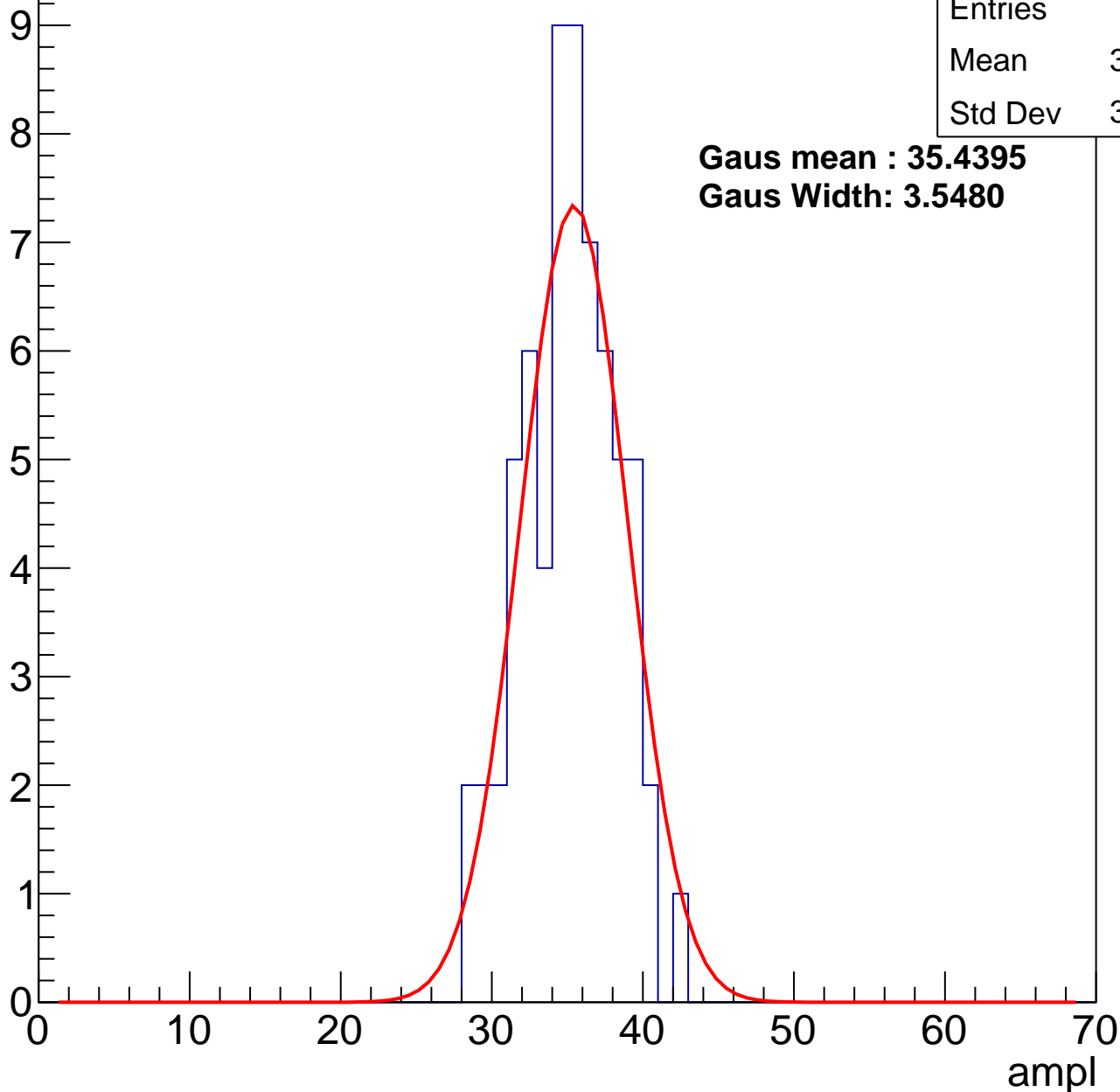
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.69
Std Dev	3.108

**Gaus mean : 35.4395**

**Gaus Width: 3.5480**



# B0L000S, U18-ch116, adc2

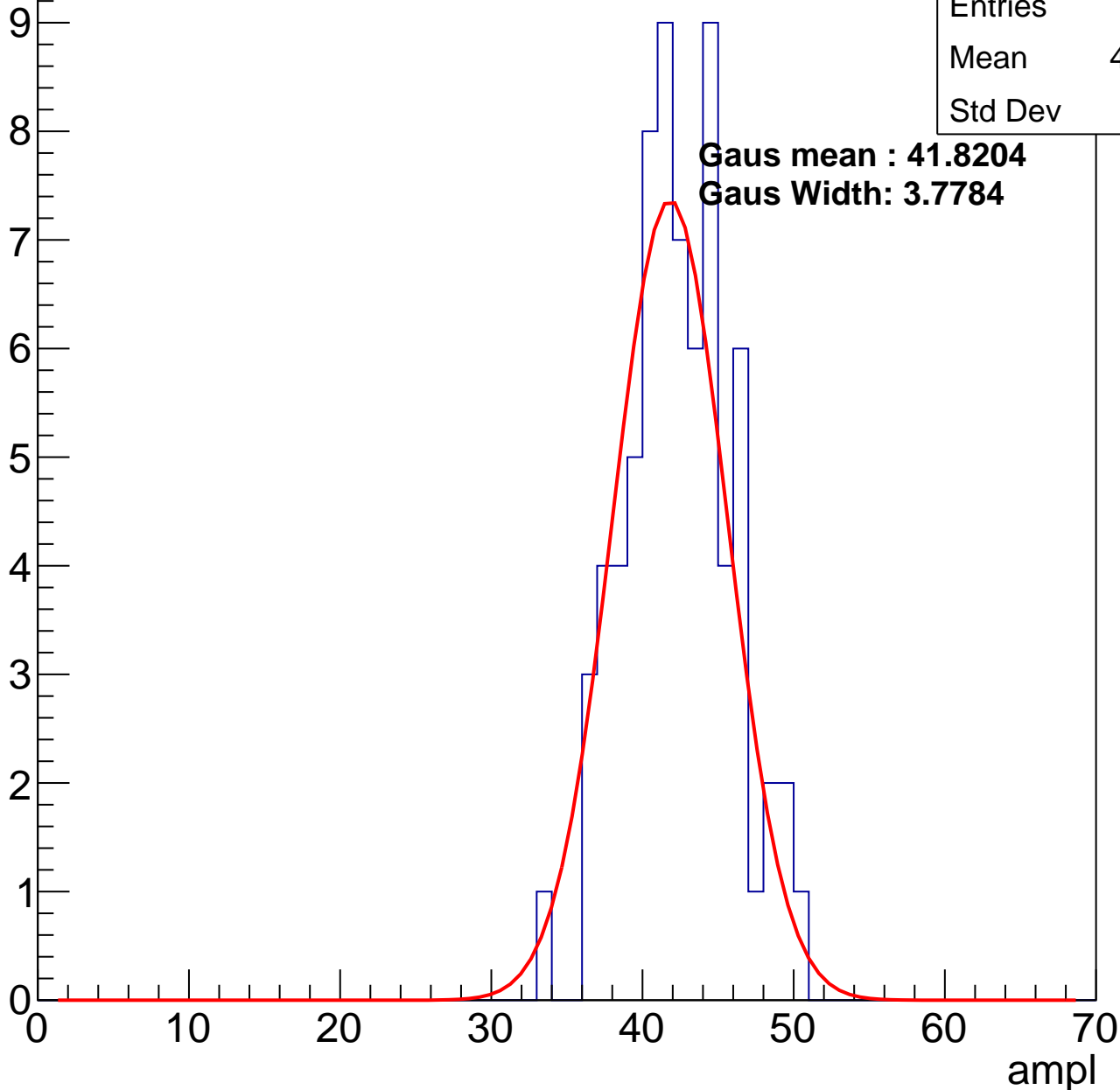
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	41.94
Std Dev	3.48

**Gaus mean : 41.8204**

**Gaus Width: 3.7784**

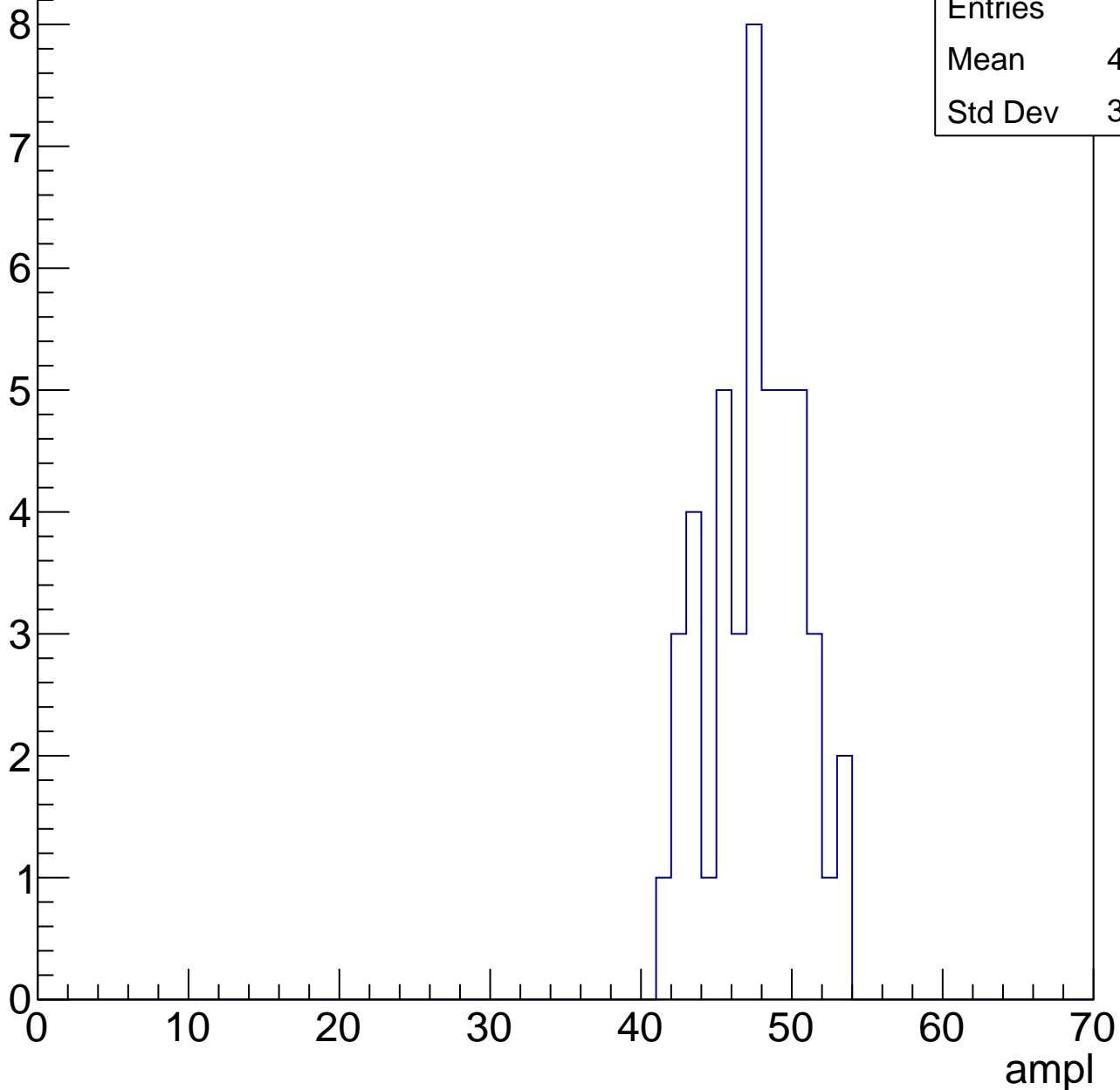


# B0L000S, U18-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	47.13
Std Dev	3.026

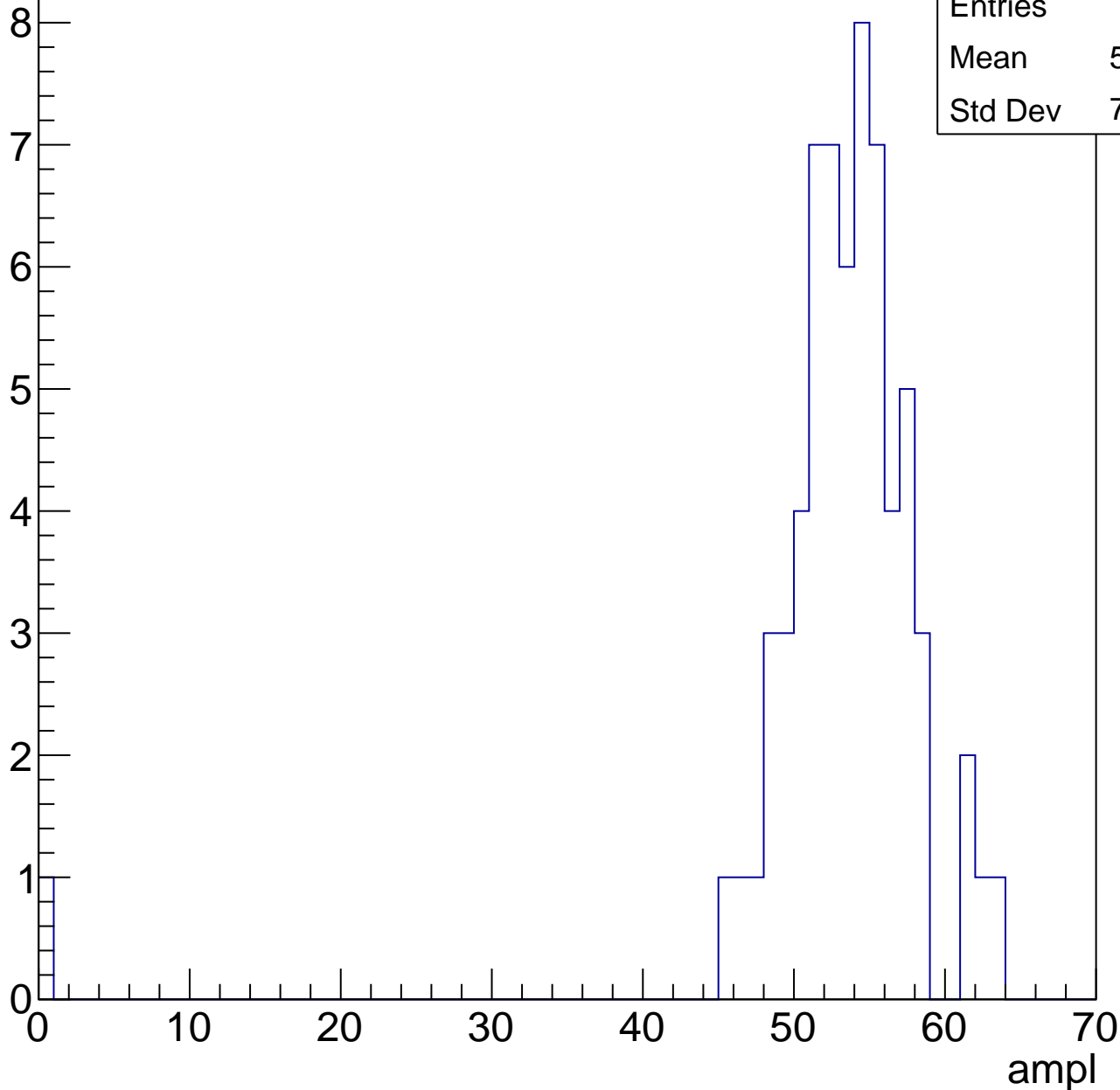


# B0L000S, U18-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	52.54
Std Dev	7.518

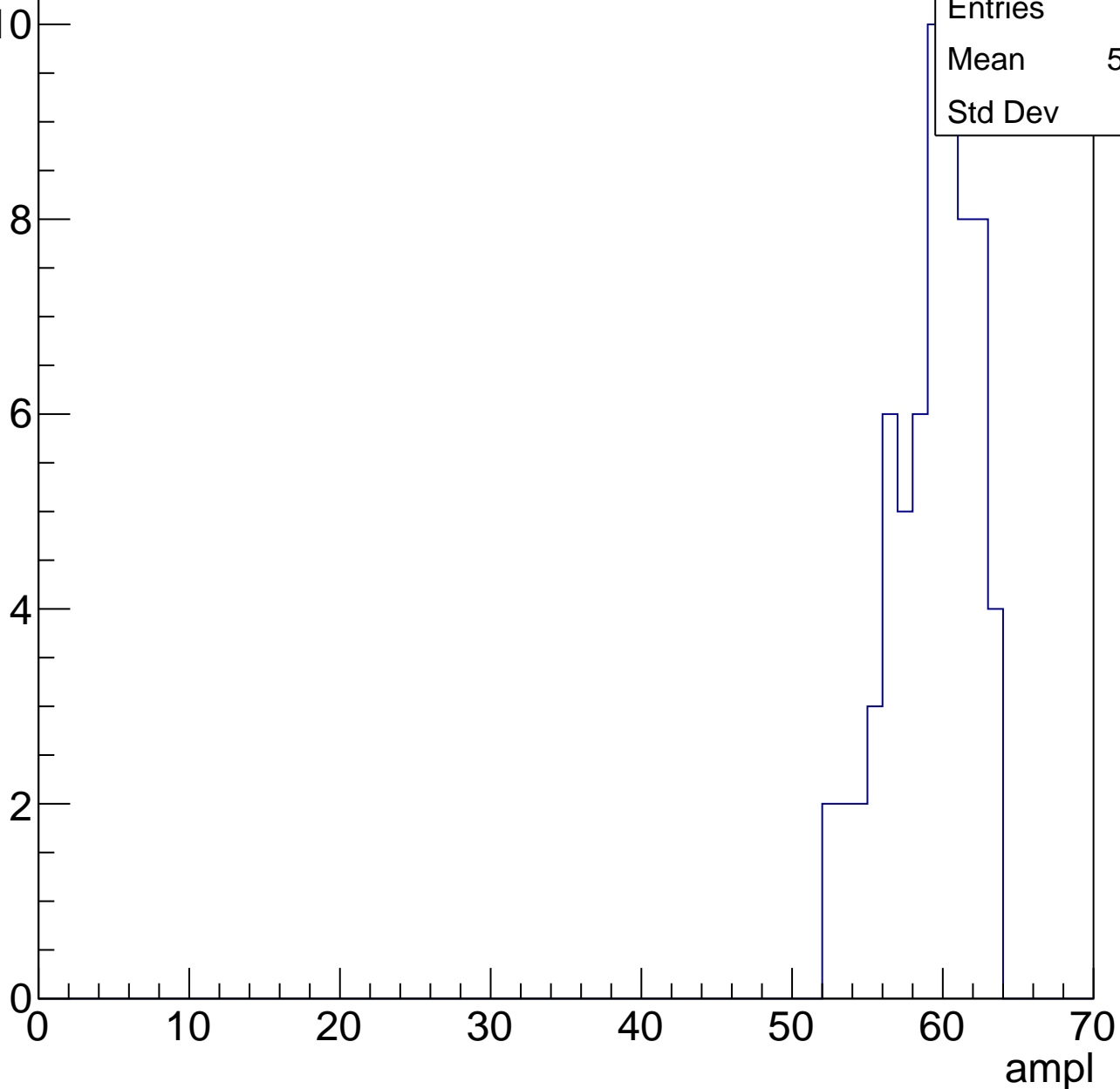


# B0L000S, U18-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

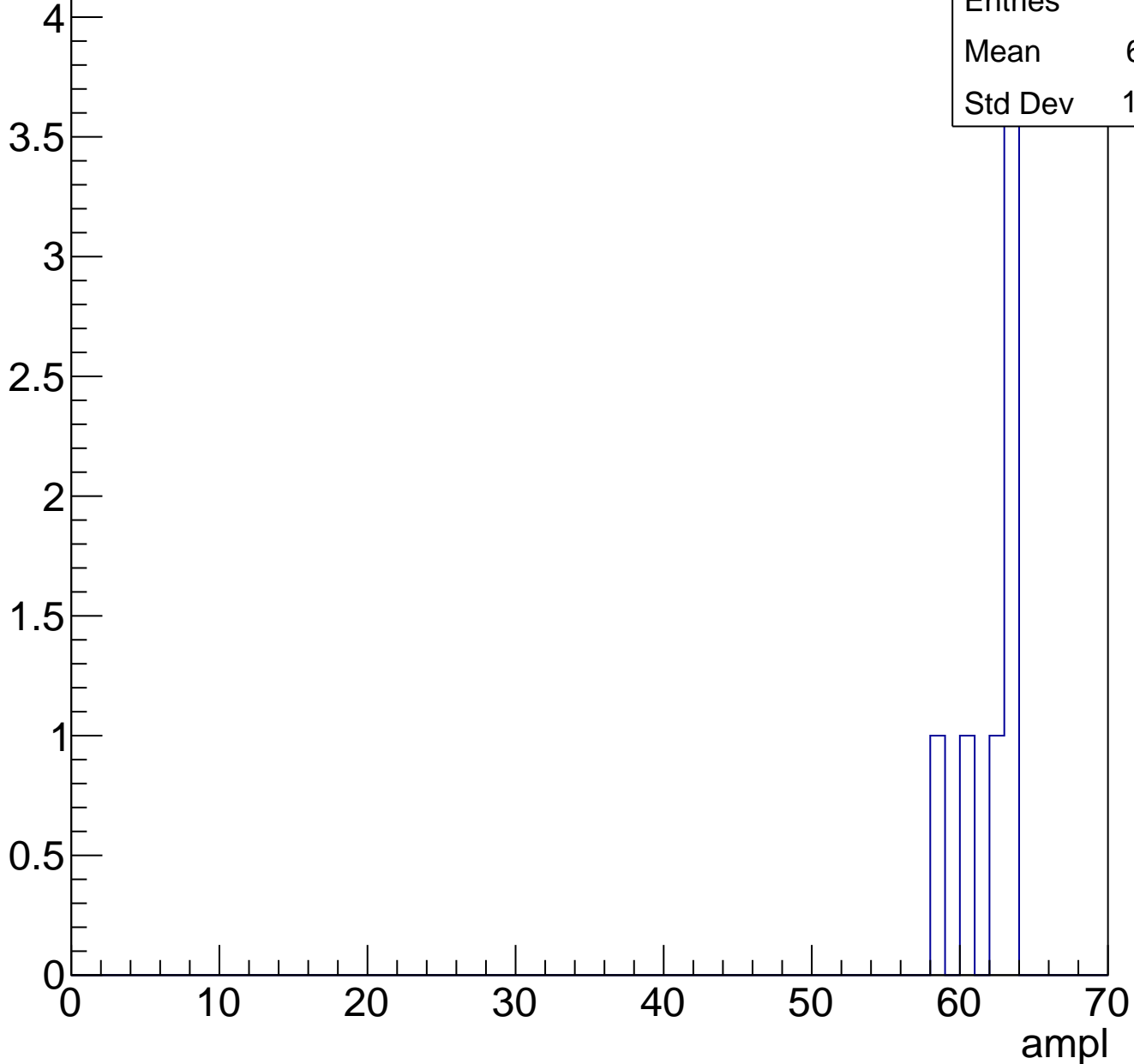
Entries	65
Mean	58.74
Std Dev	2.83



# B0L000S, U18-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch117, adc0

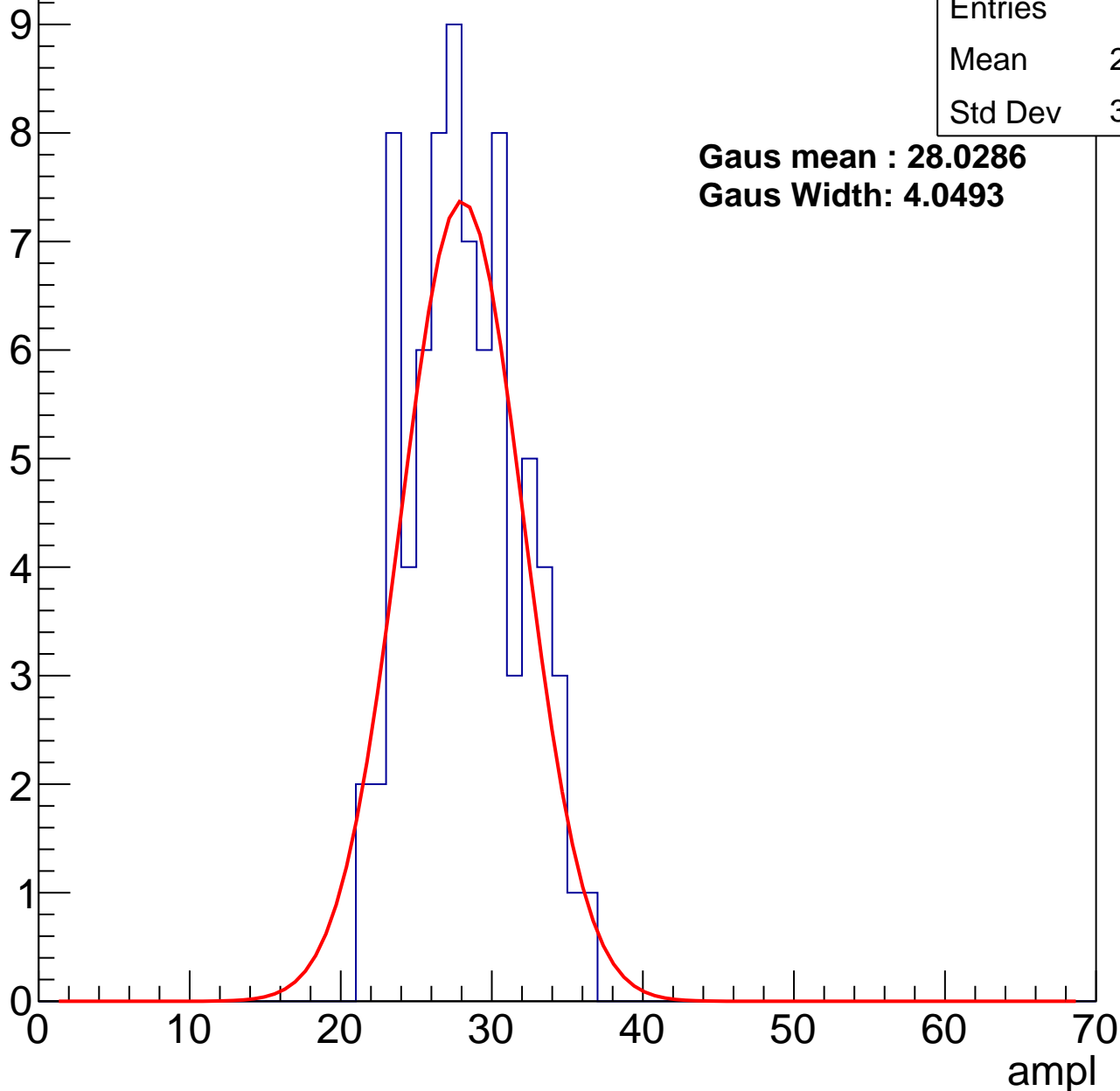
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	27.73
Std Dev	3.584

**Gaus mean : 28.0286**

**Gaus Width: 4.0493**



# B0L000S, U18-ch117, adc1

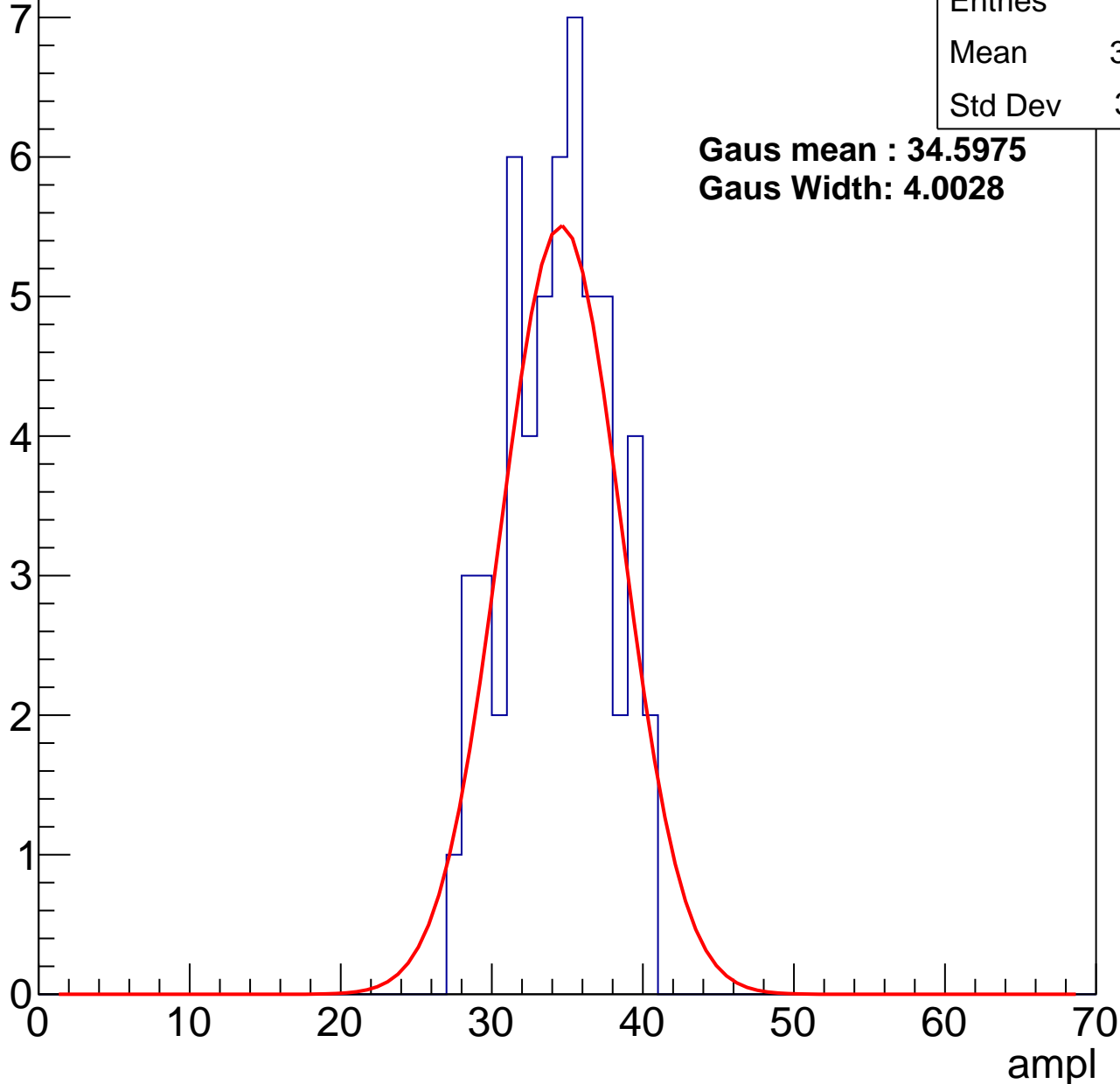
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	33.87
Std Dev	3.341

**Gaus mean : 34.5975**

**Gaus Width: 4.0028**



# B0L000S, U18-ch117, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	41.1
Std Dev	3.726

**Gaus mean : 40.9357**

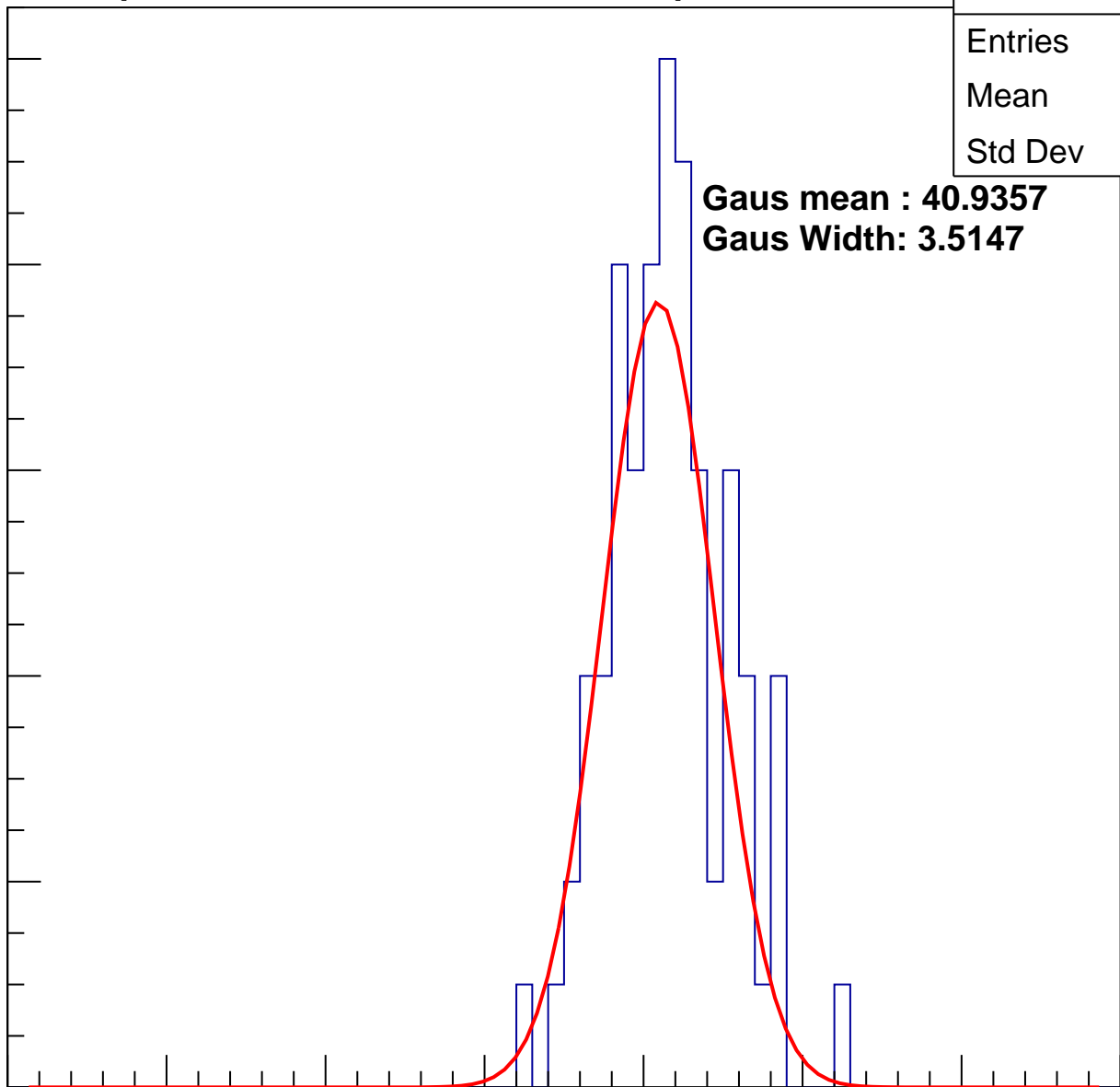
**Gaus Width: 3.5147**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U18-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

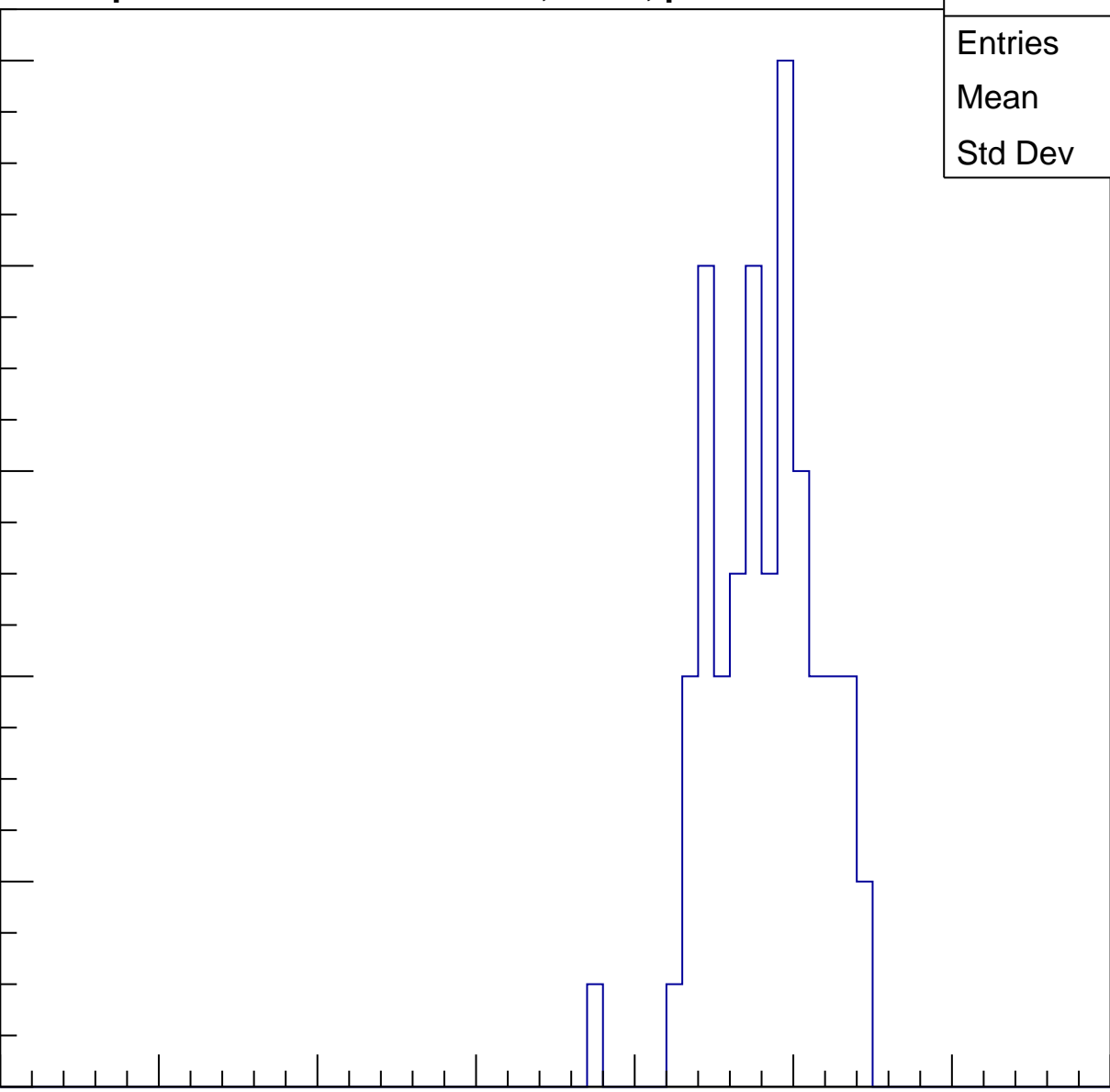
Entries	66
Mean	47.74
Std Dev	3.372

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

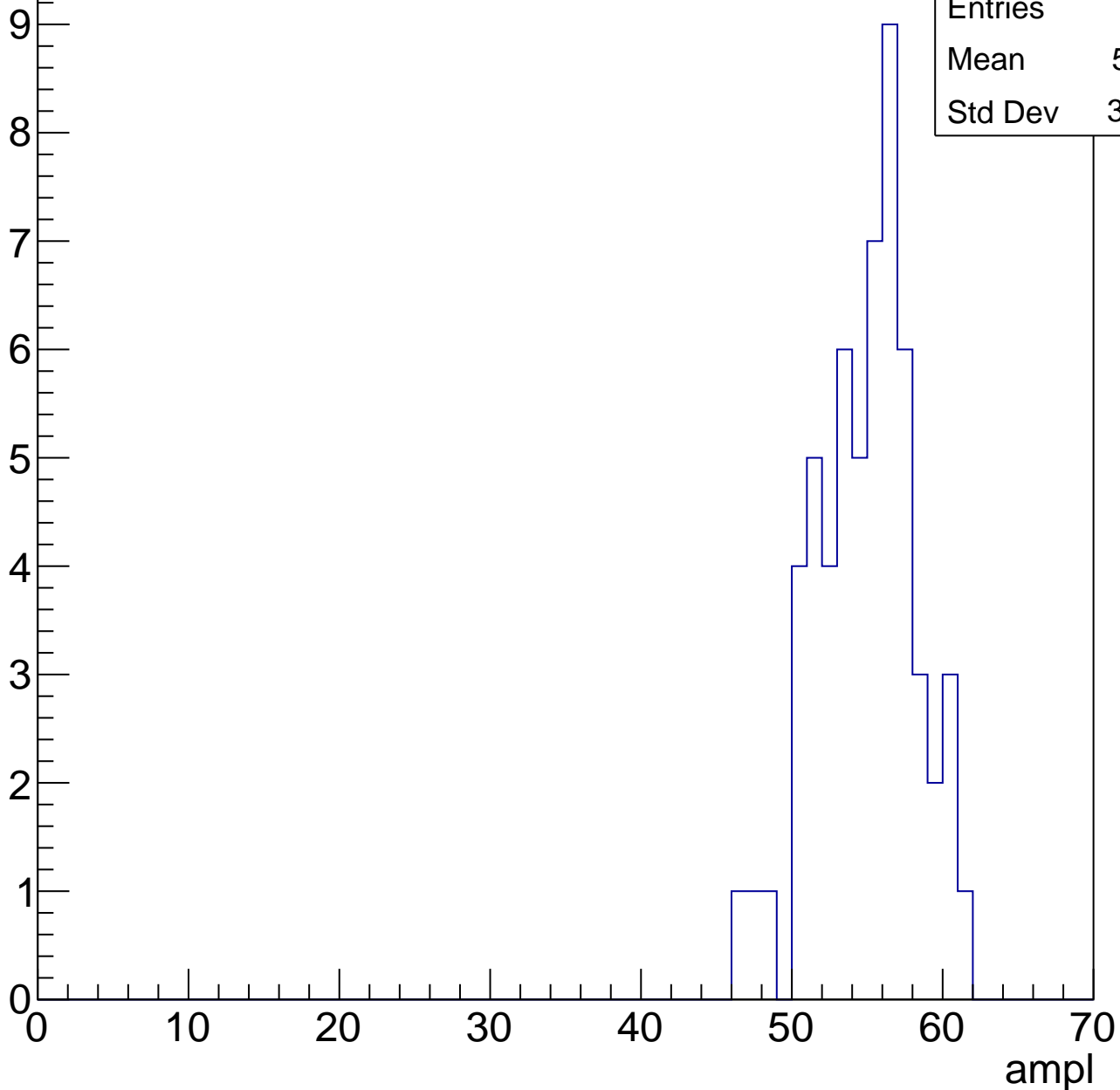


# B0L000S, U18-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

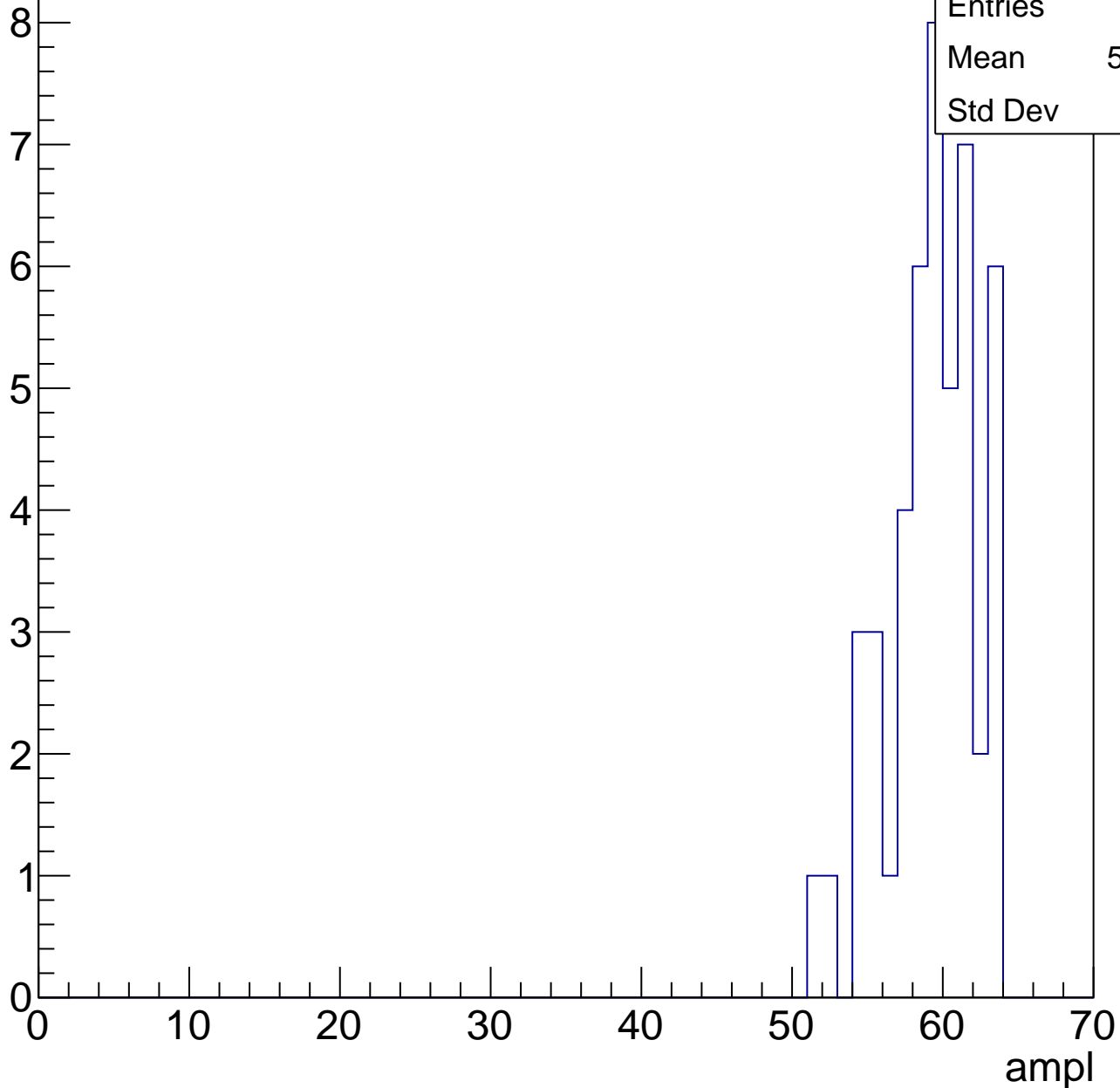
Entries	58
Mean	54.41
Std Dev	3.275



# B0L000S, U18-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

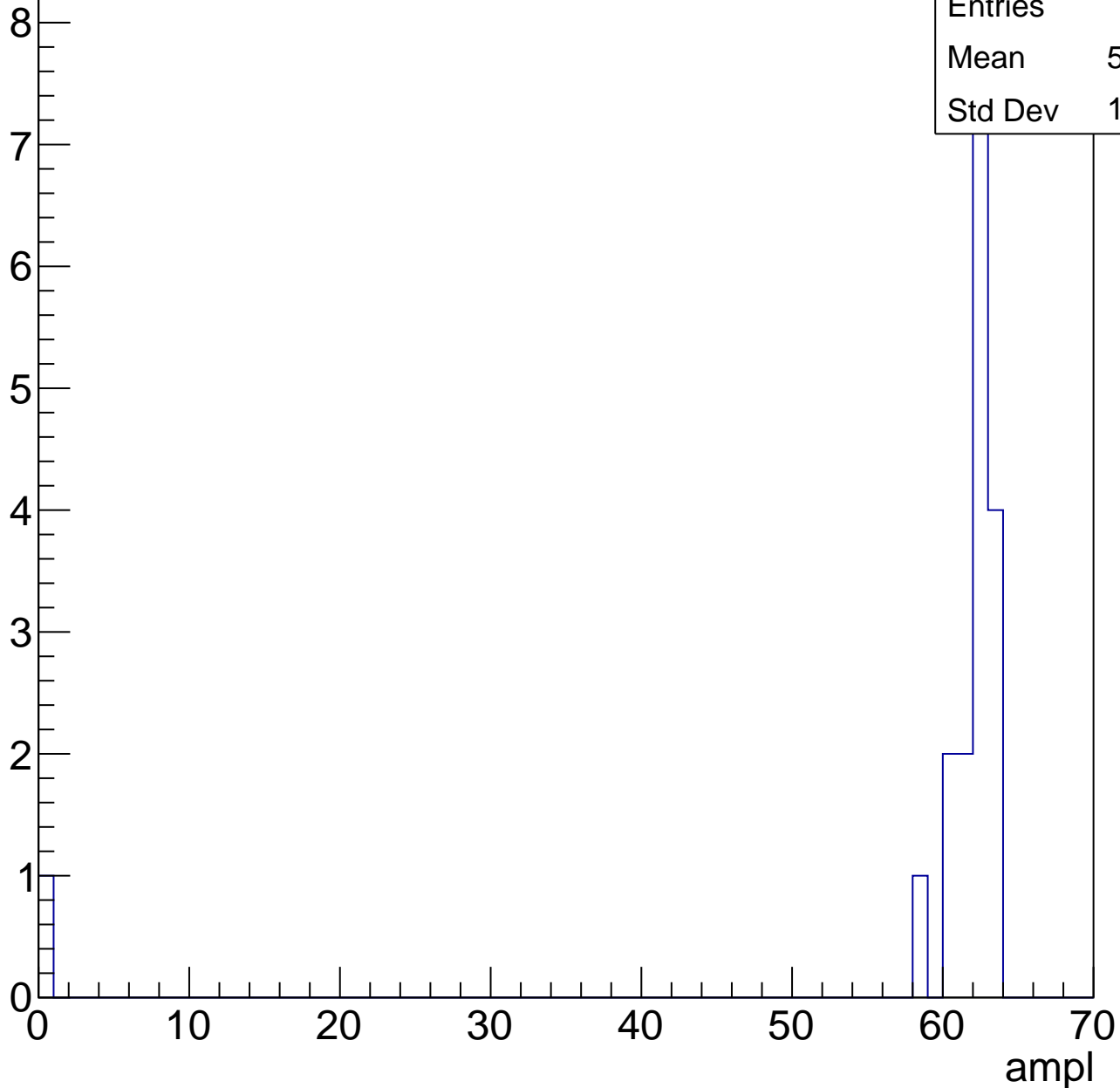


# B0L000S, U18-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	58.22
Std Dev	14.18





# B0L000S, U18-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch118, adc0

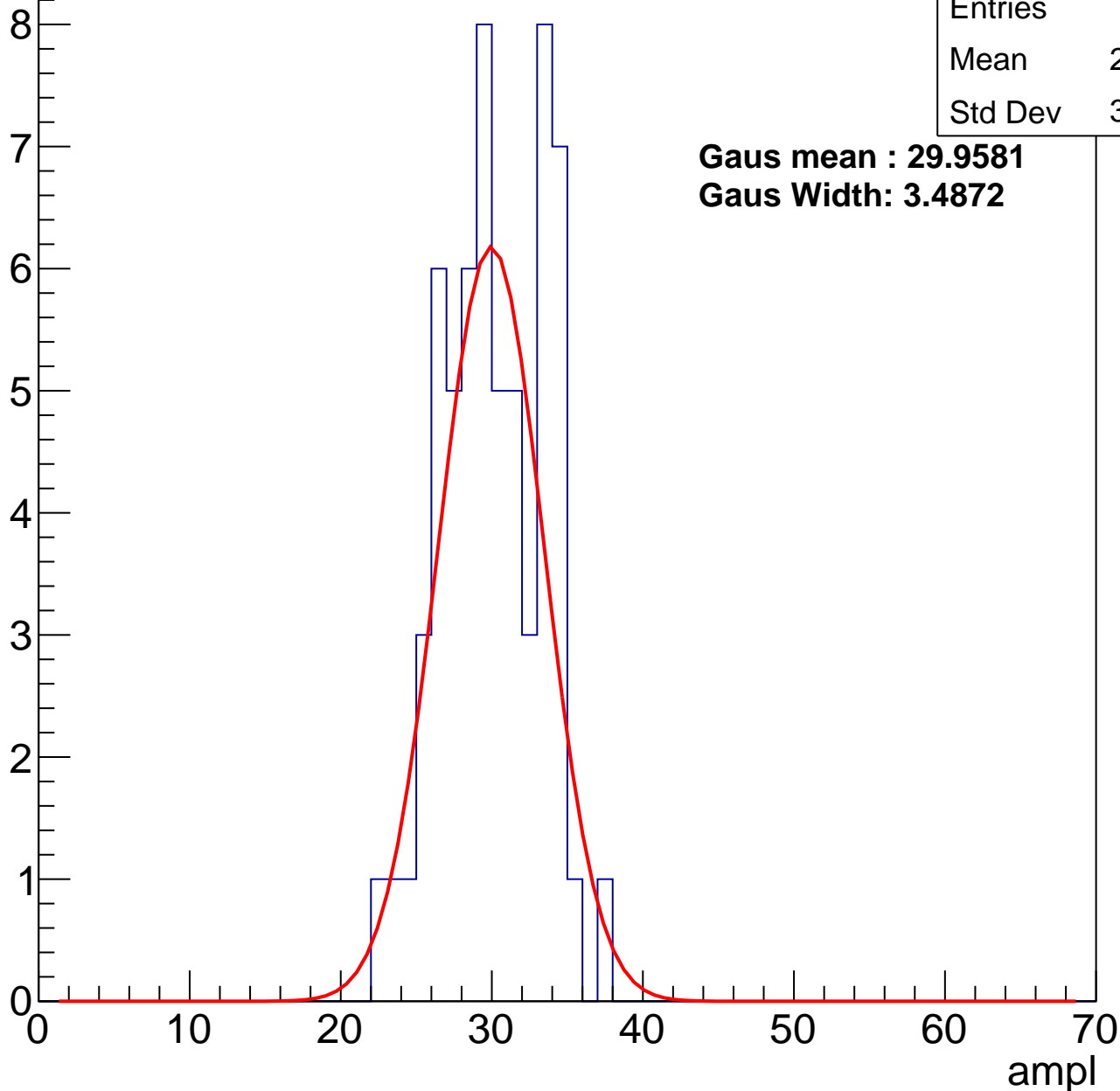
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	29.67
Std Dev	3.303

**Gaus mean : 29.9581**

**Gaus Width: 3.4872**



# B0L000S, U18-ch118, adc1

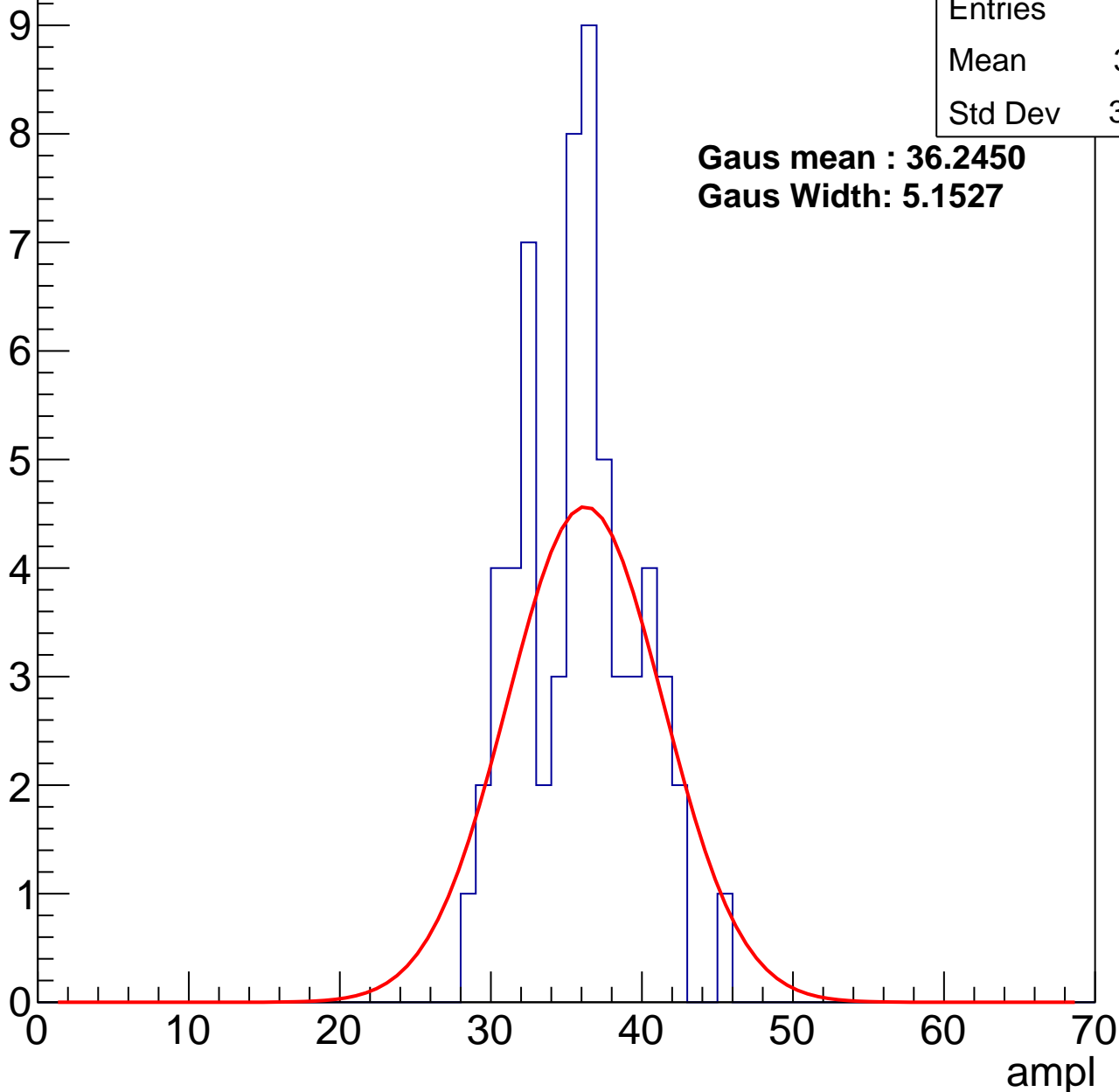
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.31
Std Dev	3.762

**Gaus mean : 36.2450**

**Gaus Width: 5.1527**



# B0L000S, U18-ch118, adc2

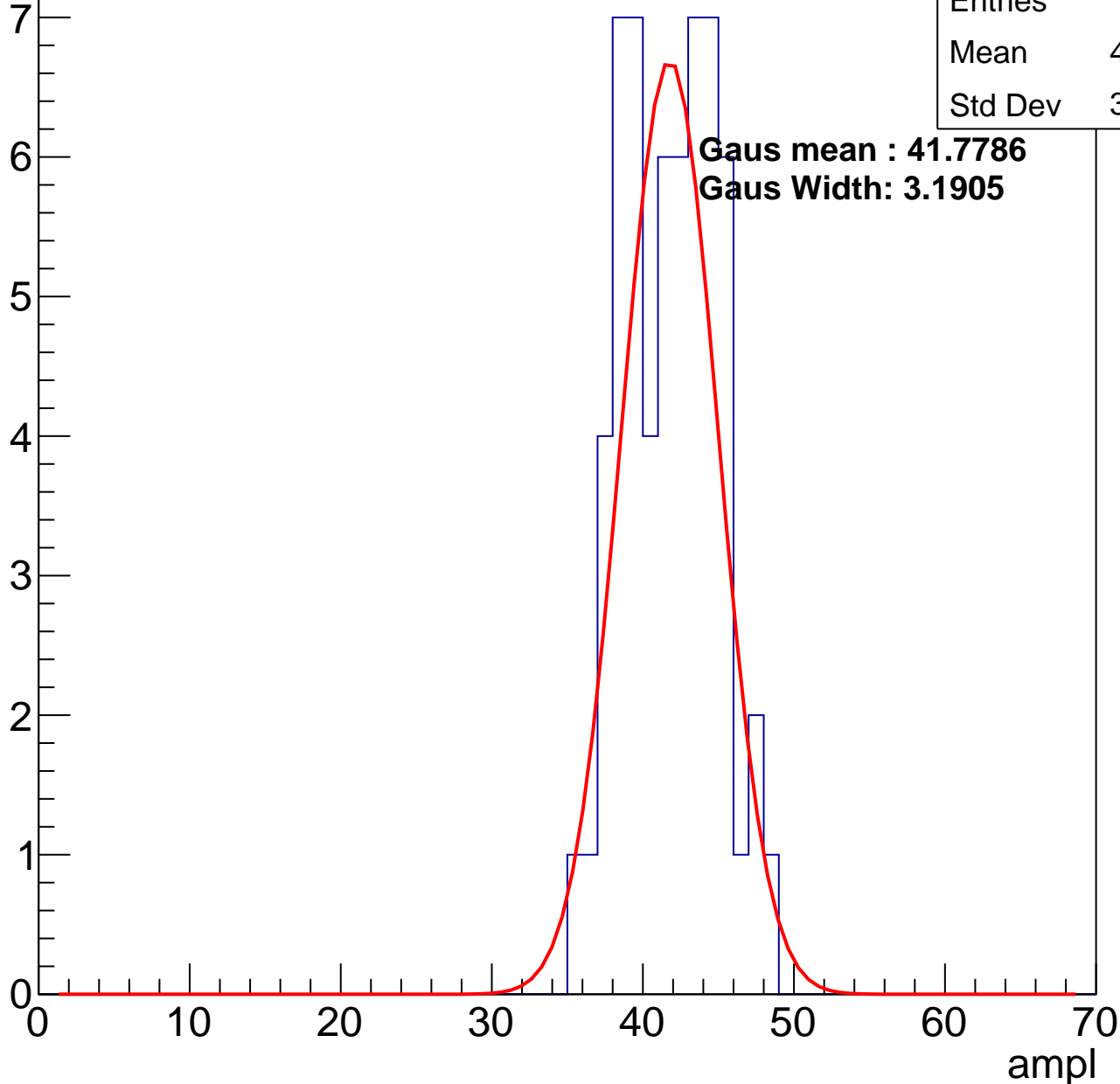
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.38
Std Dev	3.028

**Gaus mean : 41.7786**

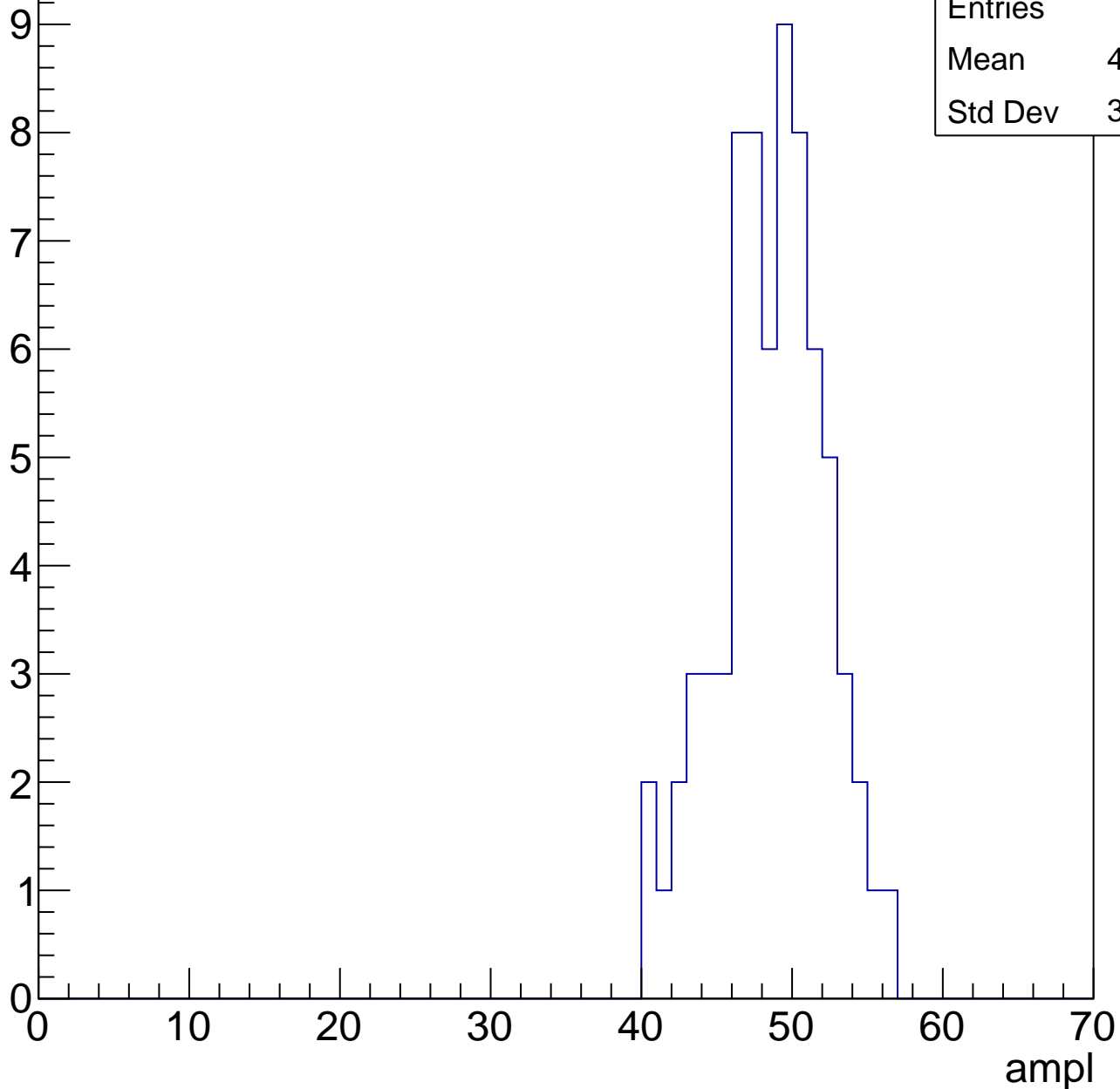
**Gaus Width: 3.1905**



# B0L000S, U18-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

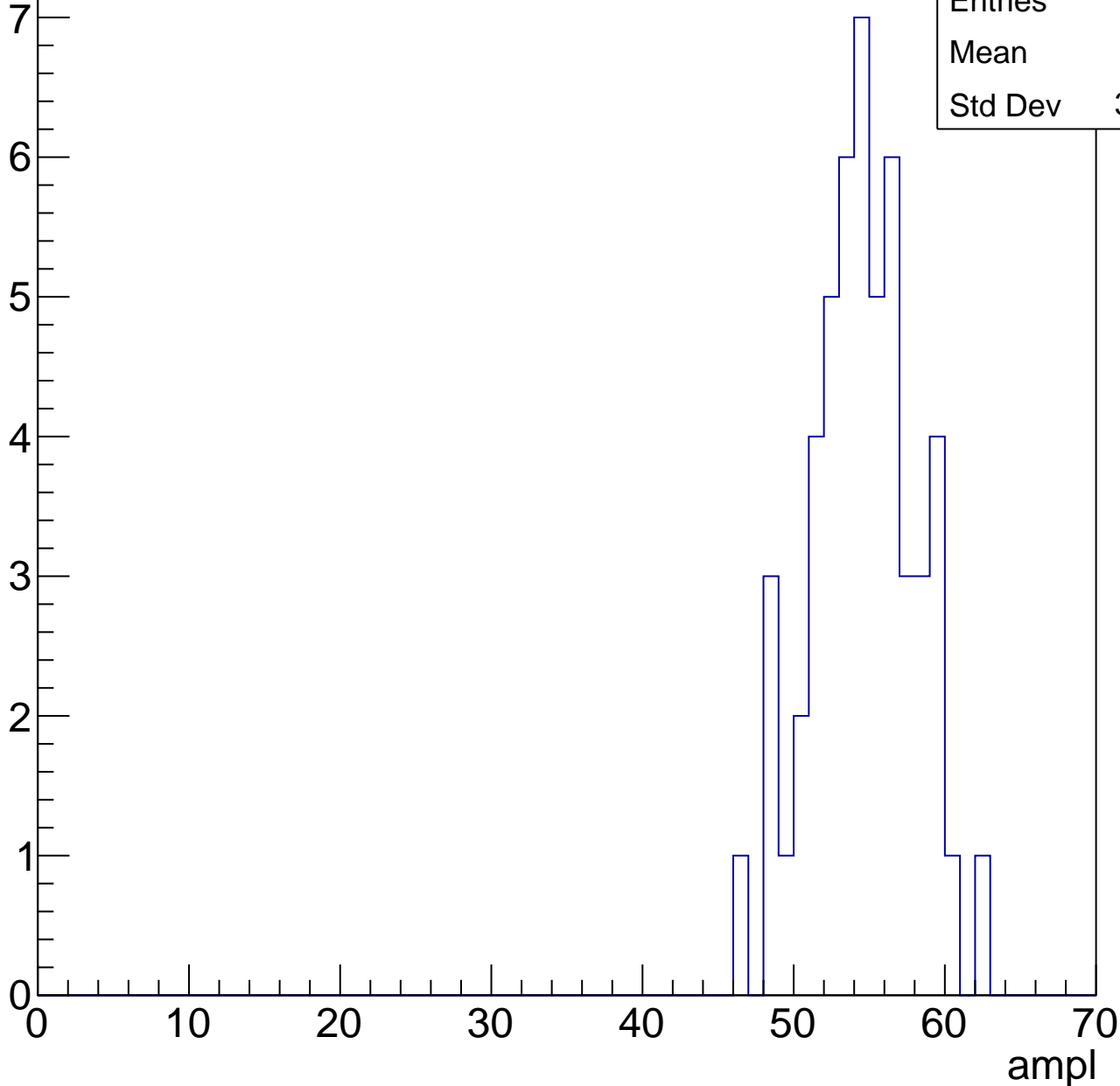


# B0L000S, U18-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	54.1
Std Dev	3.381

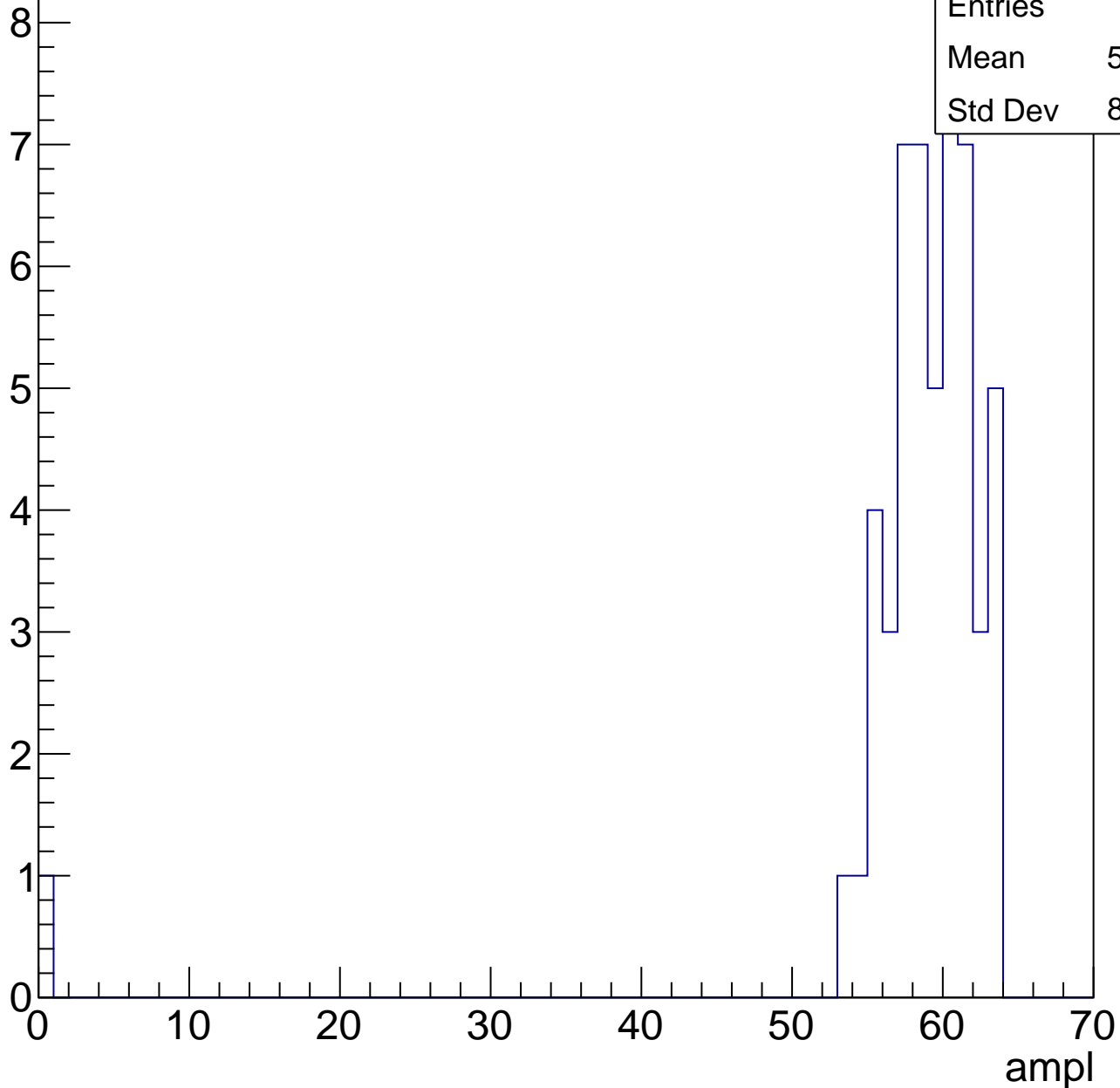


# B0L000S, U18-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	57.75
Std Dev	8.469

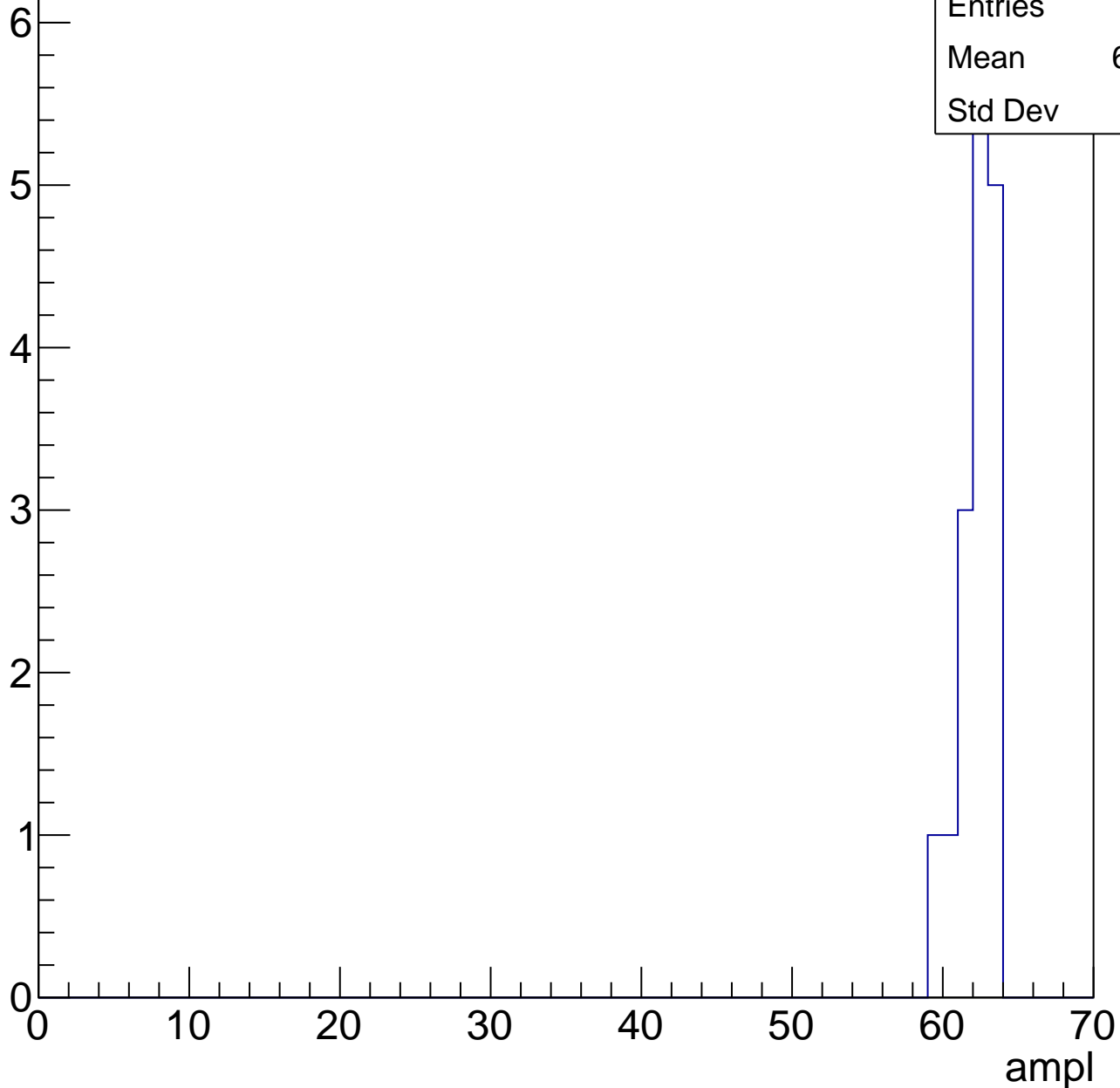


# B0L000S, U18-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	61.81
Std Dev	1.13





# B0L000S, U18-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch119, adc0

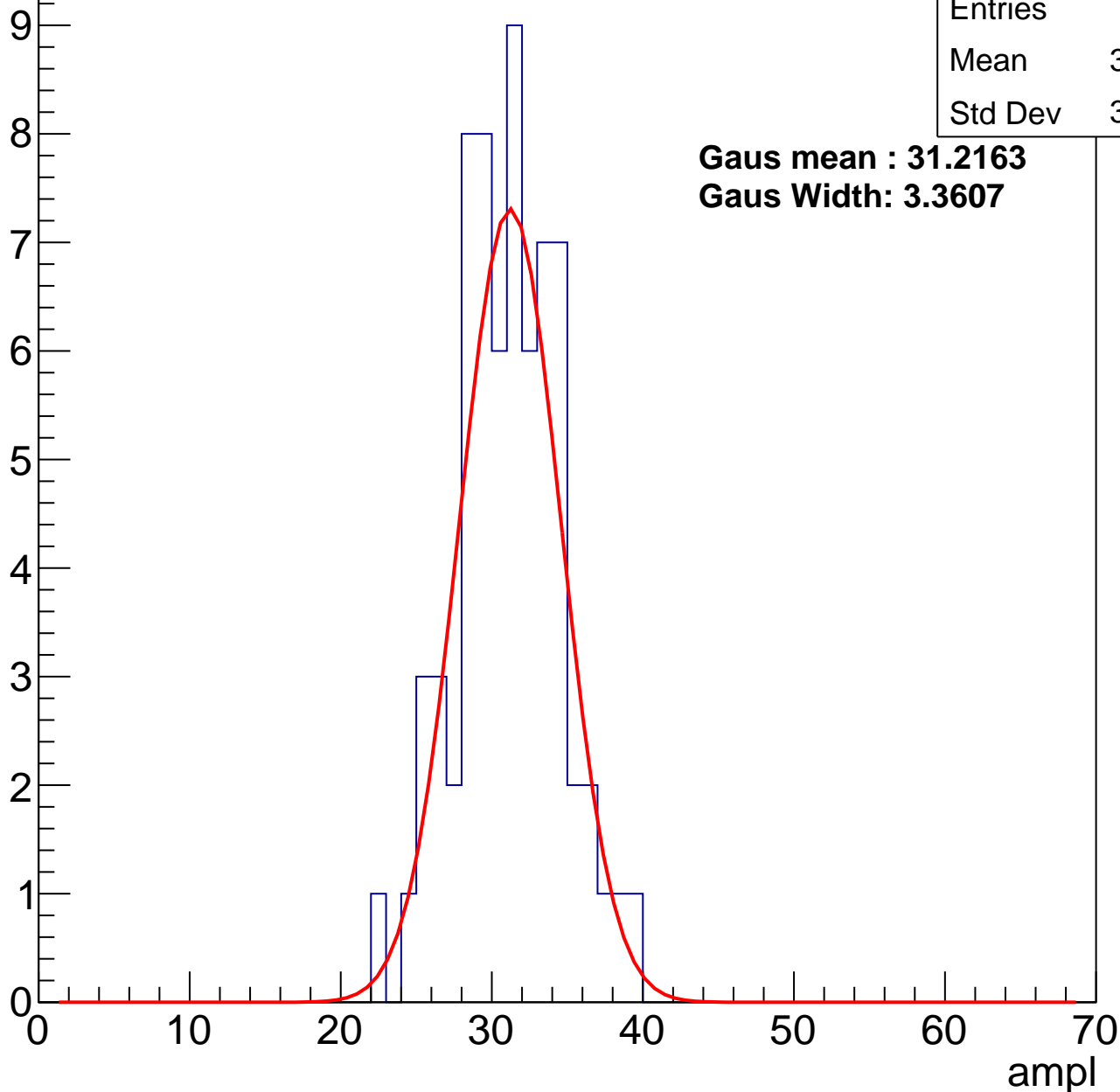
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.66
Std Dev	3.376

**Gaus mean : 31.2163**

**Gaus Width: 3.3607**



# B0L000S, U18-ch119, adc1

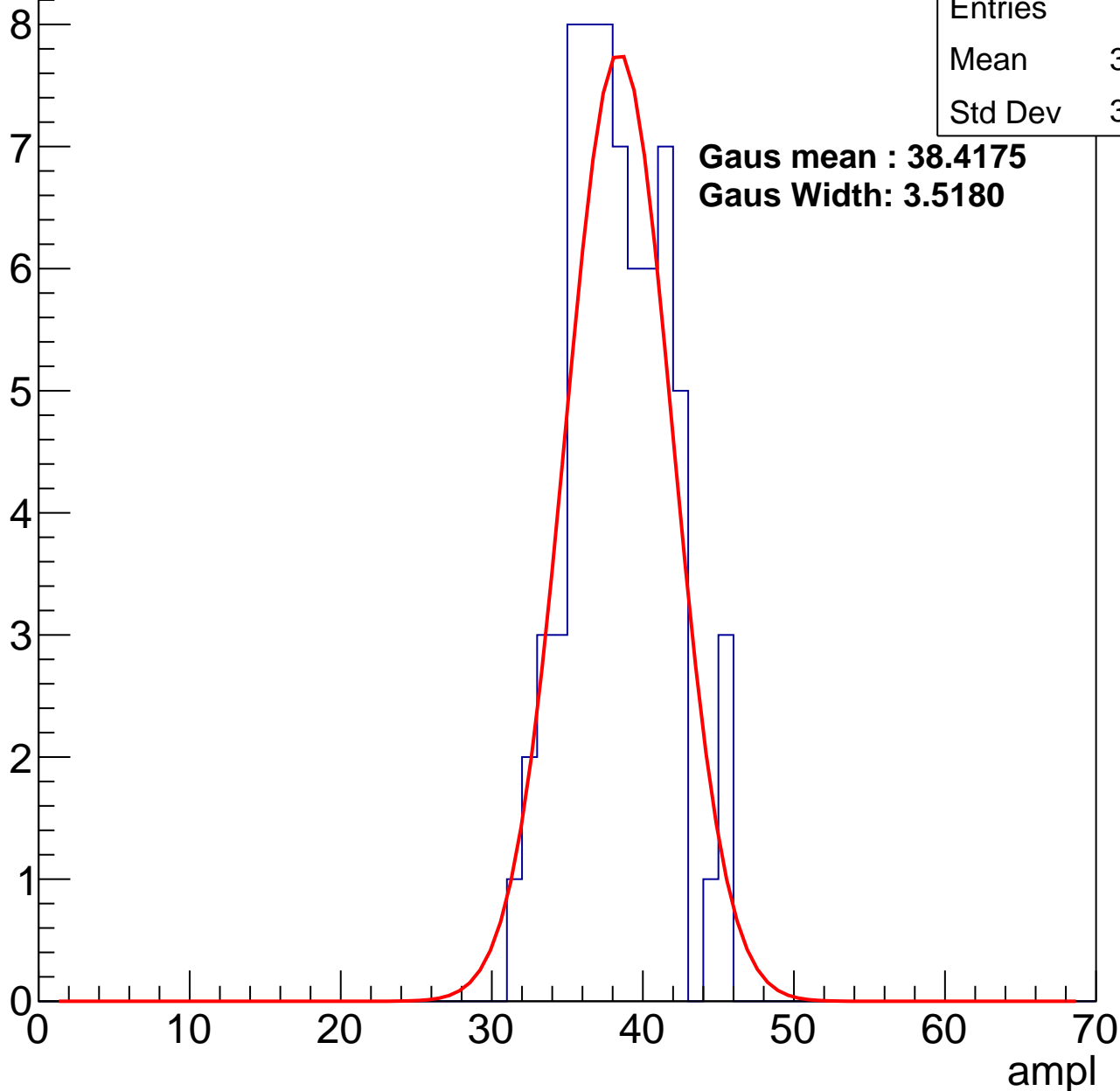
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	37.88
Std Dev	3.234

**Gaus mean : 38.4175**

**Gaus Width: 3.5180**



# B0L000S, U18-ch119, adc2

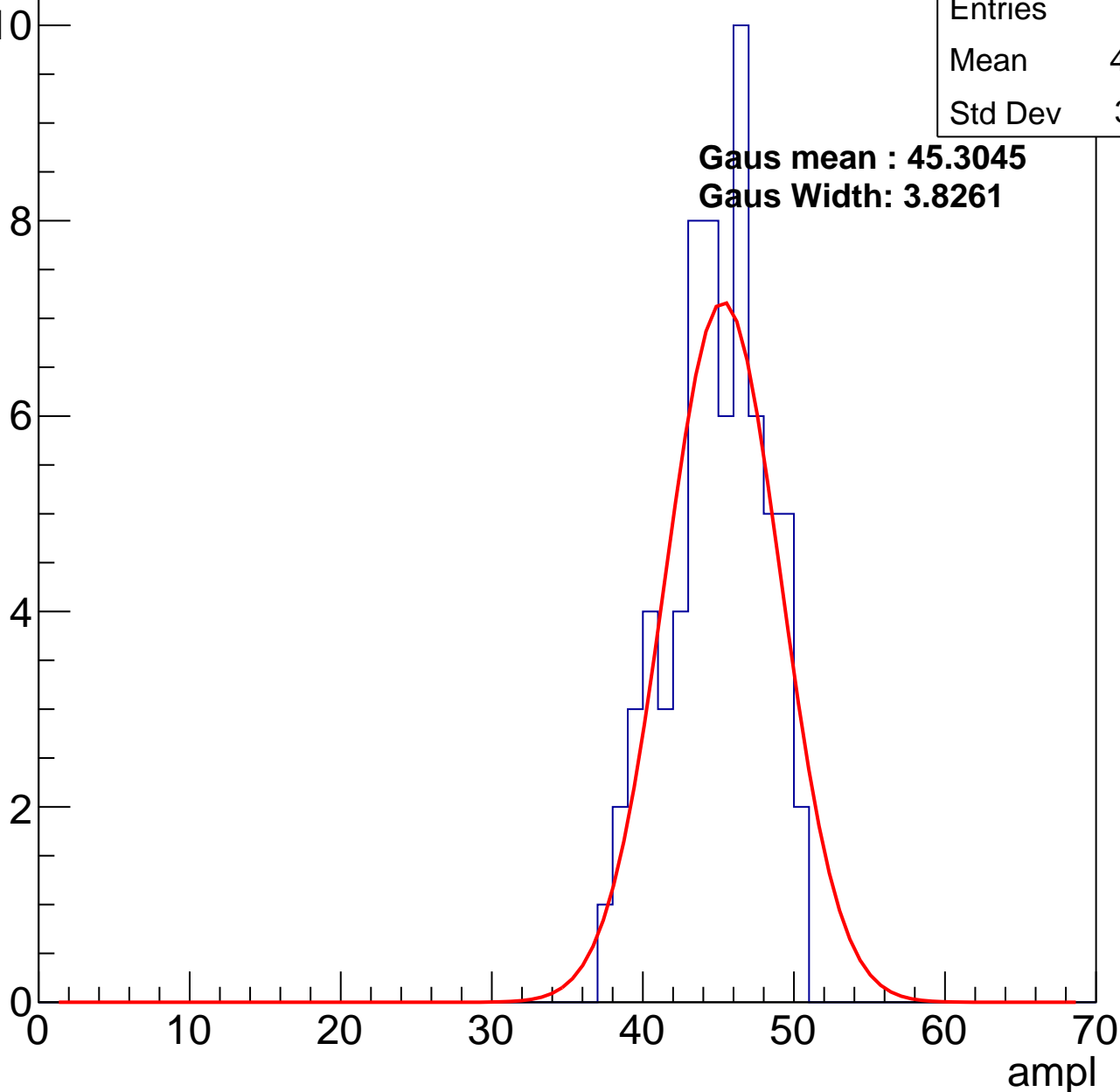
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	44.39
Std Dev	3.181

**Gaus mean : 45.3045**

**Gaus Width: 3.8261**



# B0L000S, U18-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

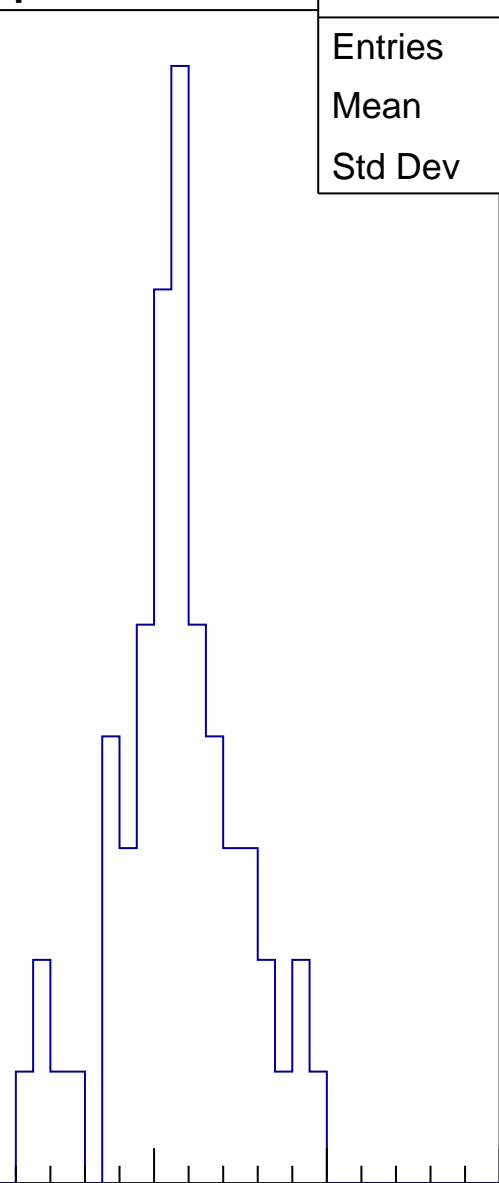
Entries	56
Mean	50.84
Std Dev	3.673

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

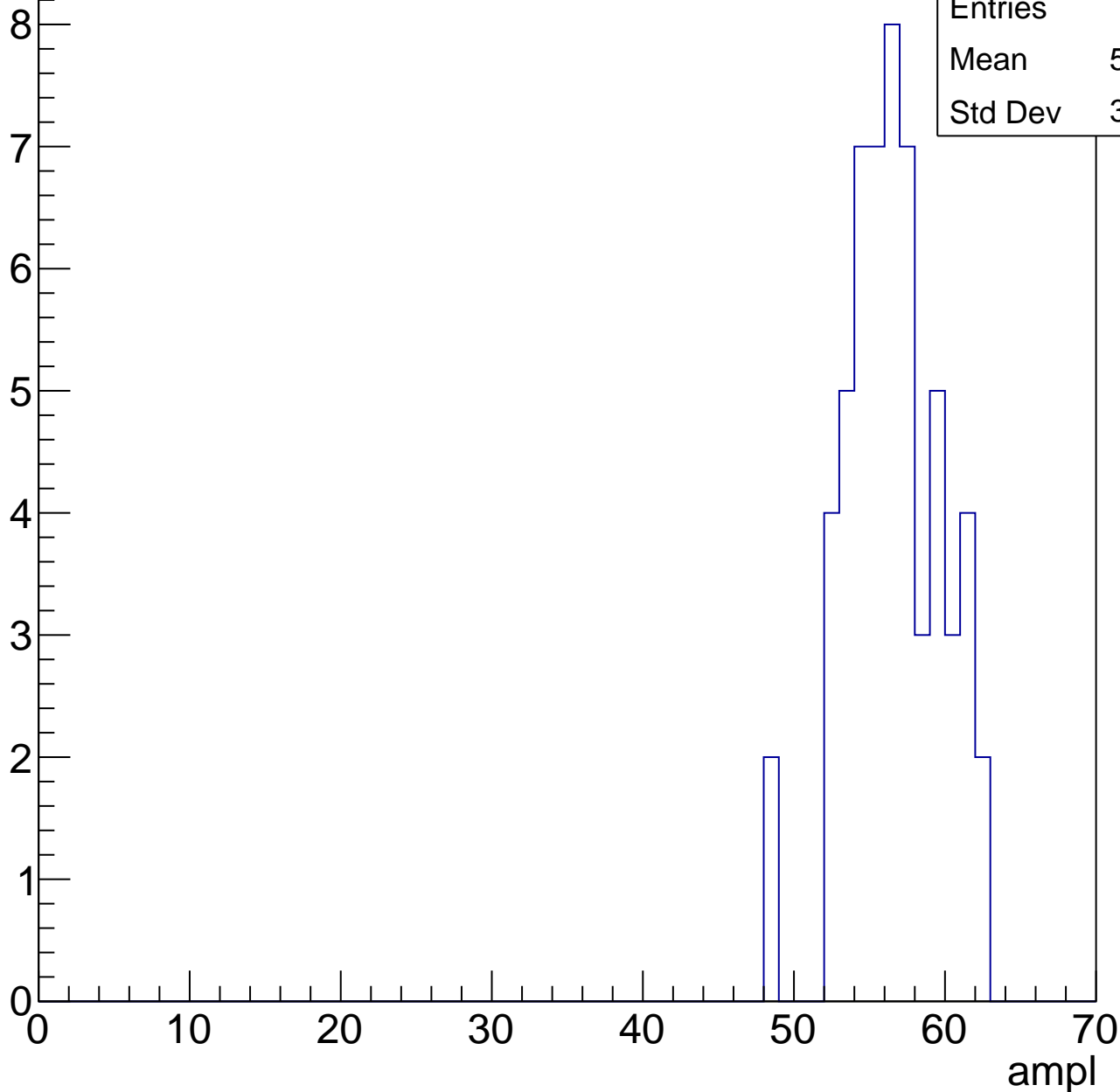


# B0L000S, U18-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.07
Std Dev	3.122

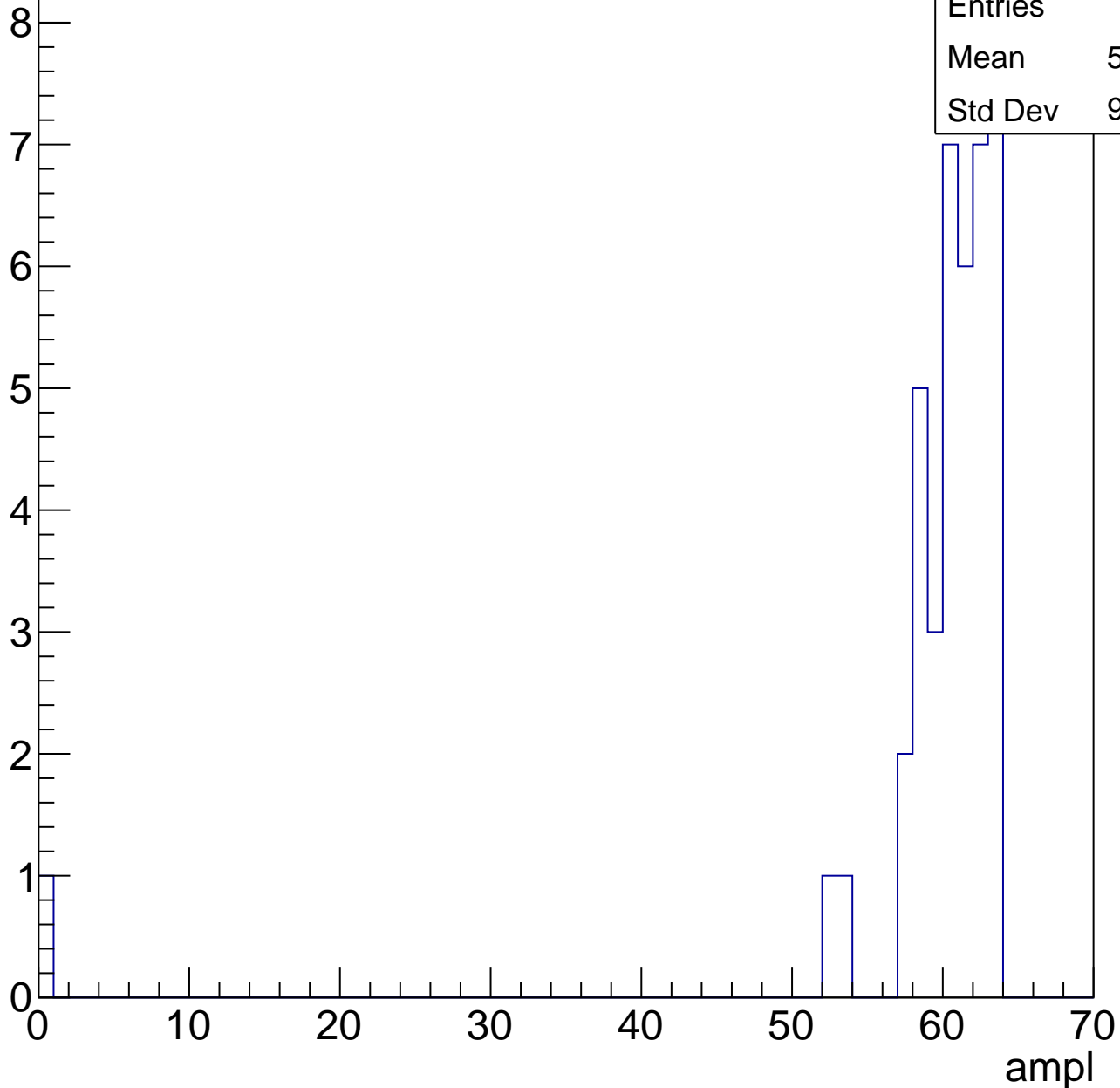


# B0L000S, U18-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.78
Std Dev	9.626



# B0L000S, U18-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U18-ch120, adc0

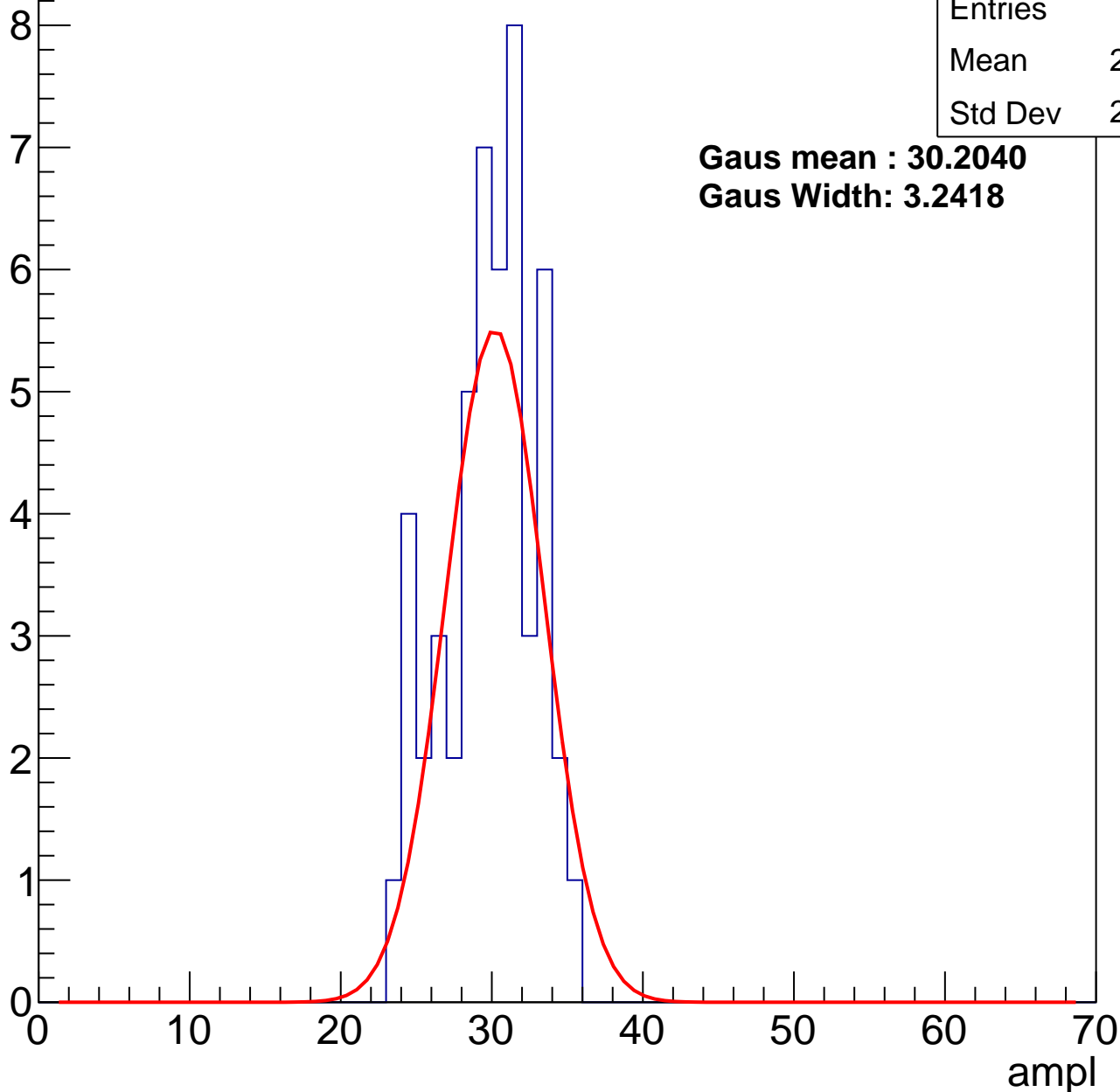
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	29.38
Std Dev	2.993

**Gaus mean : 30.2040**

**Gaus Width: 3.2418**



# B0L000S, U18-ch120, adc1

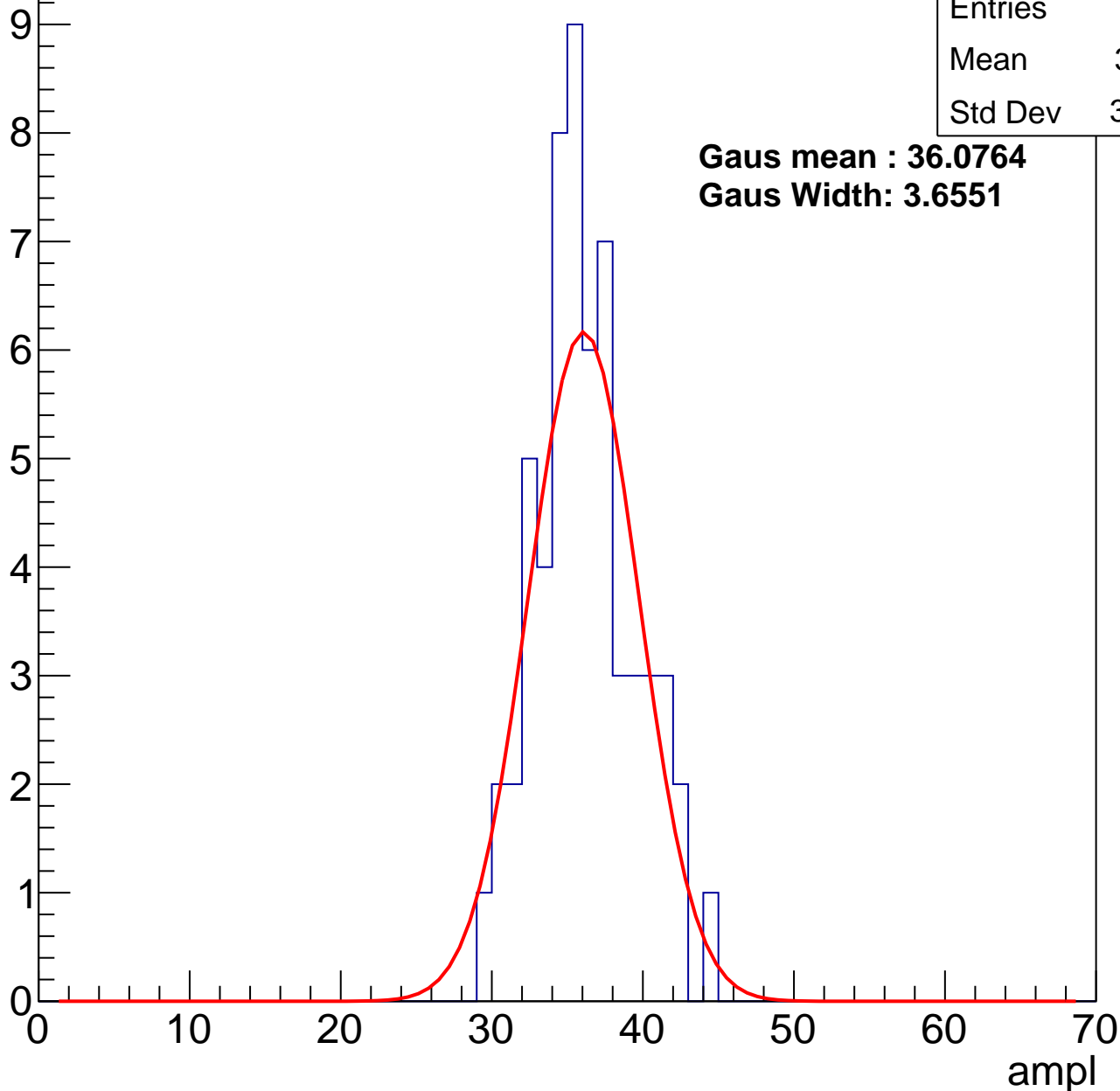
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	35.71
Std Dev	3.263

**Gaus mean : 36.0764**

**Gaus Width: 3.6551**



# B0L000S, U18-ch120, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	42.14
Std Dev	3.284

**Gaus mean : 42.8295**

**Gaus Width: 4.4977**

Entry

10

8

6

4

2

0

0

10

20

30

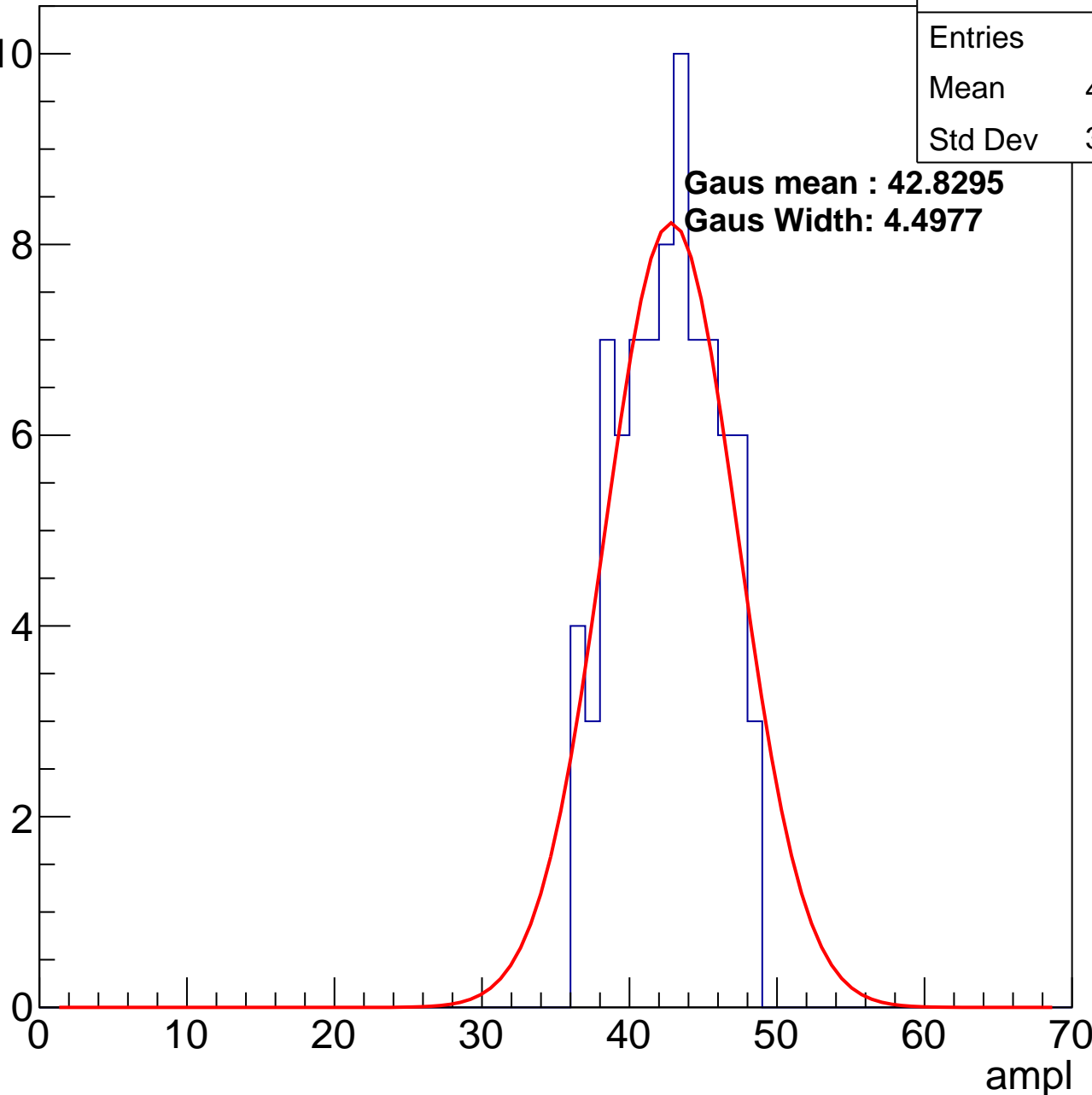
40

50

60

70

ampl

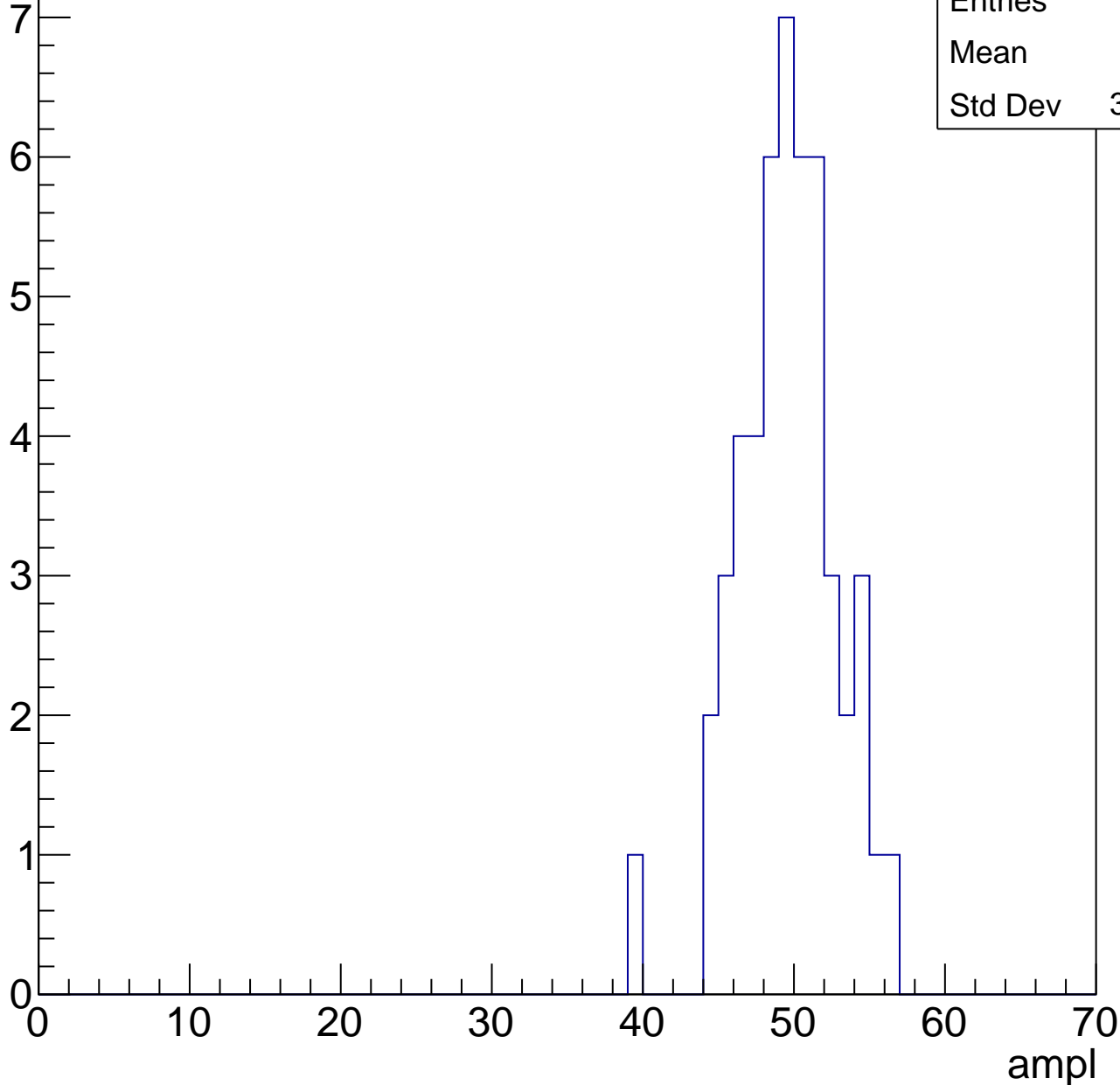


# B0L000S, U18-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	49.1
Std Dev	3.209

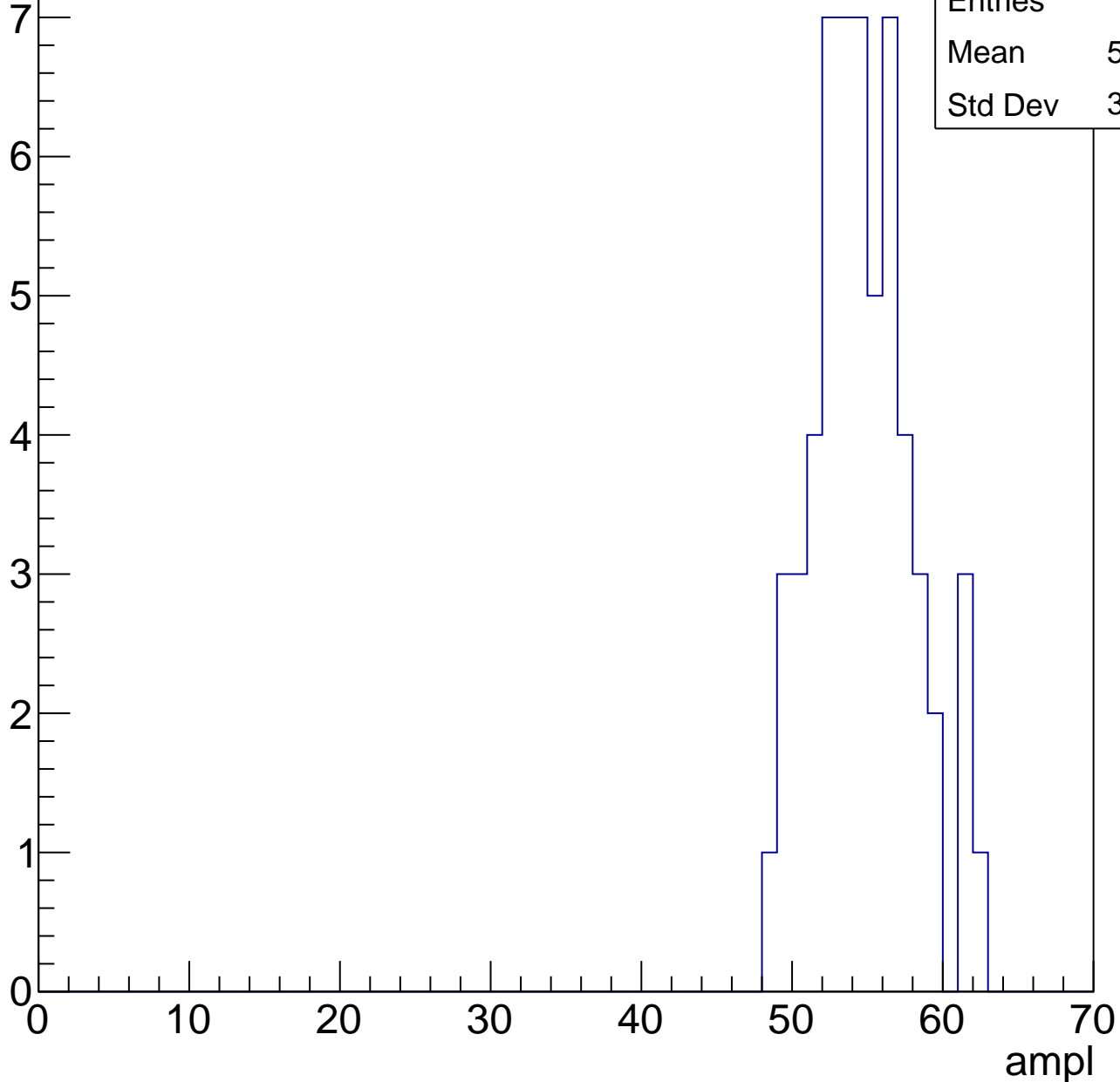


# B0L000S, U18-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	54.28
Std Dev	3.254

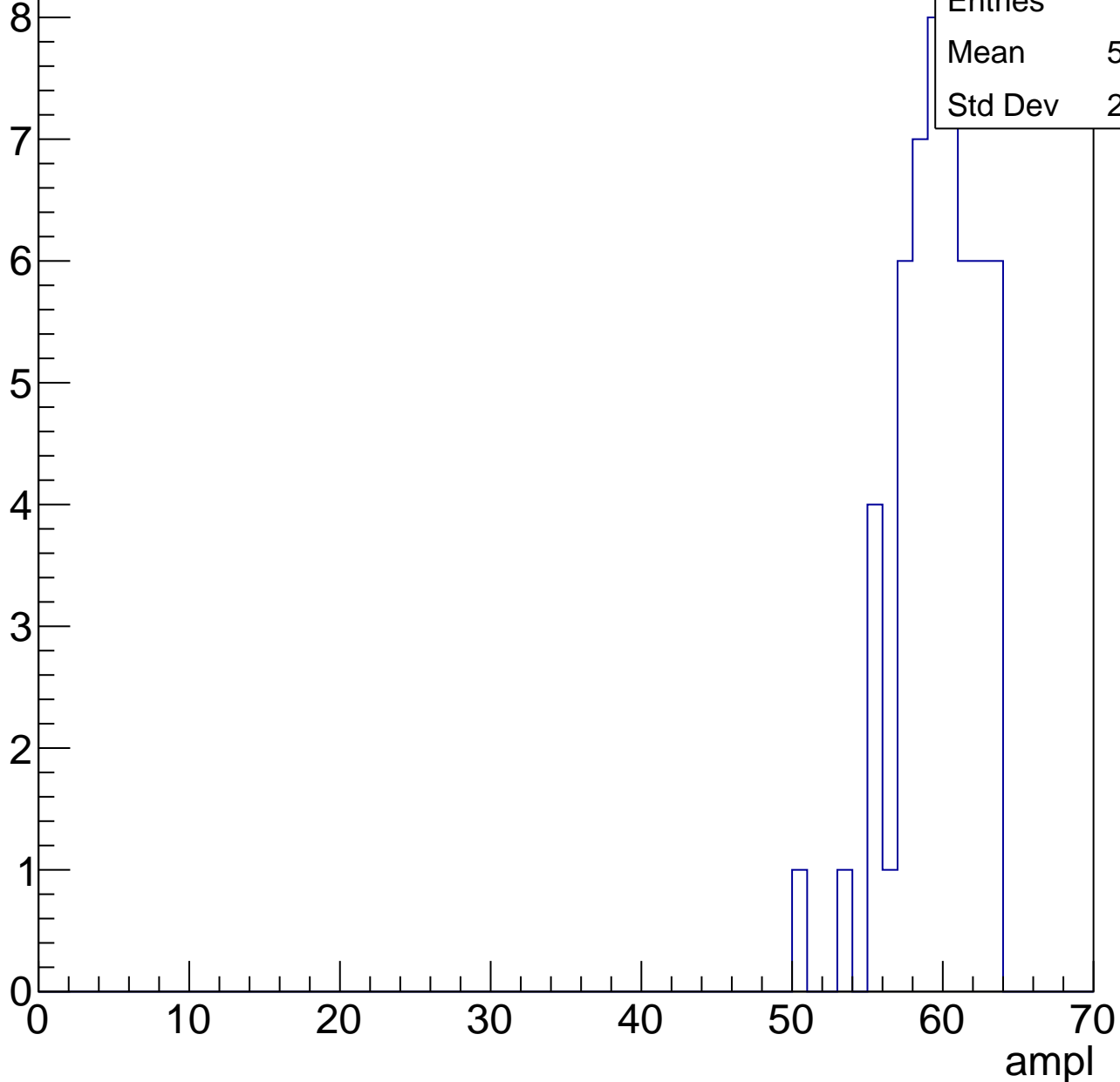


# B0L000S, U18-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

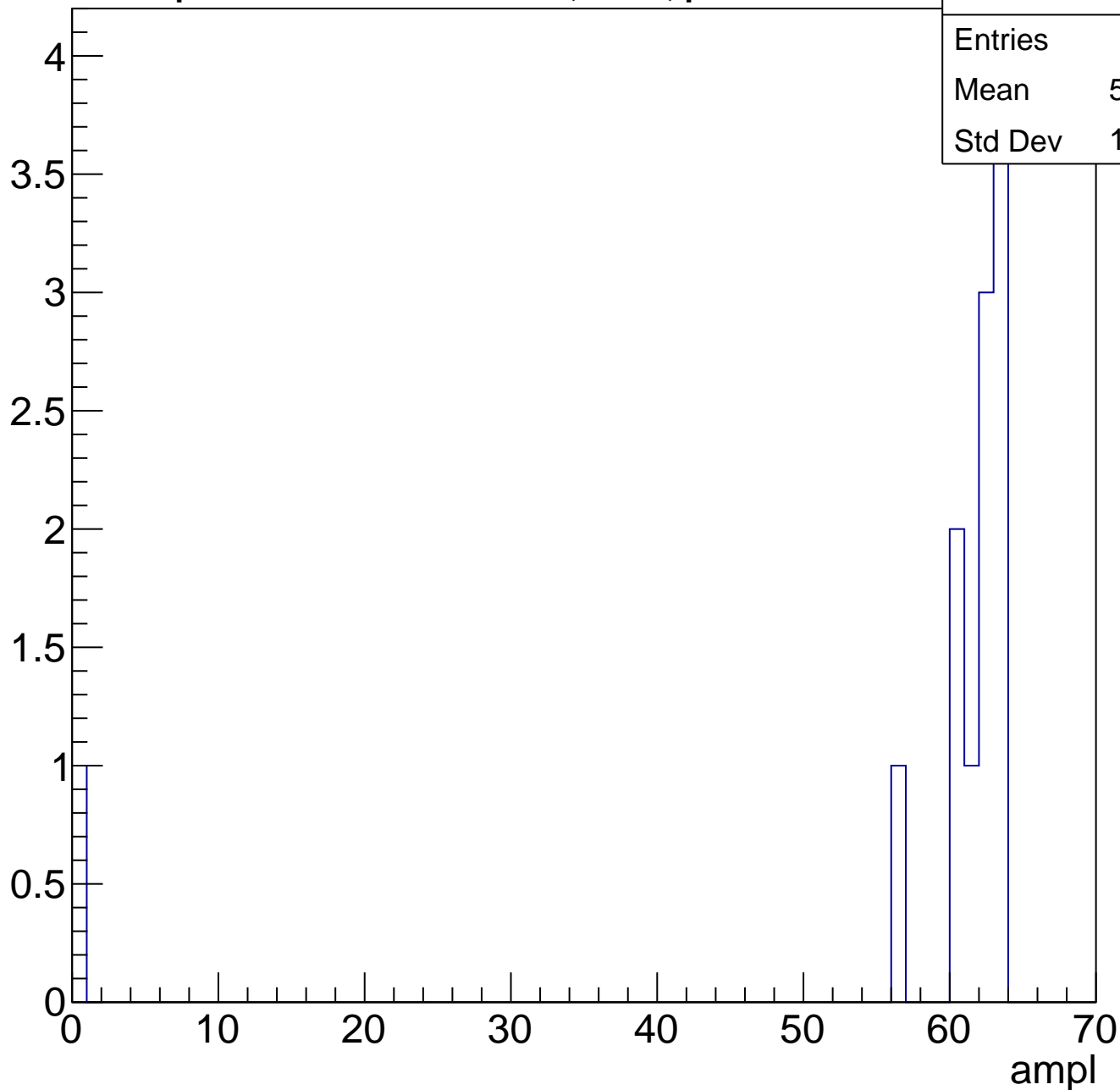
Entries	54
Mean	59.17
Std Dev	2.727



# B0L000S, U18-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch121, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	30.14
Std Dev	4.822

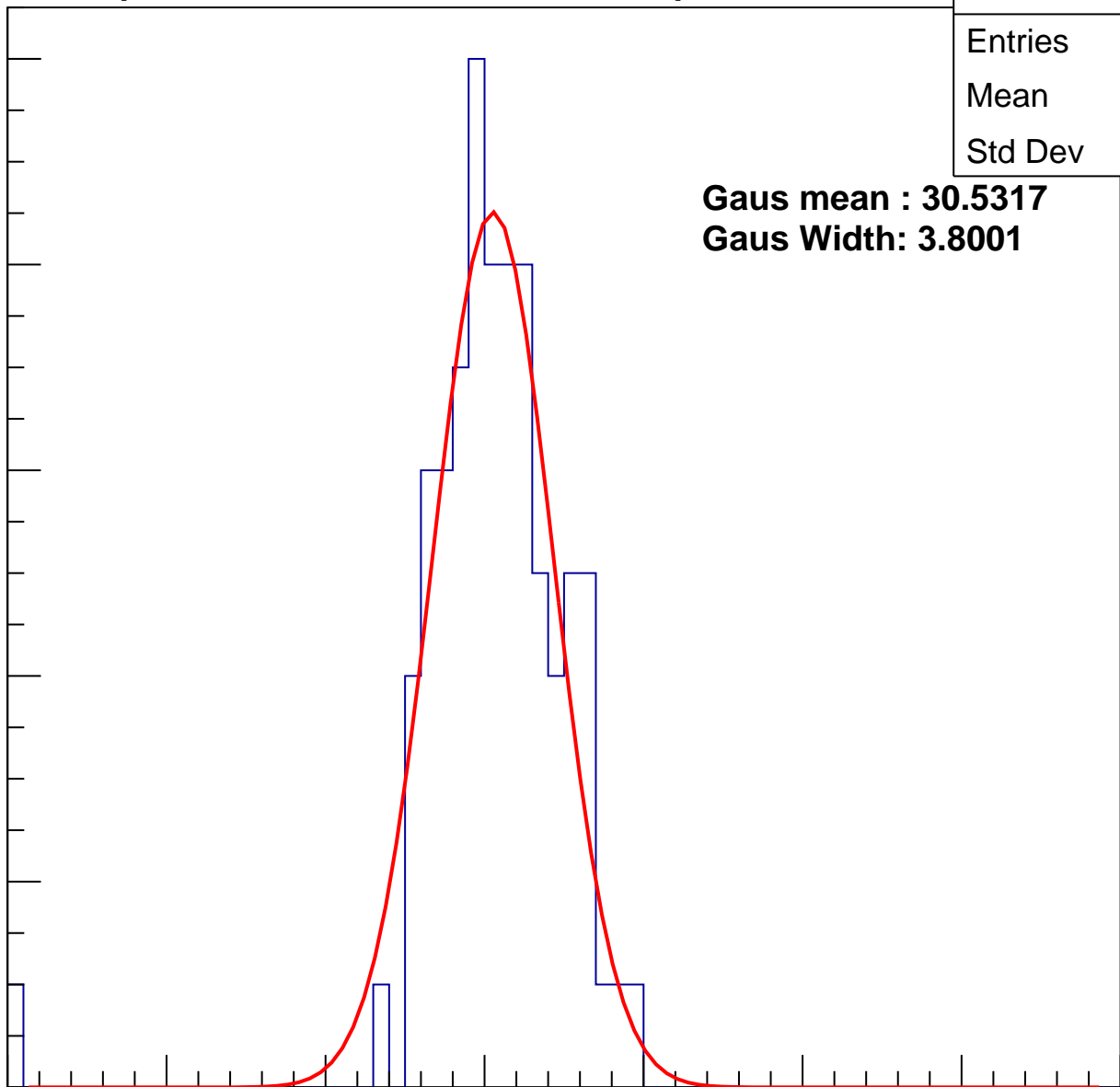
**Gaus mean : 30.5317**  
**Gaus Width: 3.8001**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch121, adc1

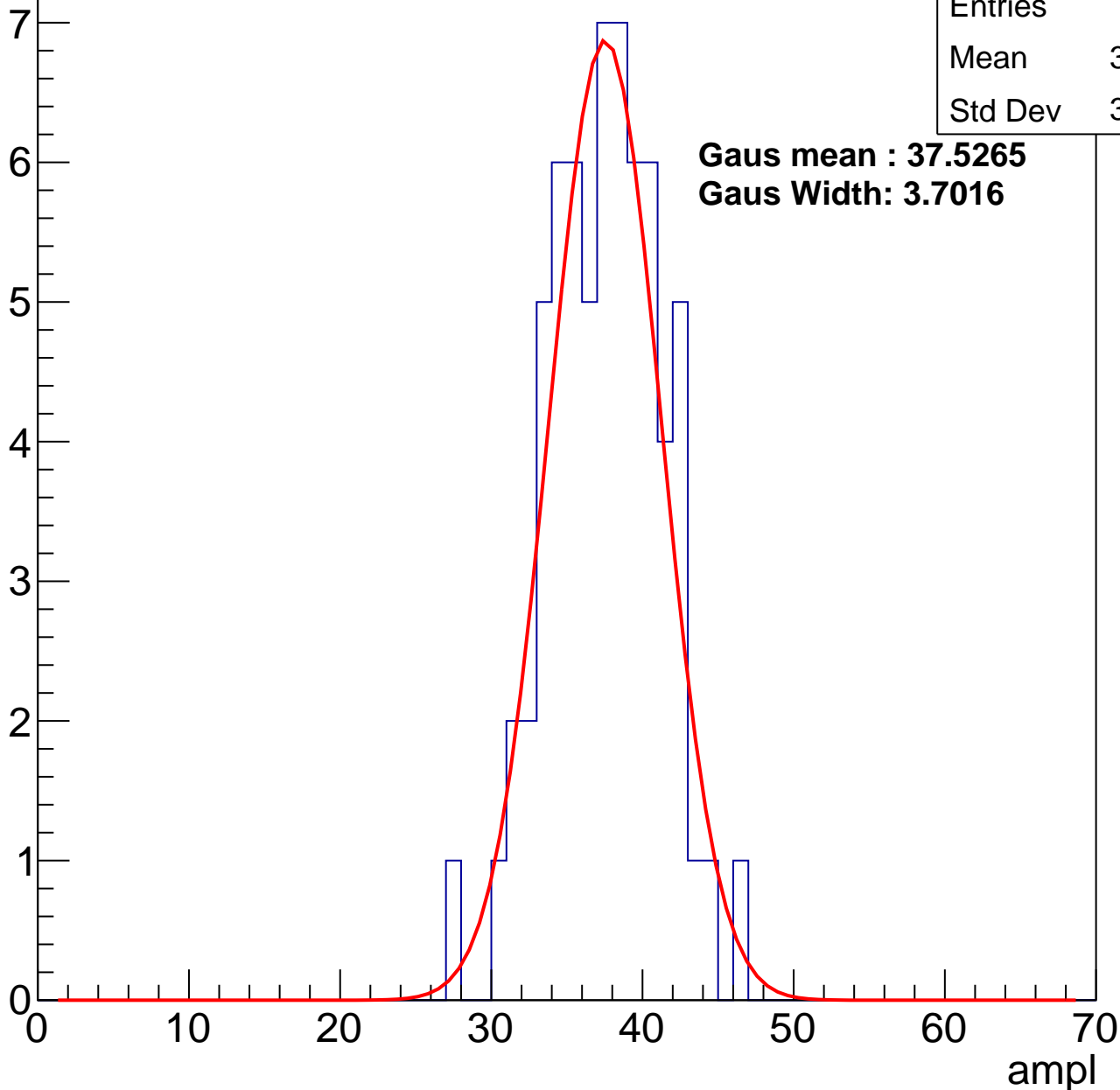
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.09
Std Dev	3.638

**Gaus mean : 37.5265**

**Gaus Width: 3.7016**



# B0L000S, U18-ch121, adc2

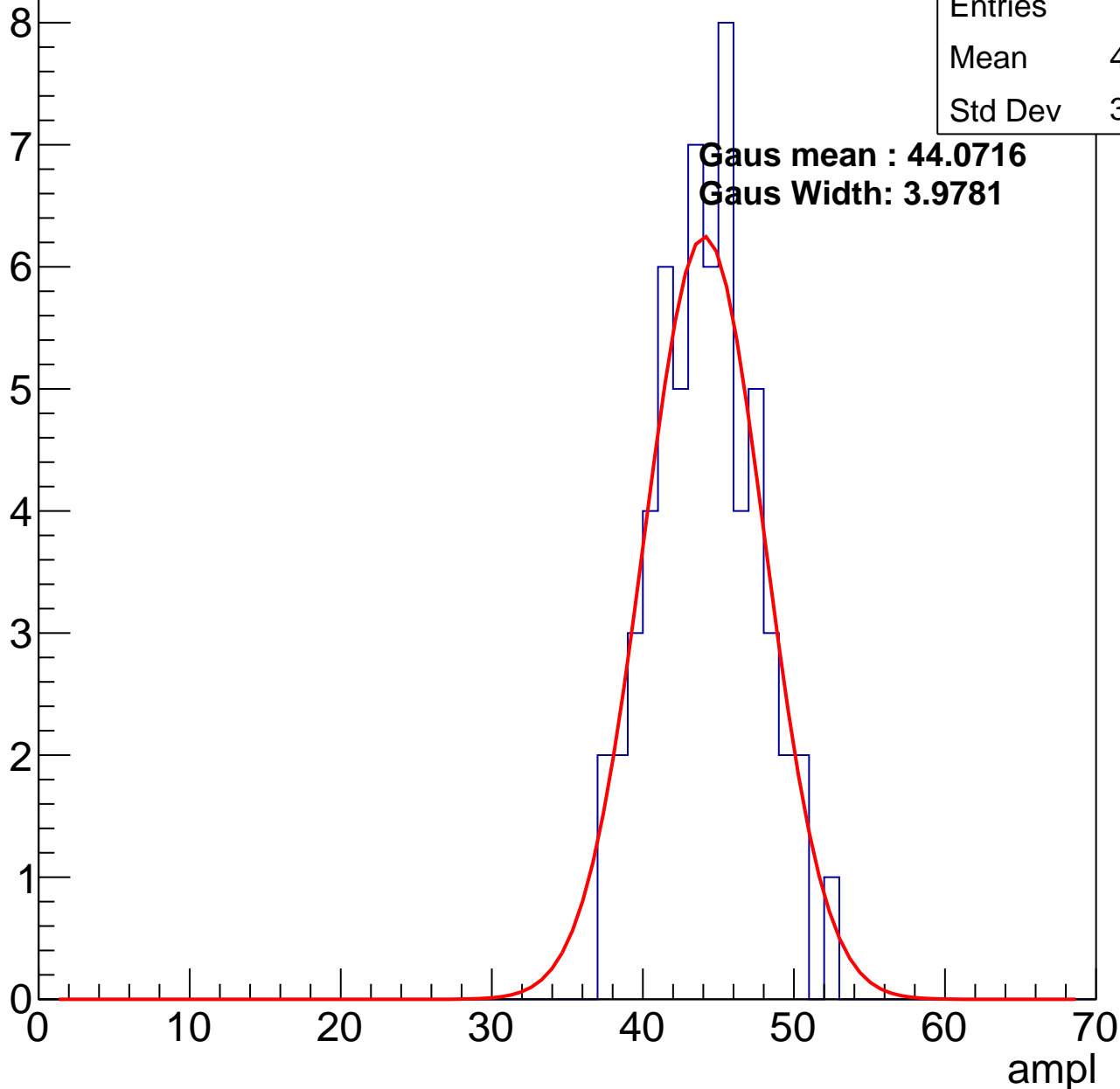
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.68
Std Dev	3.384

**Gaus mean : 44.0716**

**Gaus Width: 3.9781**

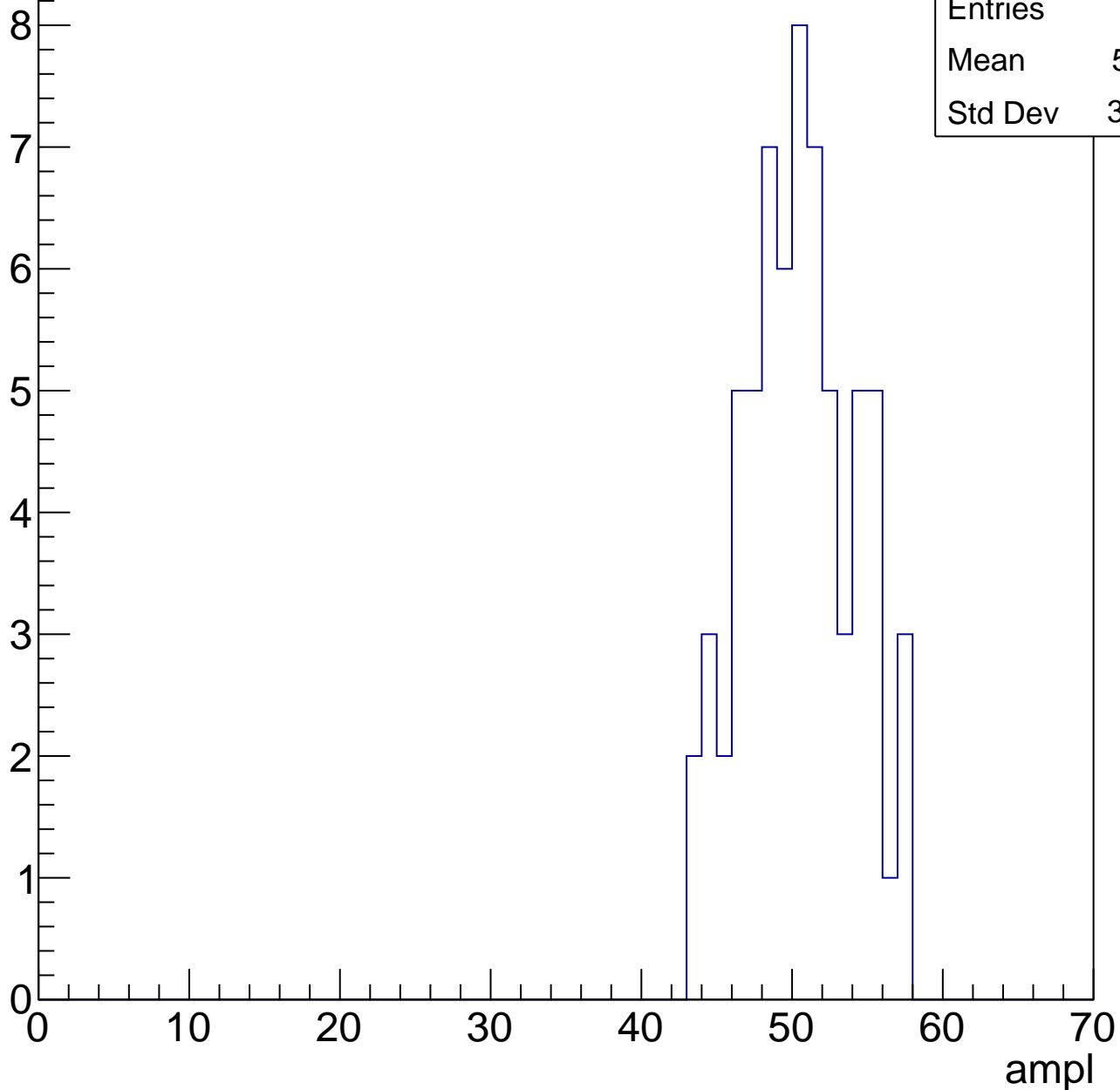


# B0L000S, U18-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	50.01
Std Dev	3.576

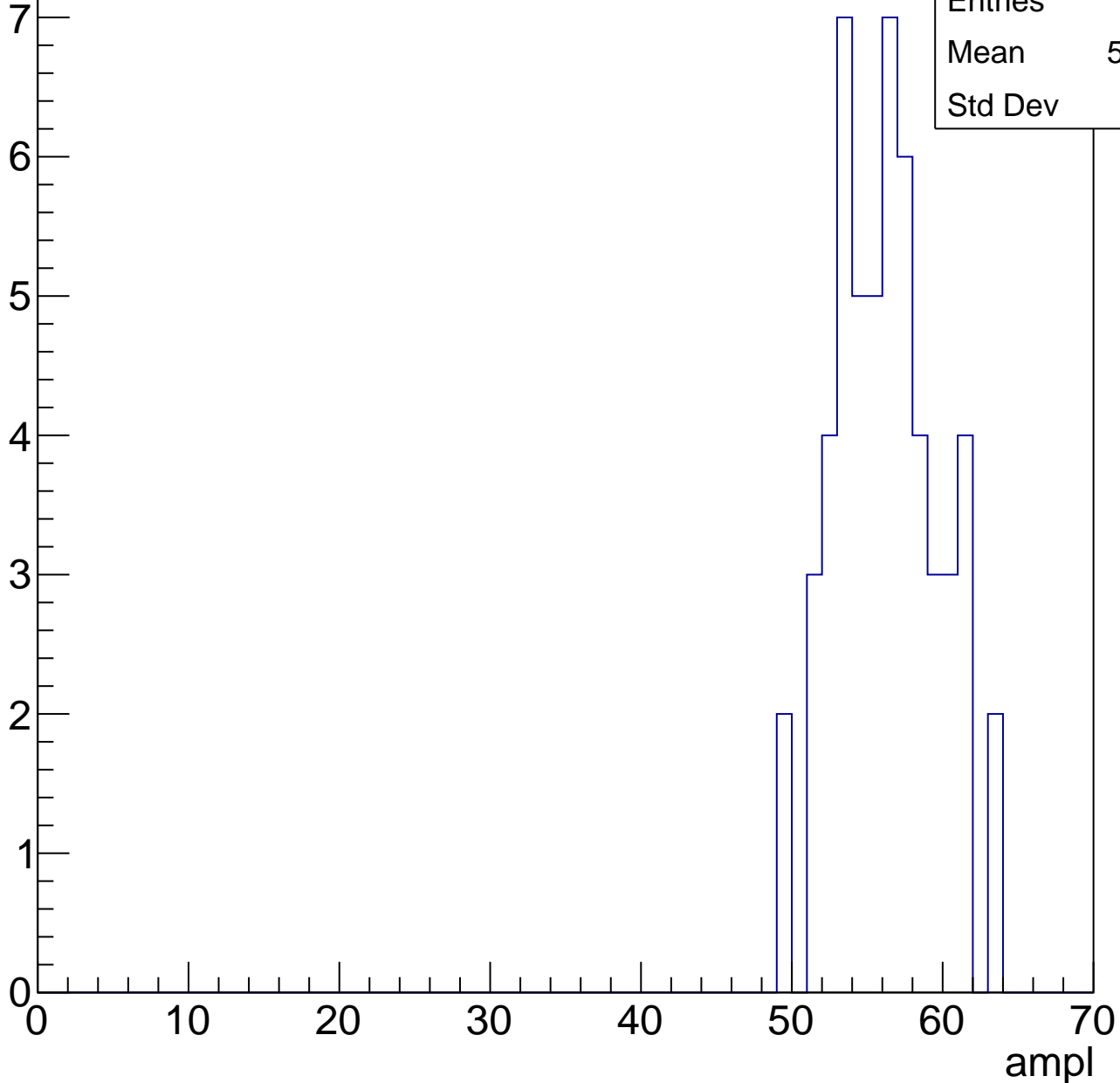


# B0L000S, U18-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.78
Std Dev	3.35

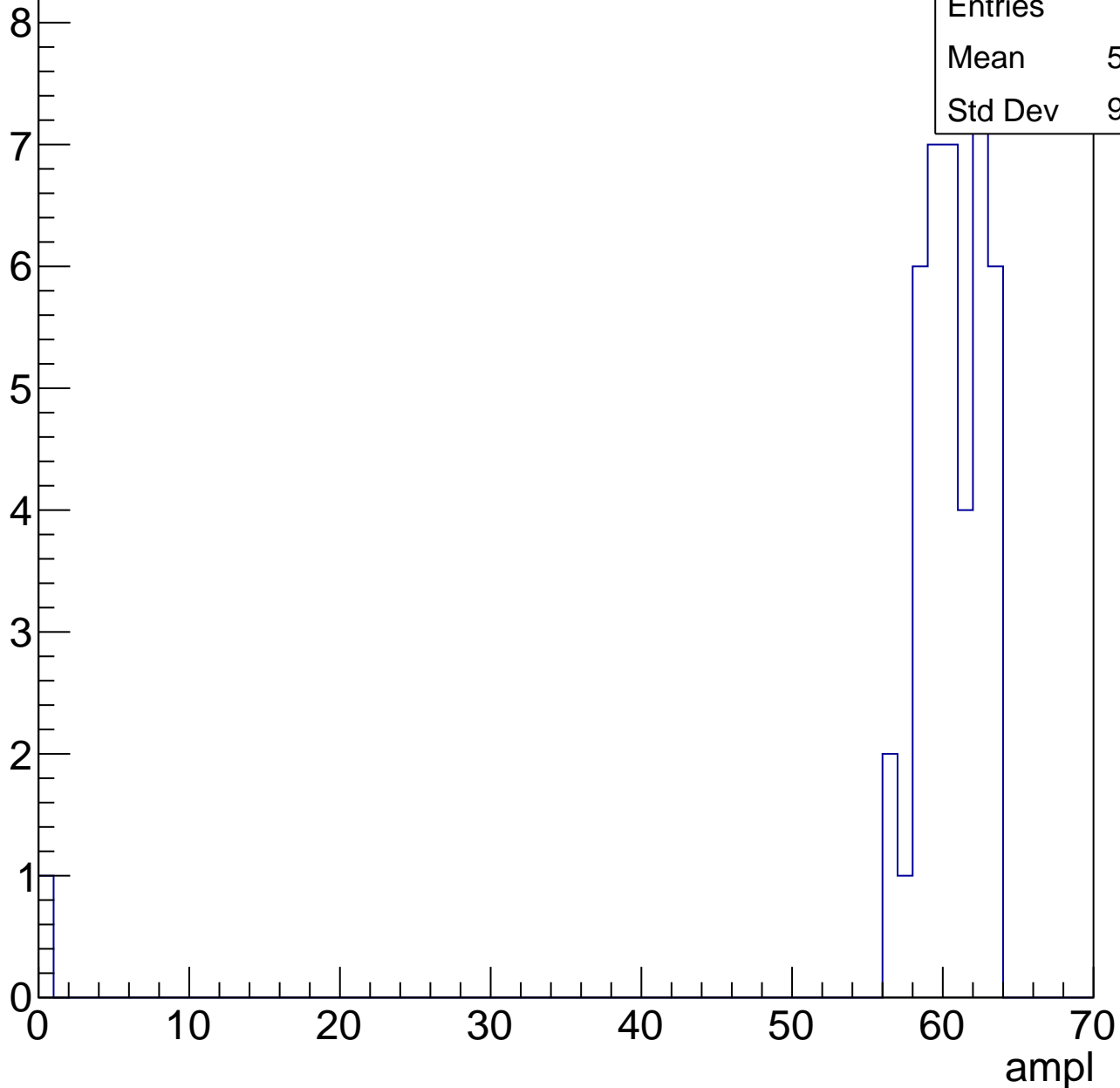


# B0L000S, U18-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

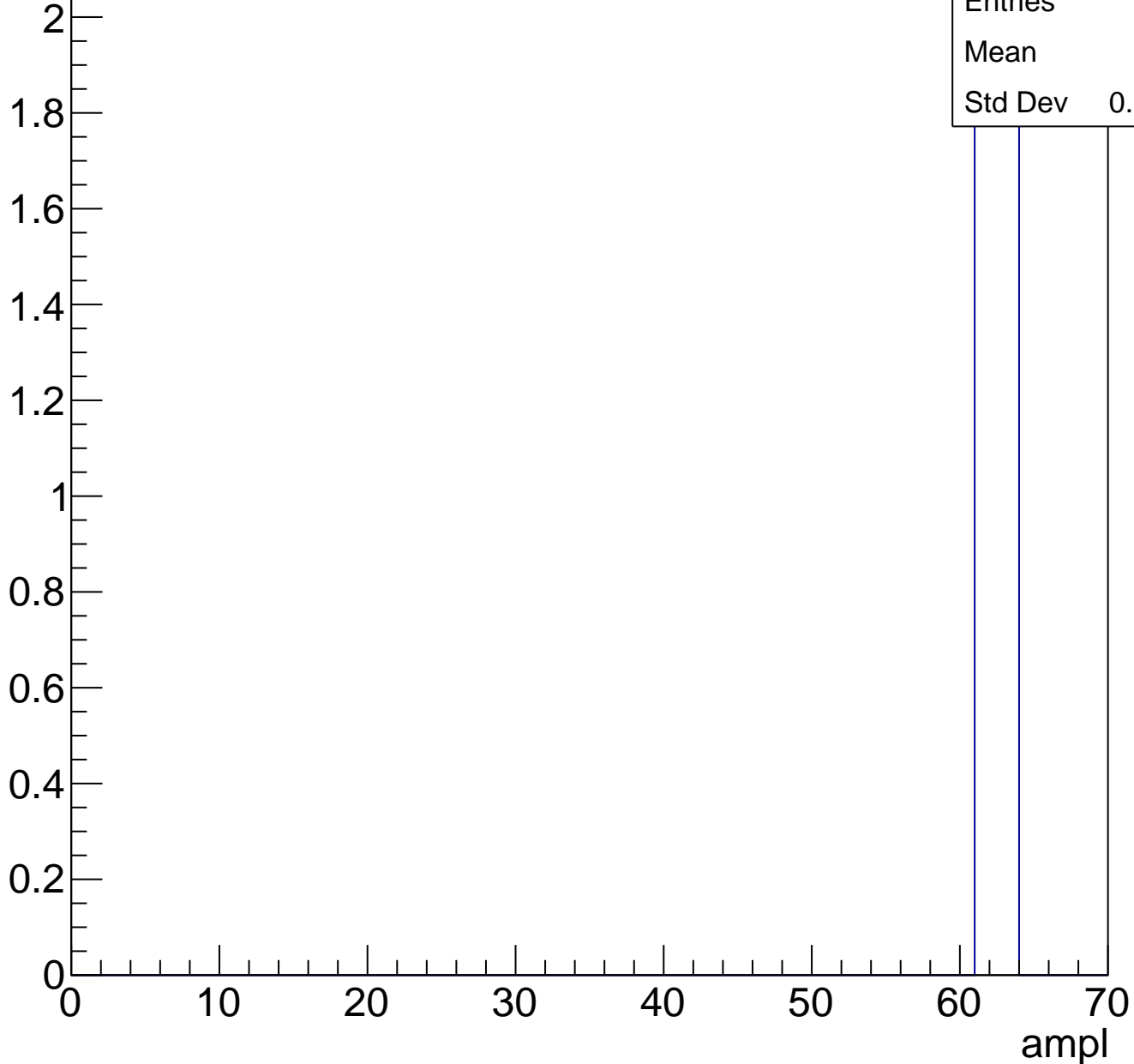
Entries	42
Mean	58.76
Std Dev	9.383



# B0L000S, U18-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

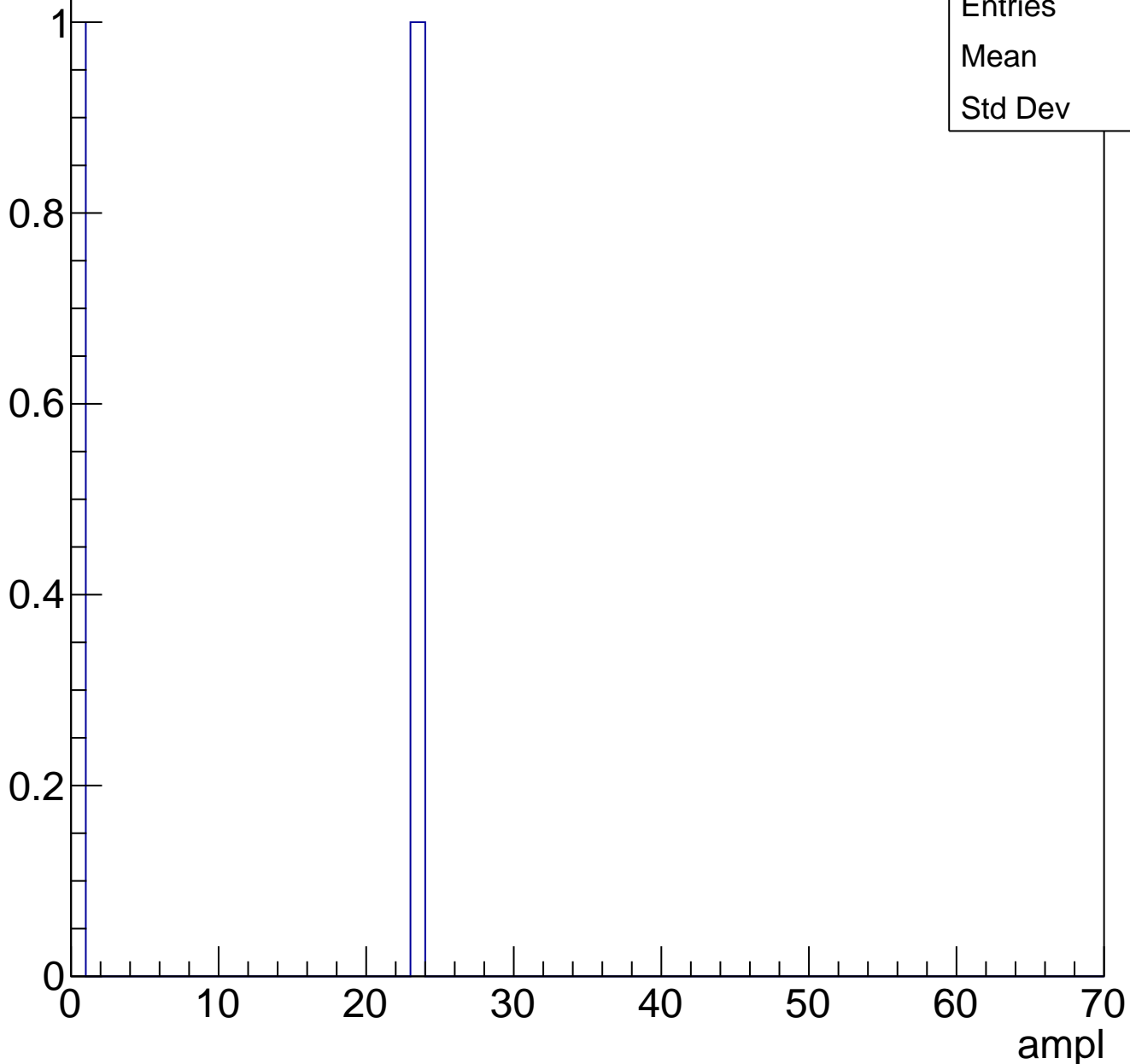




# B0L000S, U18-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch122, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.3
Std Dev	4.631

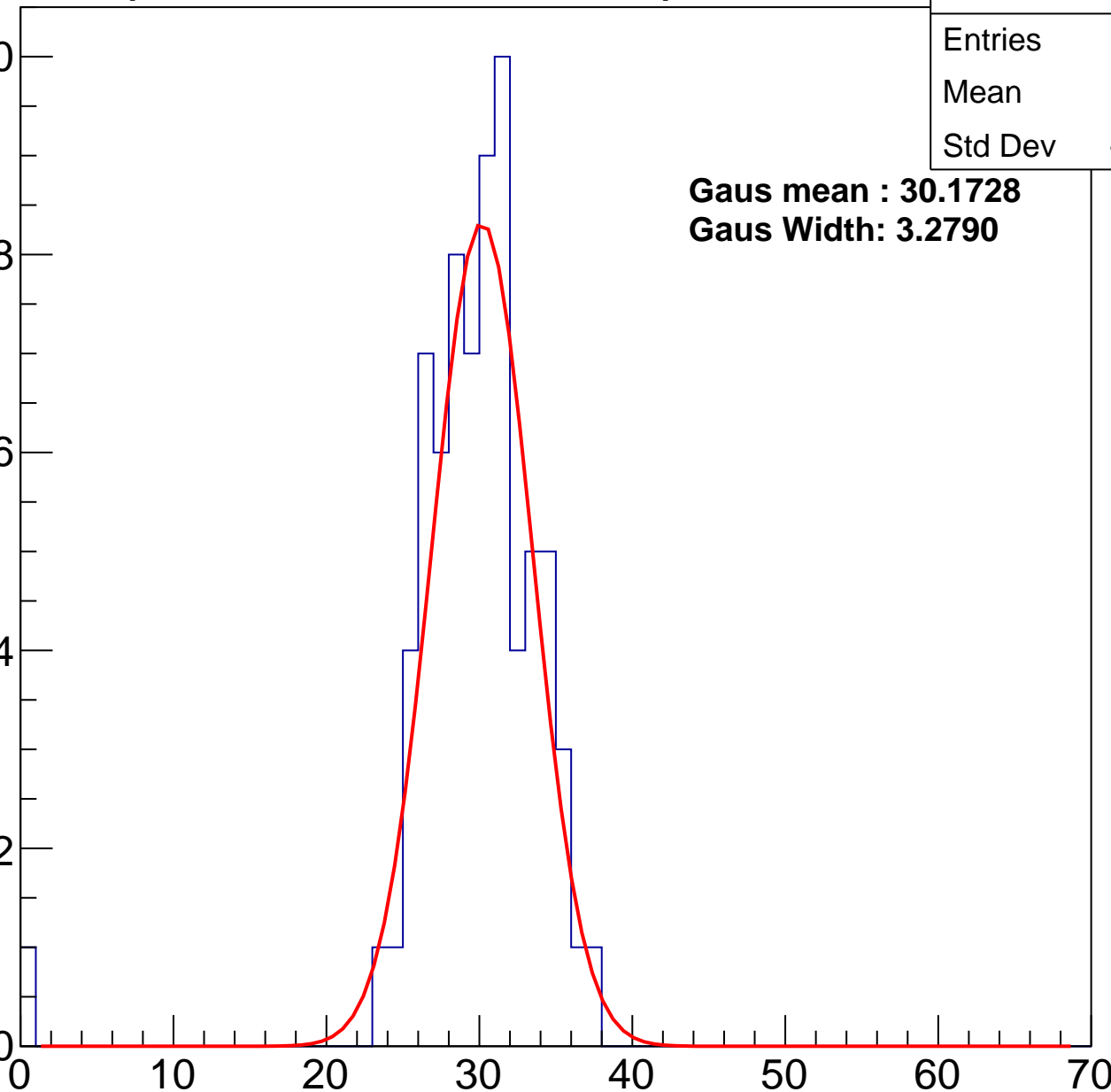
**Gaus mean : 30.1728**

**Gaus Width: 3.2790**

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U18-ch122, adc1

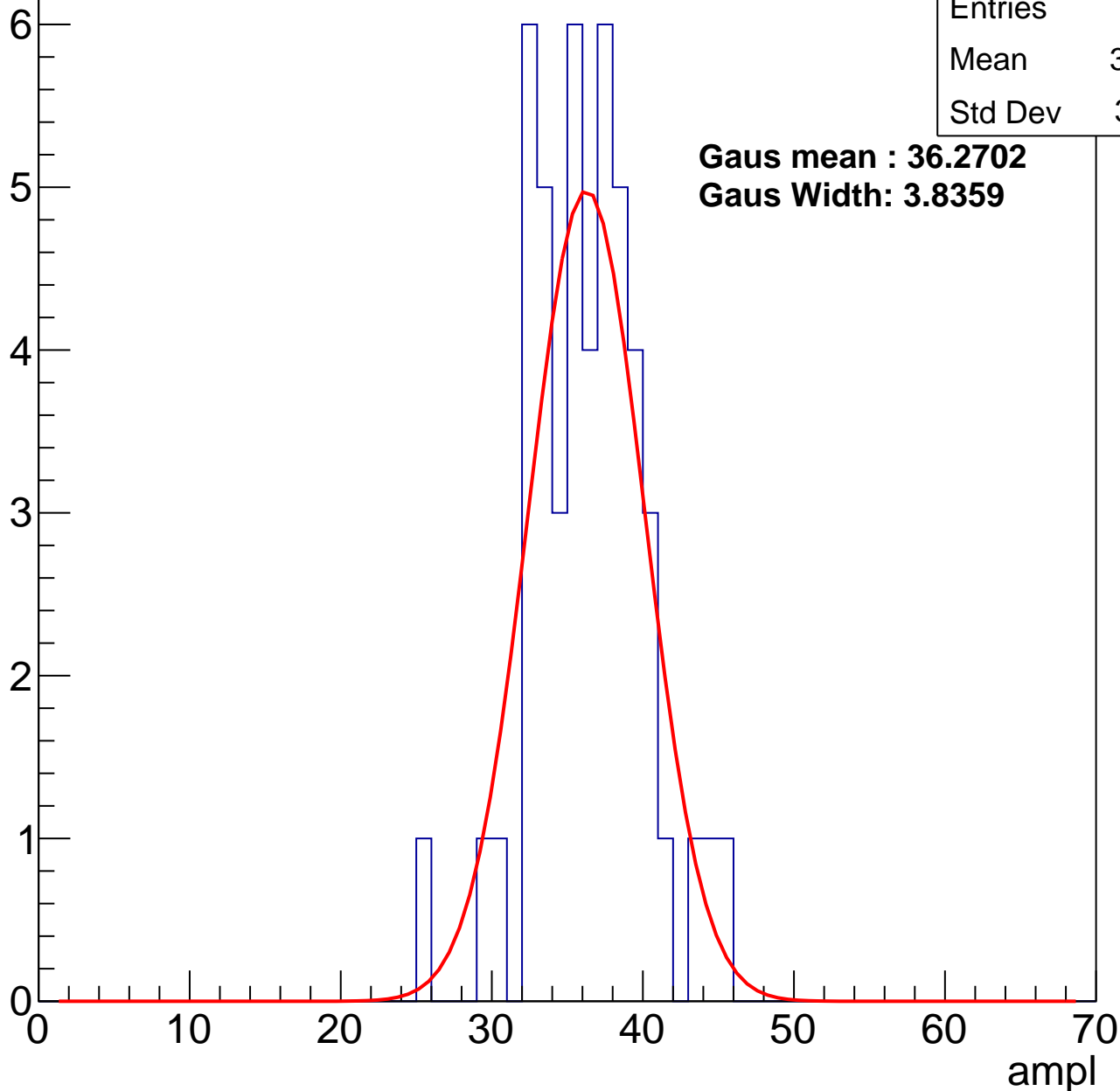
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	35.88
Std Dev	3.761

**Gaus mean : 36.2702**

**Gaus Width: 3.8359**



# B0L000S, U18-ch122, adc2

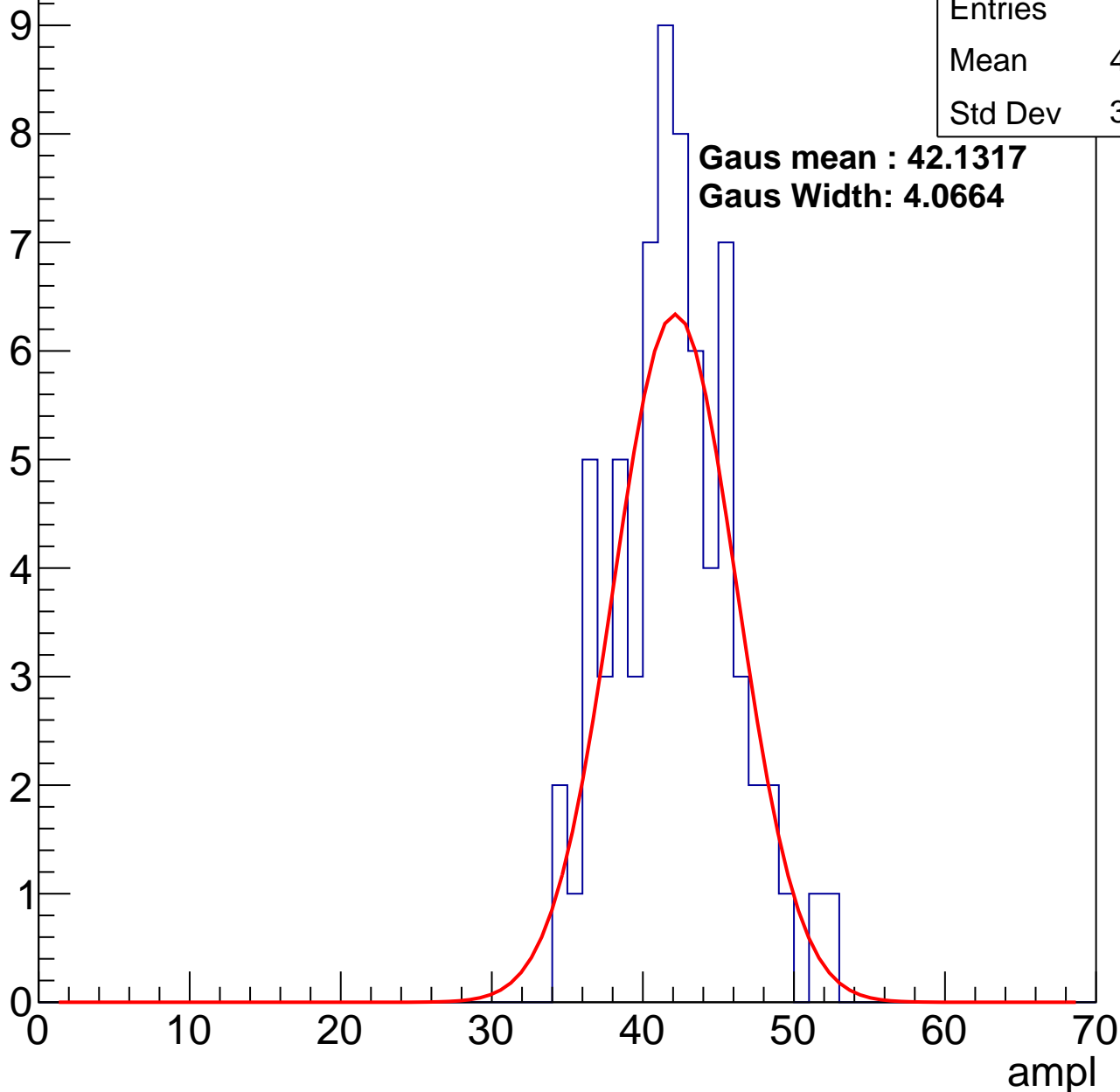
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	41.64
Std Dev	3.877

**Gaus mean : 42.1317**

**Gaus Width: 4.0664**

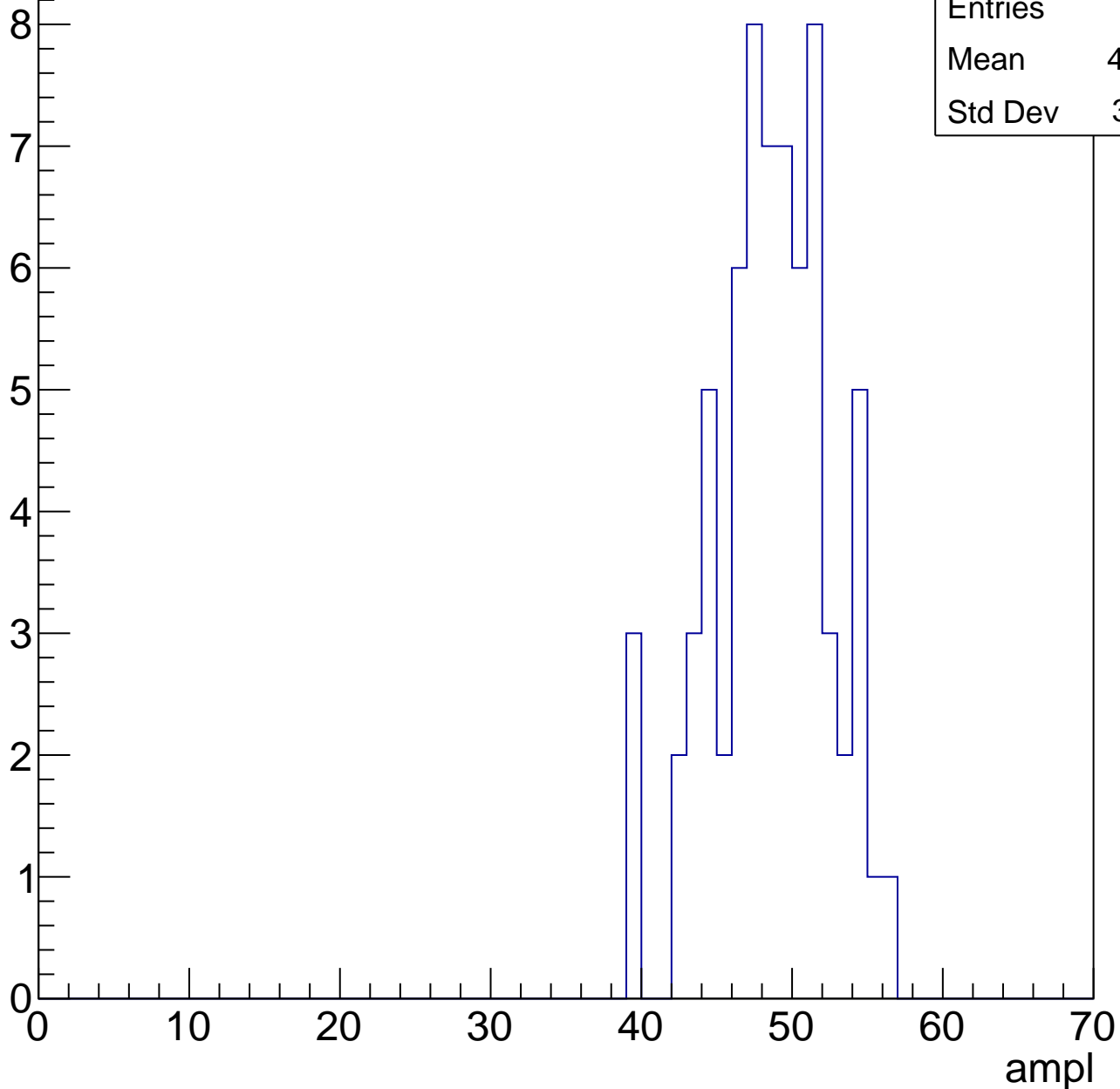


# B0L000S, U18-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	48.14
Std Dev	3.831

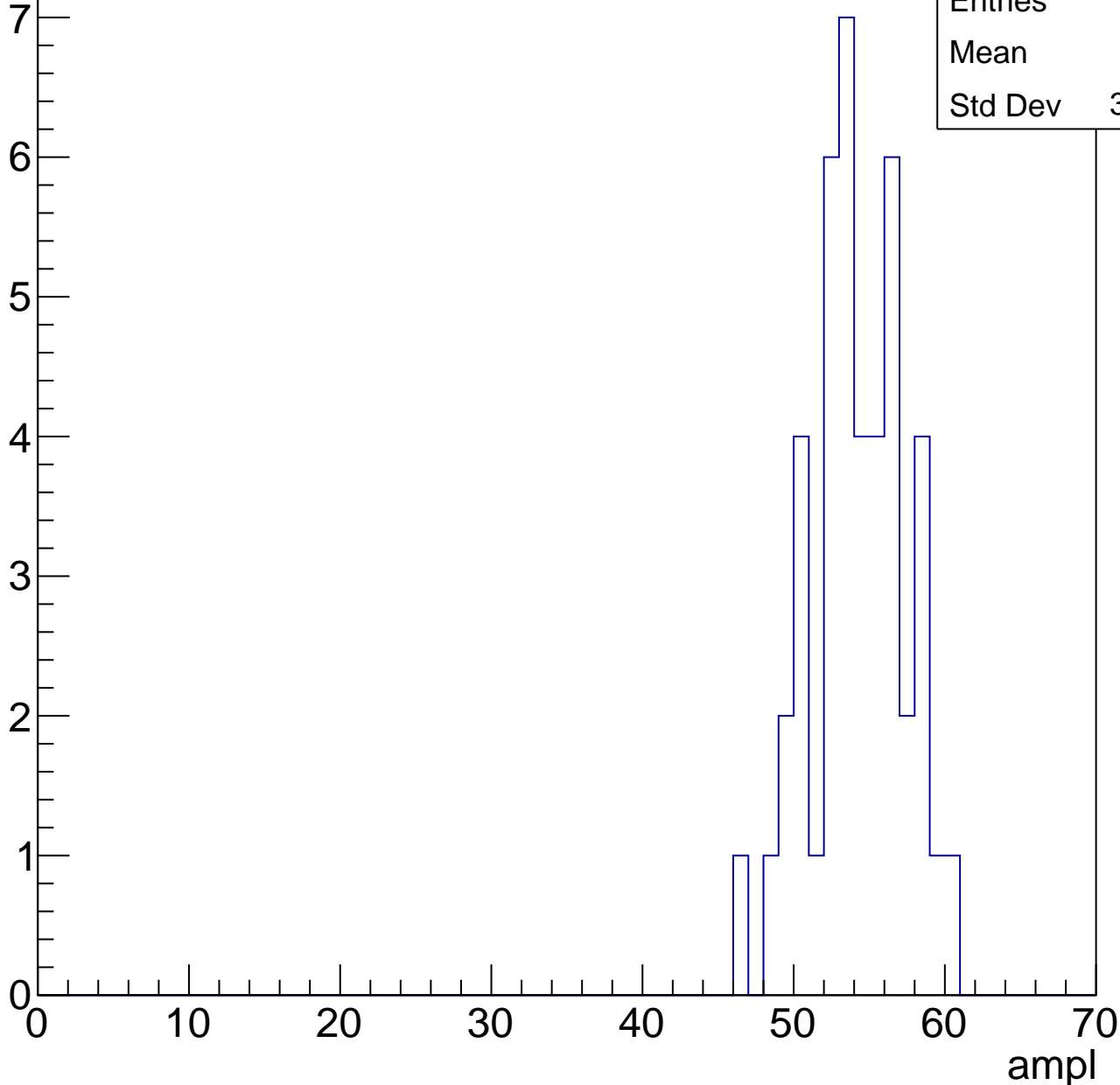


# B0L000S, U18-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

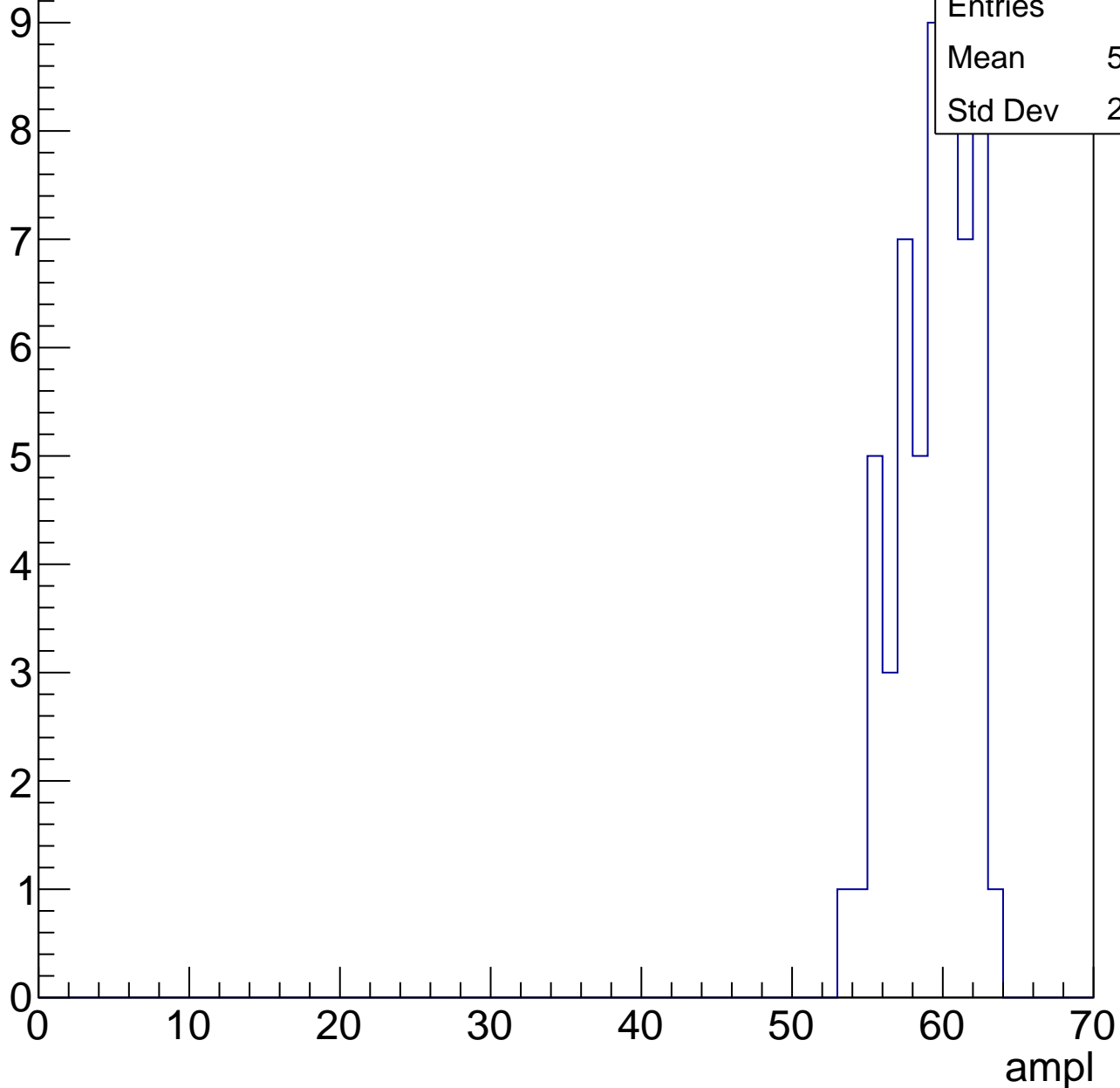
Entries	44
Mean	53.7
Std Dev	3.094



# B0L000S, U18-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

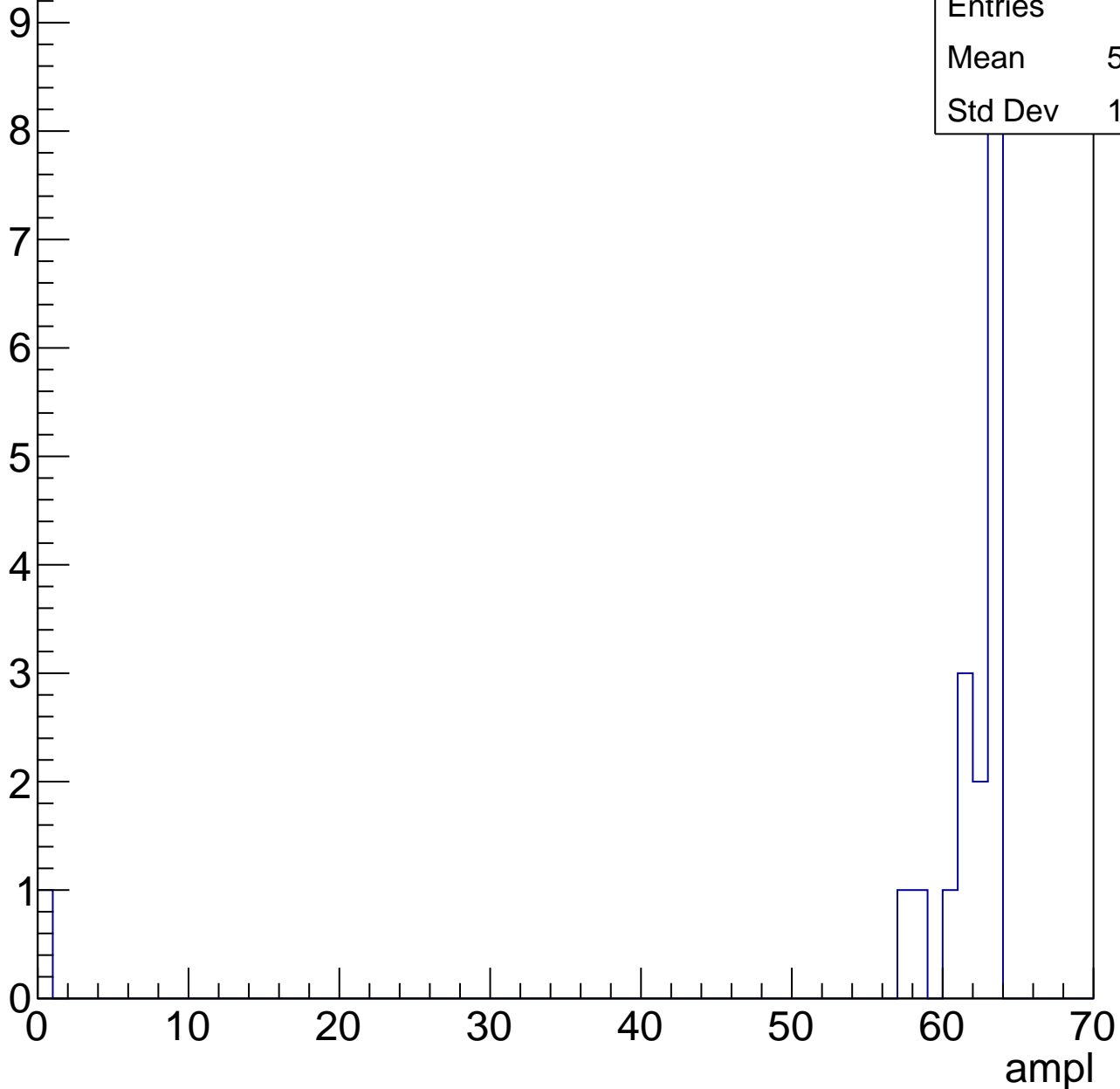


# B0L000S, U18-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	18
Mean	58.28
Std Dev	14.24





# B0L000S, U18-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch123, adc0

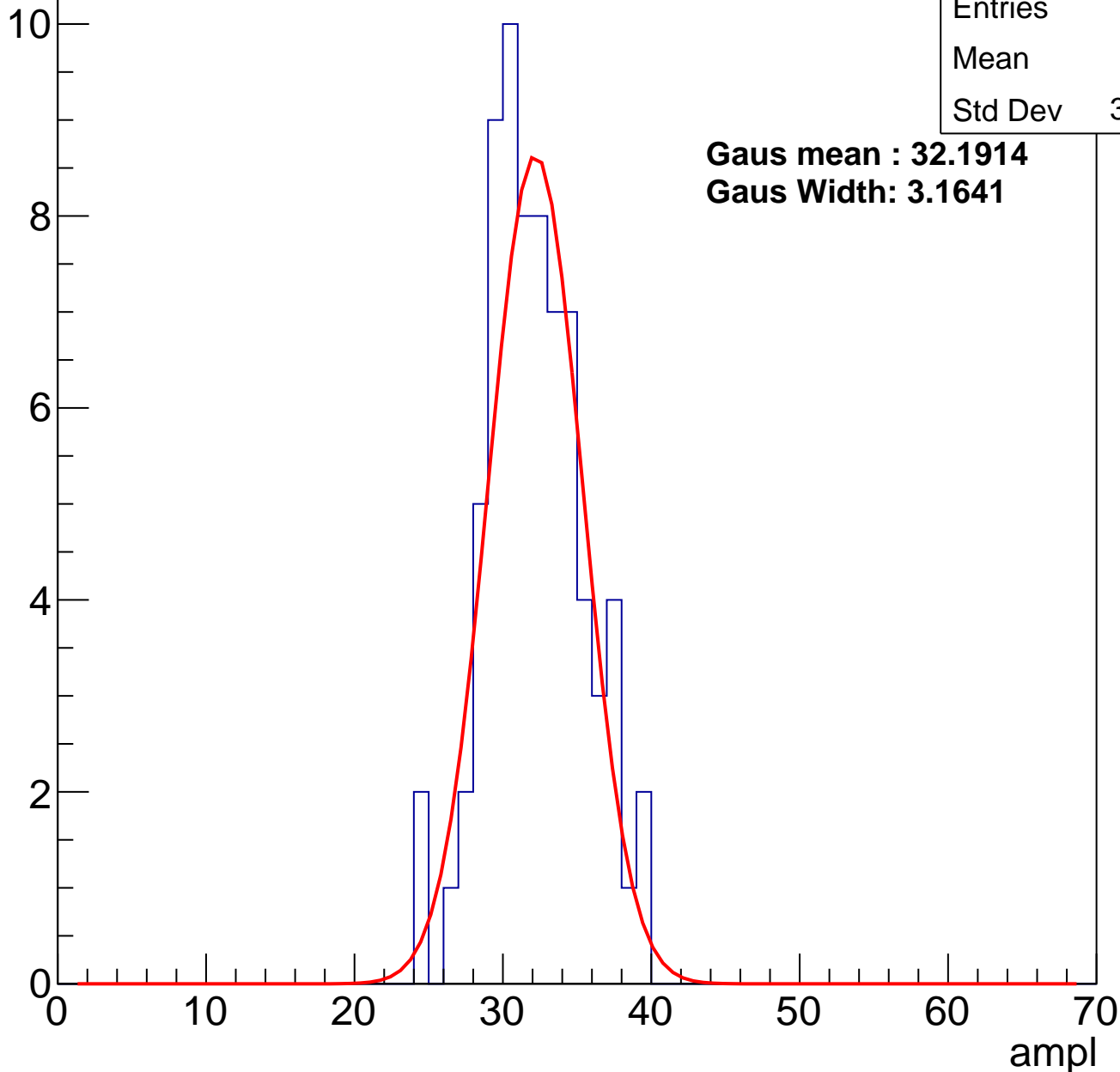
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	31.7
Std Dev	3.246

**Gaus mean : 32.1914**

**Gaus Width: 3.1641**

Entry



# B0L000S, U18-ch123, adc1

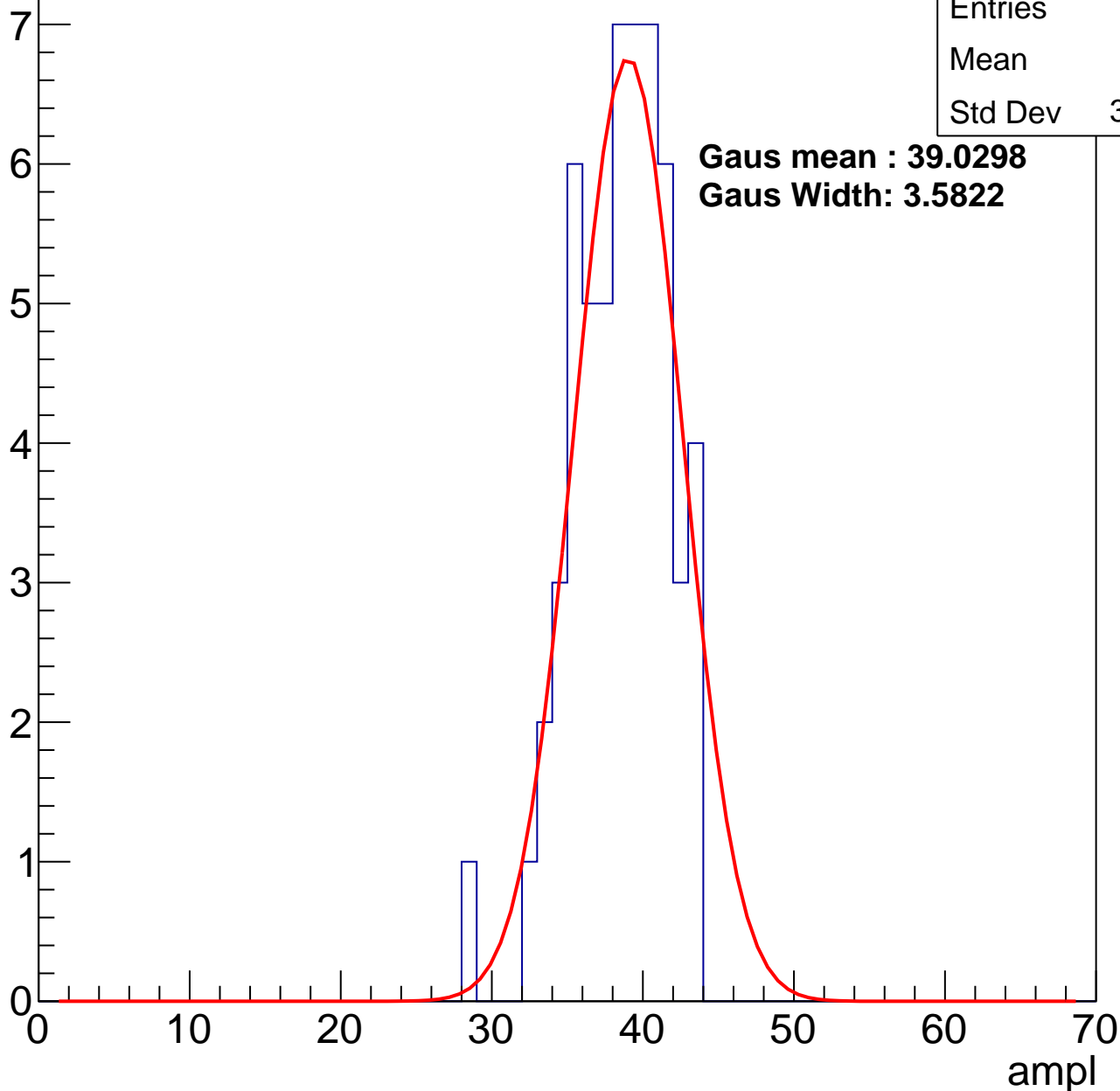
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	38
Std Dev	3.106

**Gaus mean : 39.0298**

**Gaus Width: 3.5822**



# B0L000S, U18-ch123, adc2

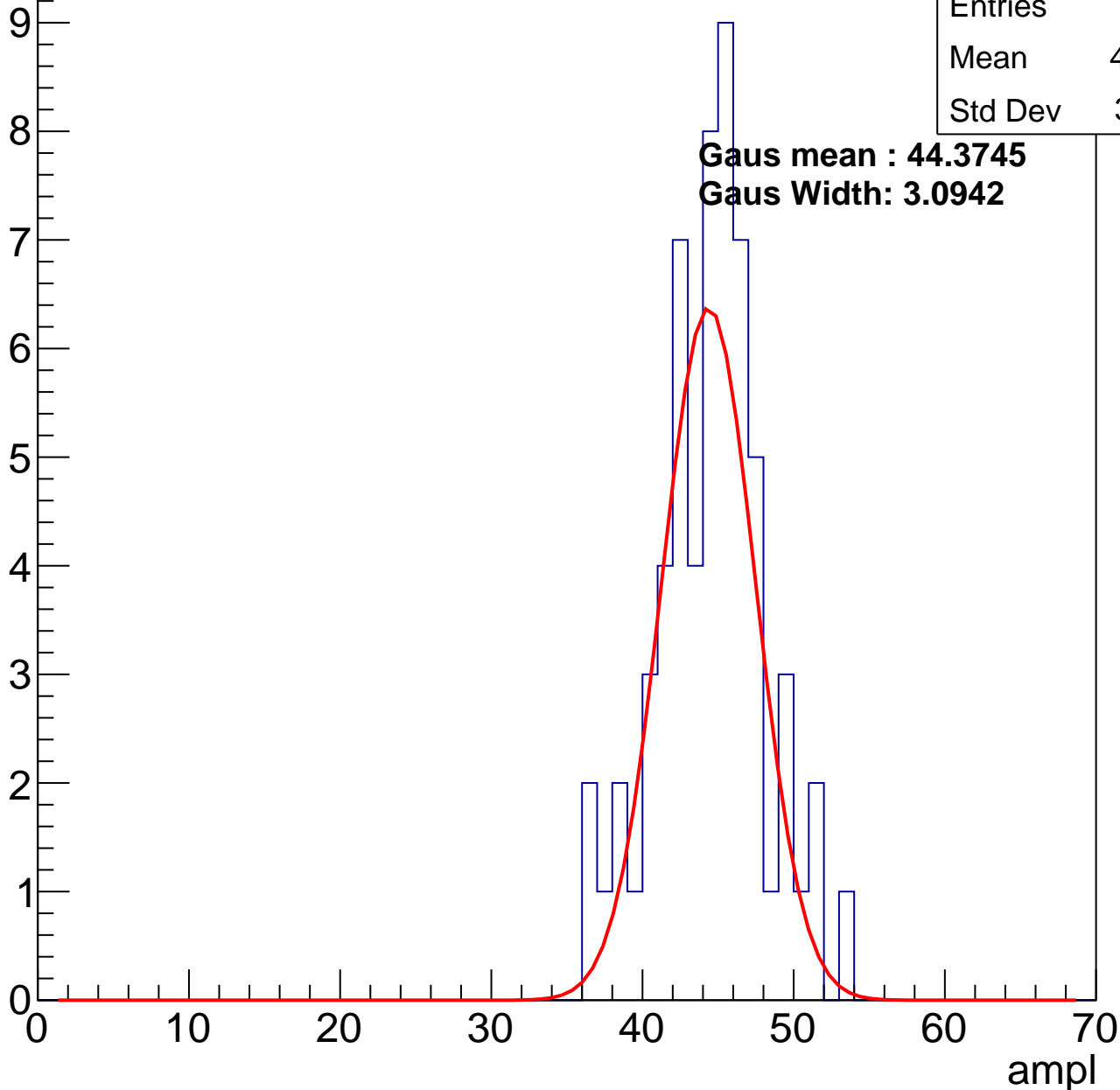
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.07
Std Dev	3.571

**Gaus mean : 44.3745**

**Gaus Width: 3.0942**

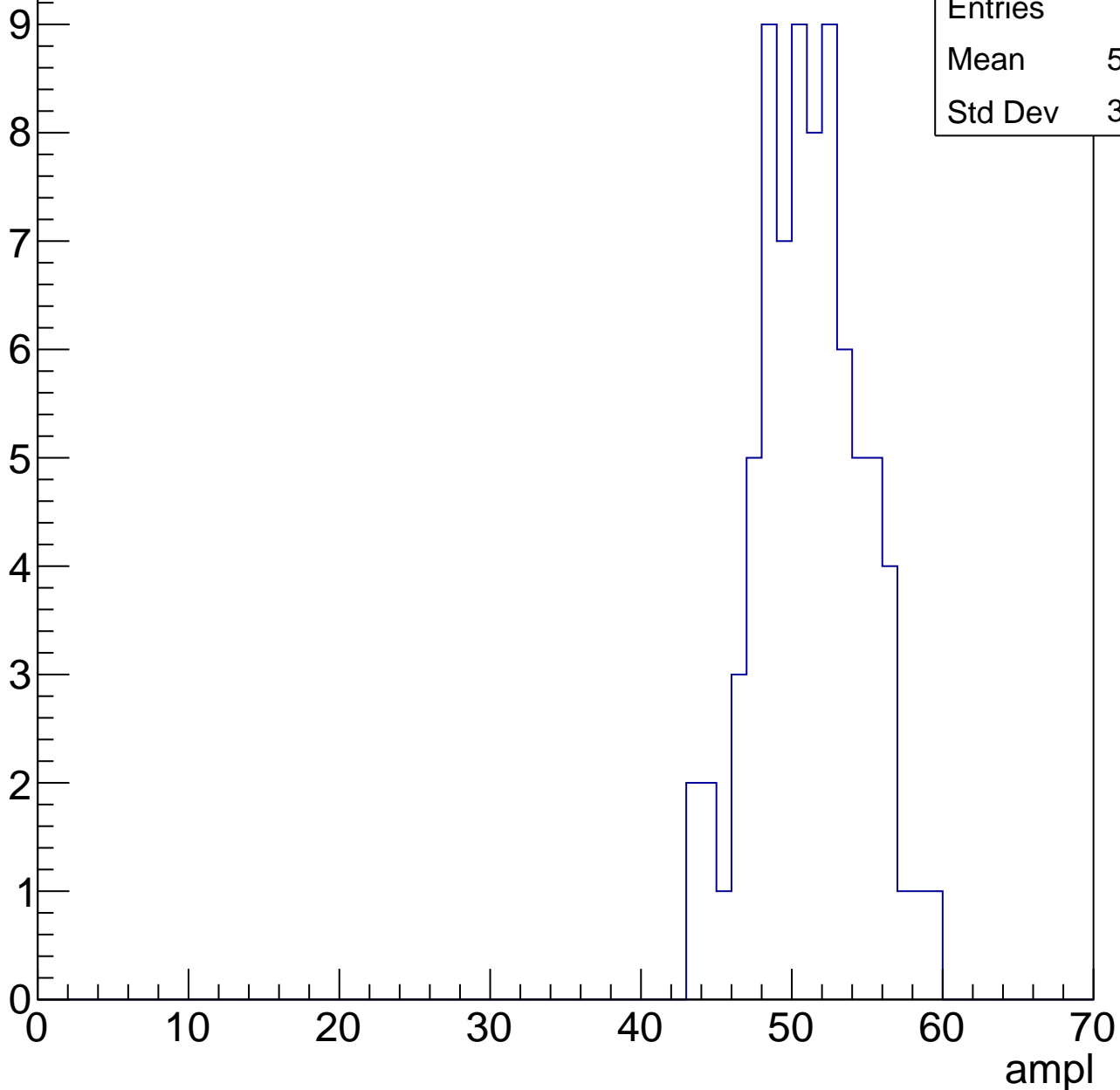


# B0L000S, U18-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	50.69
Std Dev	3.462

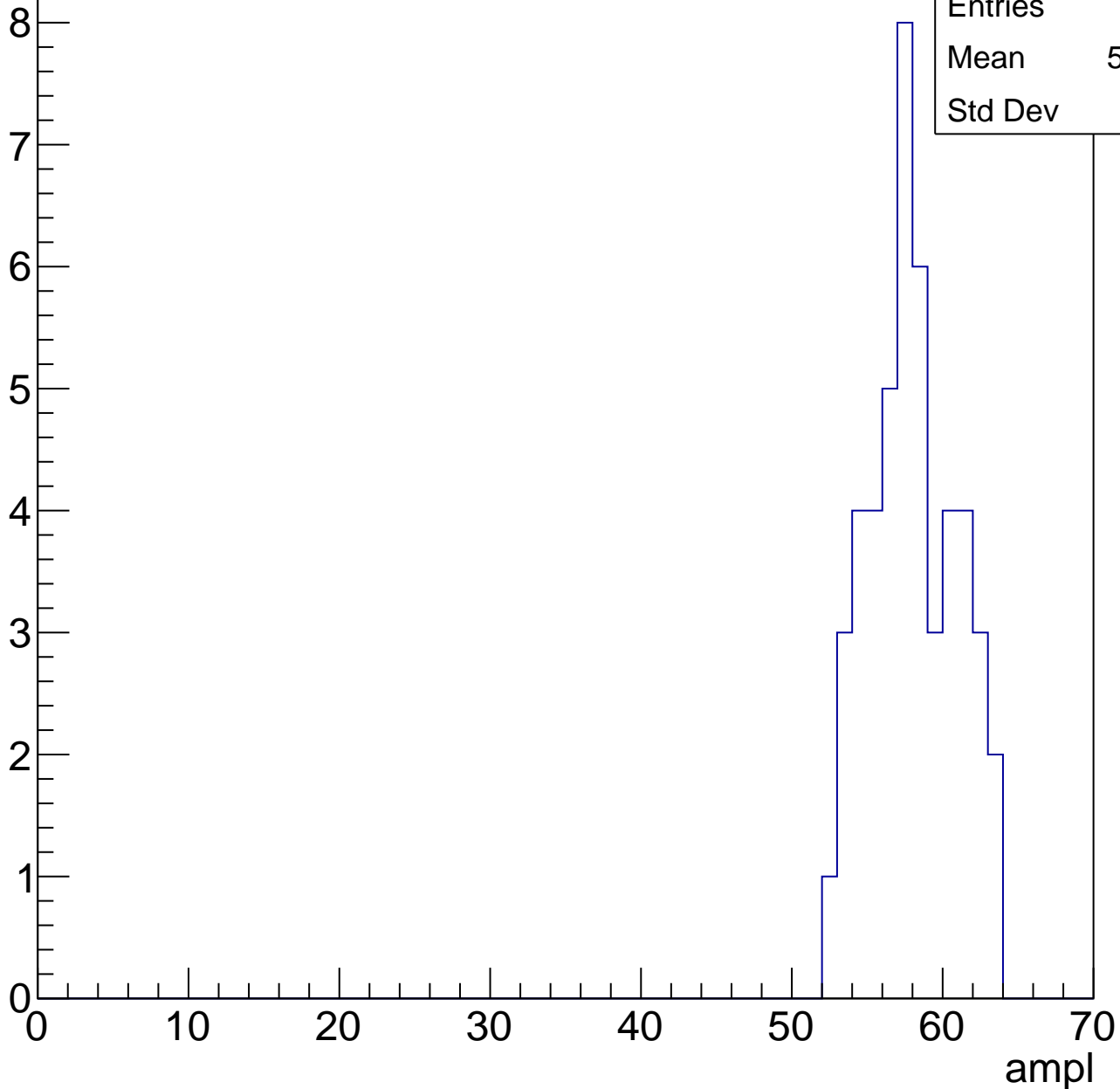


# B0L000S, U18-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

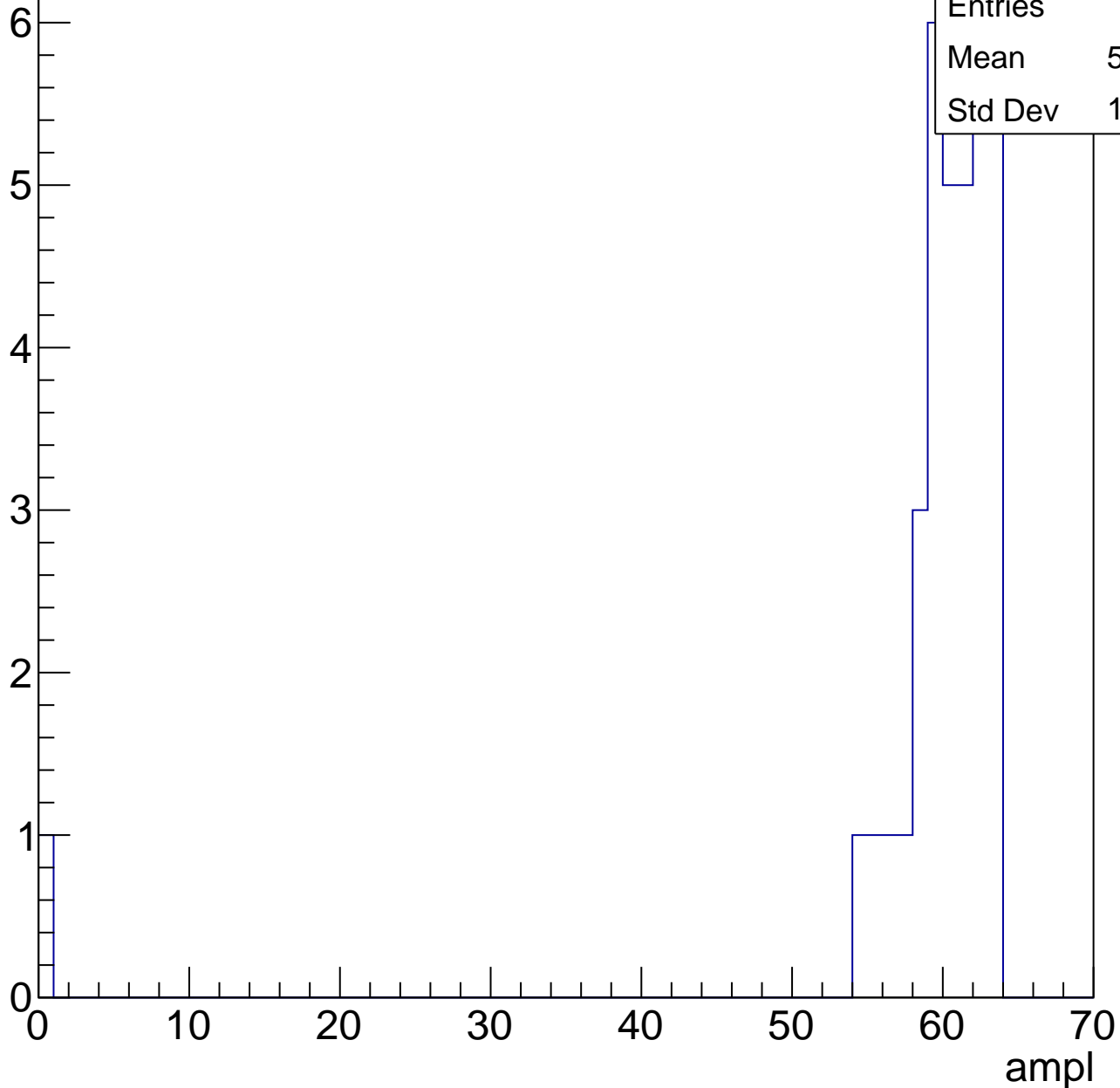
Entries	47
Mean	57.53
Std Dev	2.85



# B0L000S, U18-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

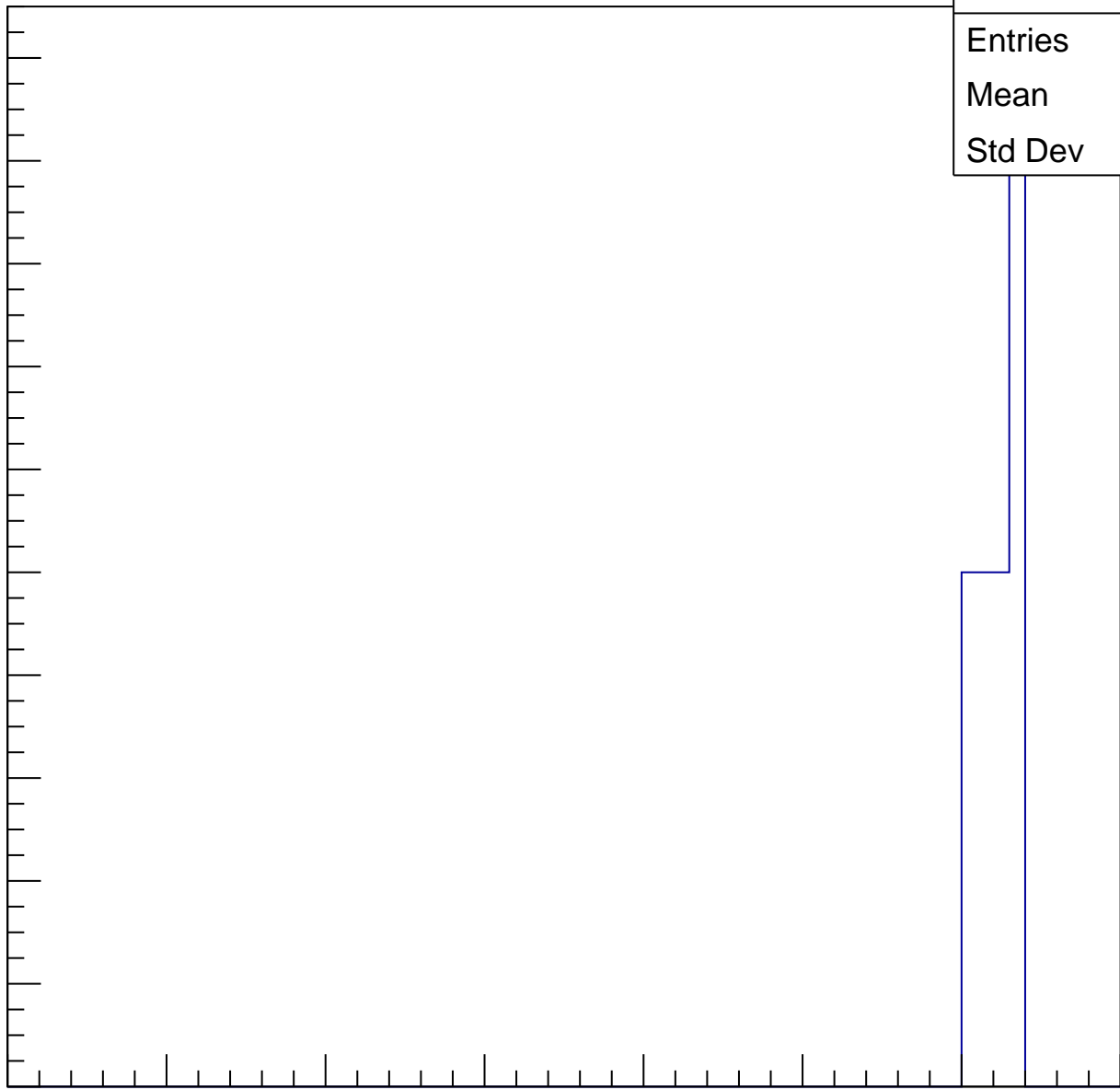
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70





# B0L000S, U18-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U18-ch124, adc0

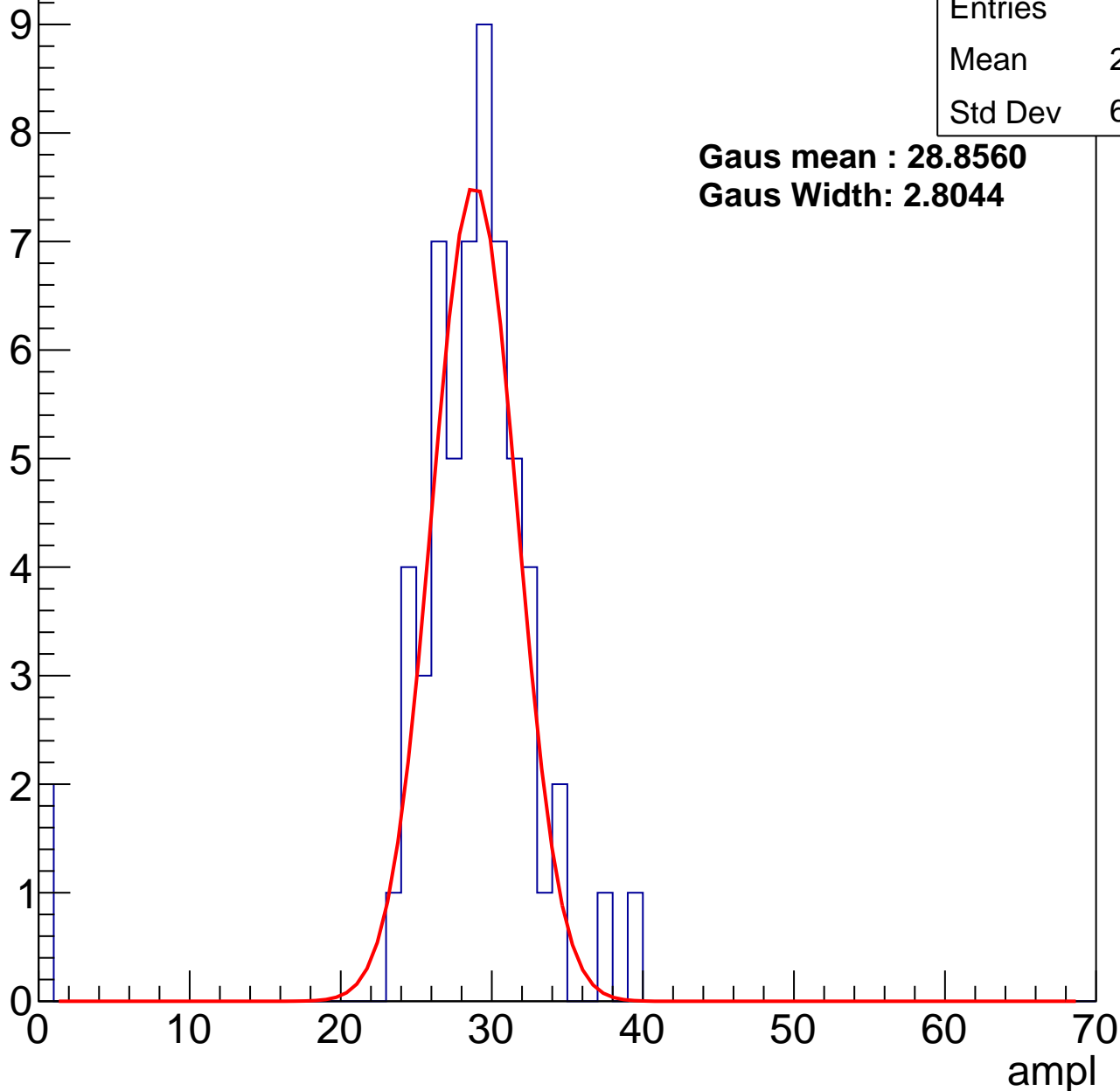
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	27.76
Std Dev	6.049

**Gaus mean : 28.8560**

**Gaus Width: 2.8044**



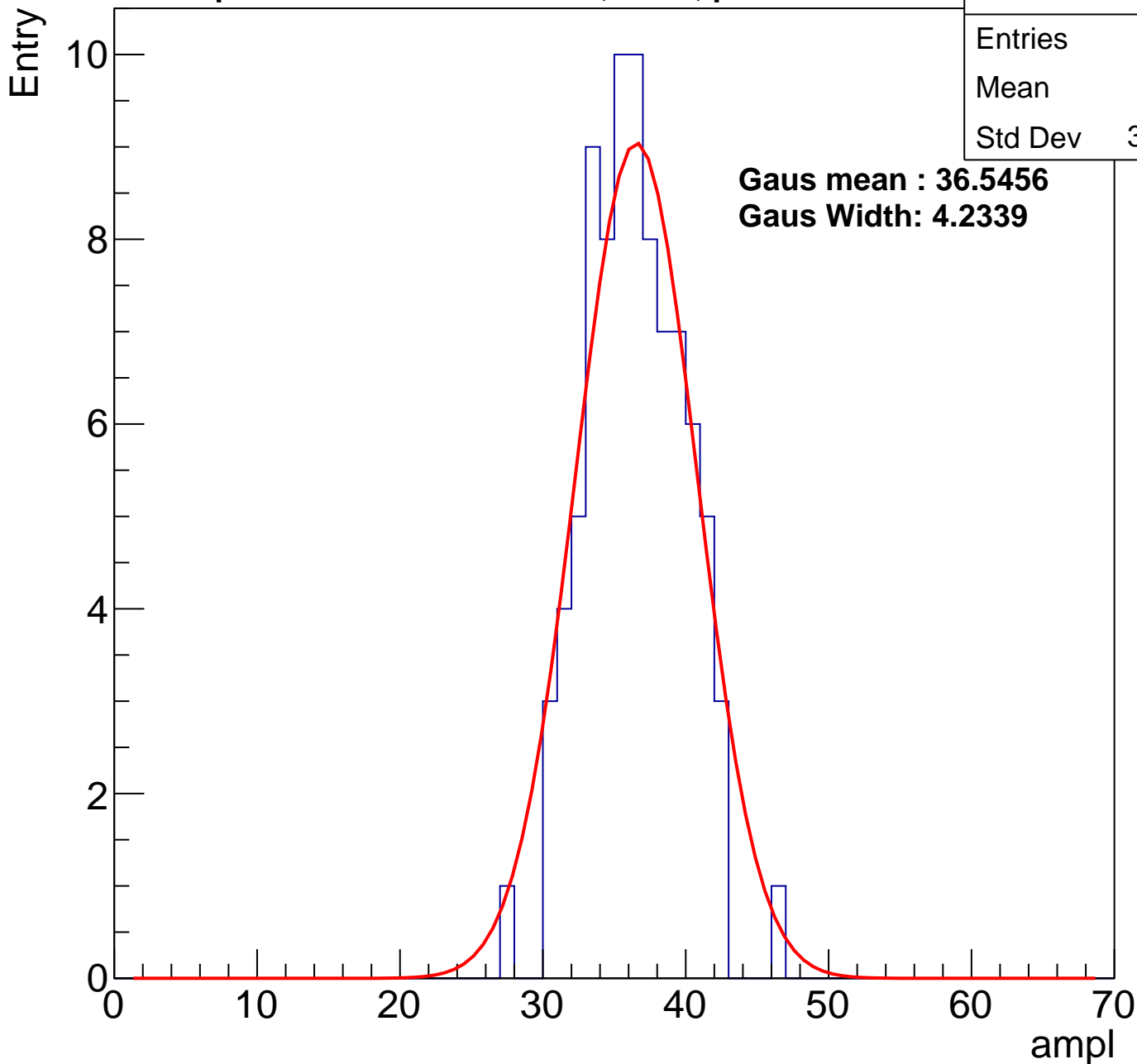
# B0L000S, U18-ch124, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	36
Std Dev	3.424

**Gaus mean : 36.5456**

**Gaus Width: 4.2339**



# B0L000S, U18-ch124, adc2

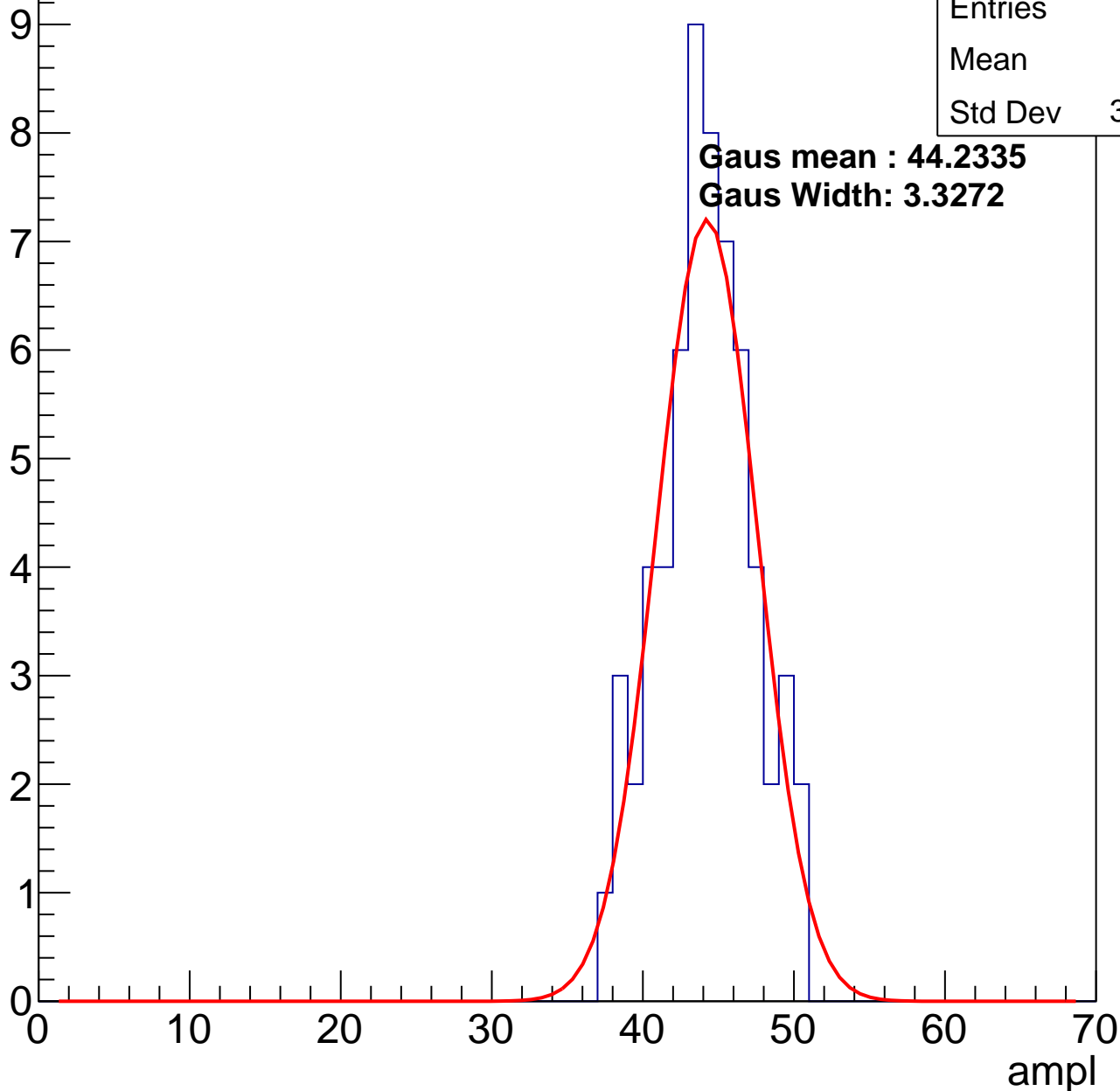
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.7
Std Dev	3.085

**Gaus mean : 44.2335**

**Gaus Width: 3.3272**

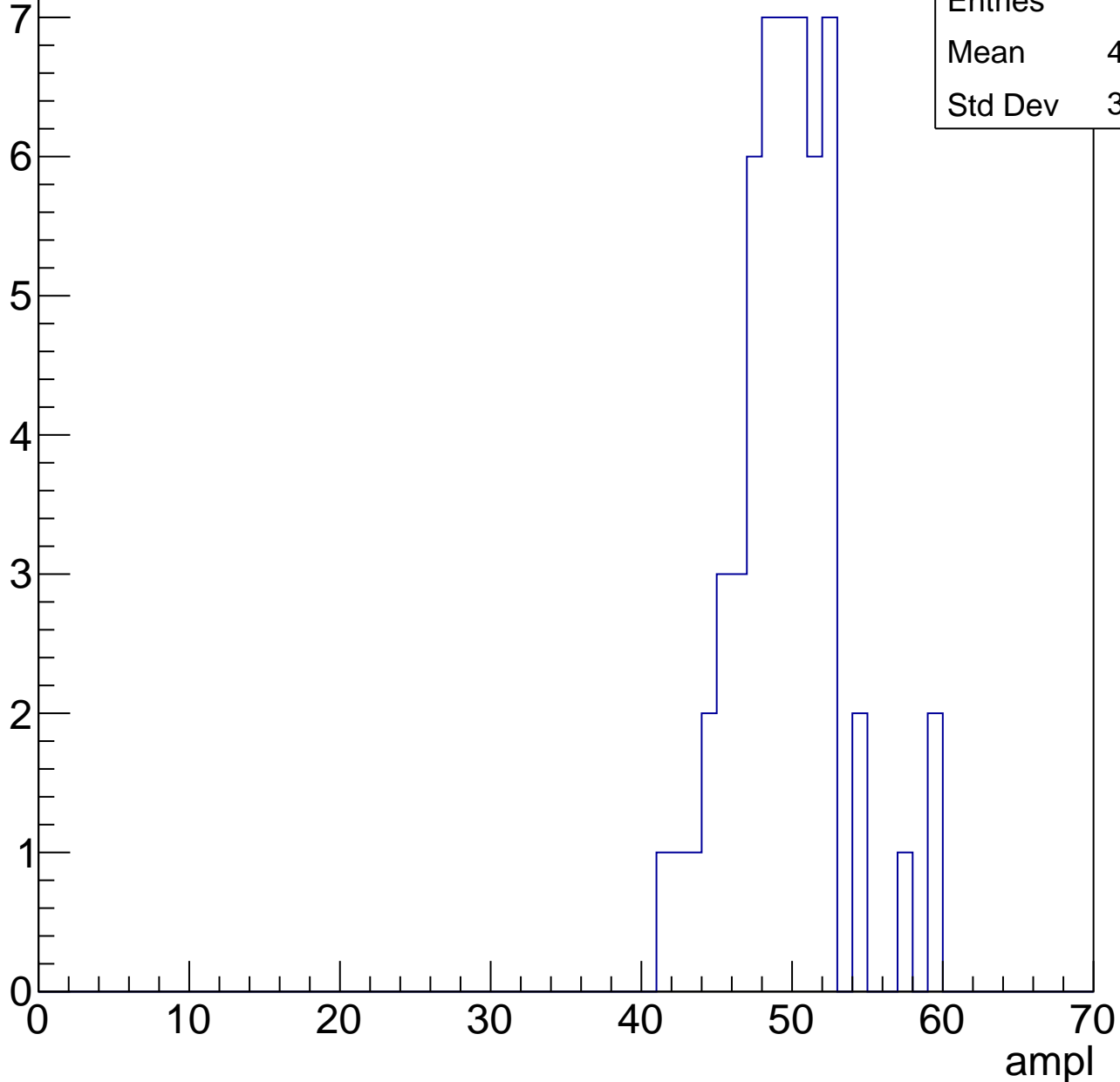


# B0L000S, U18-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.12
Std Dev	3.566

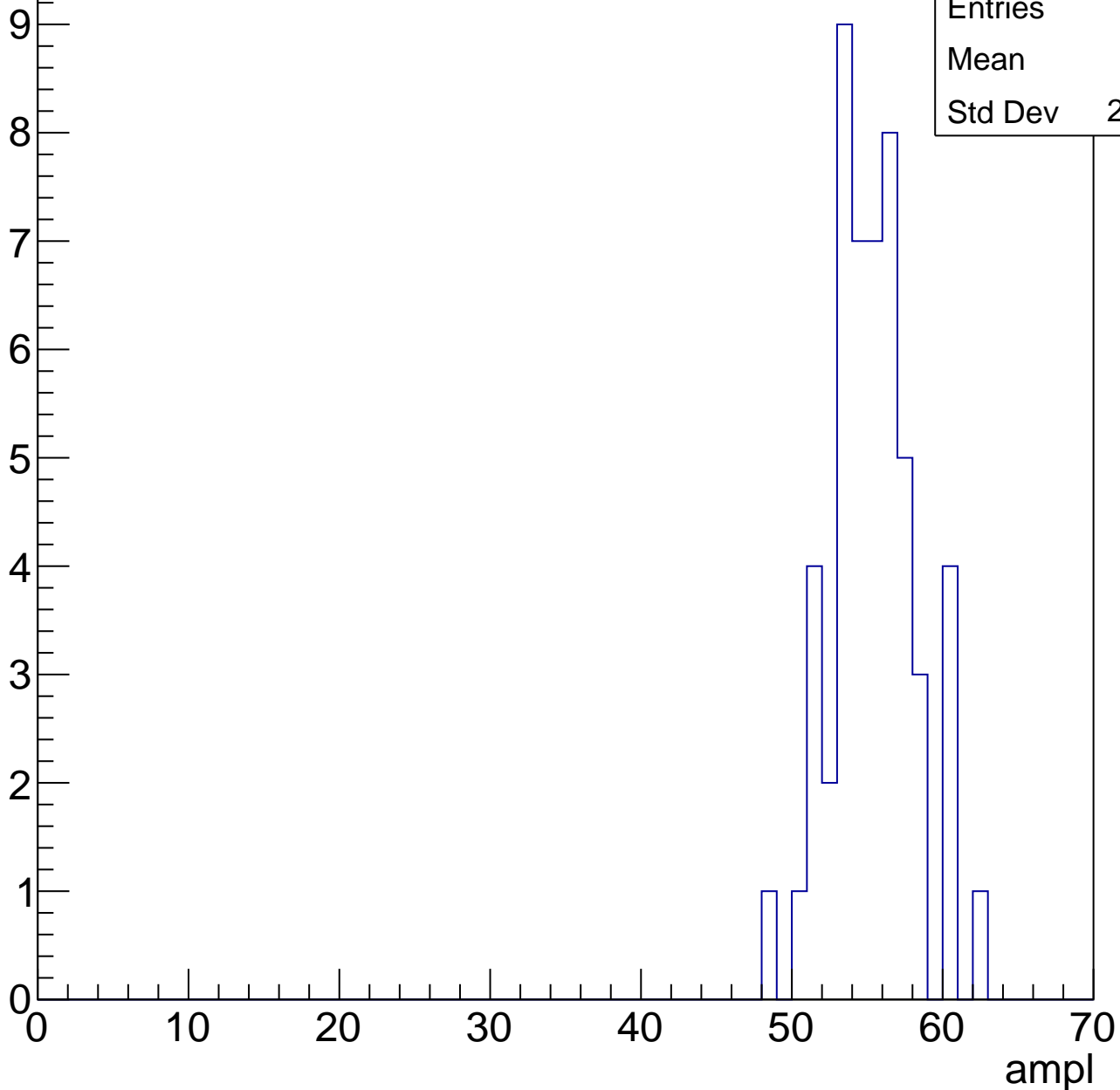


# B0L000S, U18-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

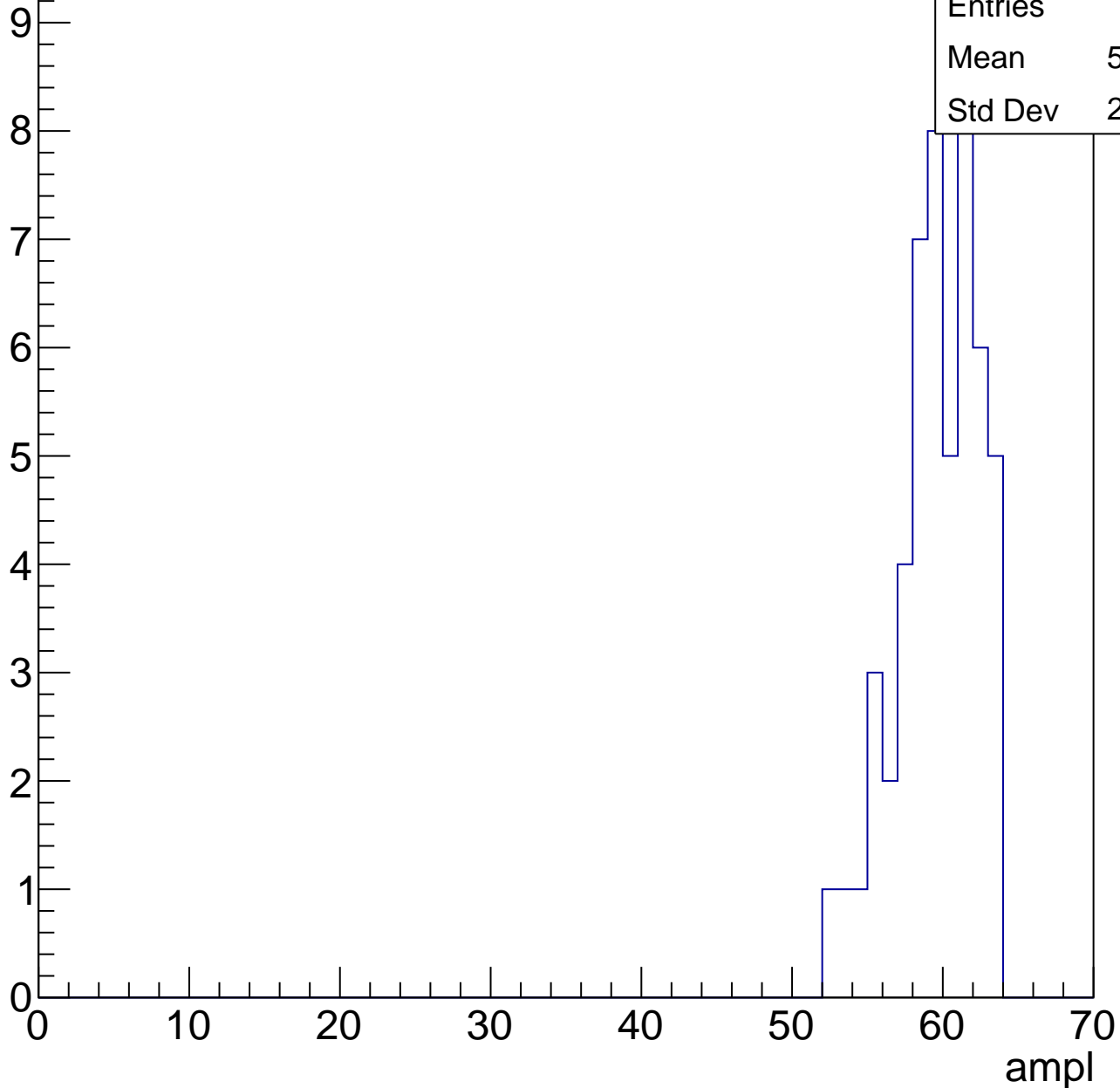
Entries	52
Mean	54.9
Std Dev	2.782



# B0L000S, U18-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

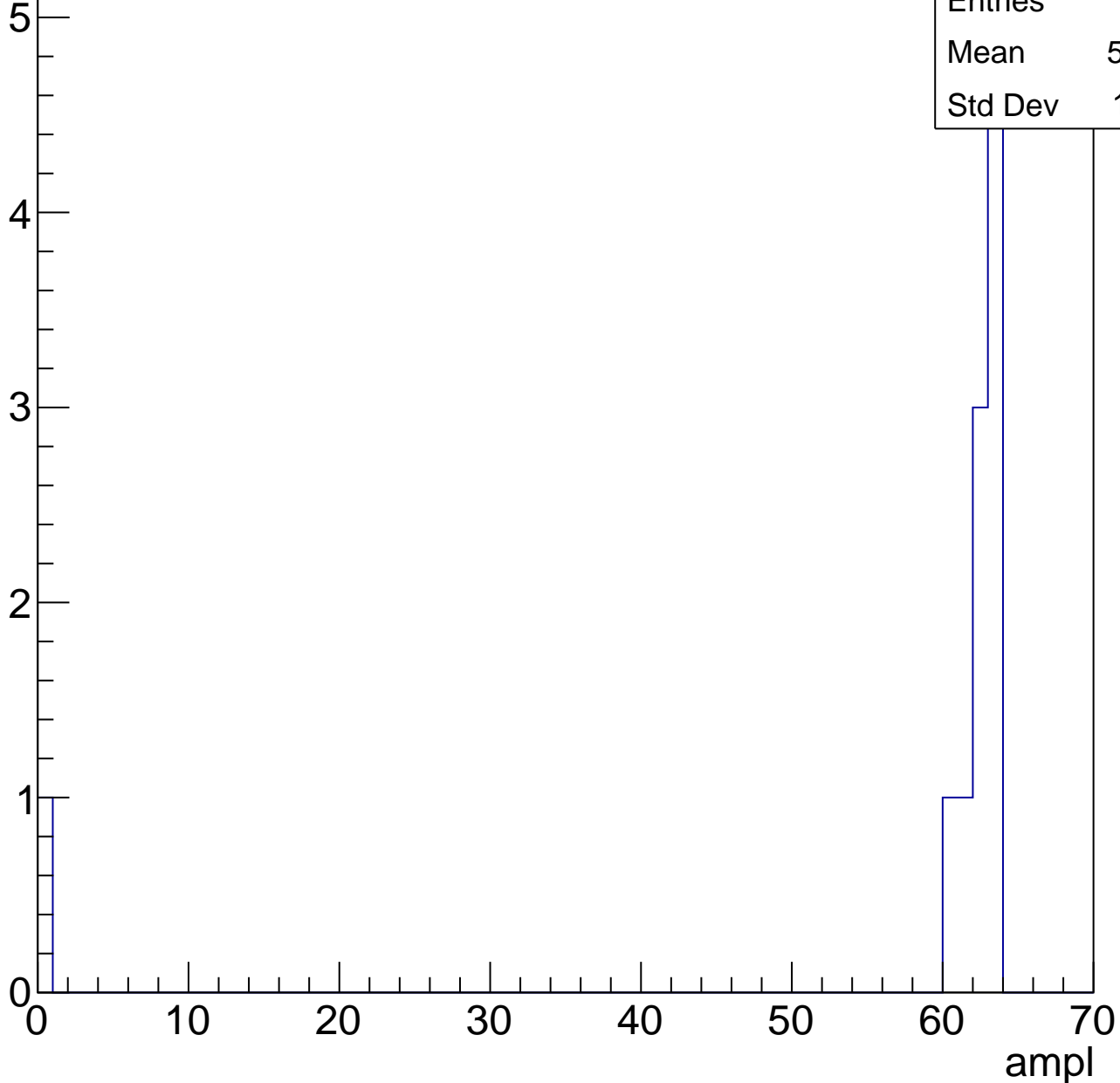


# B0L000S, U18-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	56.55
Std Dev	17.91





# B0L000S, U18-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U18-ch125, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	29.32
Std Dev	5.123

**Gaus mean : 30.7634**

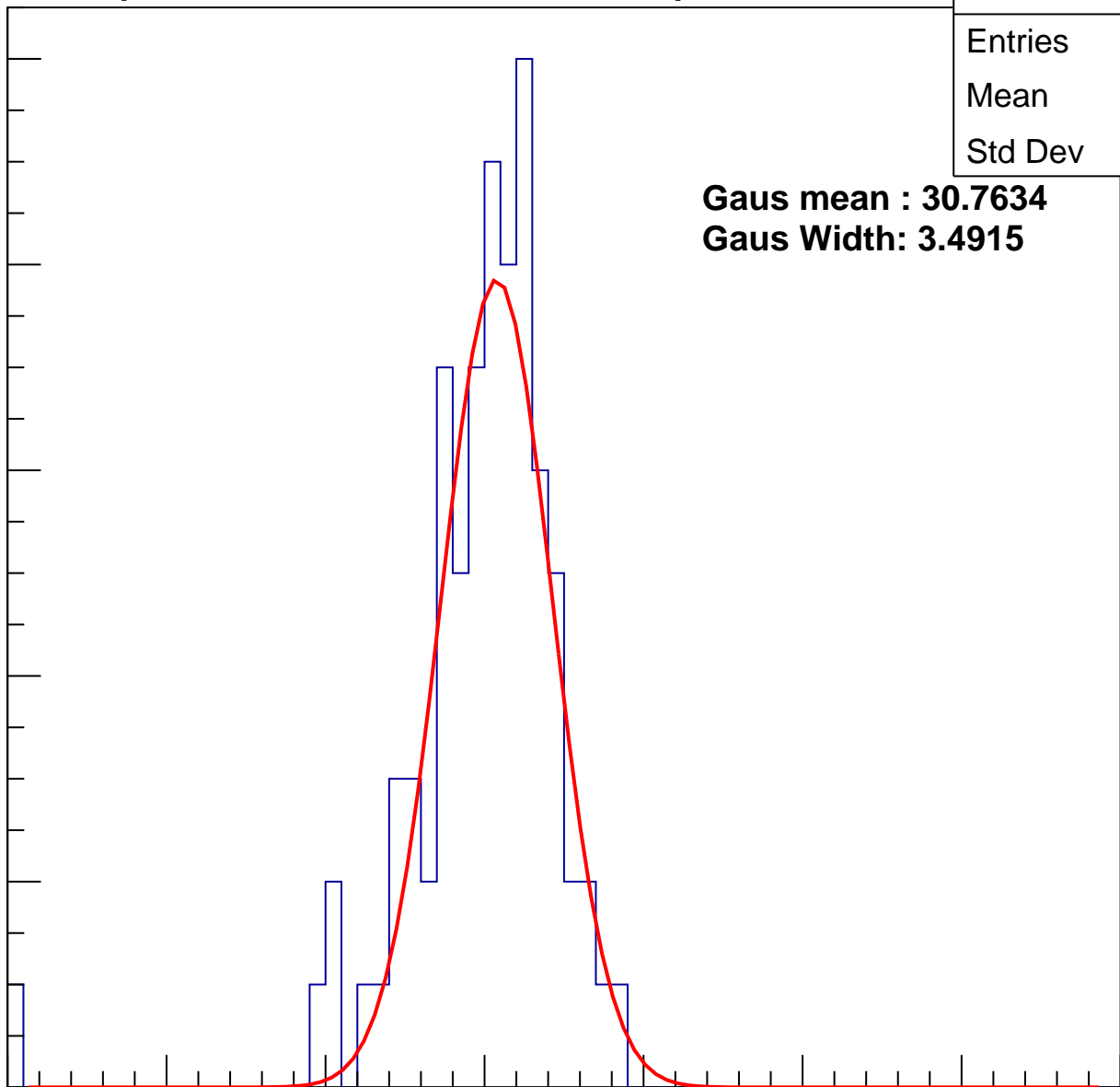
**Gaus Width: 3.4915**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch125, adc1

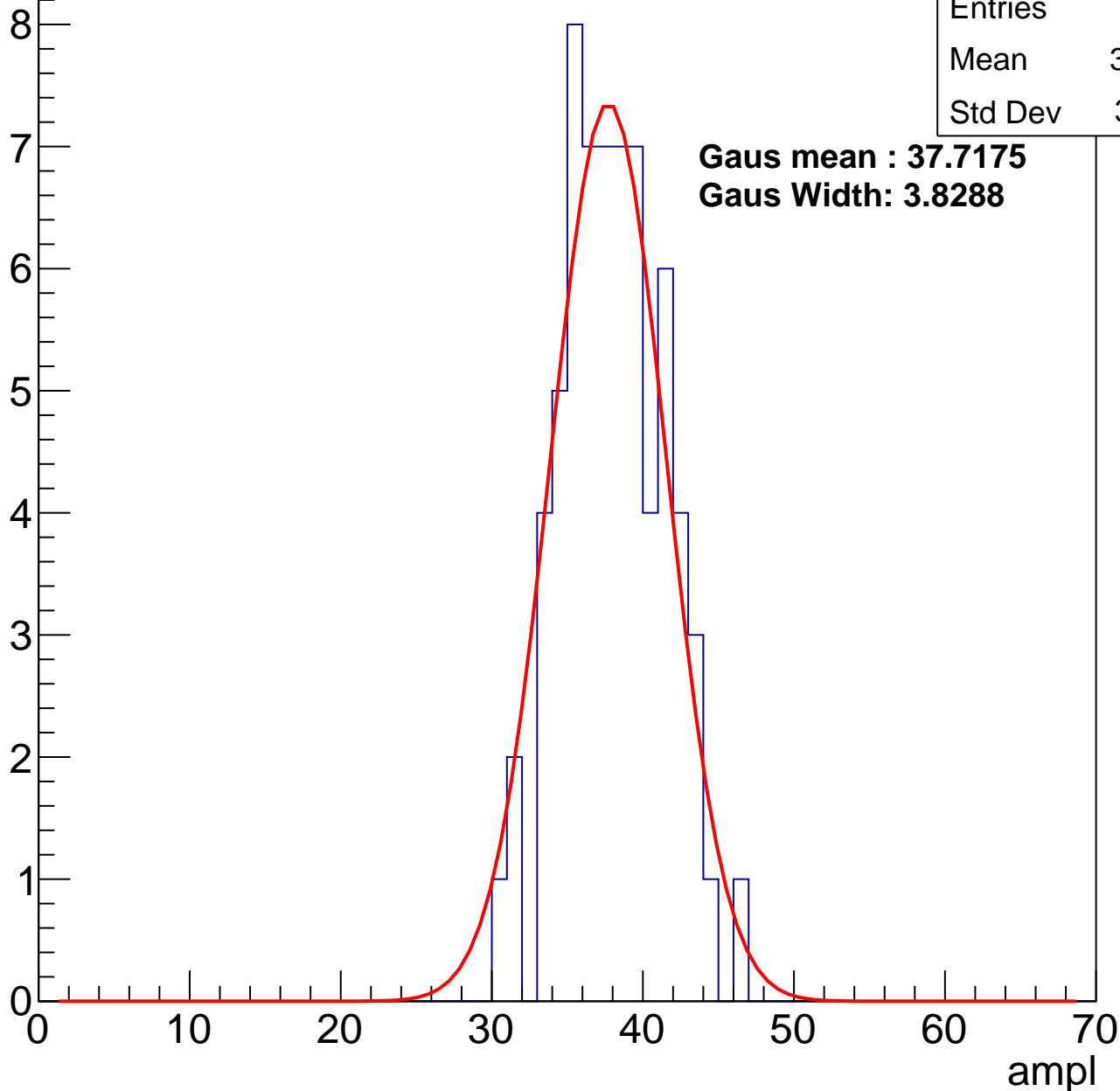
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.57
Std Dev	3.351

**Gaus mean : 37.7175**

**Gaus Width: 3.8288**



# B0L000S, U18-ch125, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	44.08
Std Dev	3.704

**Gaus mean : 43.8136**

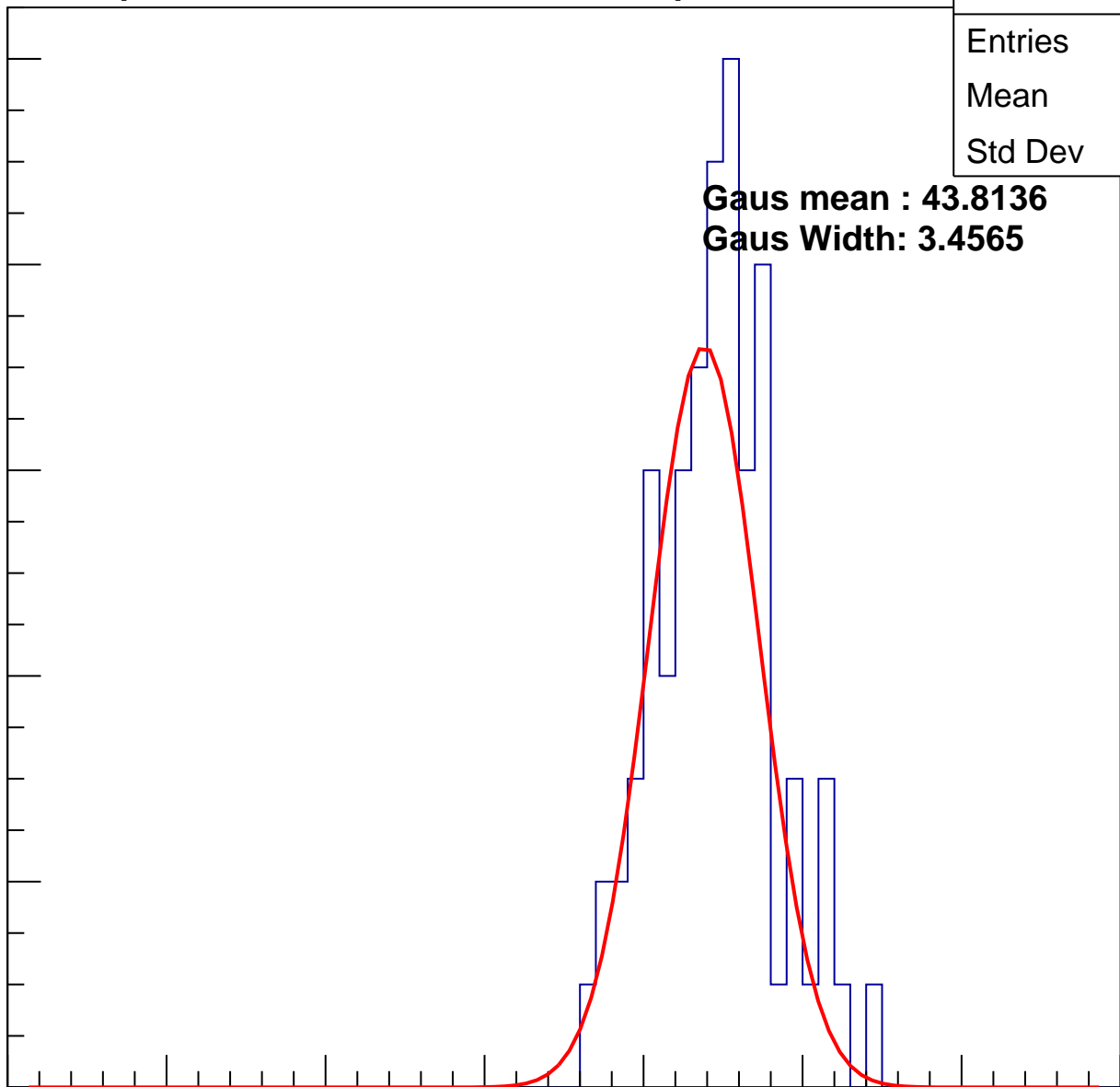
**Gaus Width: 3.4565**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

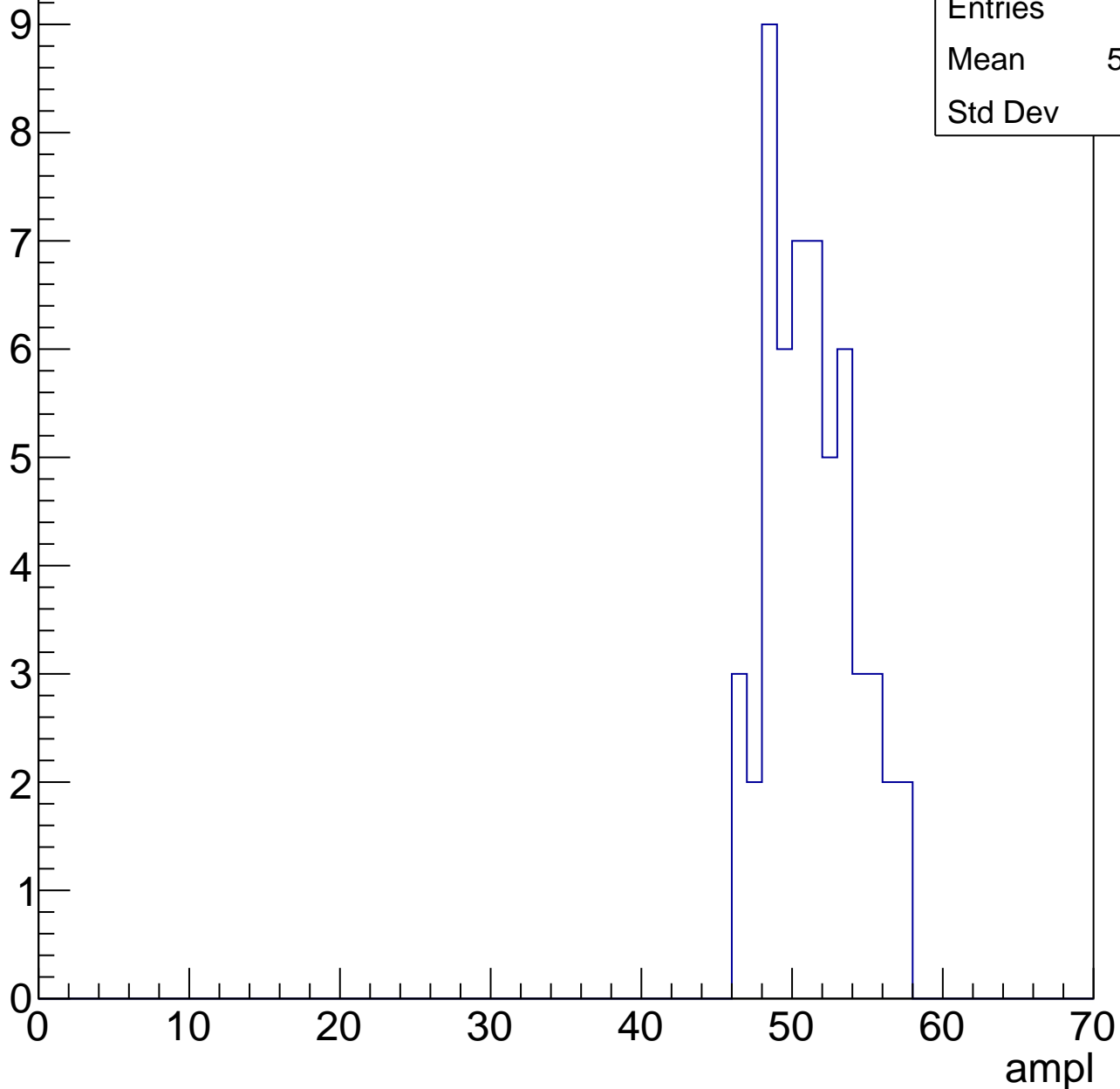


# B0L000S, U18-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	50.84
Std Dev	2.84

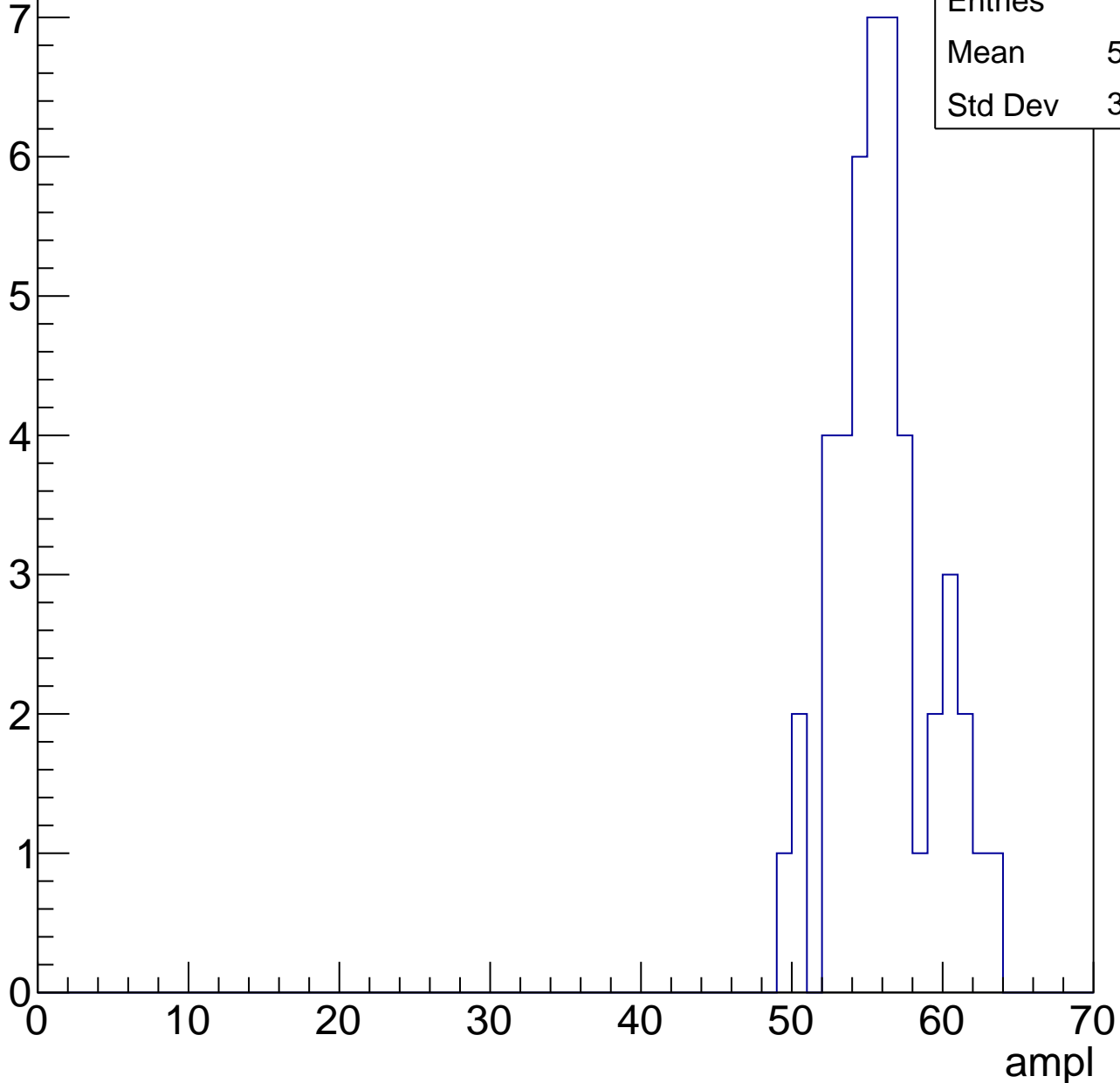


# B0L000S, U18-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

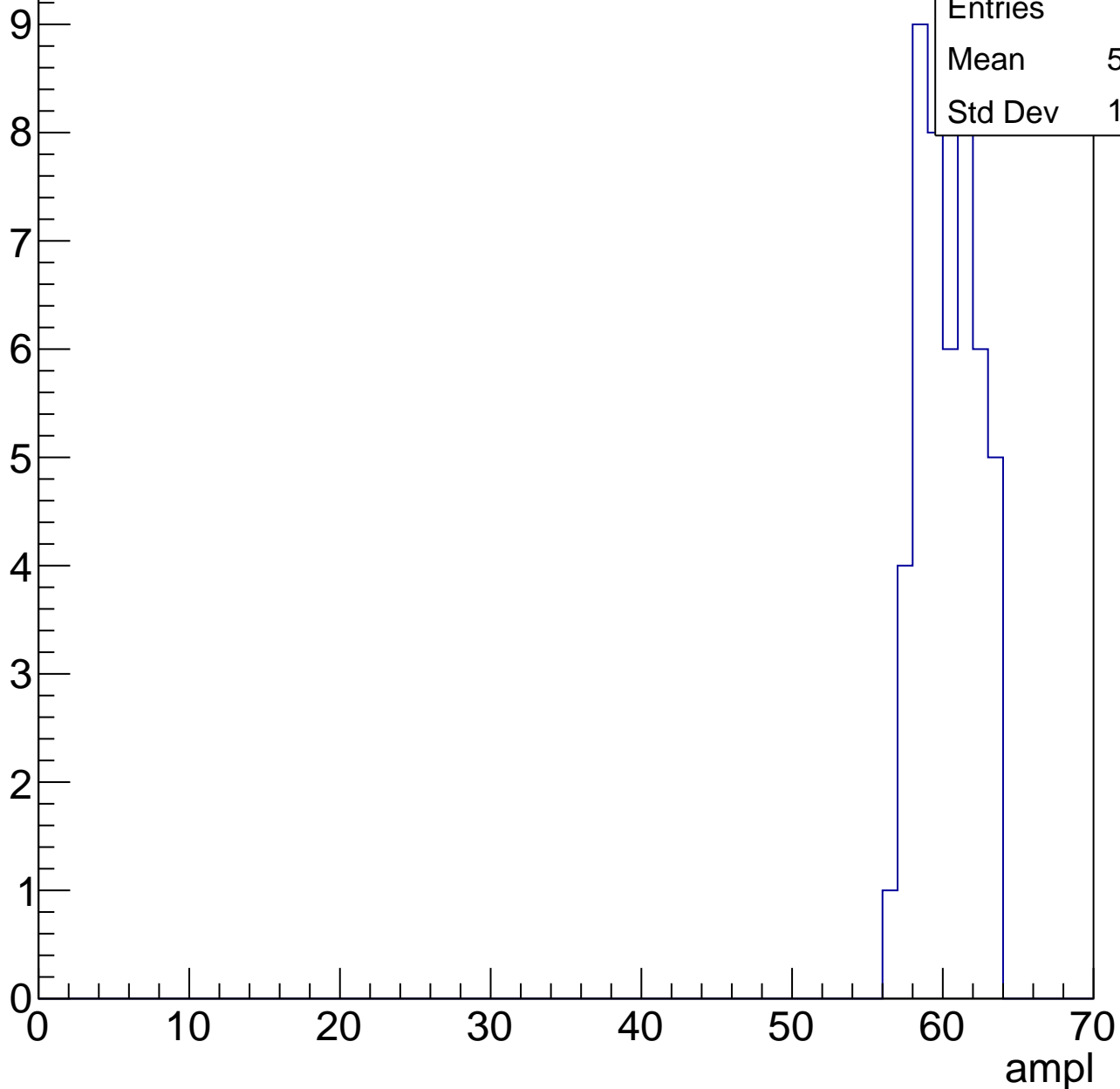
Entries	45
Mean	55.58
Std Dev	3.173



# B0L000S, U18-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

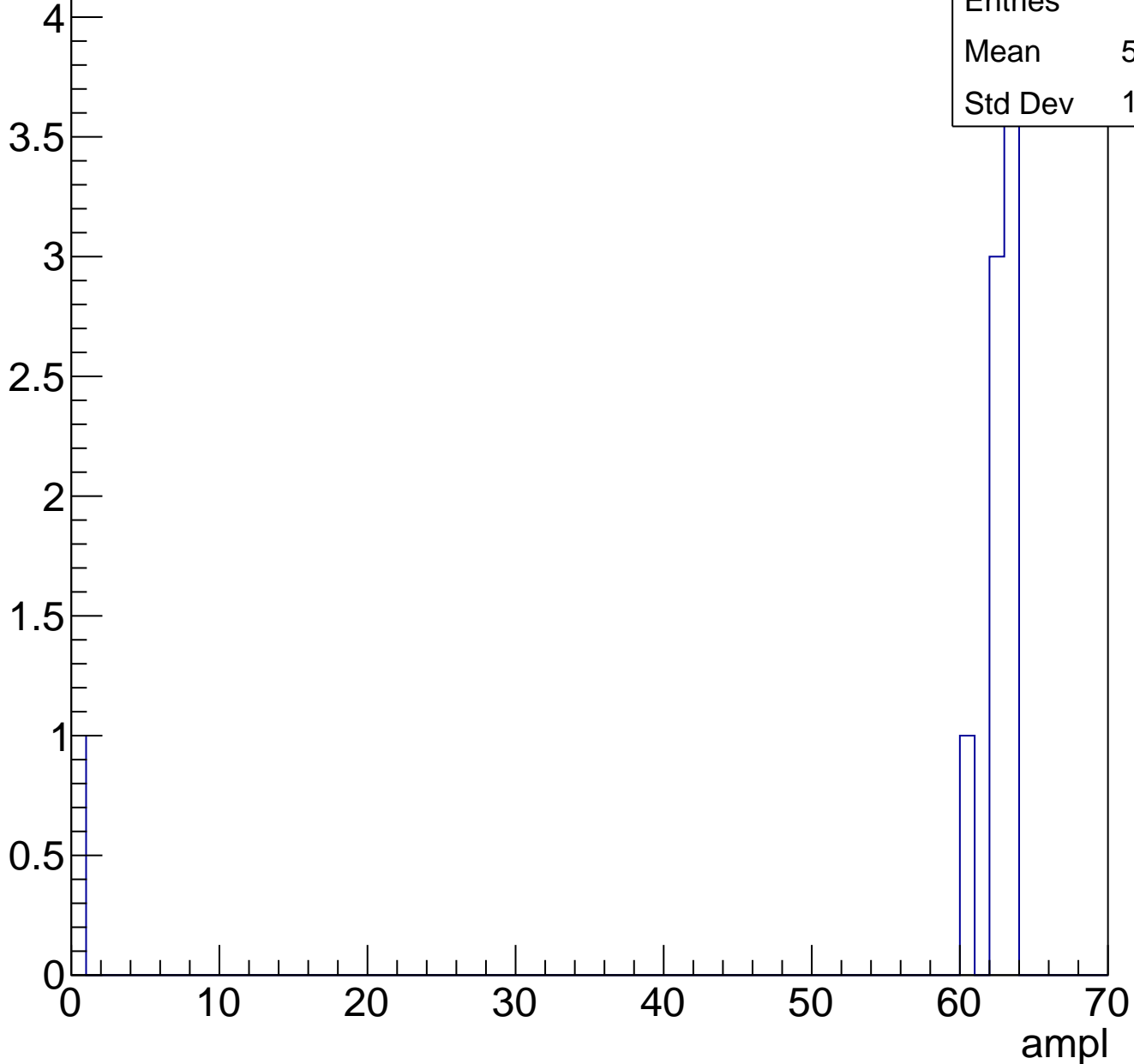
Entry



# B0L000S, U18-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U18-ch126, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	29.84
Std Dev	2.71

**Gaus mean : 30.3834**

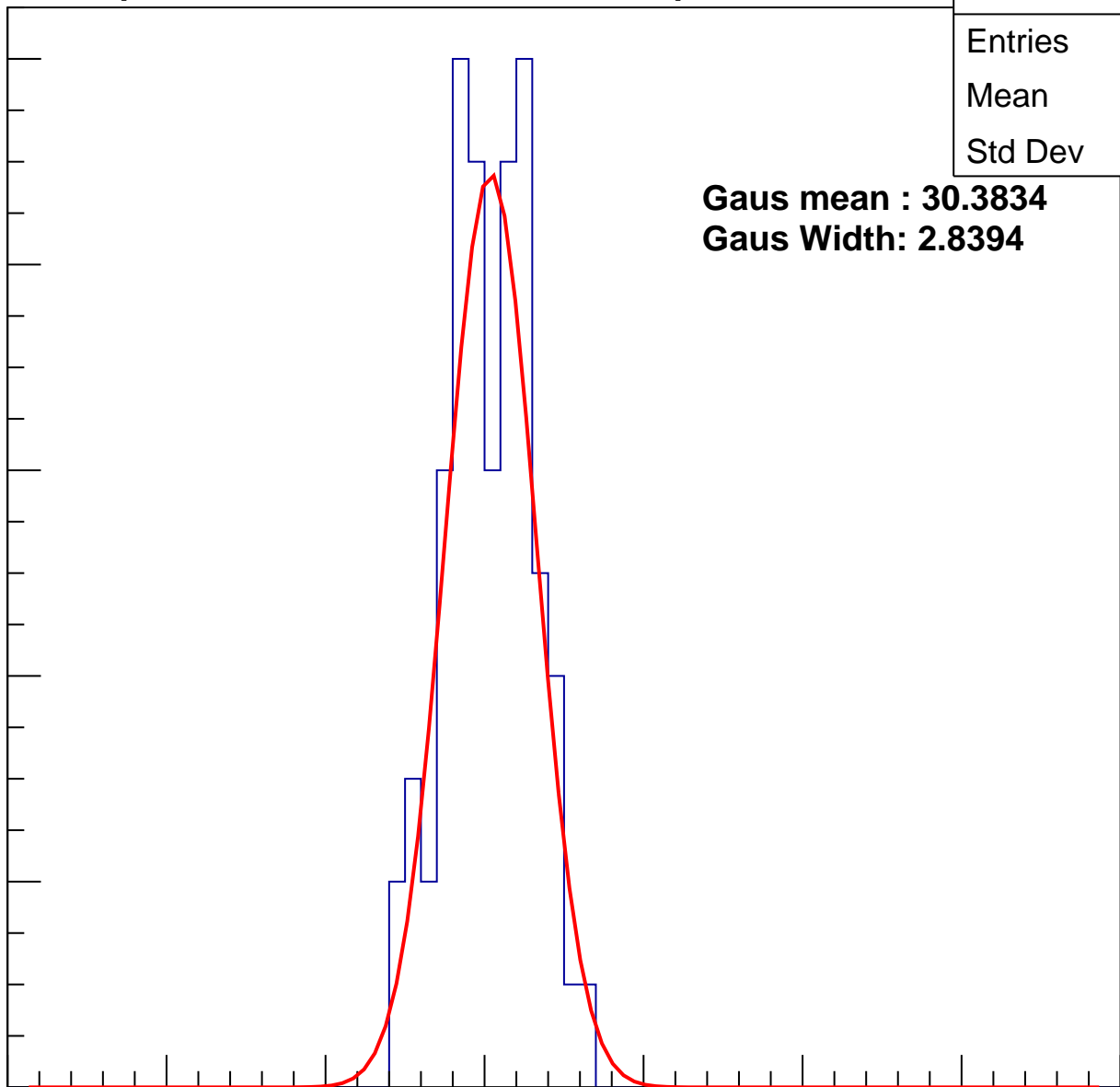
**Gaus Width: 2.8394**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U18-ch126, adc1

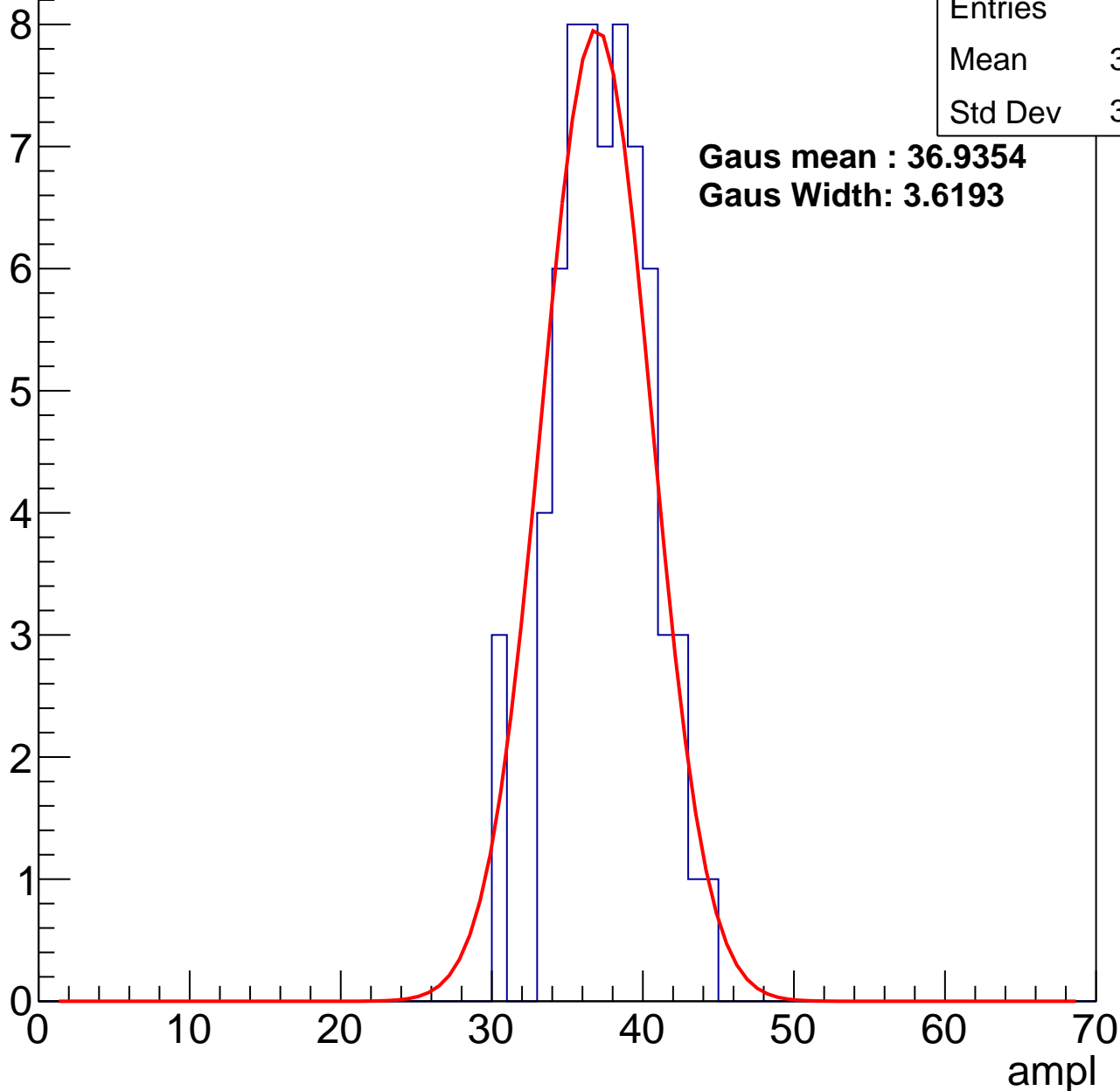
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.02
Std Dev	3.046

**Gaus mean : 36.9354**

**Gaus Width: 3.6193**



# B0L000S, U18-ch126, adc2

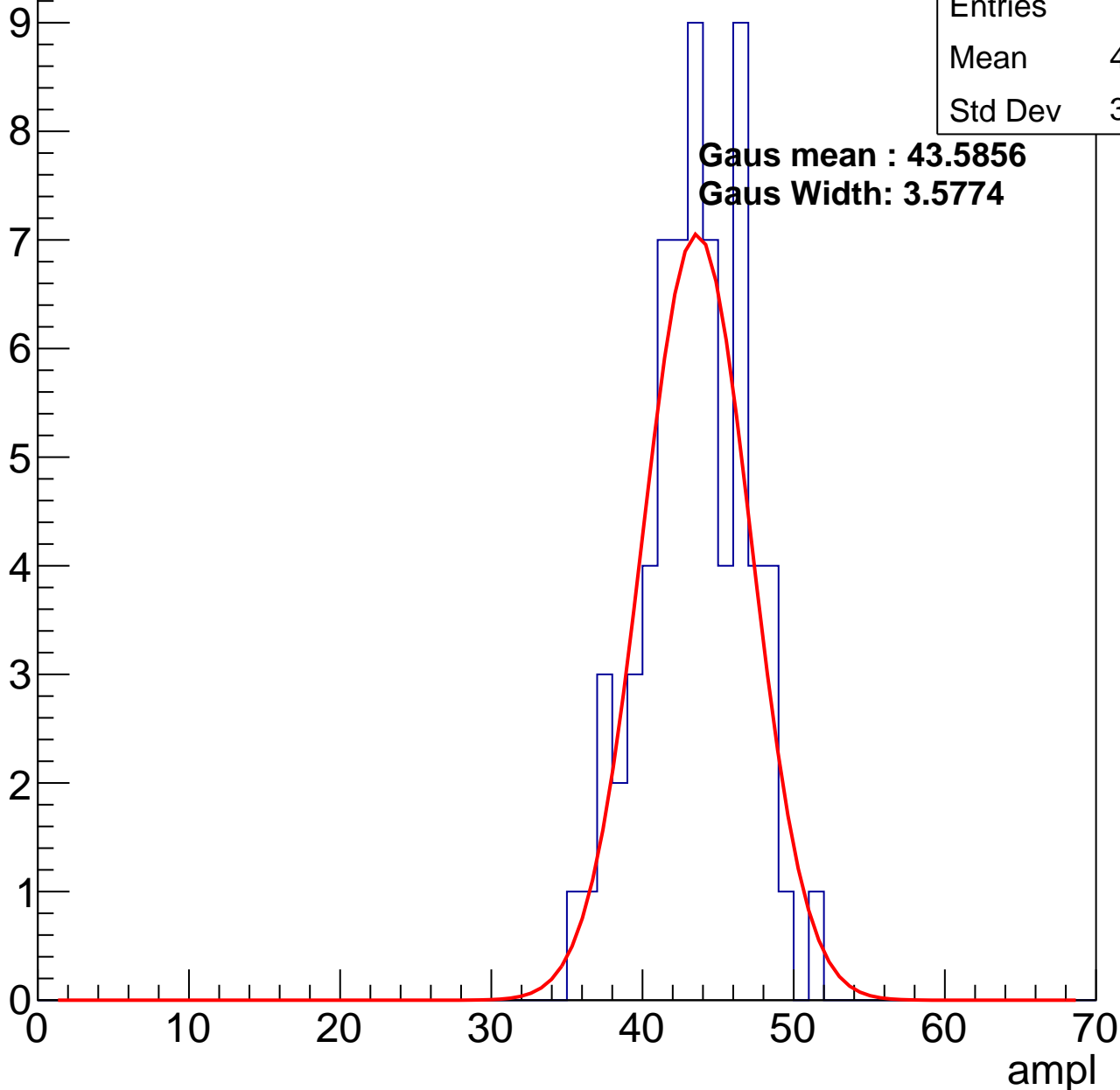
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.06
Std Dev	3.363

**Gaus mean : 43.5856**

**Gaus Width: 3.5774**

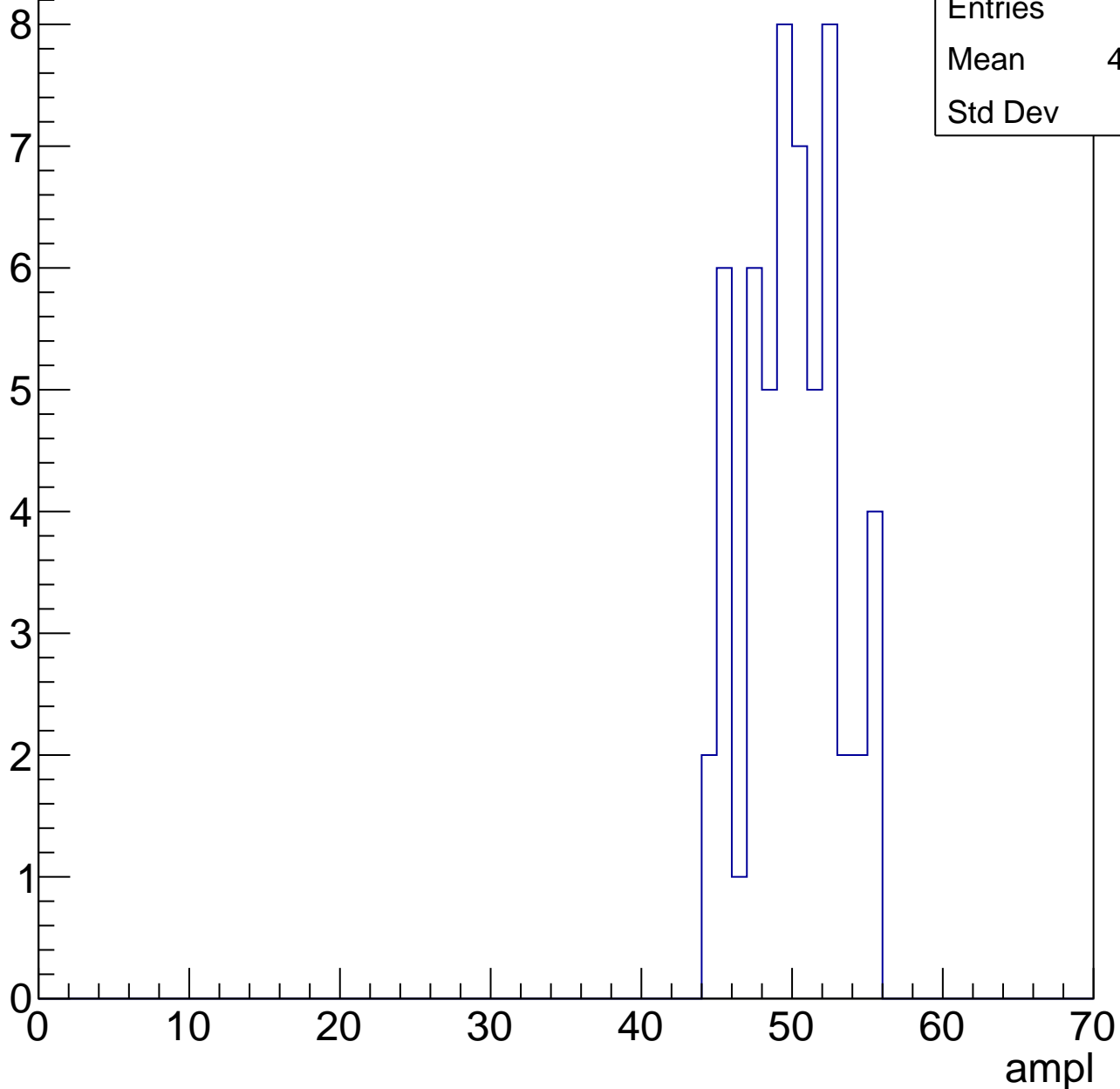


# B0L000S, U18-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.52
Std Dev	2.97

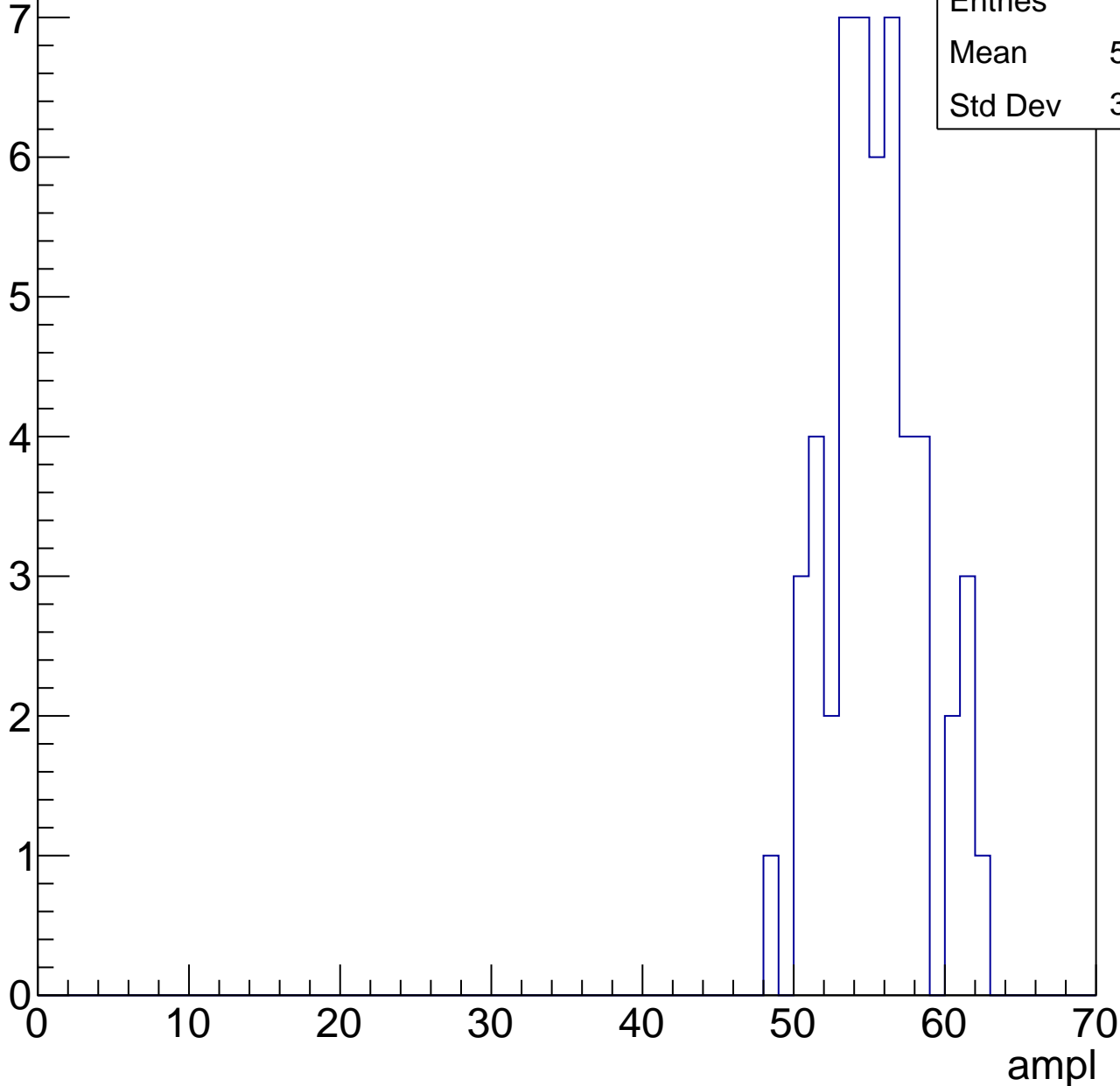


# B0L000S, U18-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.94
Std Dev	3.152



# B0L000S, U18-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	59.35
Std Dev	2.79

10

8

6

4

2

0

0

10

20

30

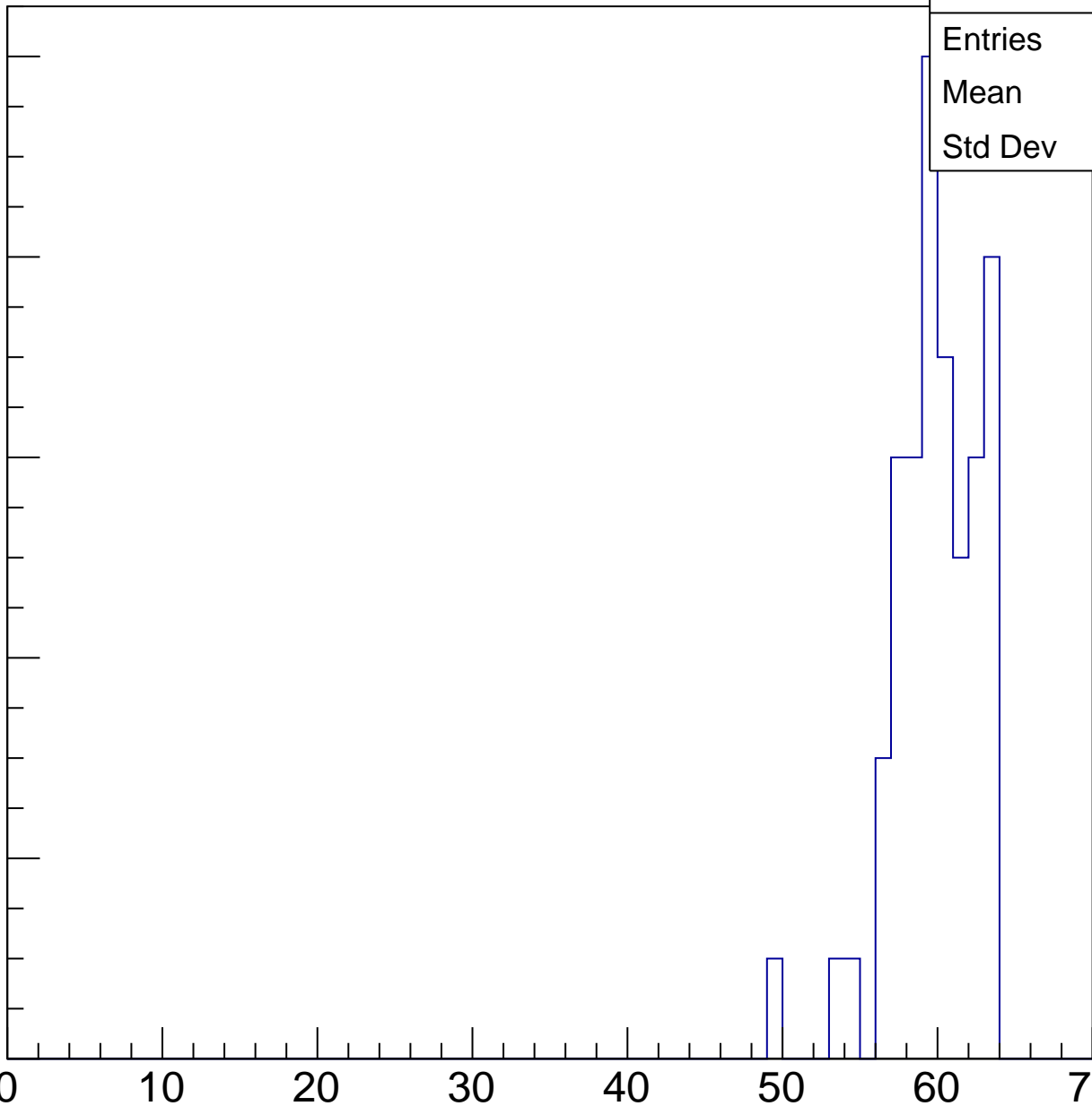
40

50

60

70

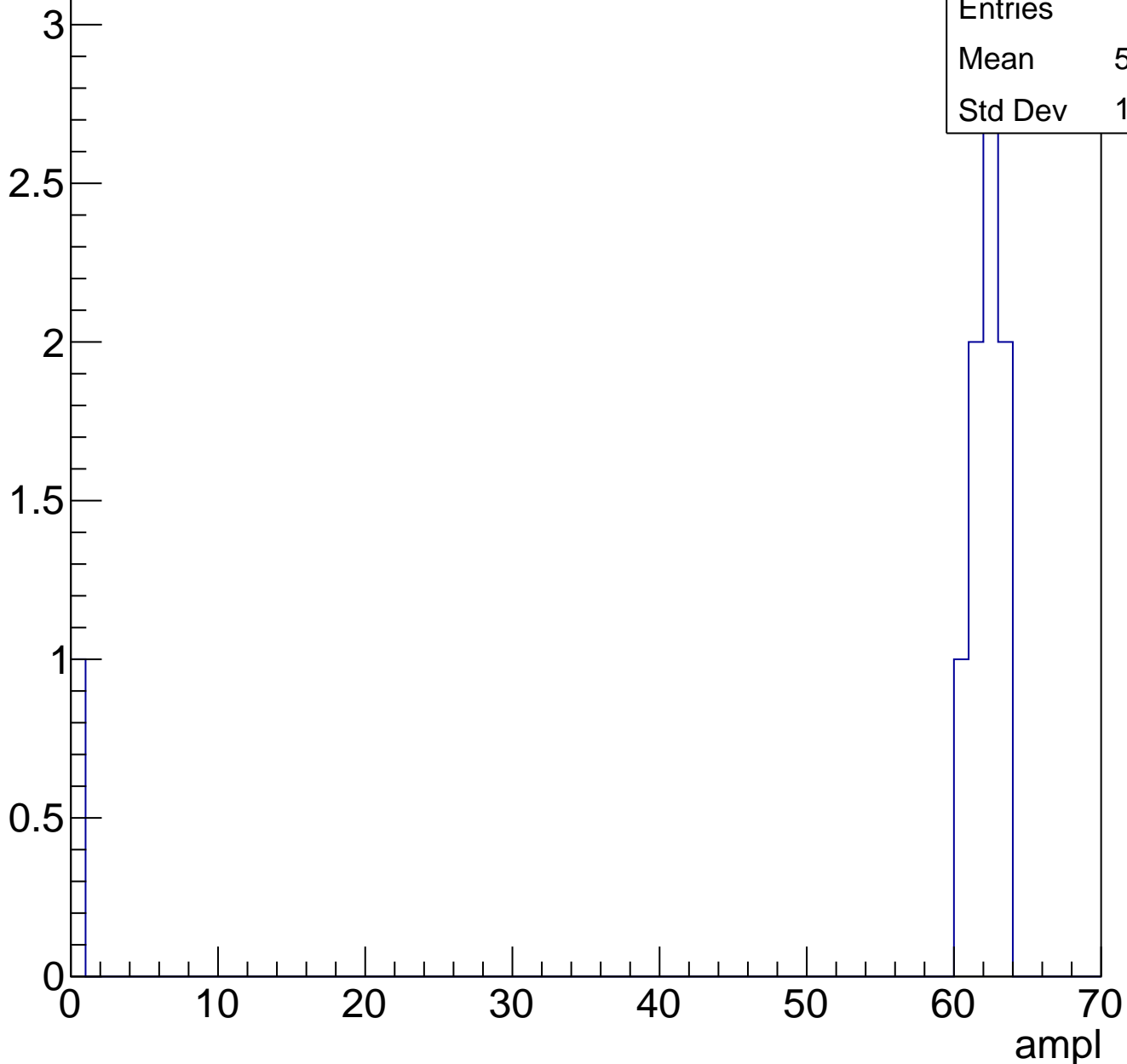
ampl



# B0L000S, U18-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U18-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U18-ch127, adc0

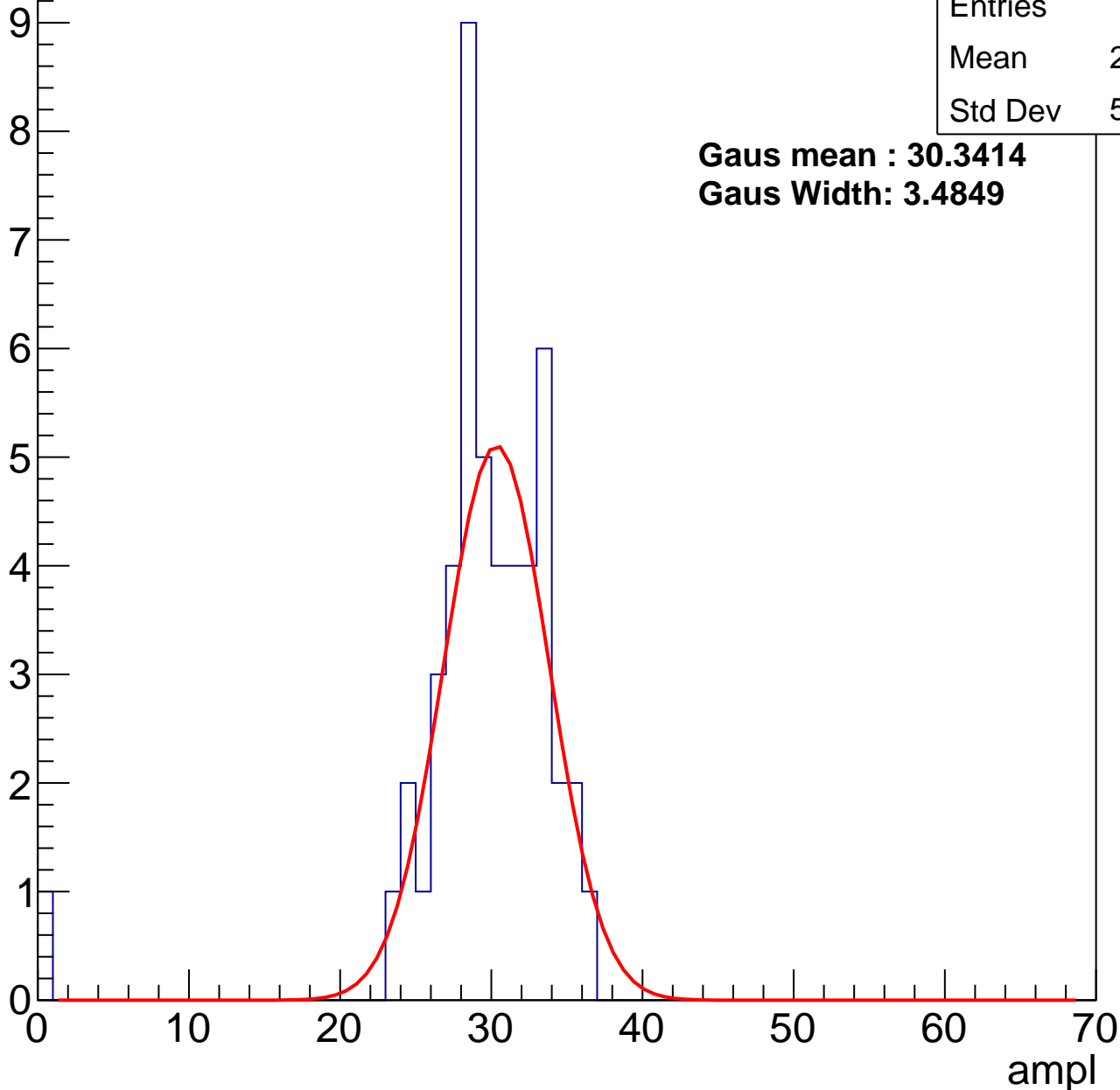
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	29.04
Std Dev	5.186

**Gaus mean : 30.3414**

**Gaus Width: 3.4849**



# B0L000S, U18-ch127, adc1

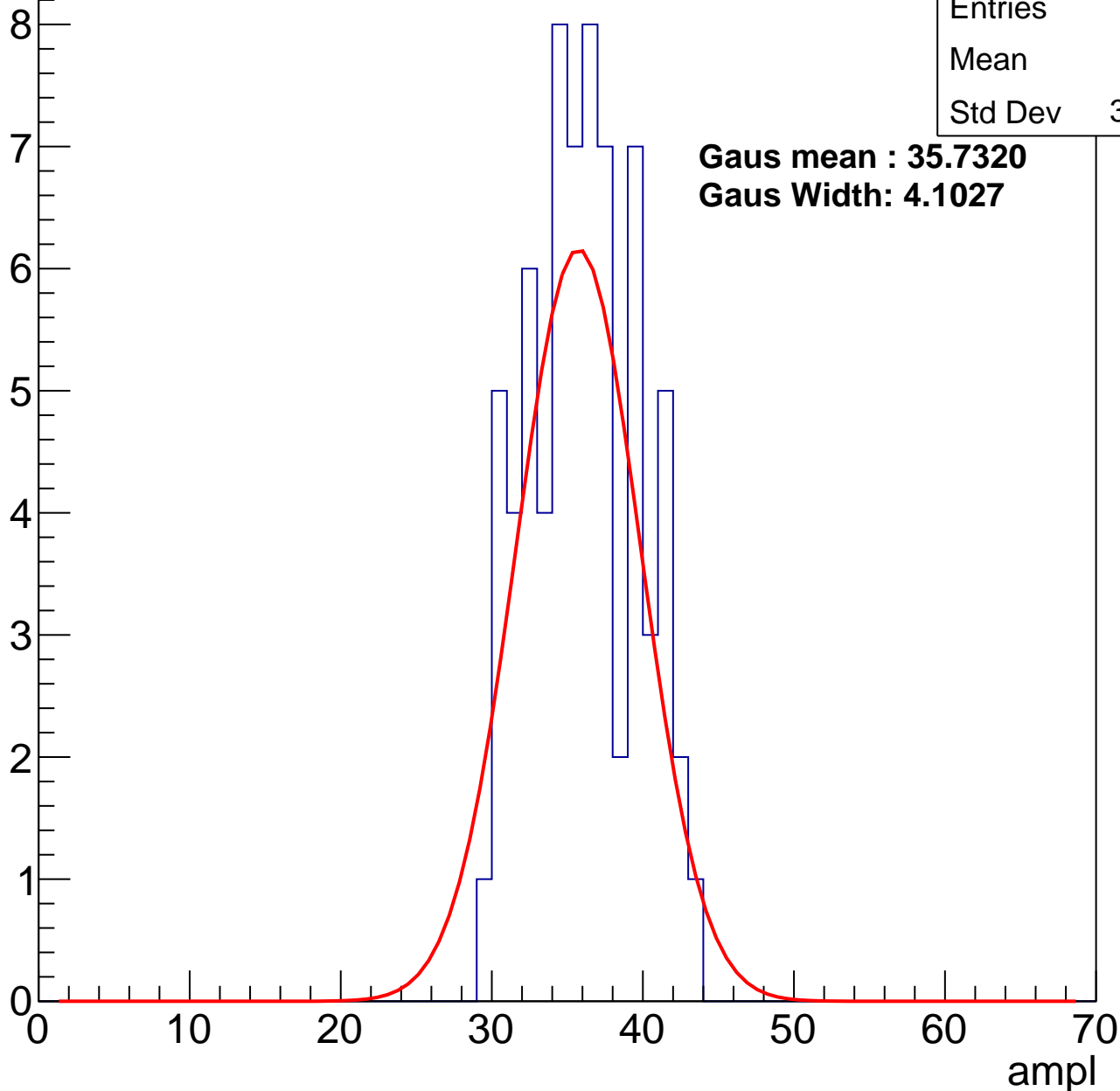
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.6
Std Dev	3.507

**Gaus mean : 35.7320**

**Gaus Width: 4.1027**



# B0L000S, U18-ch127, adc2

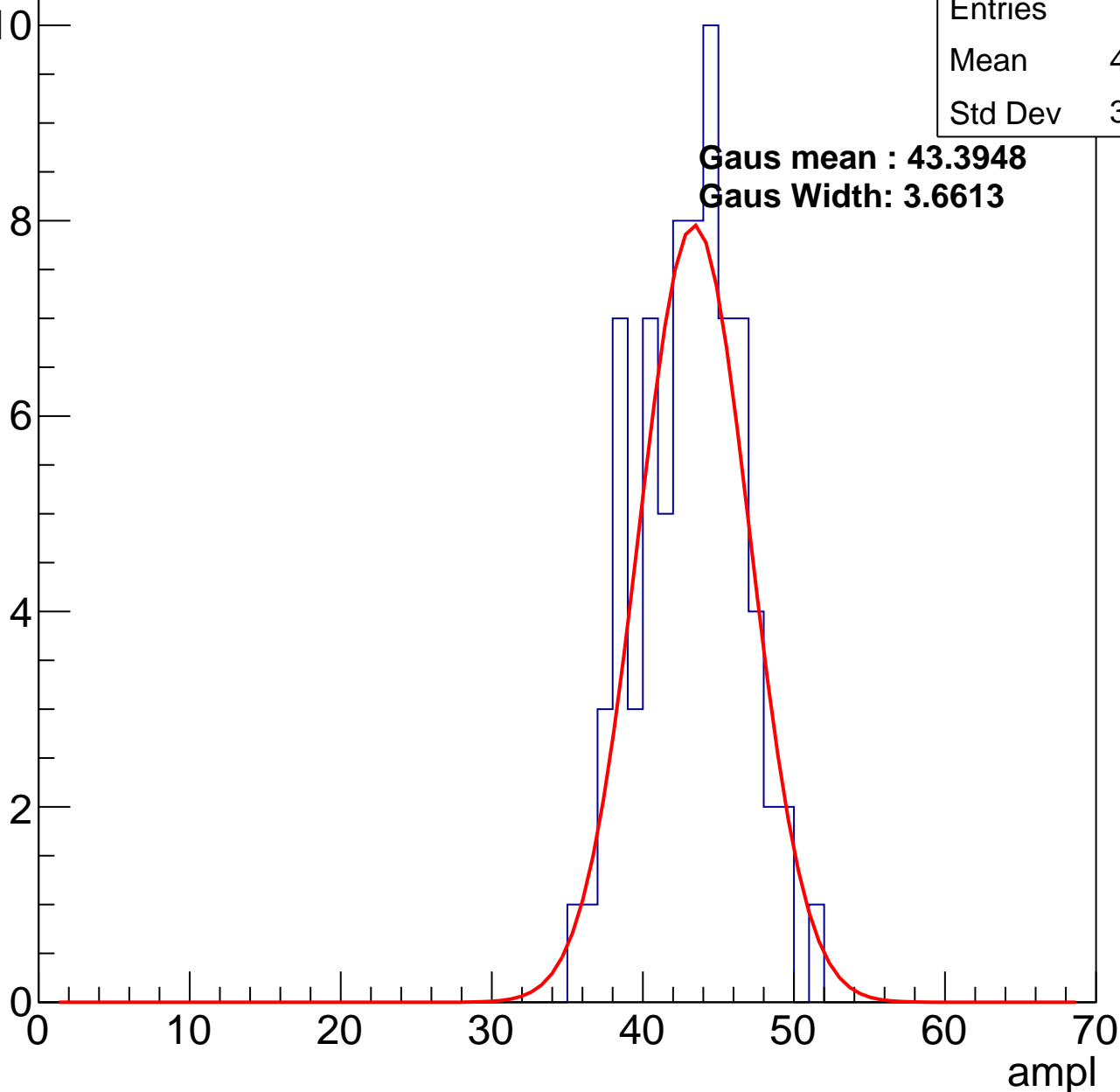
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.63
Std Dev	3.394

**Gaus mean : 43.3948**

**Gaus Width: 3.6613**

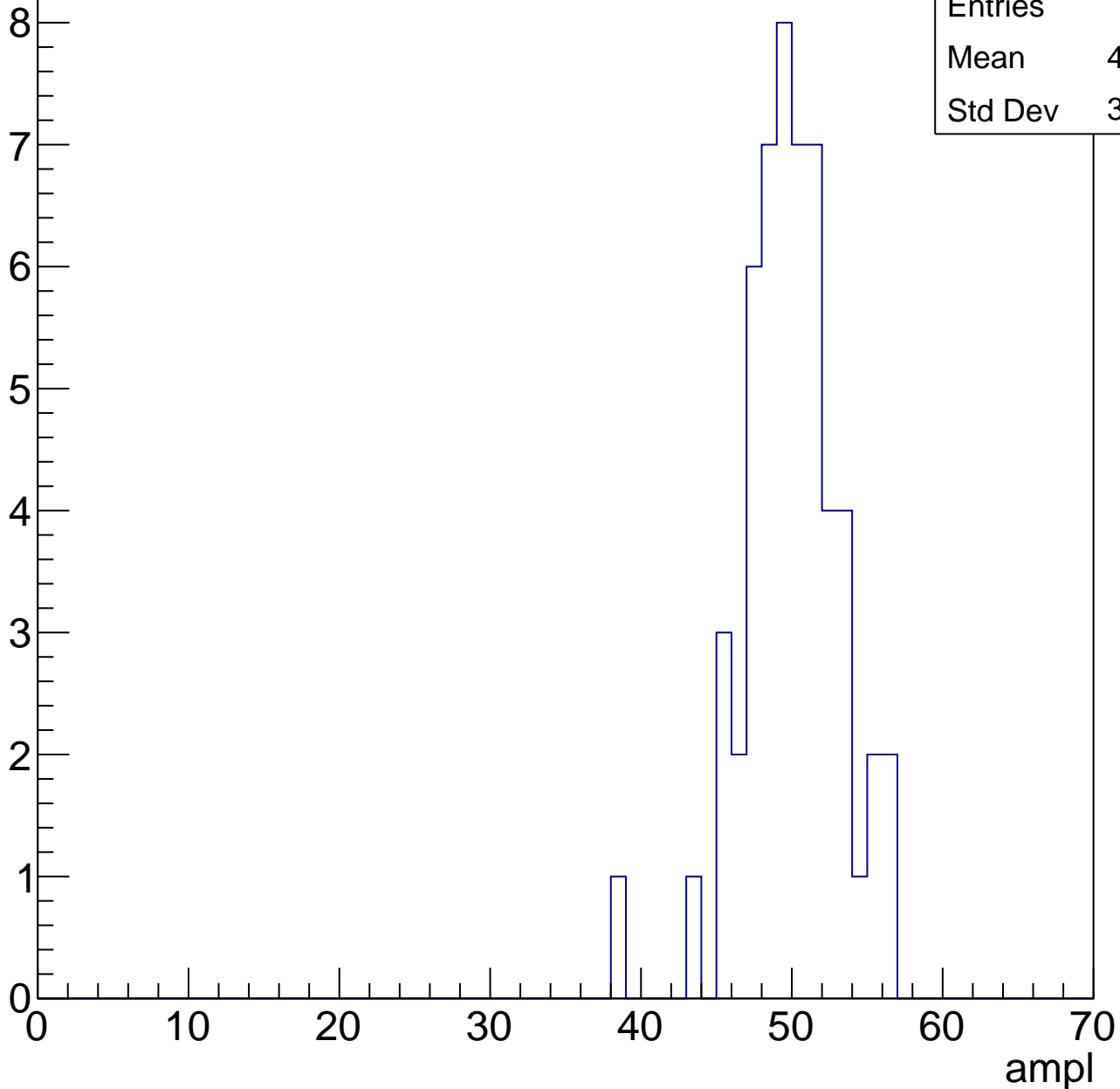


# B0L000S, U18-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.47
Std Dev	3.224

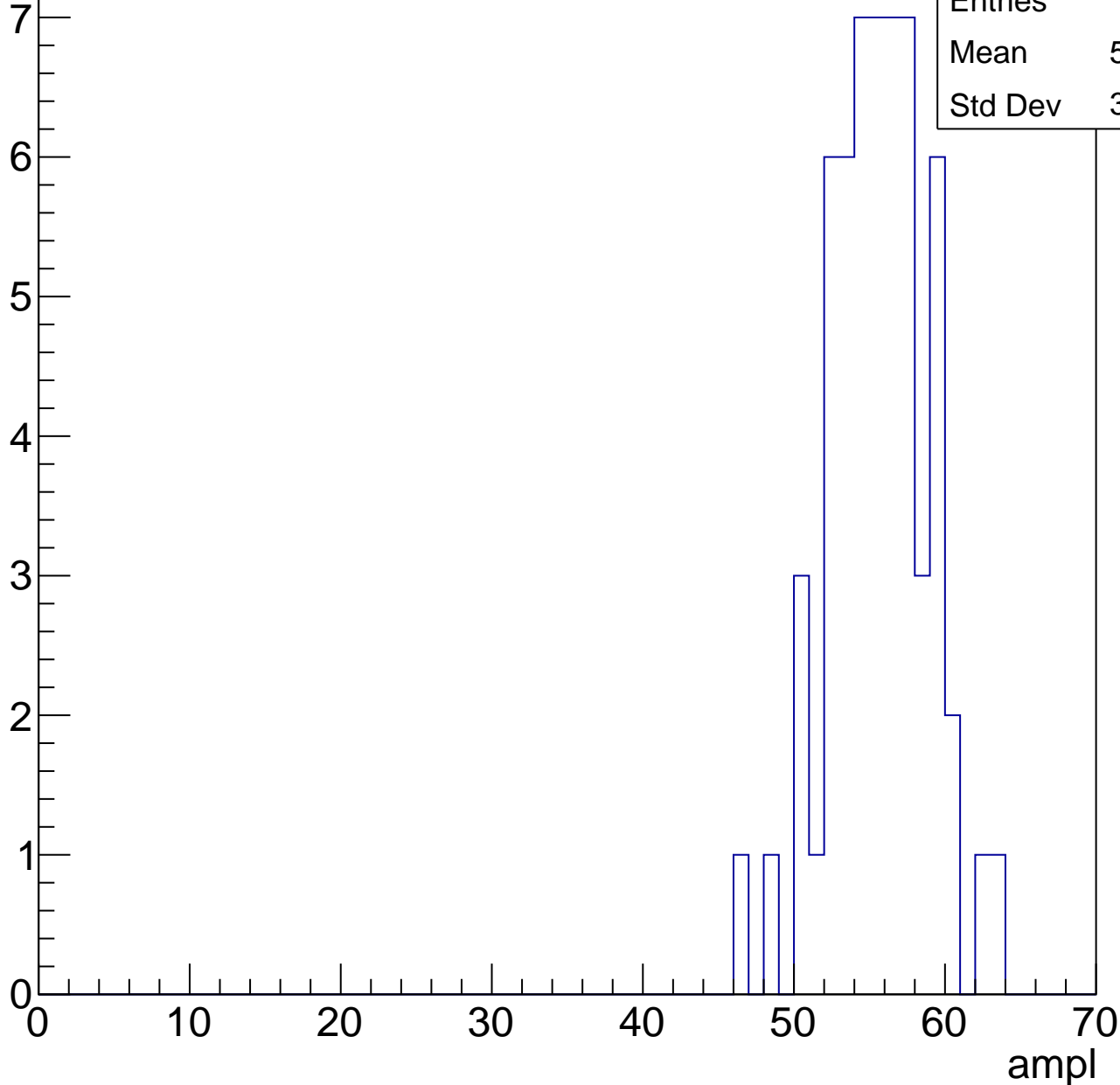


# B0L000S, U18-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.12
Std Dev	3.258

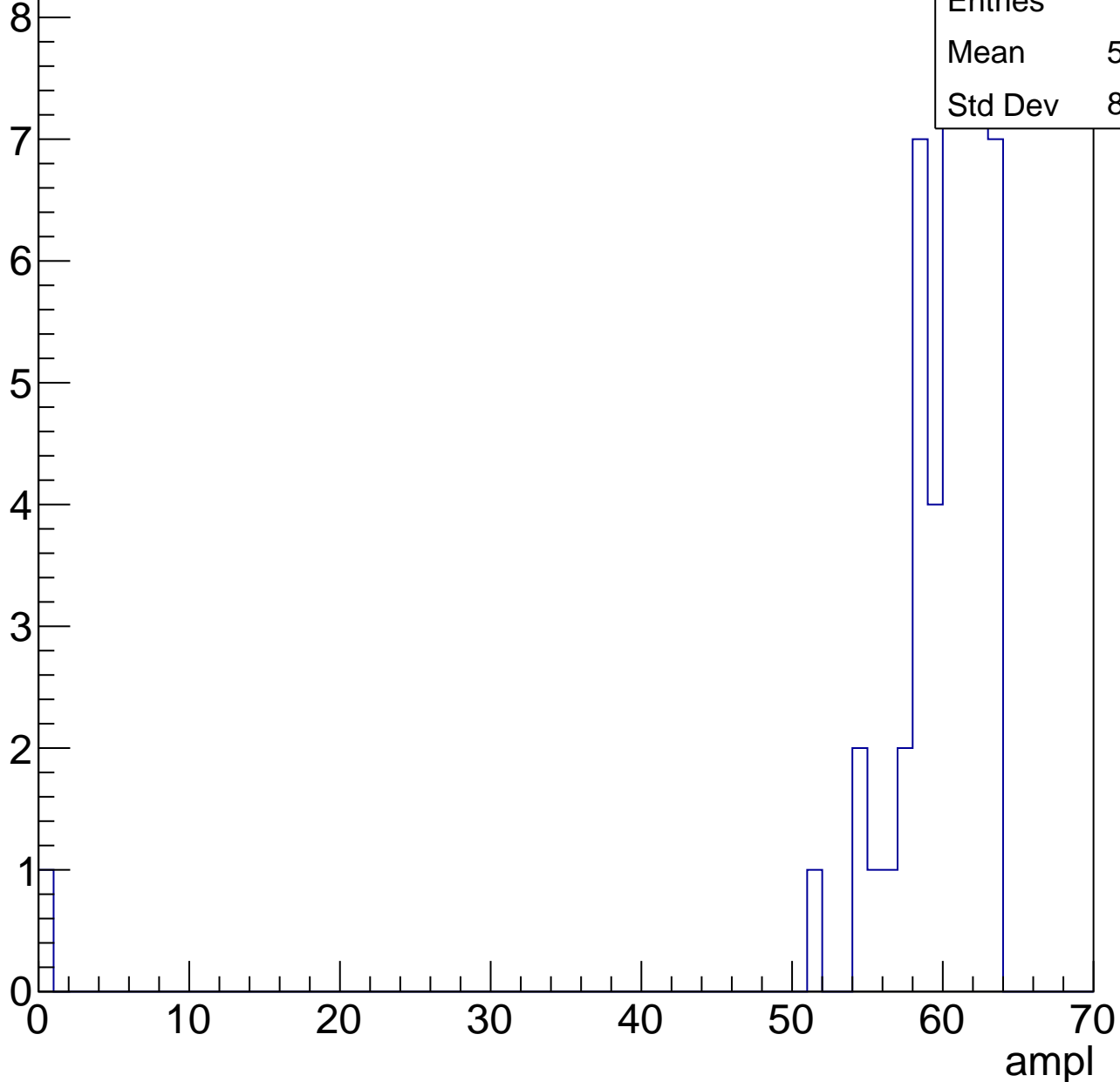


# B0L000S, U18-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	58.62
Std Dev	8.777



# B0L000S, U18-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

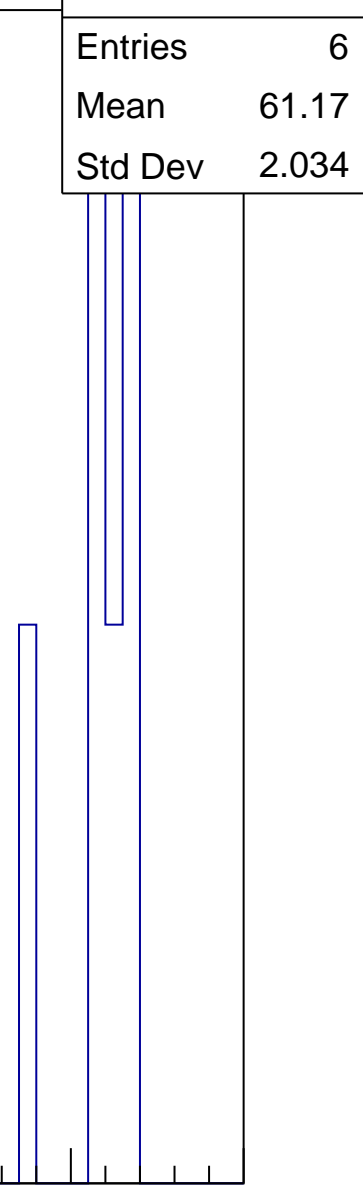
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	6
Mean	61.17
Std Dev	2.034

0 10 20 30 40 50 60 70

ampl





# B0L000S, U18-ch127, adc7

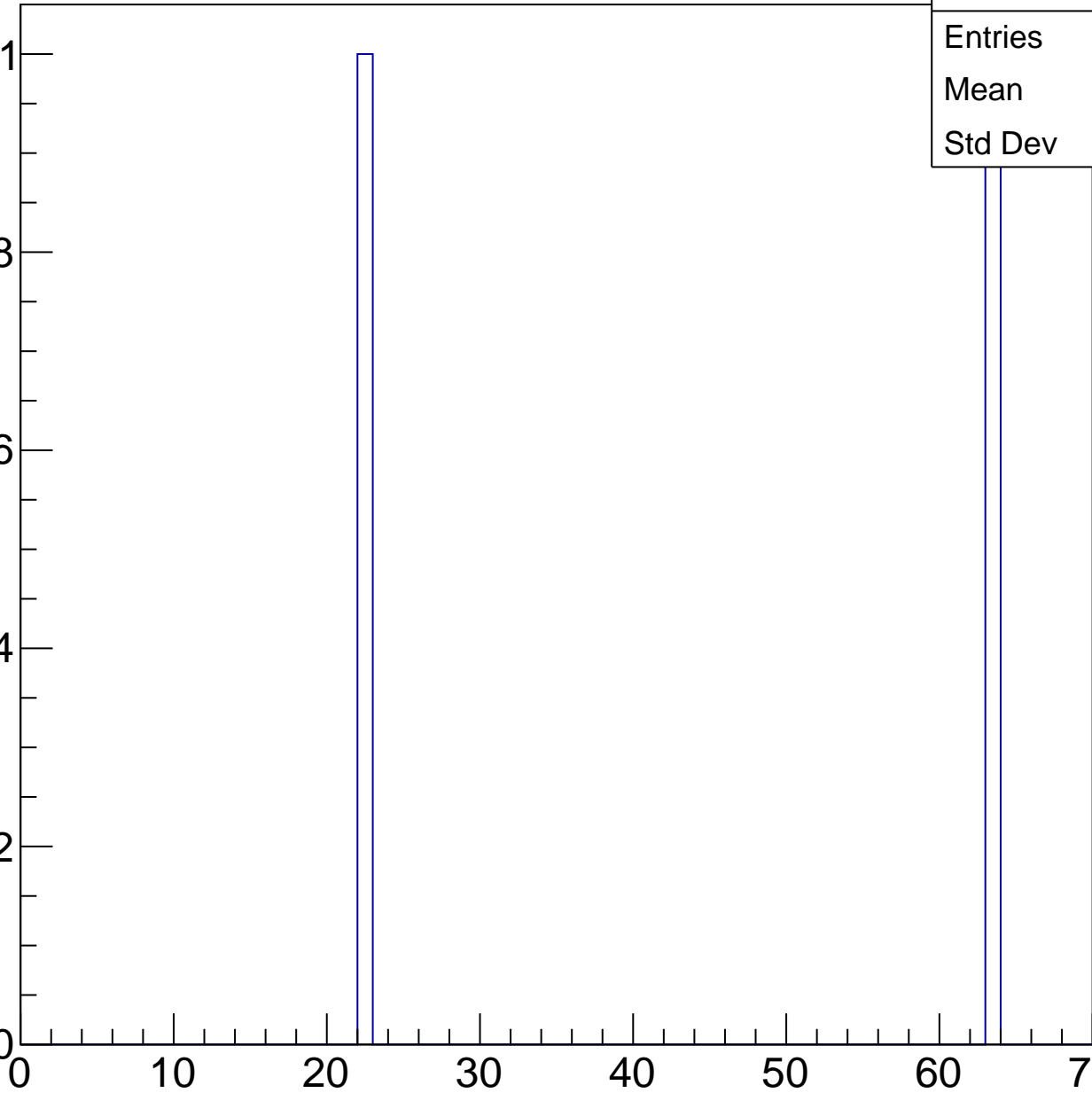
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl



# B0L000S, U18-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

