



# B0L000S, U20-ch0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.47

**Turn on : 25.5208**

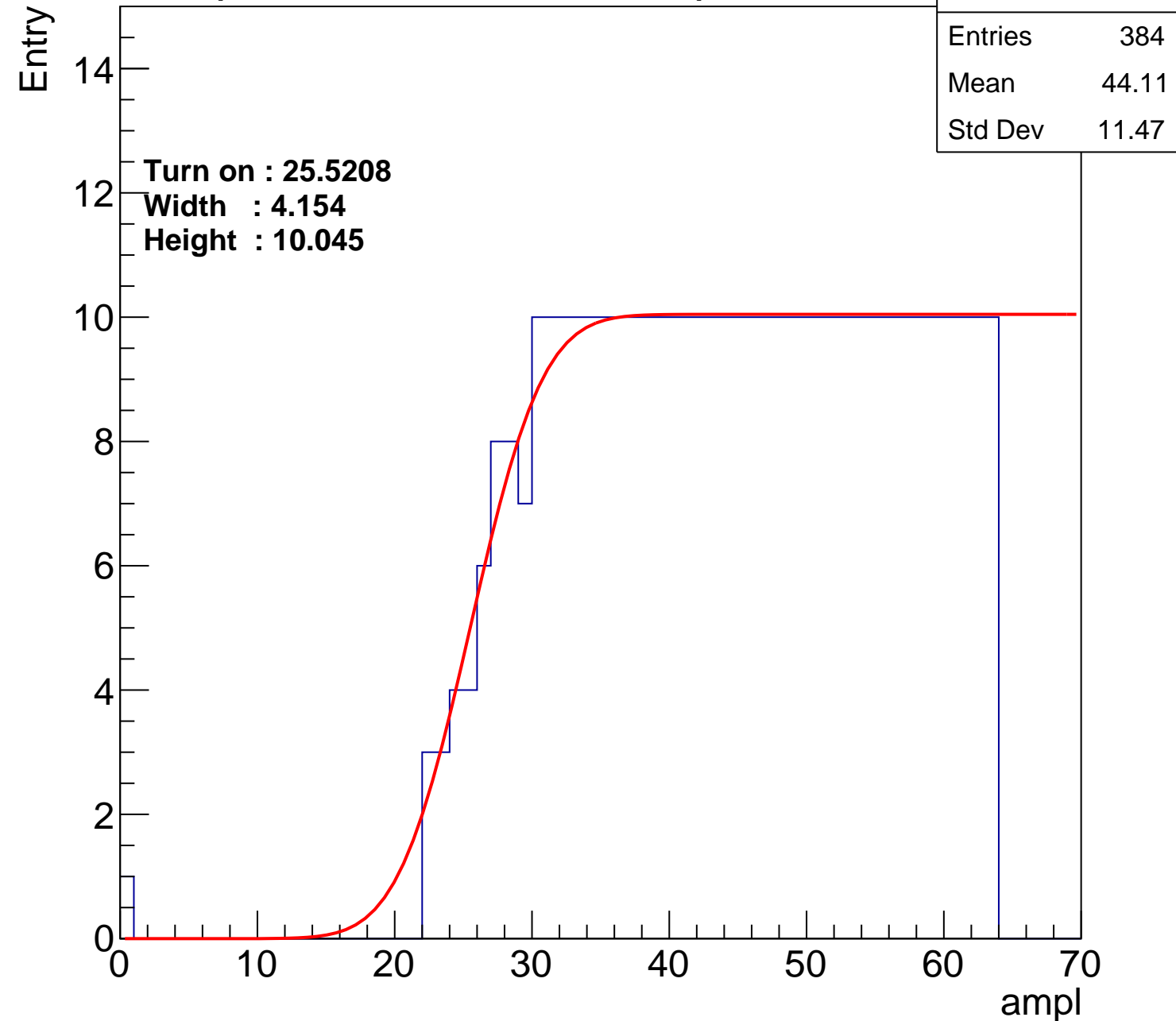
**Width : 4.154**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	390
Mean	43.65
Std Dev	12.09

Turn on : 25.1330

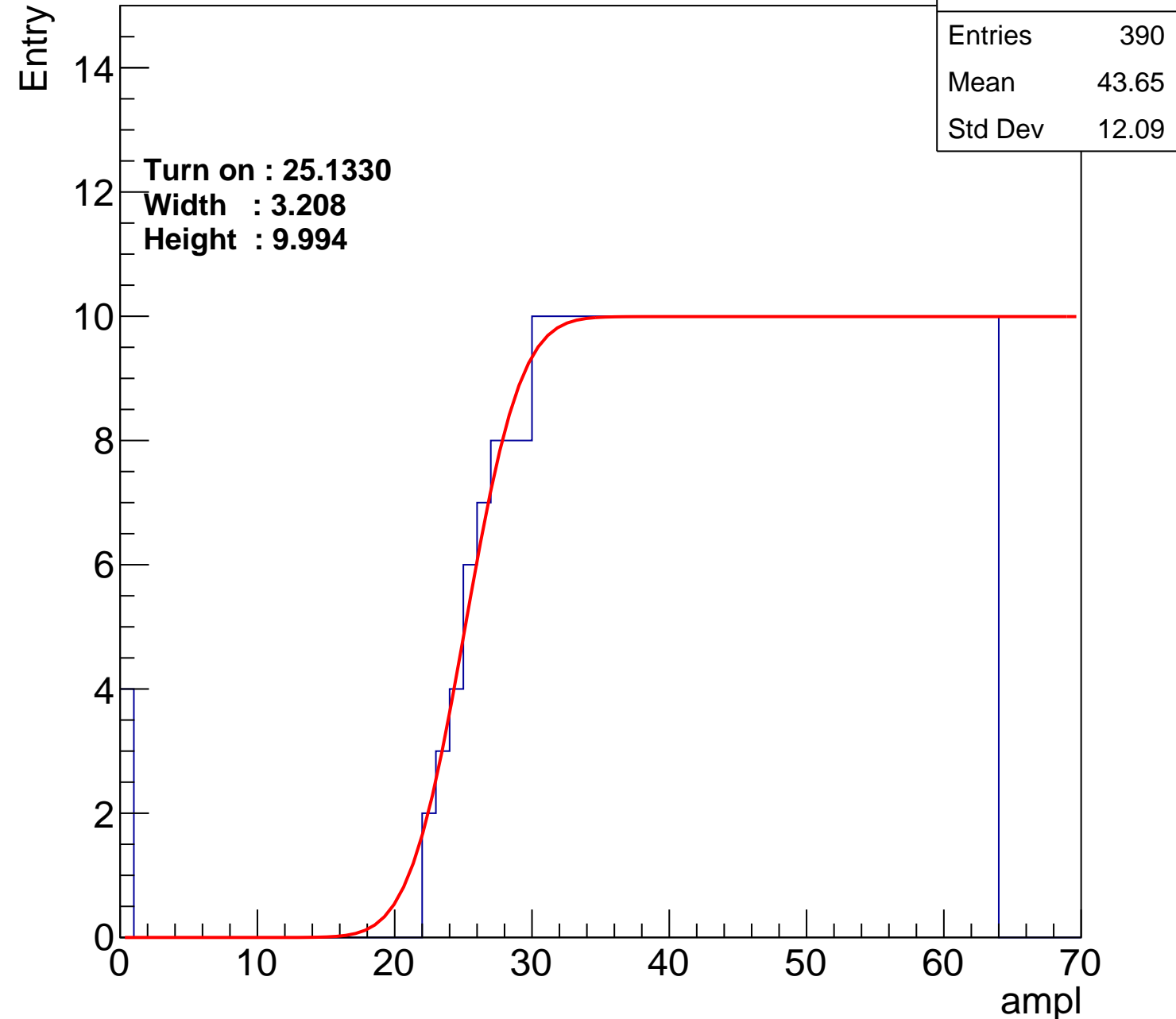
Width : 3.208

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch2

calib\_packv5\_042523\_0143.root, FC#5, port B1

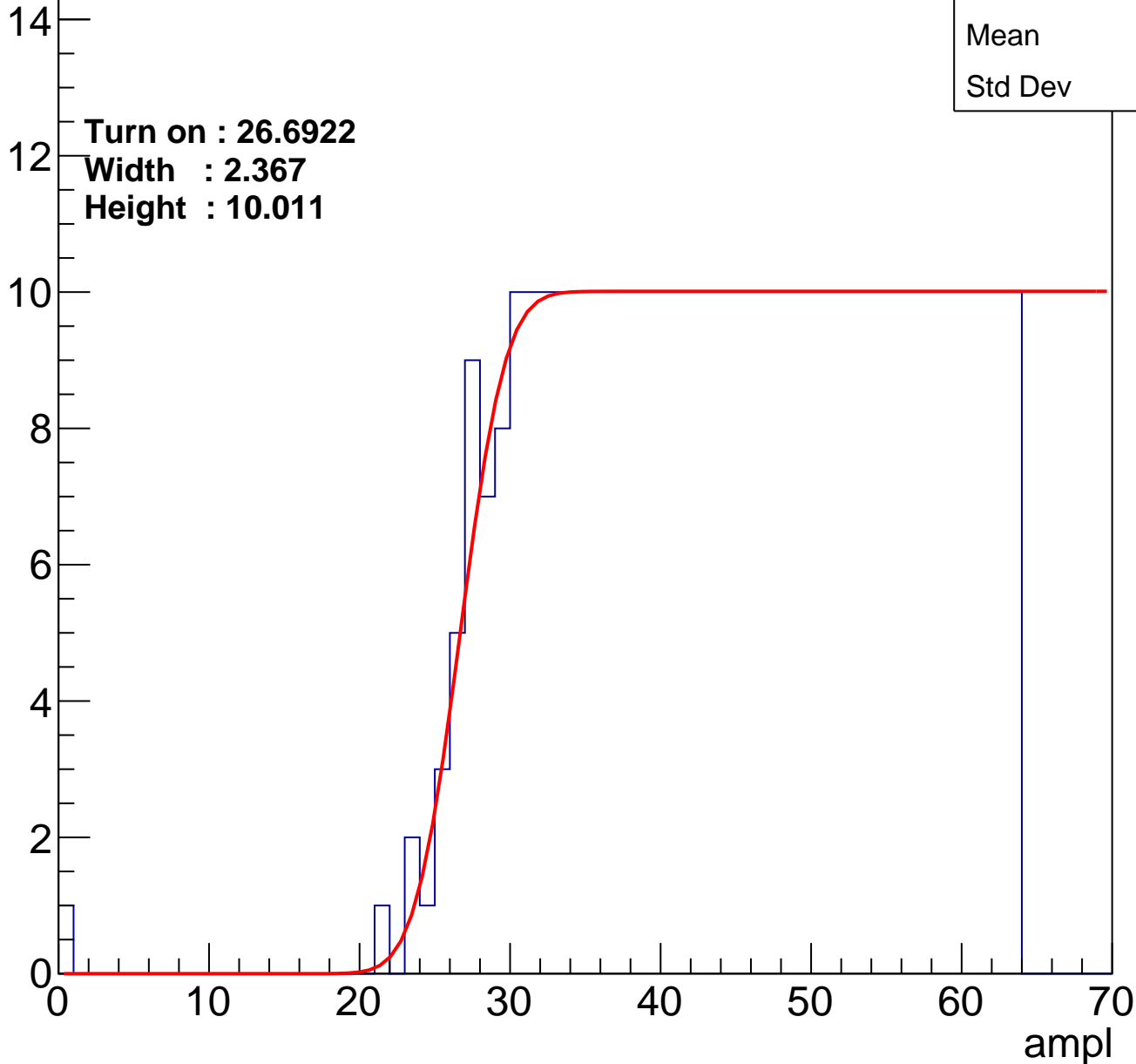
Entry

Entries	377
Mean	44.5
Std Dev	11.21

Turn on : 26.6922

Width : 2.367

Height : 10.011



# B0L000S, U20-ch3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.26
Std Dev	11.04

Turn on : 28.4581

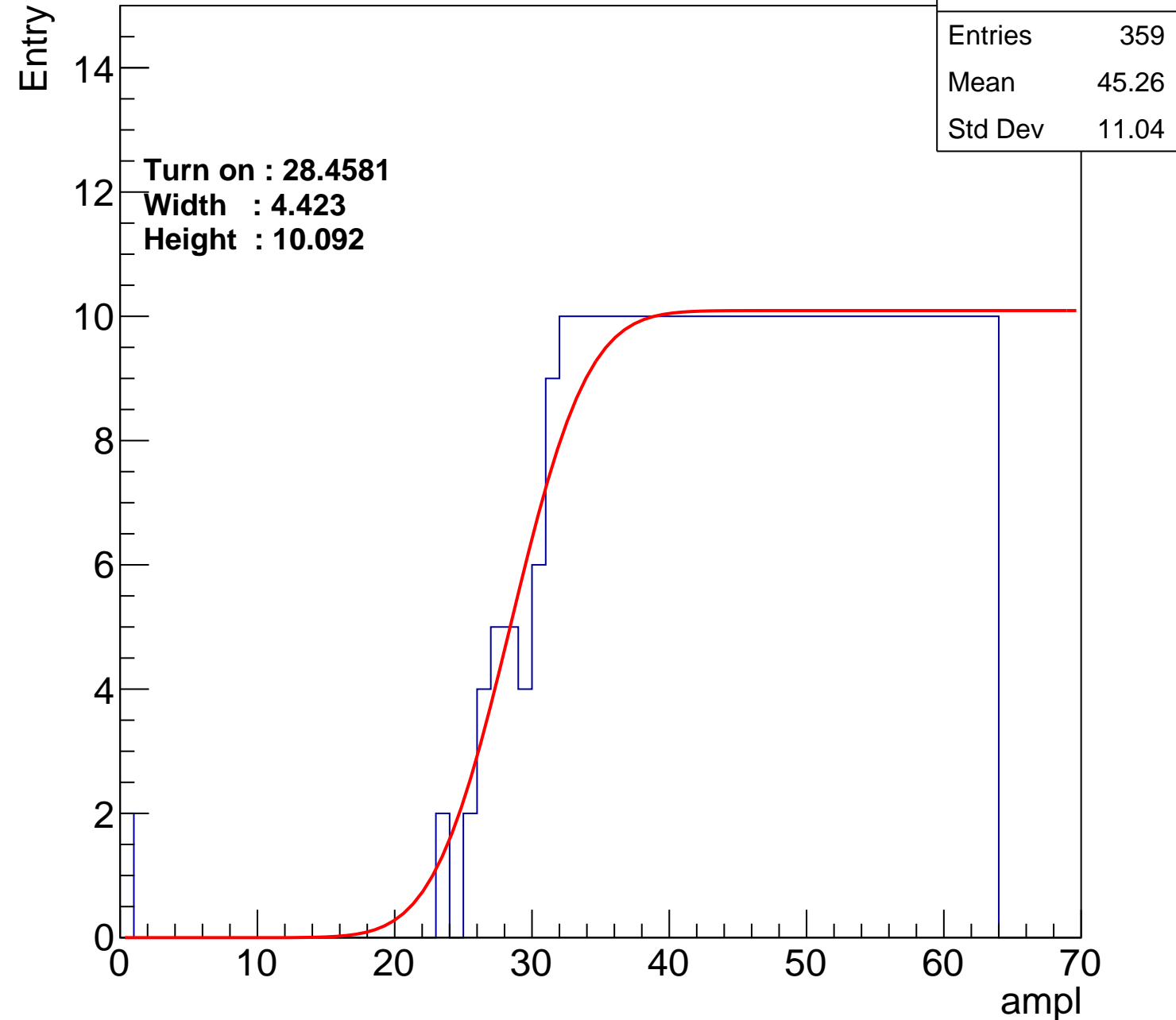
Width : 4.423

Height : 10.092

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	393
Mean	43.65
Std Dev	11.8

Turn on : 24.7832

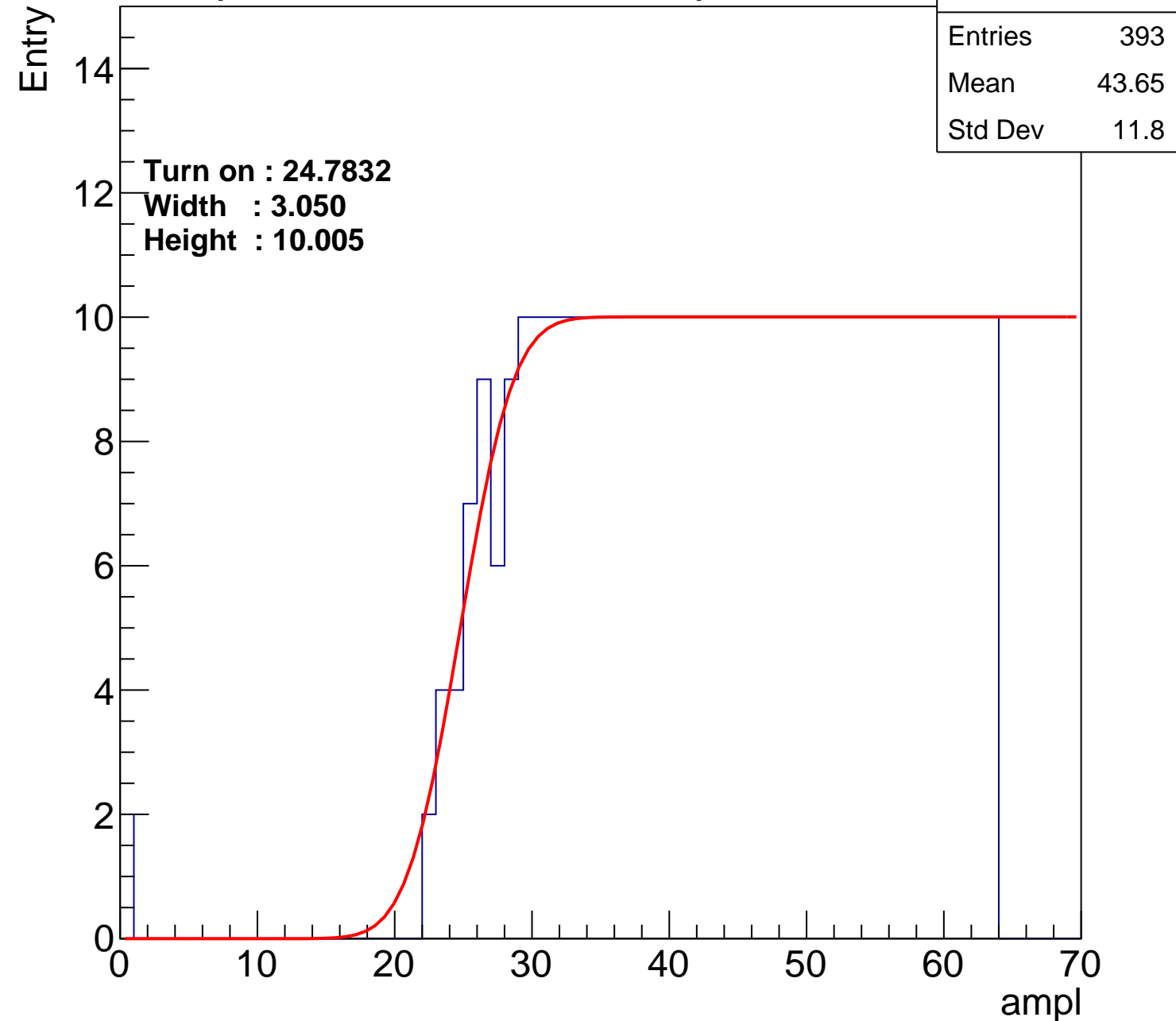
Width : 3.050

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch5

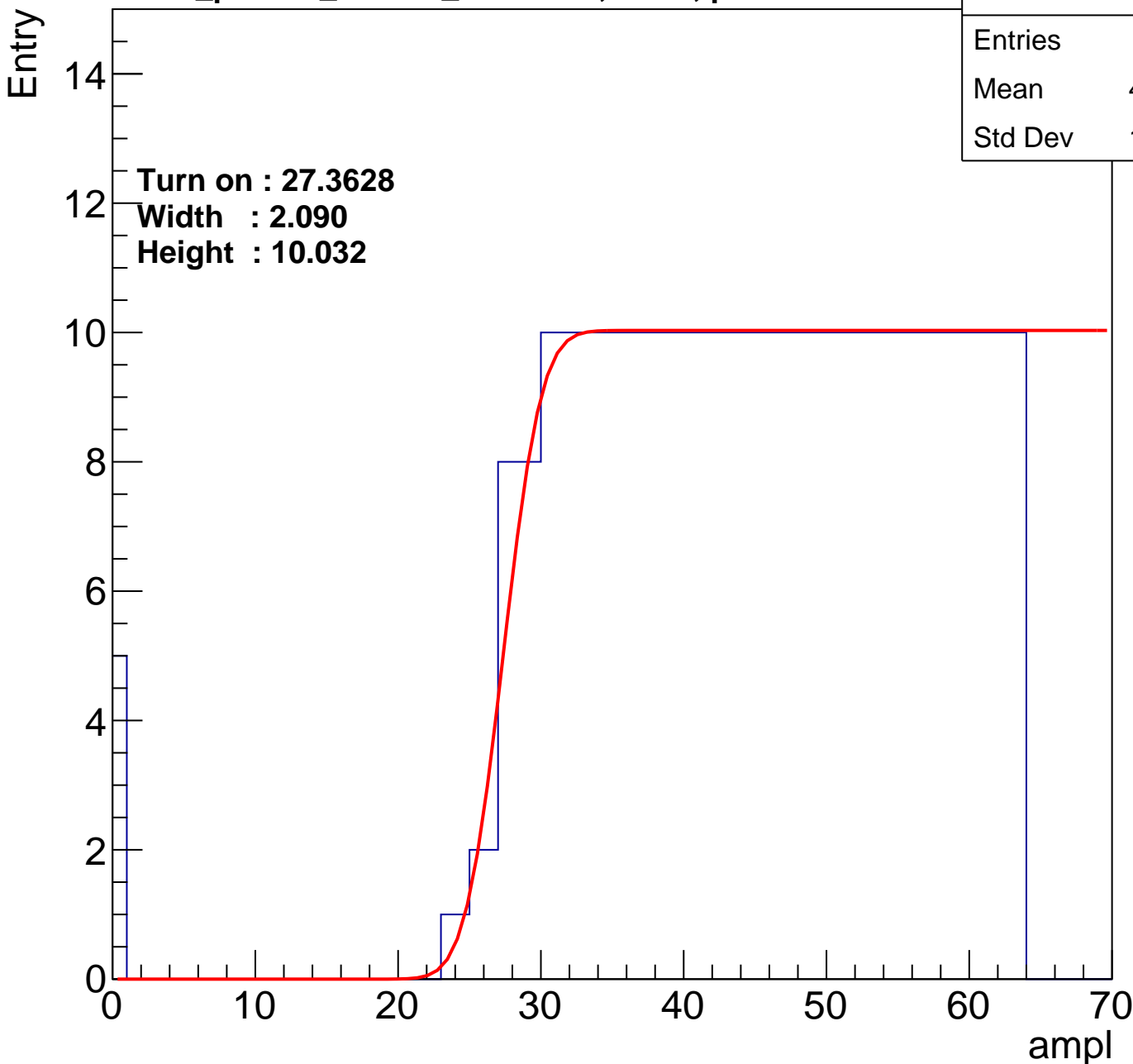
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	375
Mean	44.35
Std Dev	11.87

**Turn on : 27.3628**

**Width : 2.090**

**Height : 10.032**



# B0L000S, U20-ch6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	367
Mean	44.96
Std Dev	11.09

**Turn on : 27.5799**

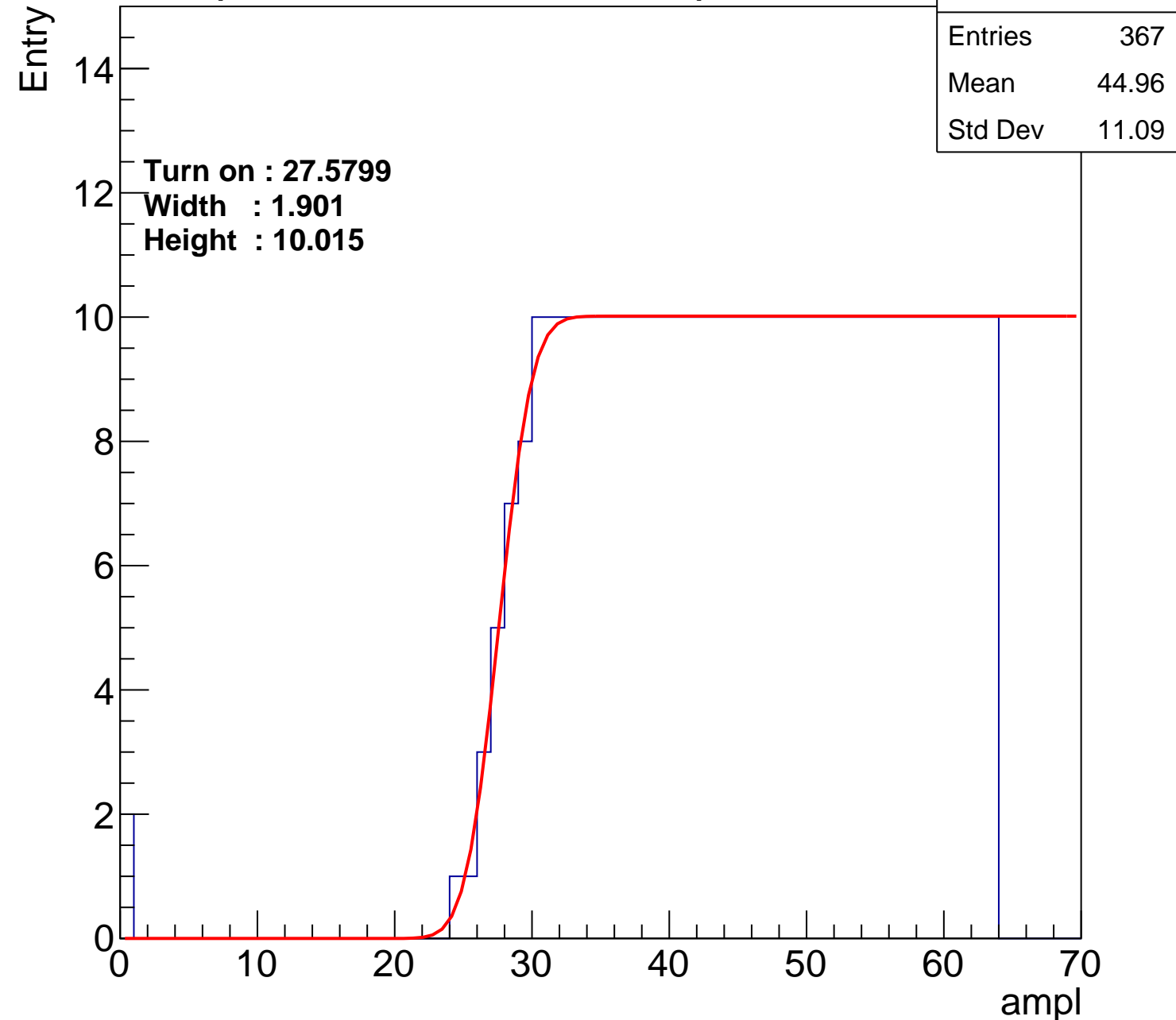
**Width : 1.901**

**Height : 10.015**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch7

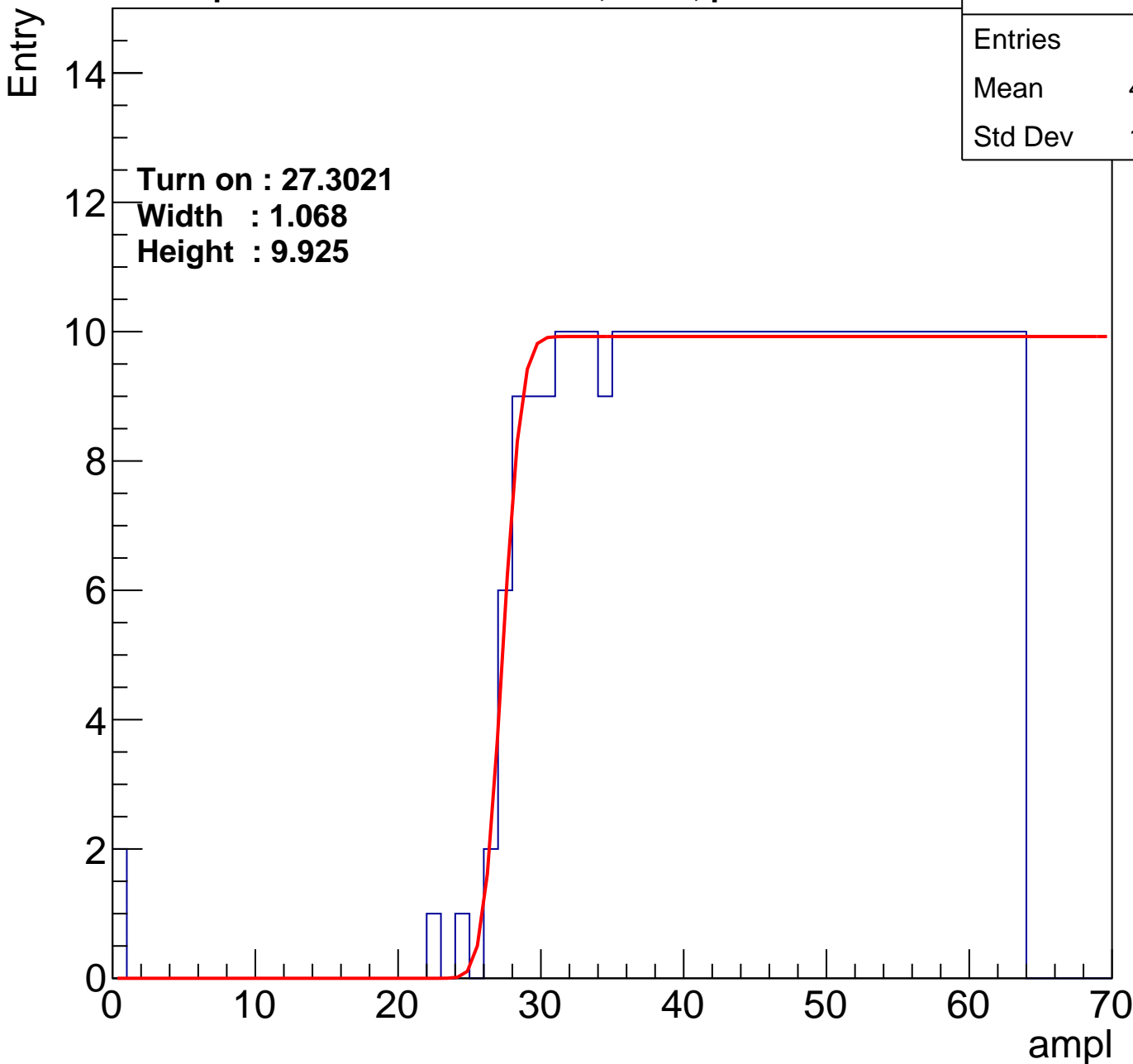
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.3021

Width : 1.068

Height : 9.925



# B0L000S, U20-ch8

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.4309

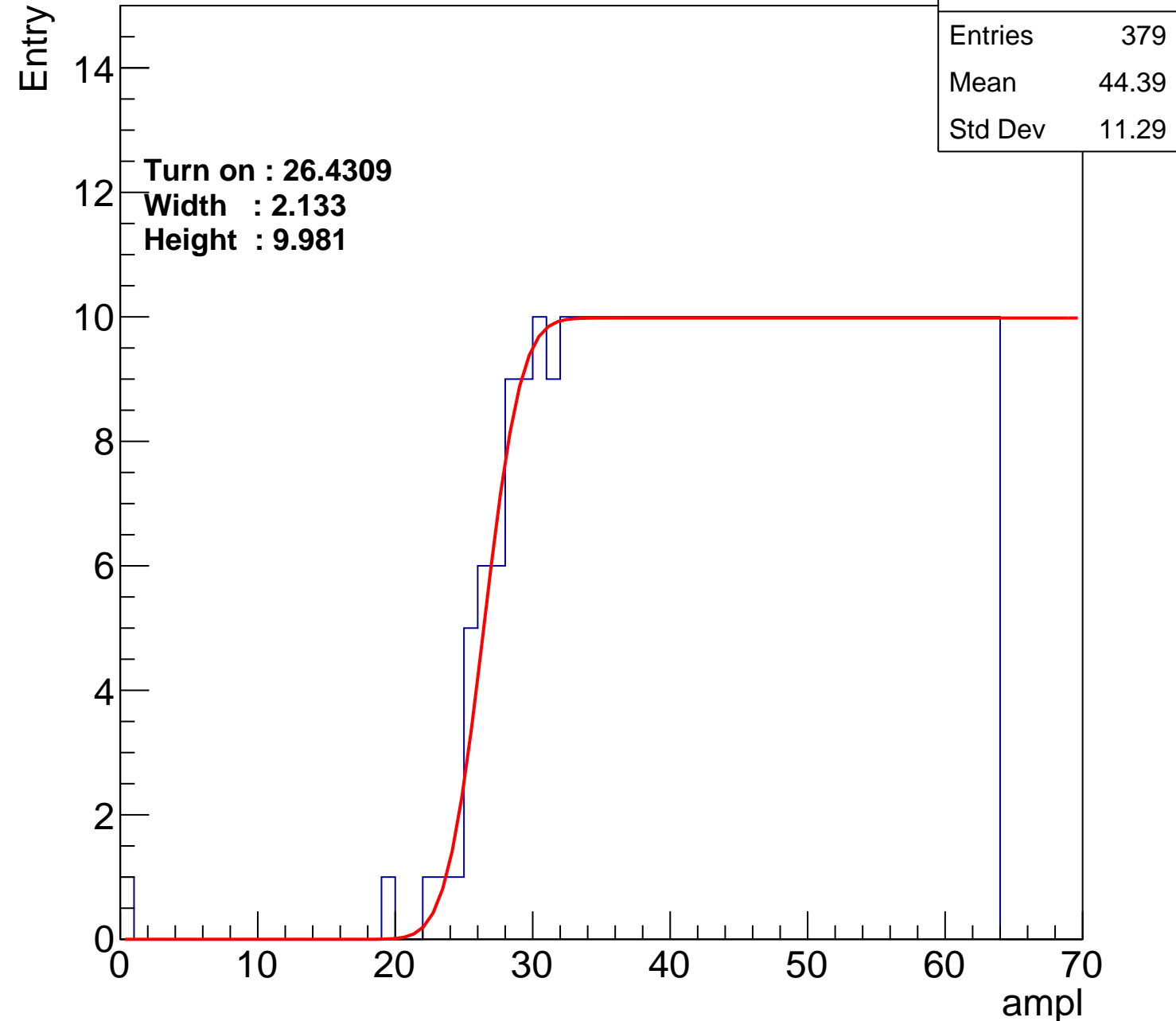
Width : 2.133

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch9

calib\_packv5\_042523\_0143.root, FC#5, port B1

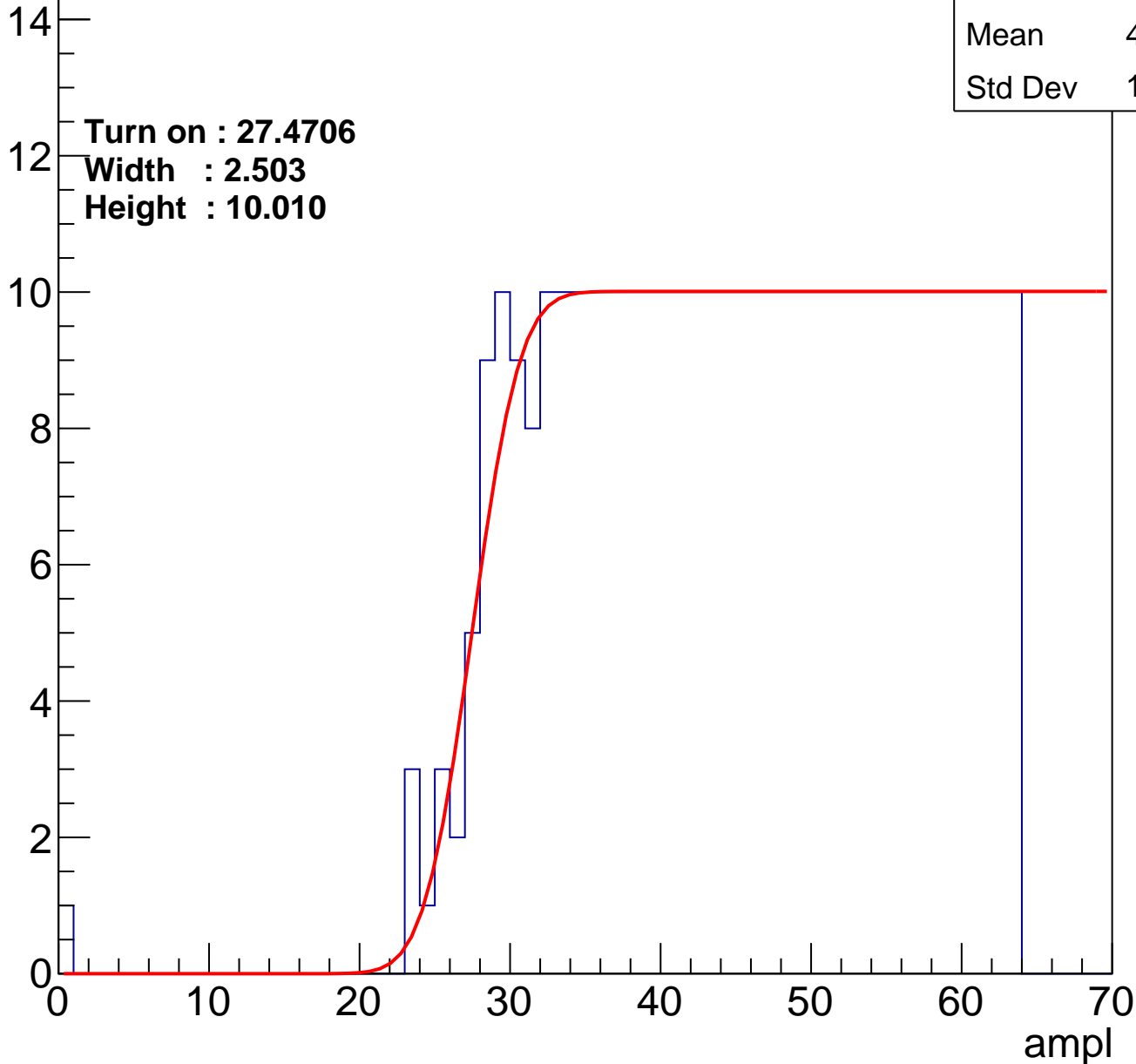
Entry

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 27.4706

Width : 2.503

Height : 10.010



# B0L000S, U20-ch10

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.23
Std Dev	11.51

**Turn on : 26.5128**

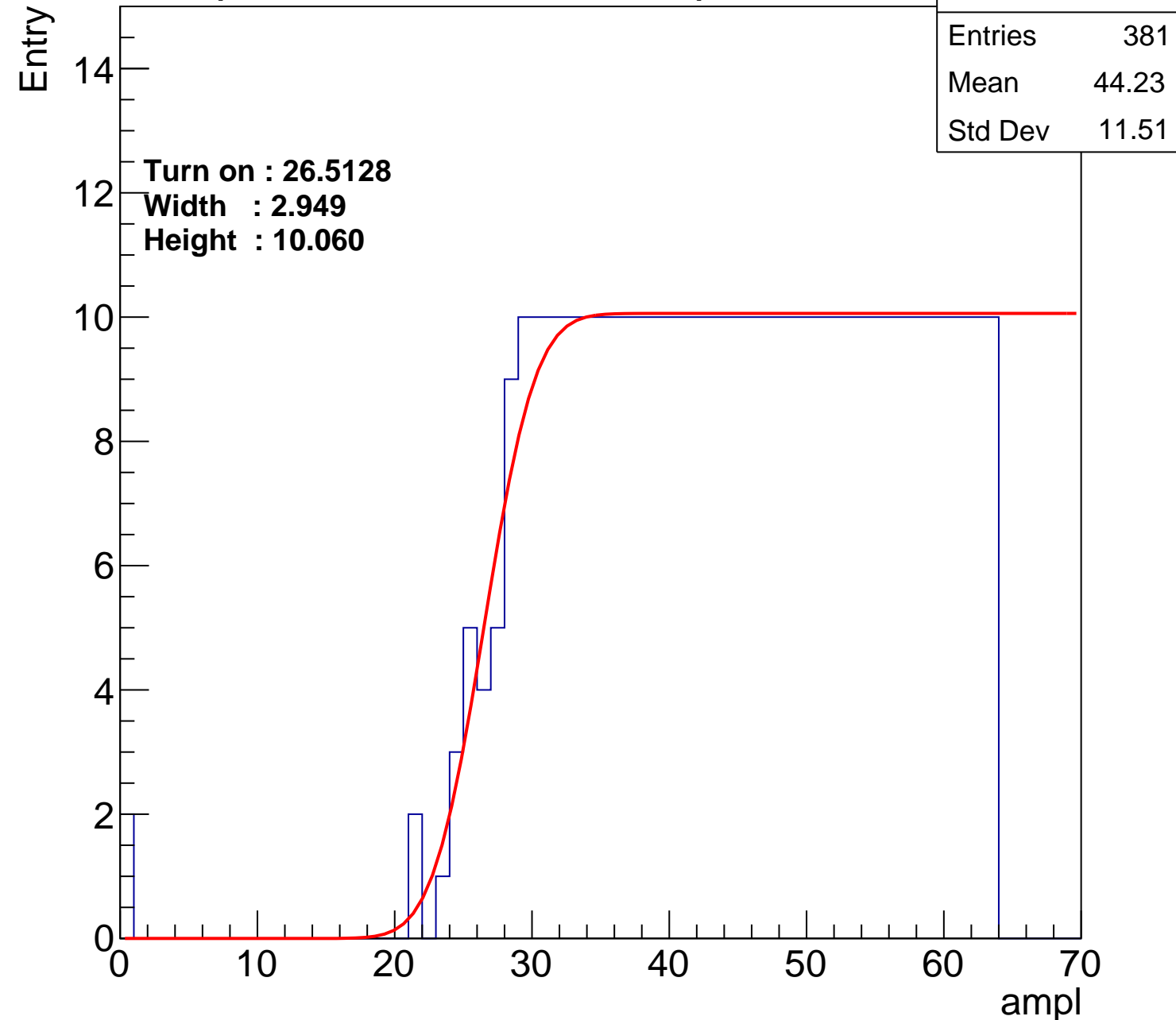
**Width : 2.949**

**Height : 10.060**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch11

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.08
Std Dev	12.15

**Turn on : 26.9116**

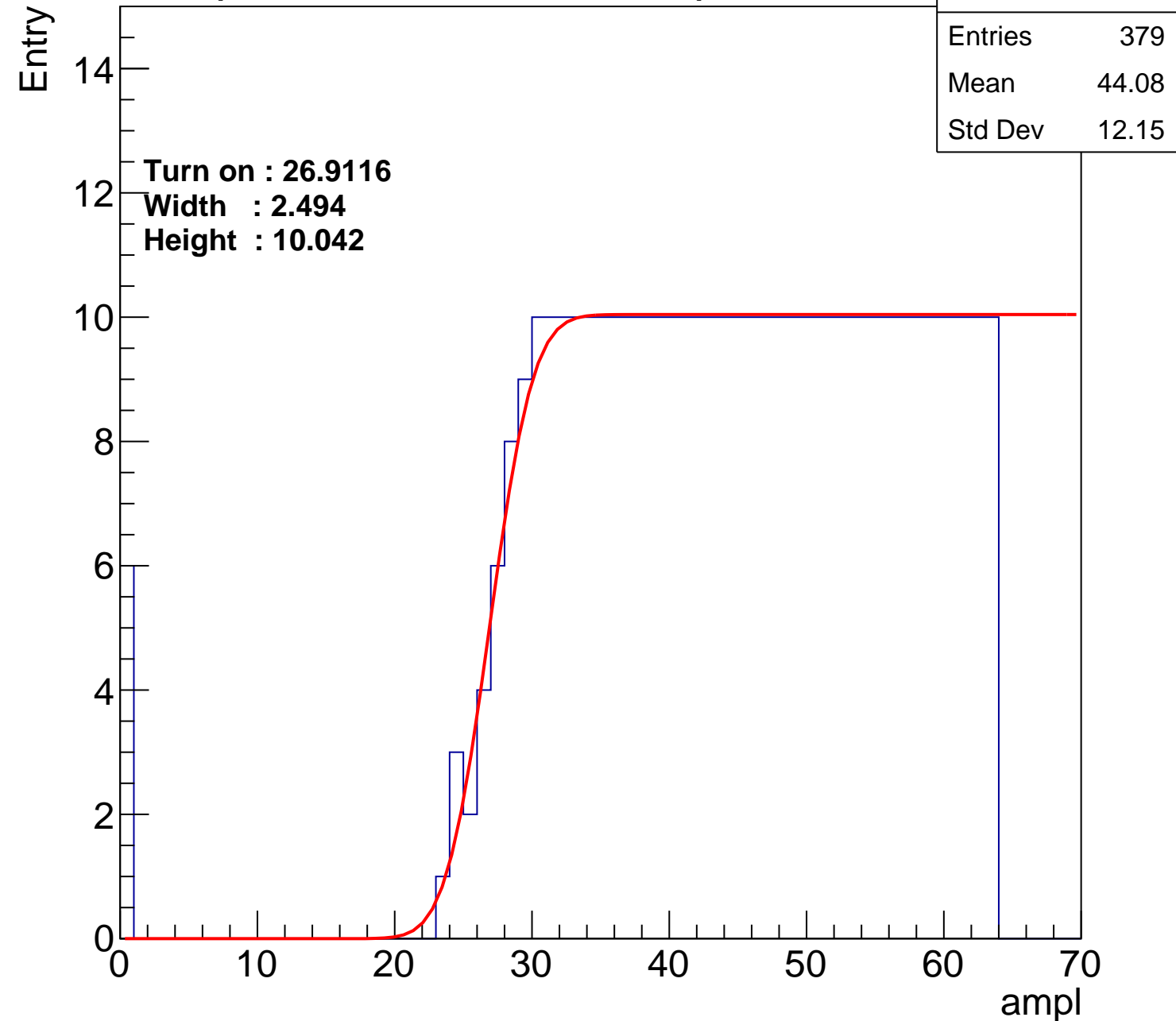
**Width : 2.494**

**Height : 10.042**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch12

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.53
Std Dev	11.23

Turn on : 26.6970

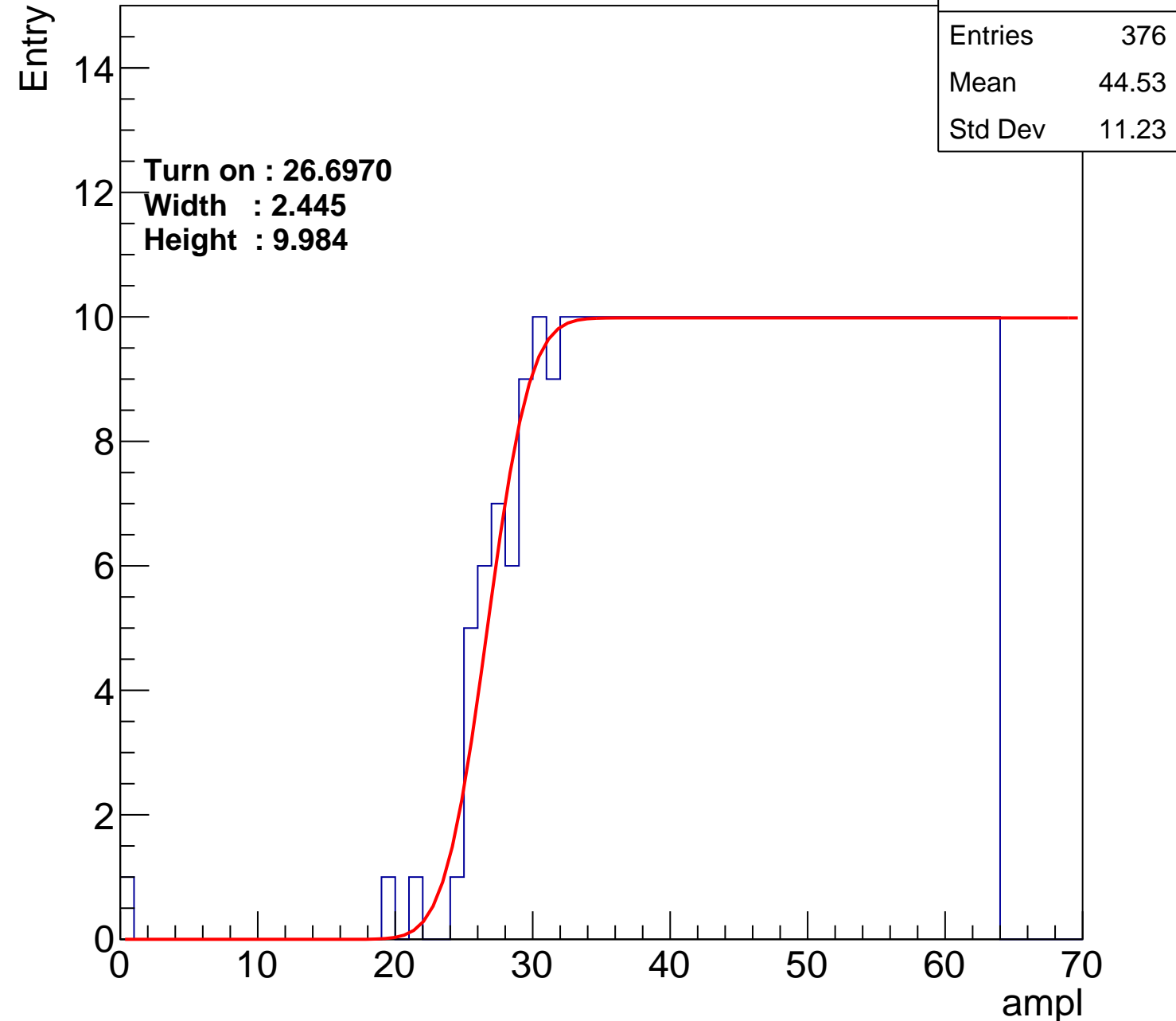
Width : 2.445

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch13

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	44.01
Std Dev	11.54

Turn on : 25.8157

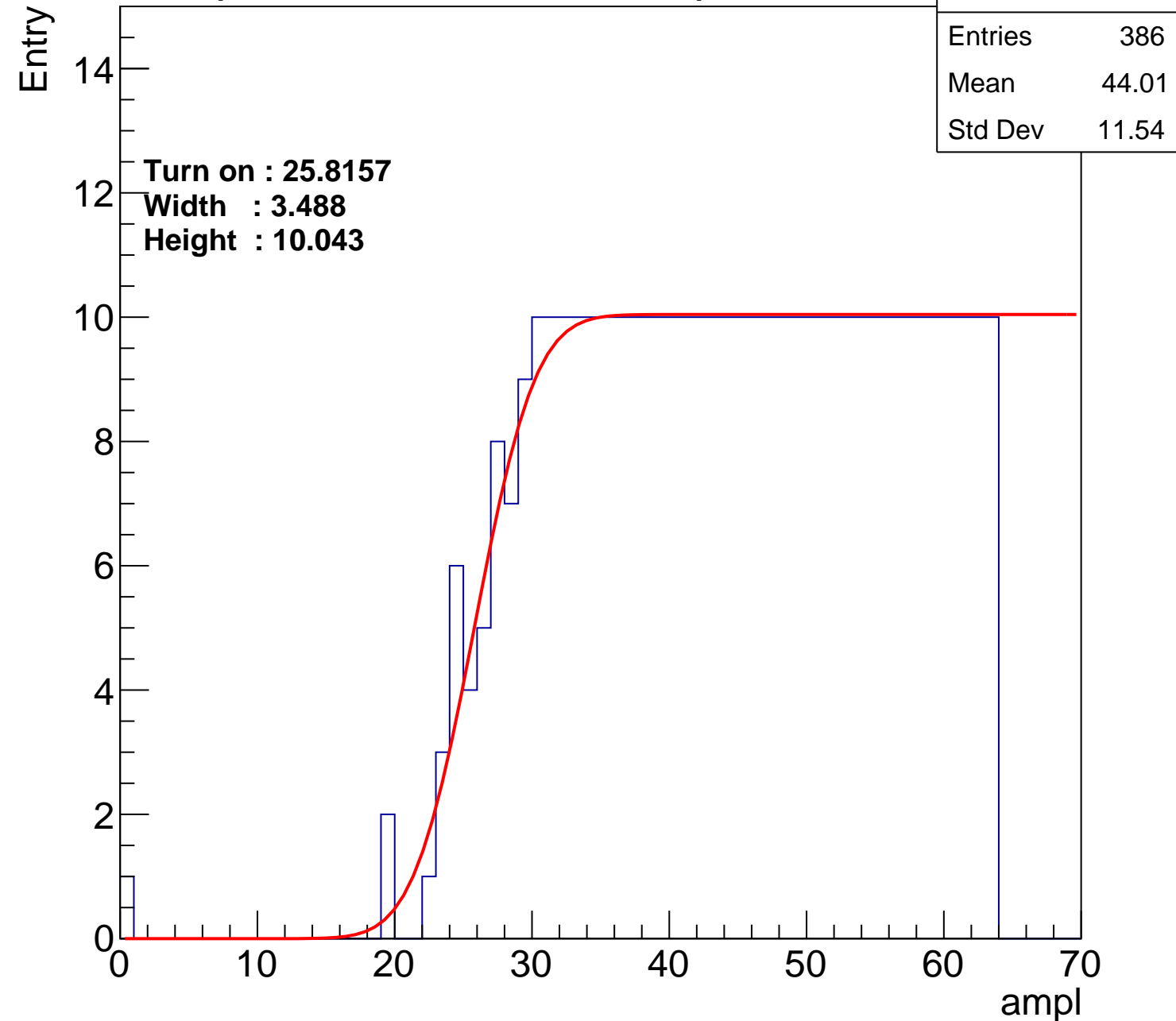
Width : 3.488

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch14

calib\_packv5\_042523\_0143.root, FC#5, port B1

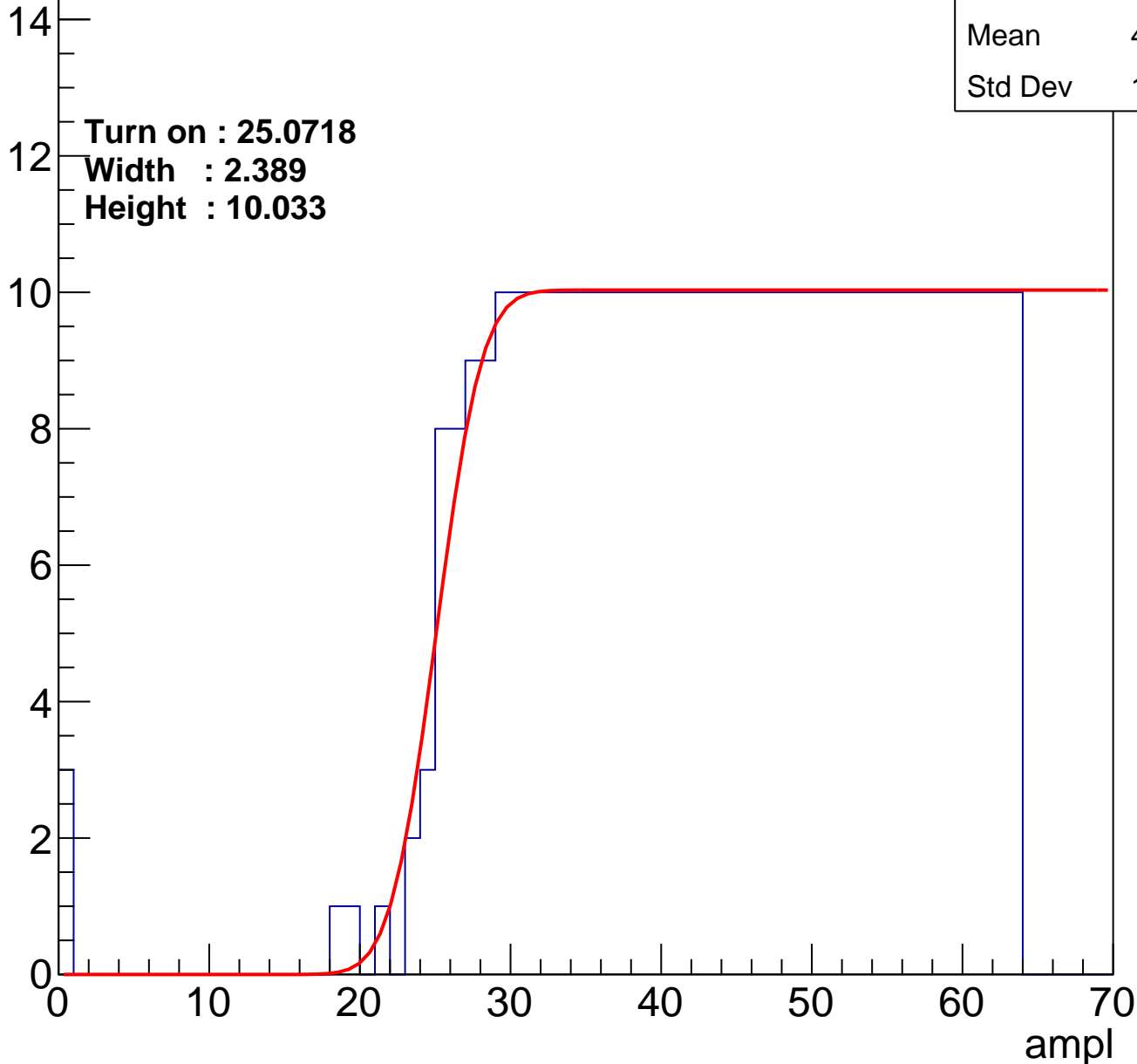
Entries	395
Mean	43.49
Std Dev	12.02

Turn on : 25.0718

Width : 2.389

Height : 10.033

Entry





# B0L000S, U20-ch15

calib\_packv5\_042523\_0143.root, FC#5, port B1

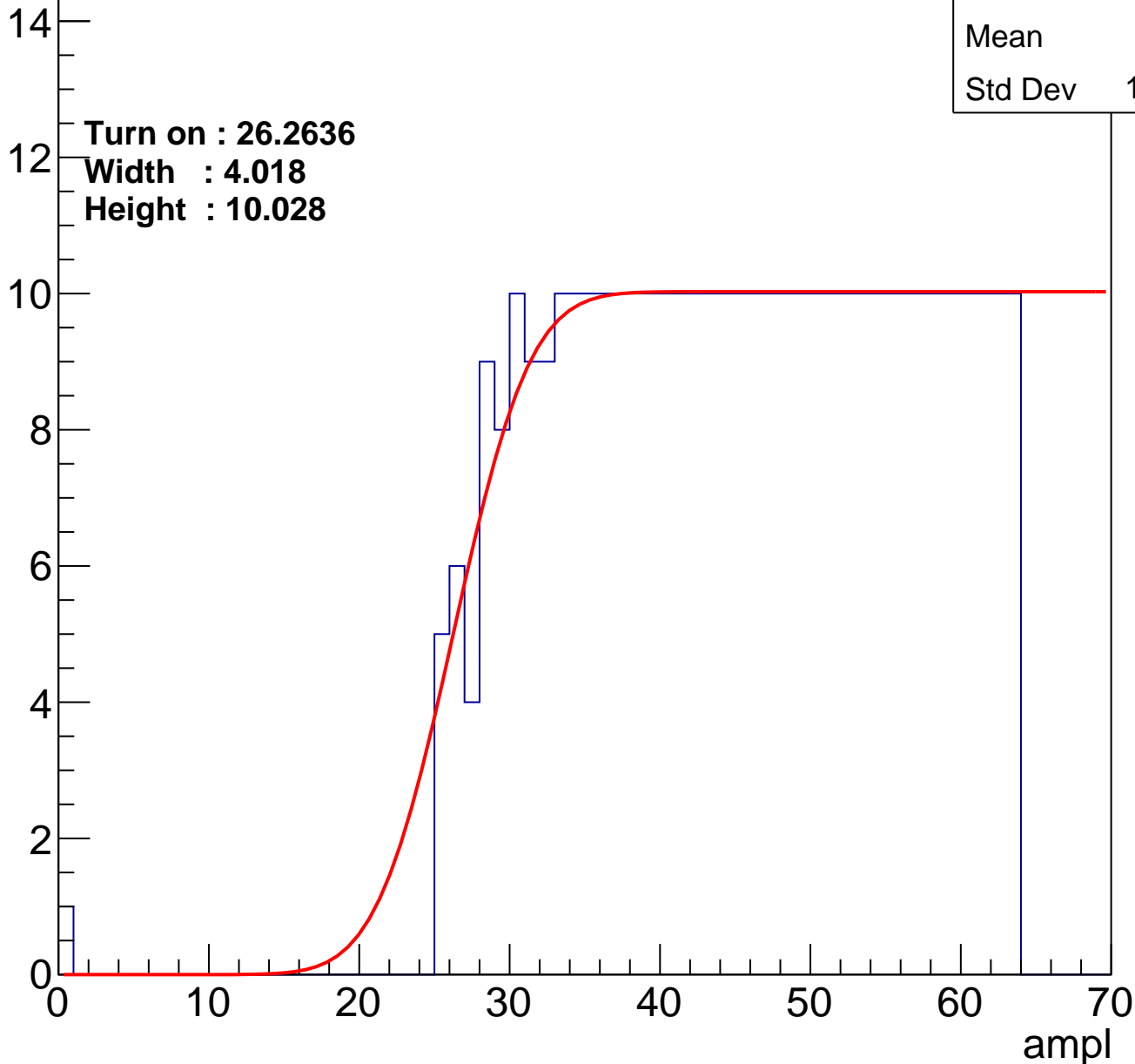
Entries	371
Mean	44.8
Std Dev	11.04

Turn on : 26.2636

Width : 4.018

Height : 10.028

Entry



# B0L000S, U20-ch16

calib\_packv5\_042523\_0143.root, FC#5, port B1

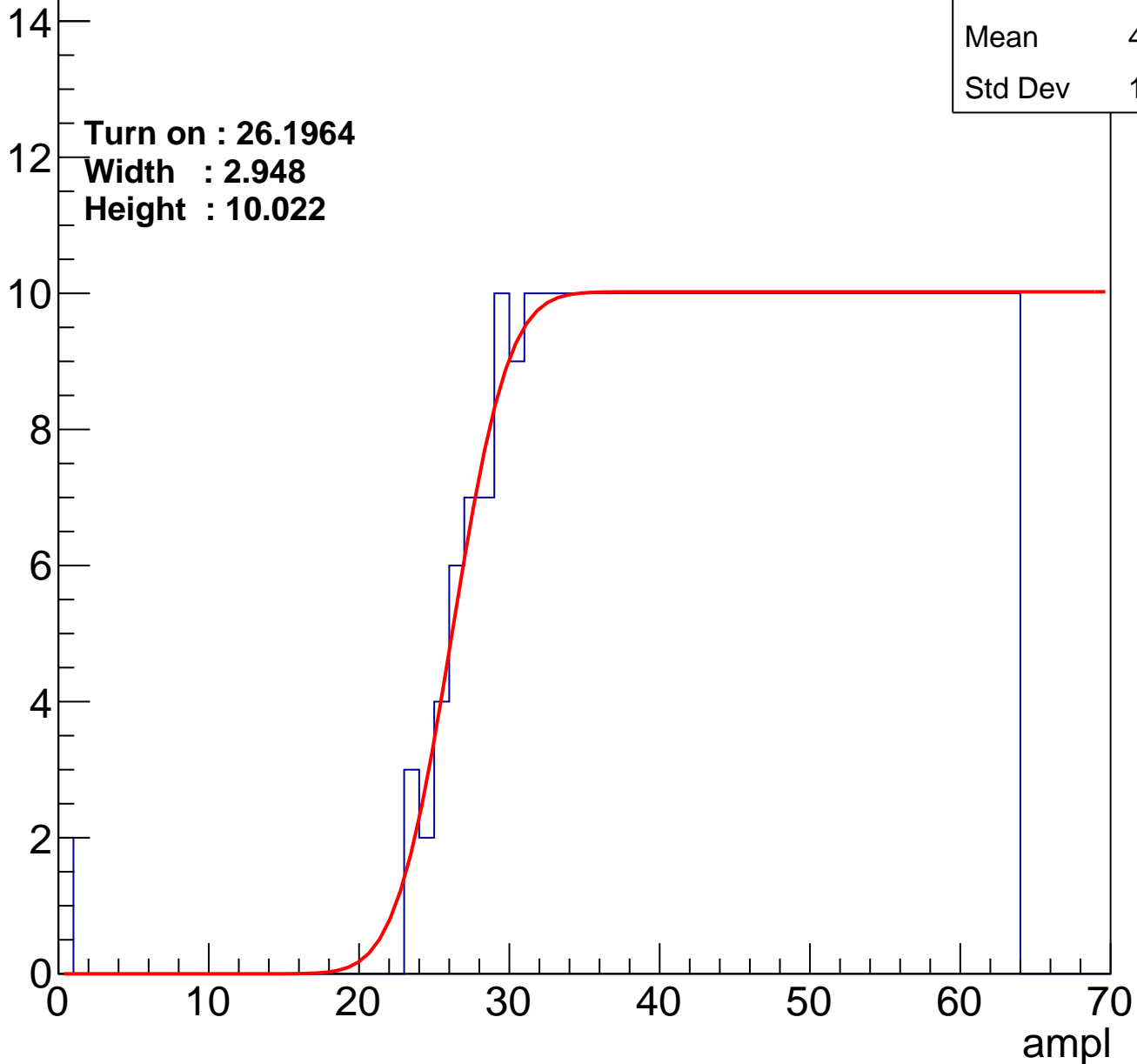
Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 26.1964

Width : 2.948

Height : 10.022

Entry



# B0L000S, U20-ch17

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	44.99
Std Dev	11.3

Turn on : 28.5959

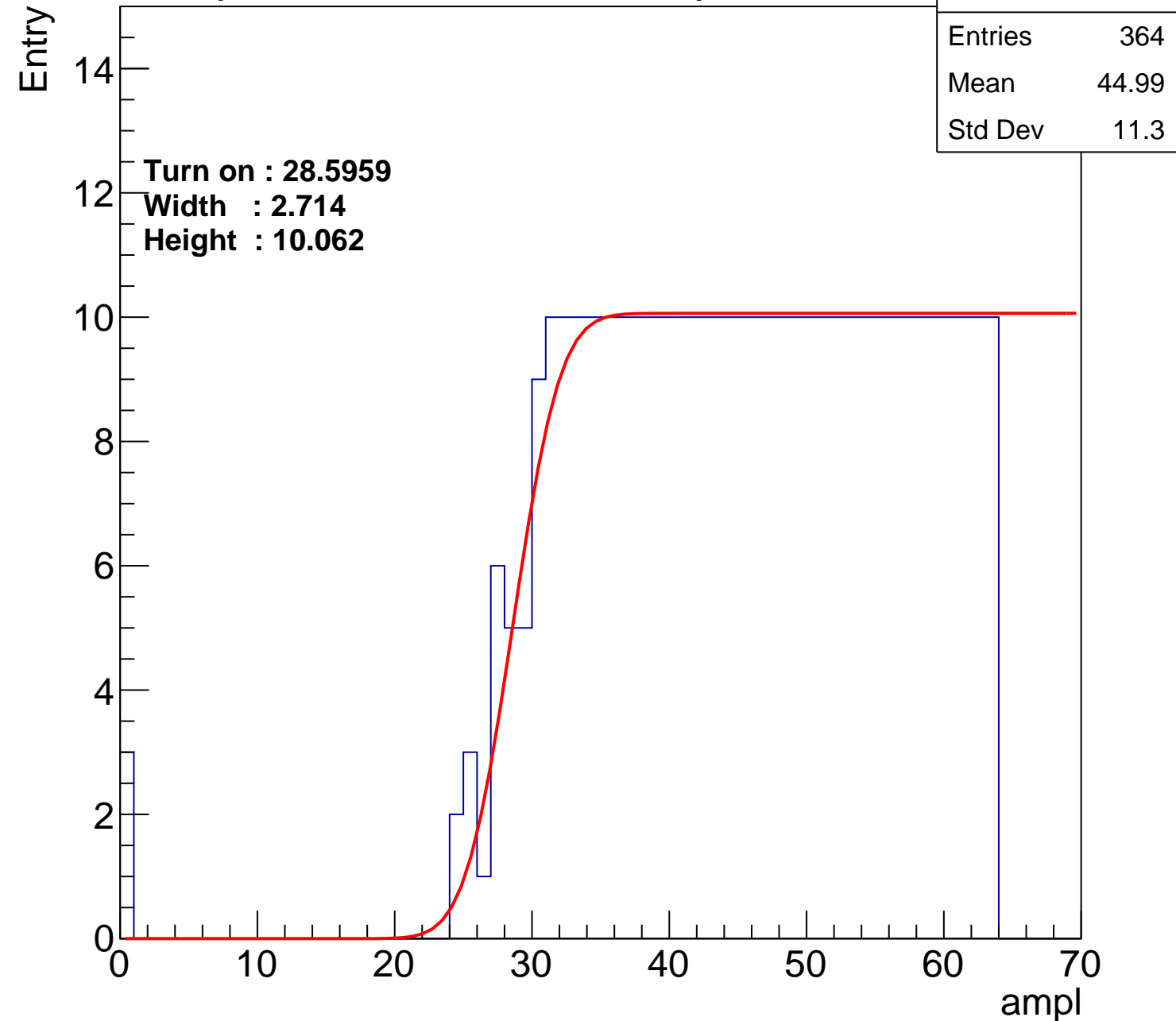
Width : 2.714

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch18

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.33
Std Dev	11.16

Turn on : 28.7566

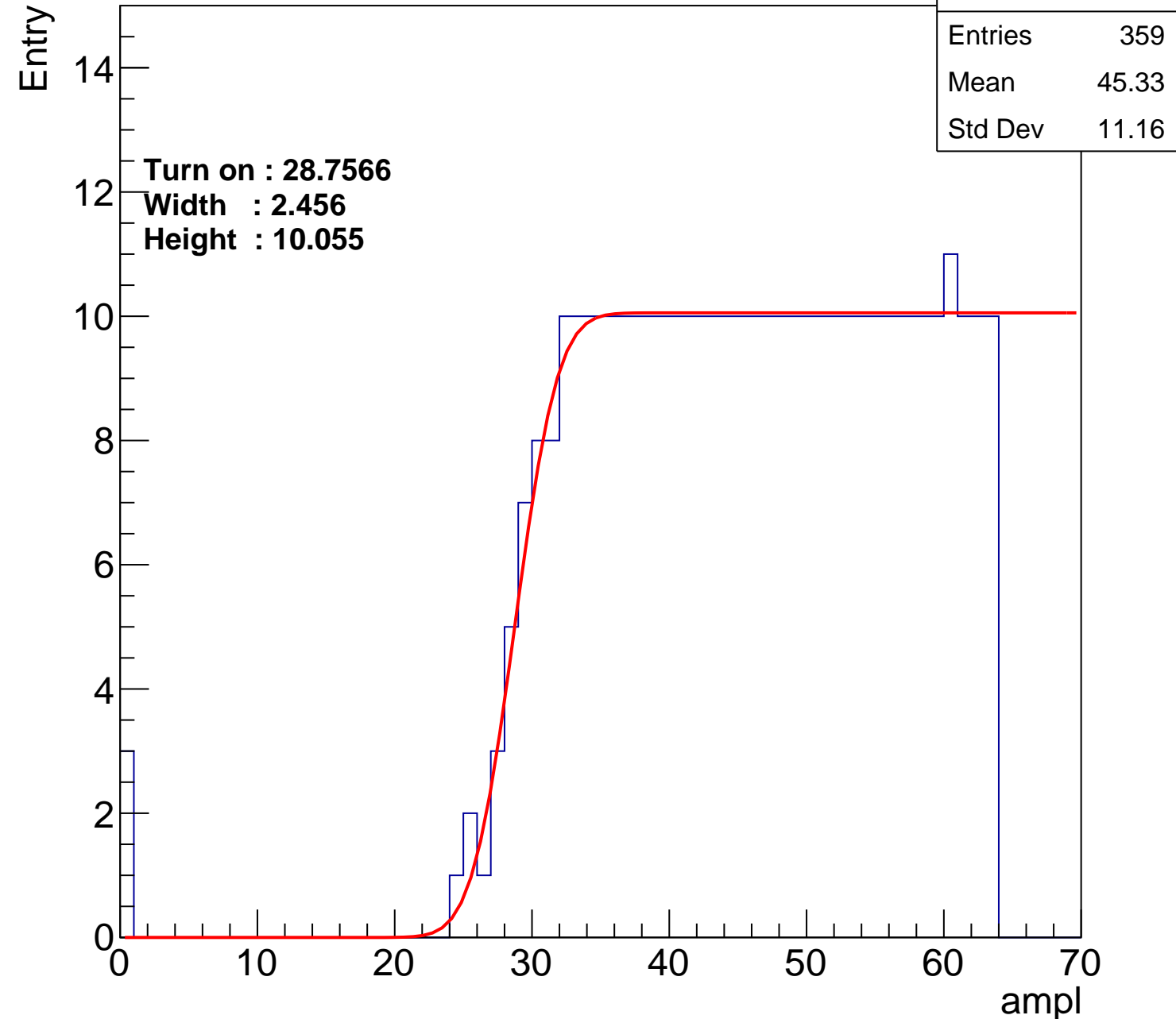
Width : 2.456

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch19

calib\_packv5\_042523\_0143.root, FC#5, port B1

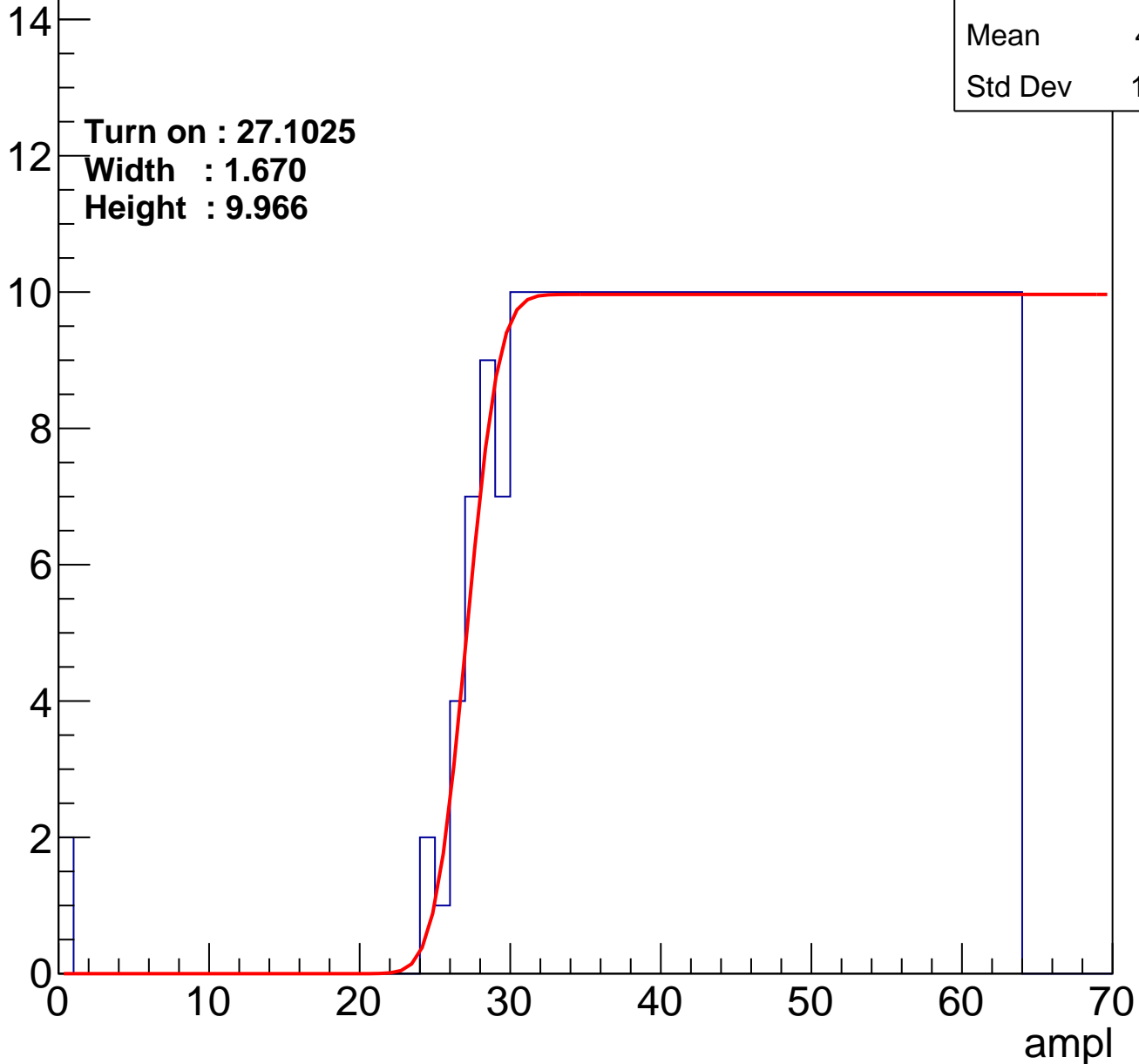
Entries	372
Mean	44.71
Std Dev	11.23

**Turn on : 27.1025**

**Width : 1.670**

**Height : 9.966**

Entry



# B0L000S, U20-ch20

calib\_packv5\_042523\_0143.root, FC#5, port B1

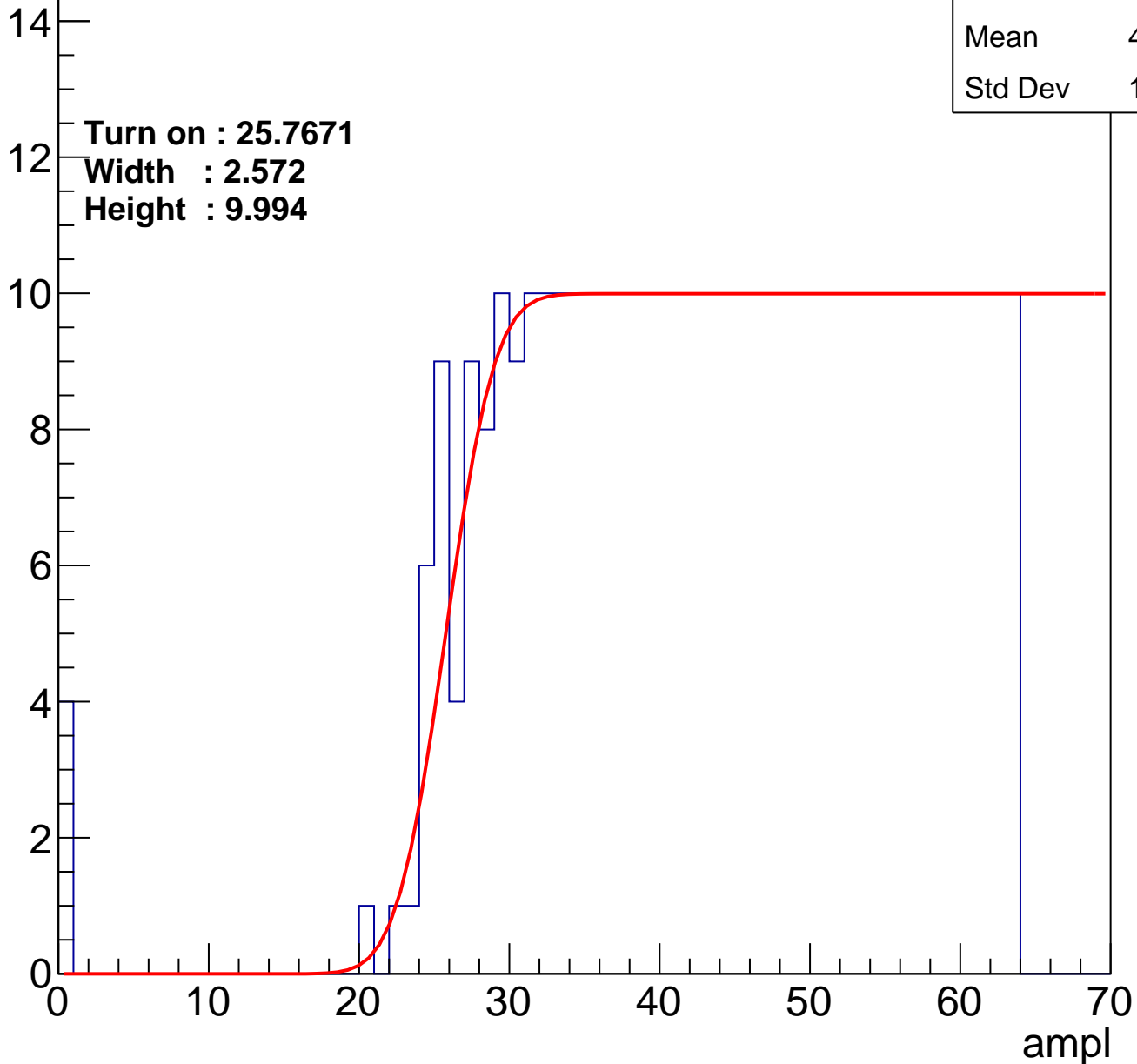
Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 25.7671

Width : 2.572

Height : 9.994

Entry



# B0L000S, U20-ch21

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.72
Std Dev	11.26

Turn on : 27.4242

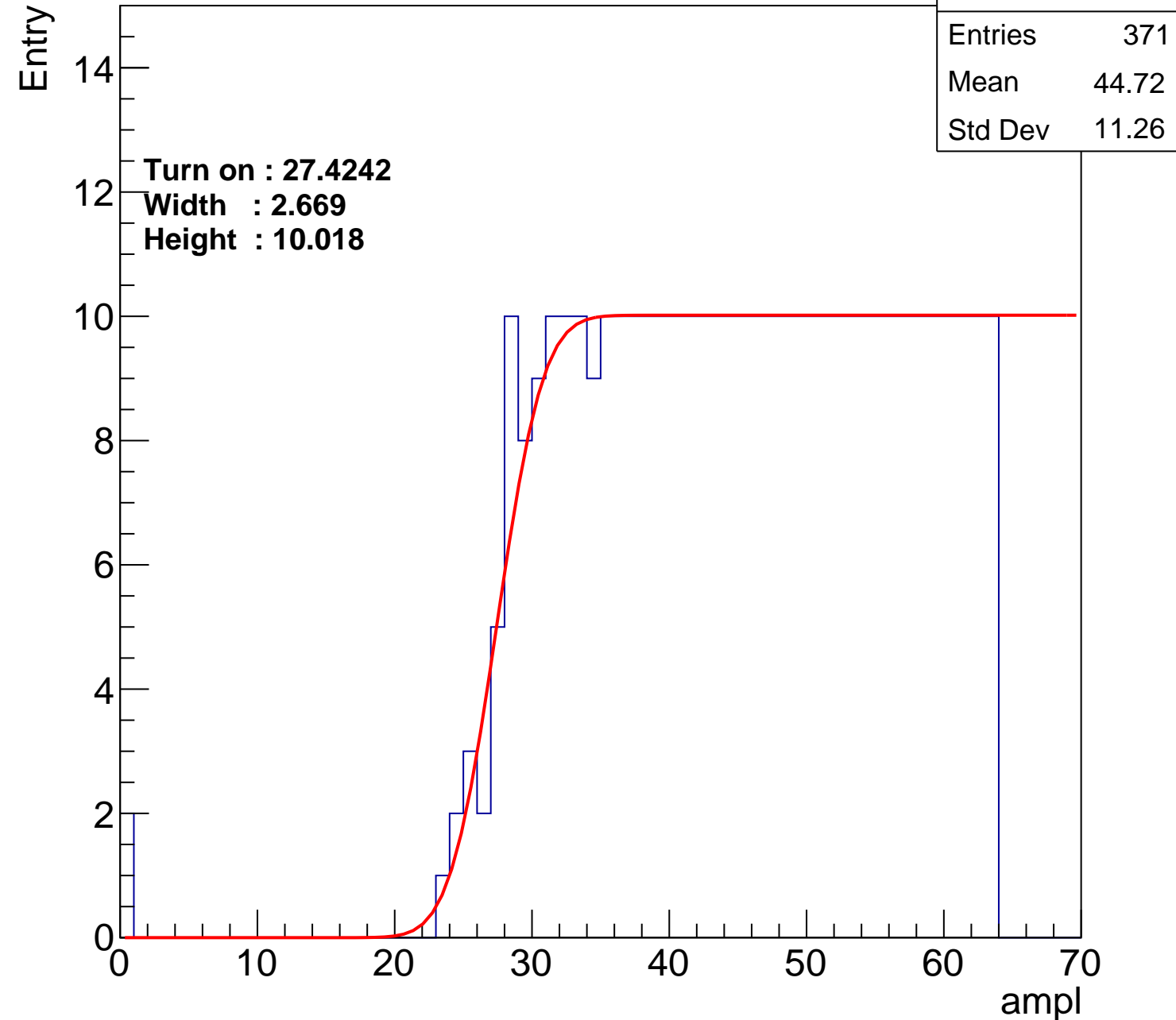
Width : 2.669

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch22

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	44.07
Std Dev	11.5

Turn on : 25.5852

Width : 2.924

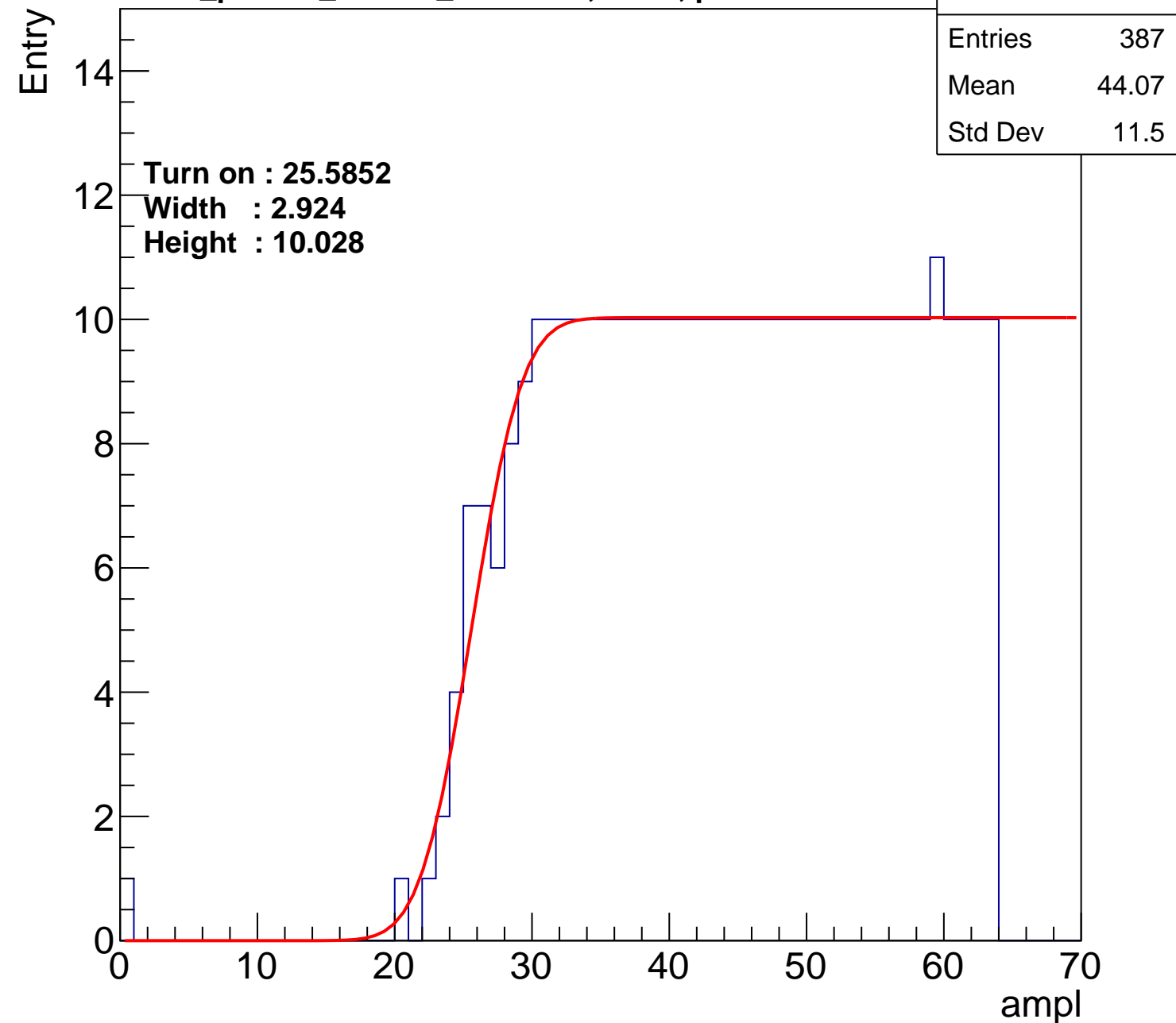
Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B0L000S, U20-ch23

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.11
Std Dev	11.49

Turn on : 25.6015

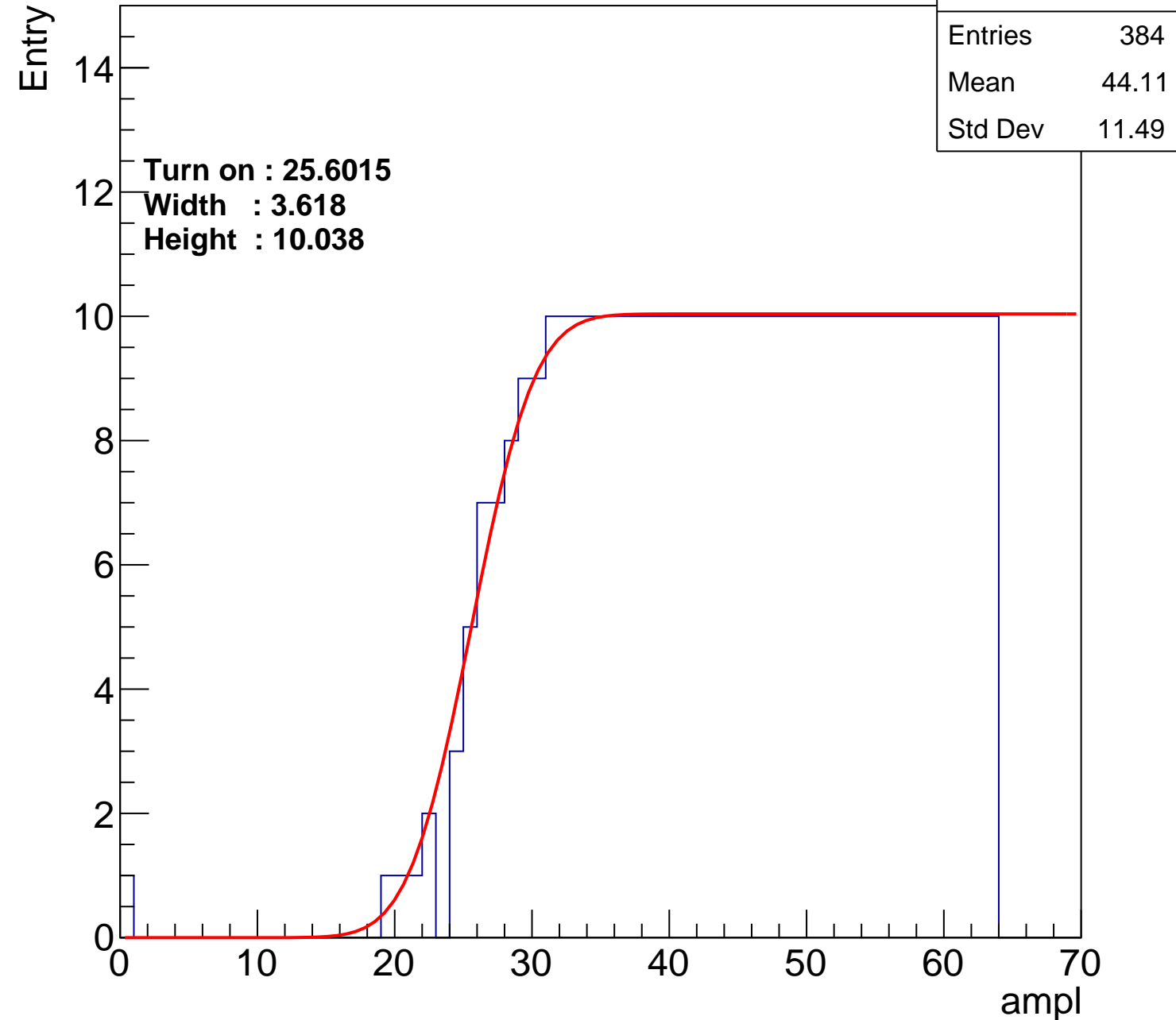
Width : 3.618

Height : 10.038

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch24

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.98
Std Dev	11.17

Turn on : 27.6456

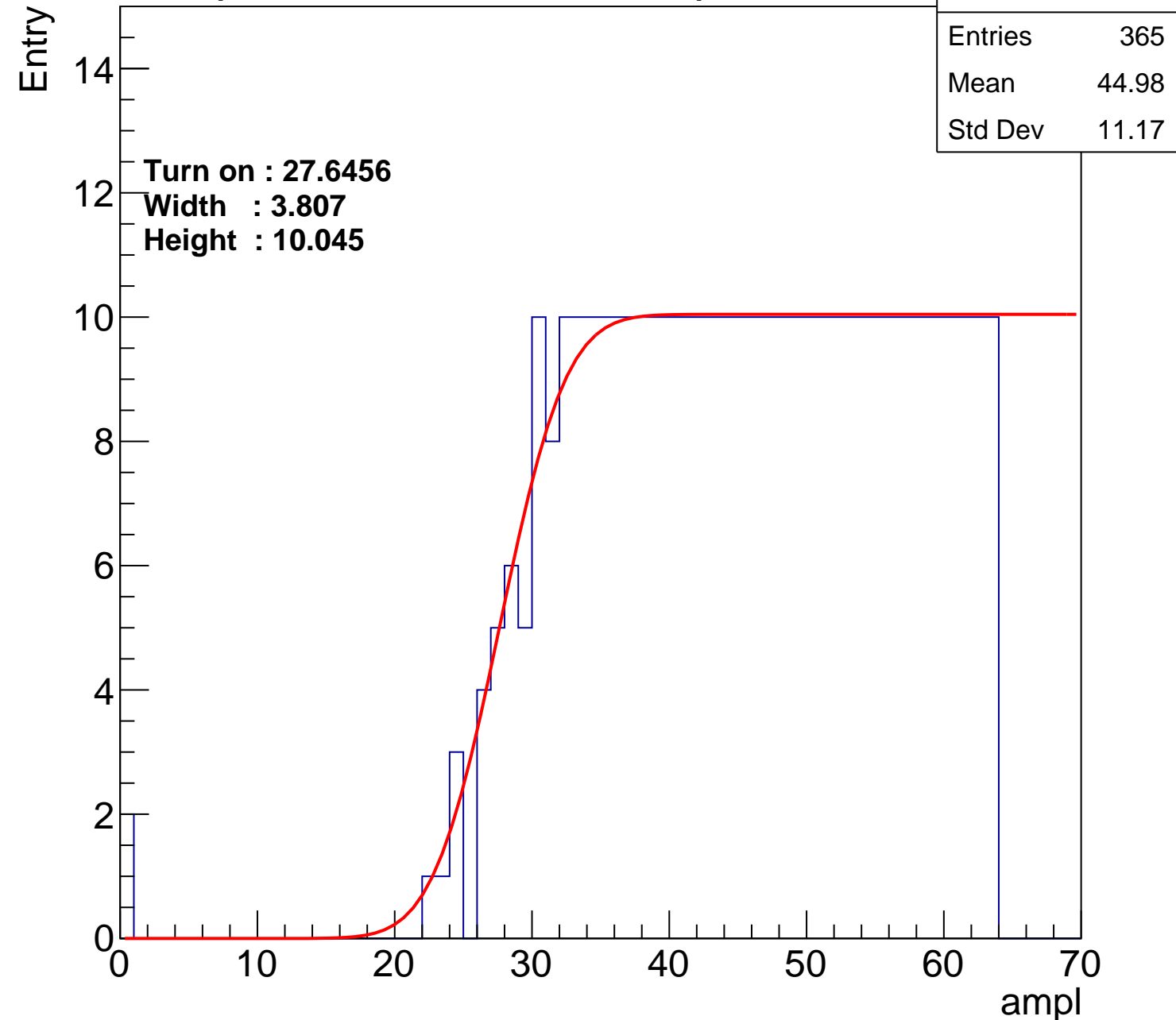
Width : 3.807

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch25

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	368
Mean	44.75
Std Dev	11.46

Turn on : 27.9960

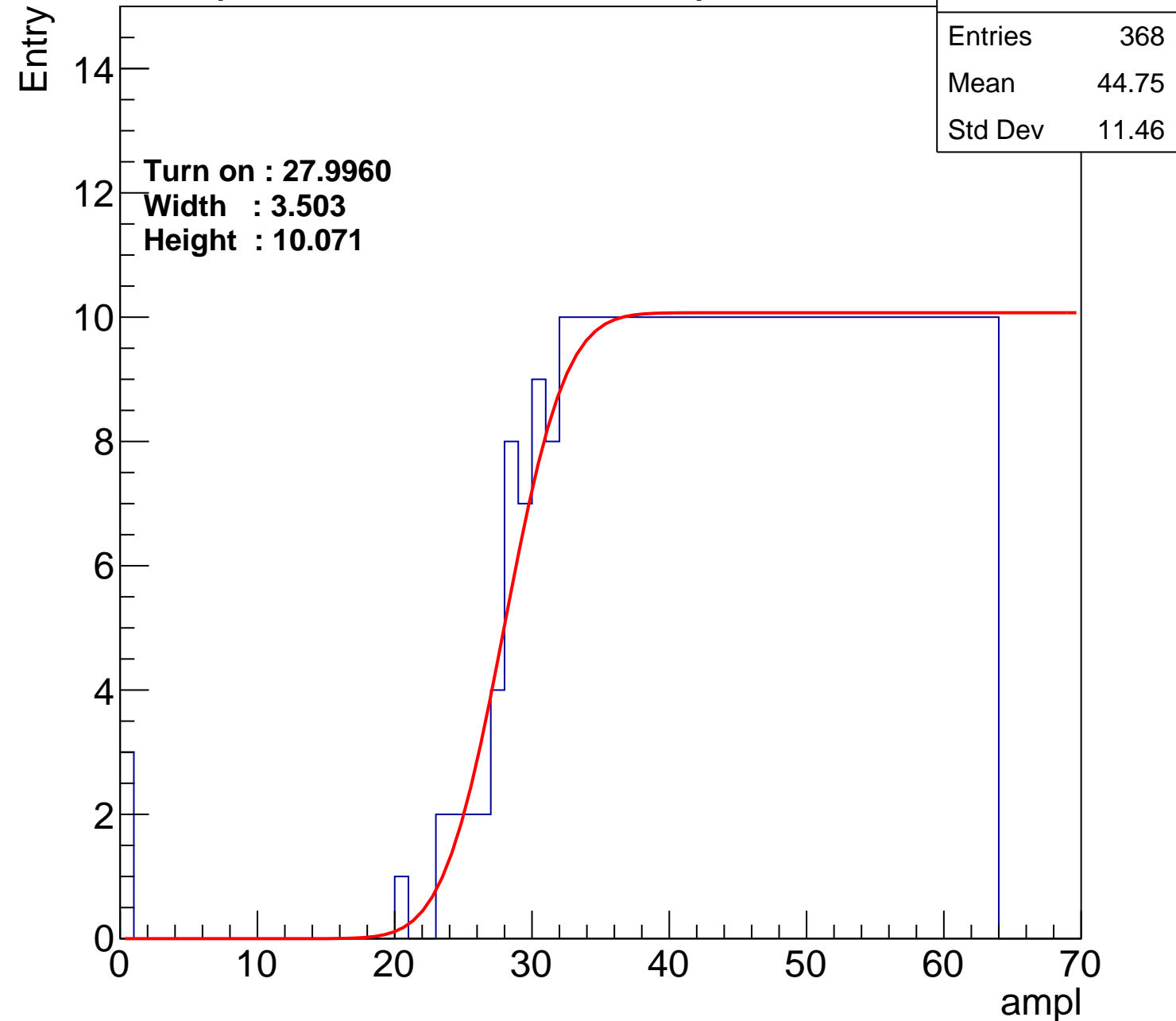
Width : 3.503

Height : 10.071

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch26

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	365
Mean	44.99
Std Dev	11.15

Turn on : 27.7778

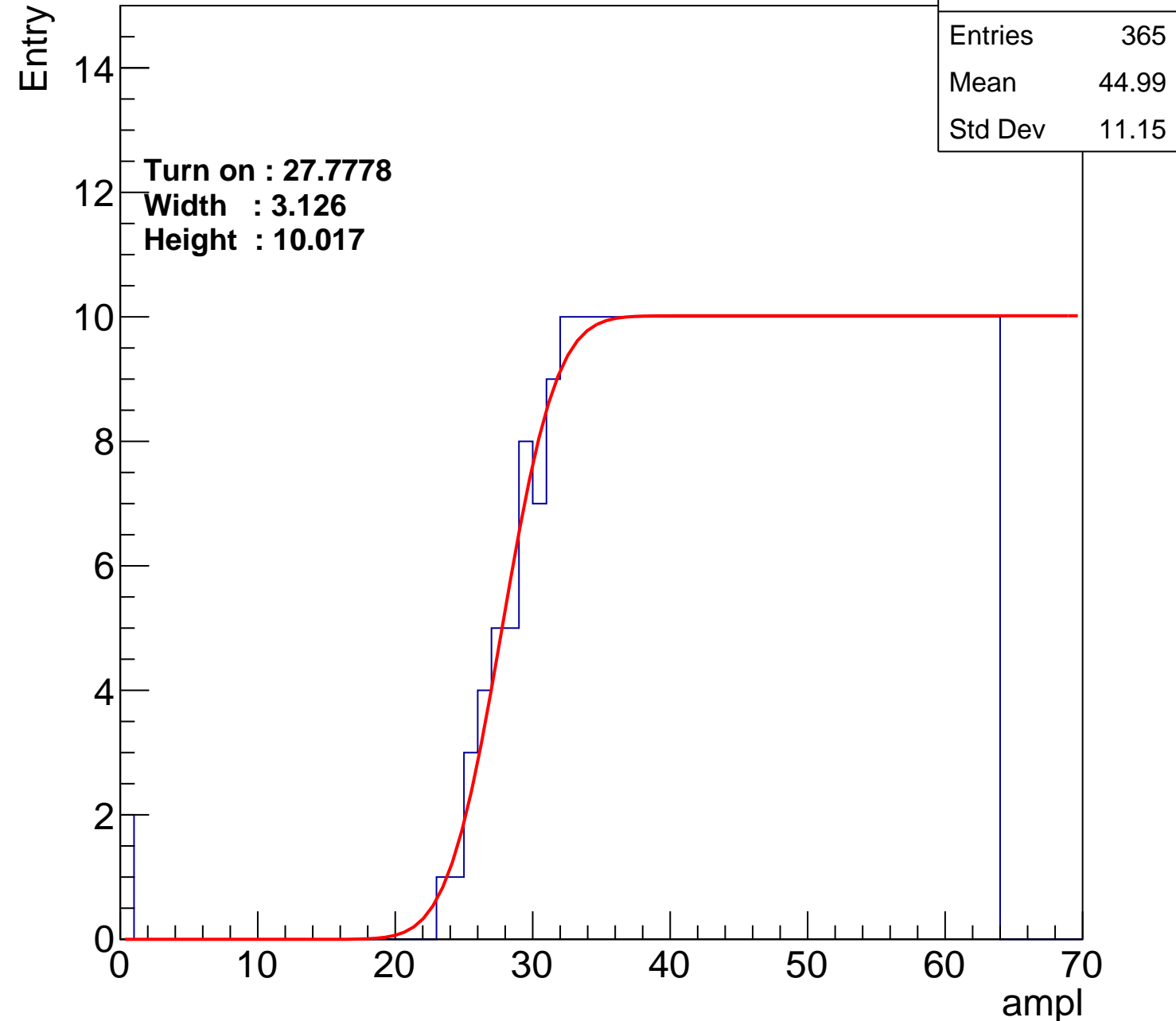
Width : 3.126

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch27

calib\_packv5\_042523\_0143.root, FC#5, port B1

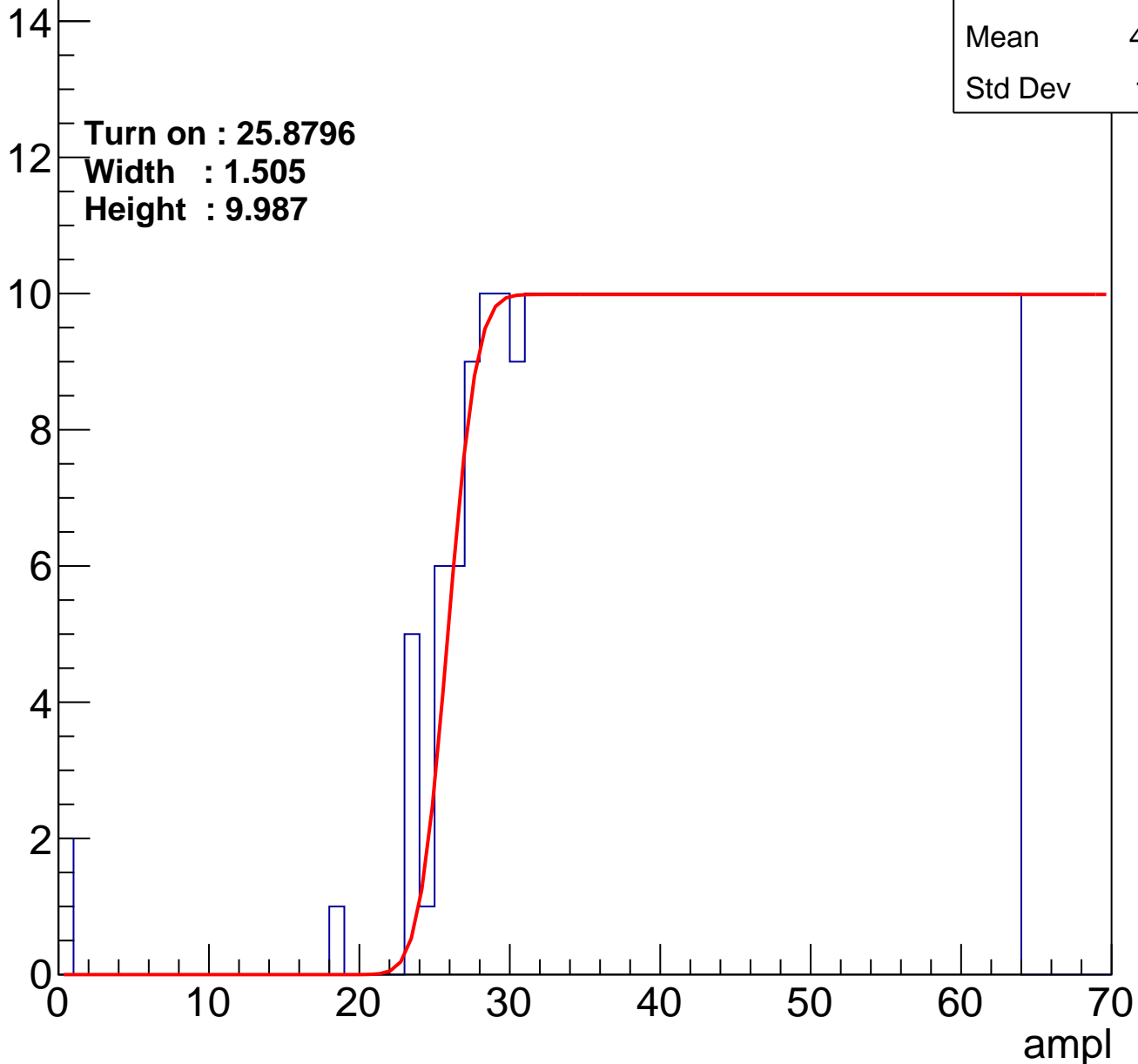
Entries	389
Mean	43.85
Std Dev	11.71

Turn on : 25.8796

Width : 1.505

Height : 9.987

Entry



# B0L000S, U20-ch28

calib\_packv5\_042523\_0143.root, FC#5, port B1

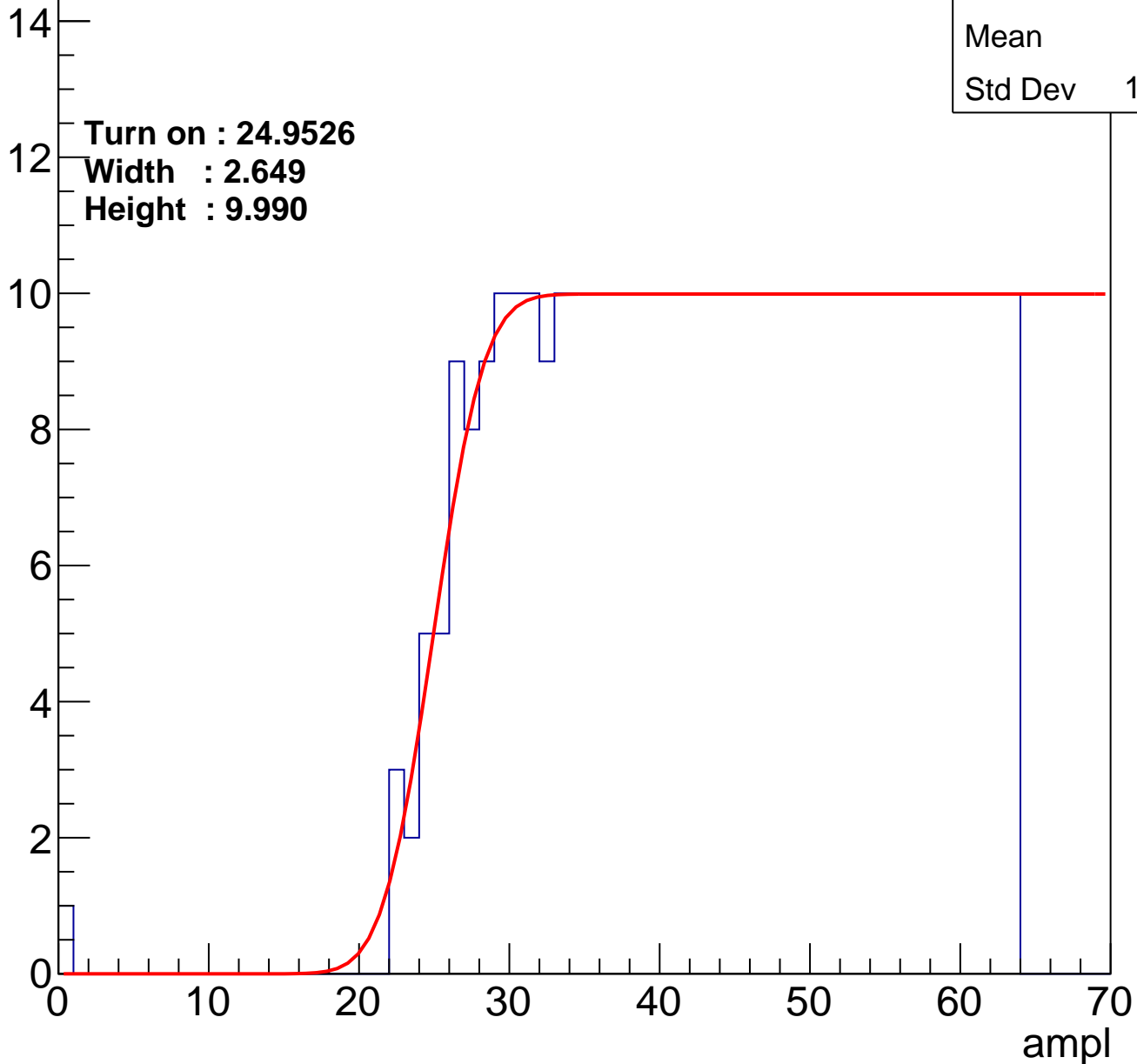
Entries	391
Mean	43.8
Std Dev	11.59

Turn on : 24.9526

Width : 2.649

Height : 9.990

Entry



# B0L000S, U20-ch29

calib\_packv5\_042523\_0143.root, FC#5, port B1

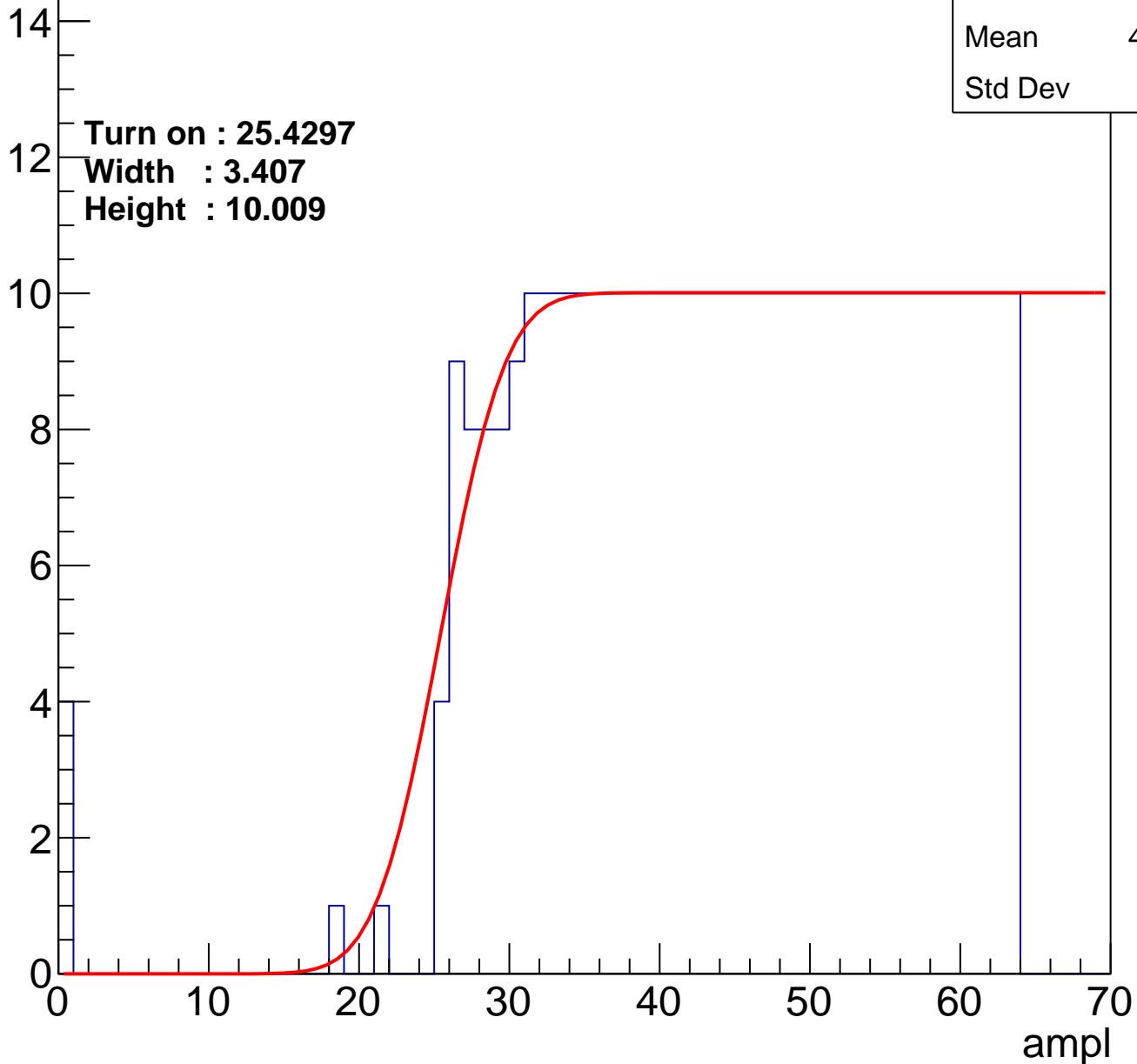
Entries	382
Mean	44.04
Std Dev	11.9

Turn on : 25.4297

Width : 3.407

Height : 10.009

Entry



# B0L000S, U20-ch30

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.5680

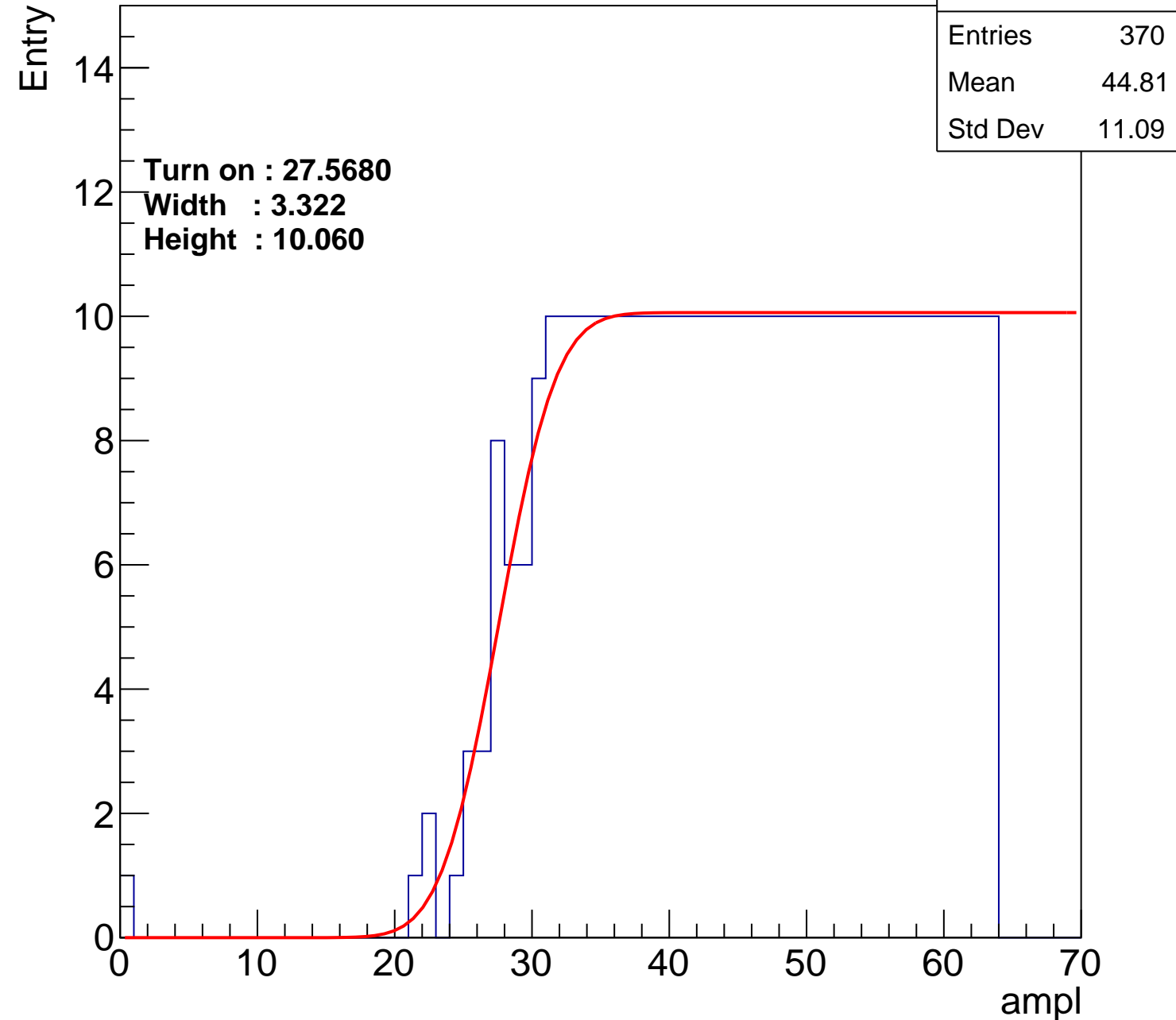
Width : 3.322

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch31

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	359
Mean	45.12
Std Dev	11.44

Turn on : 28.8378

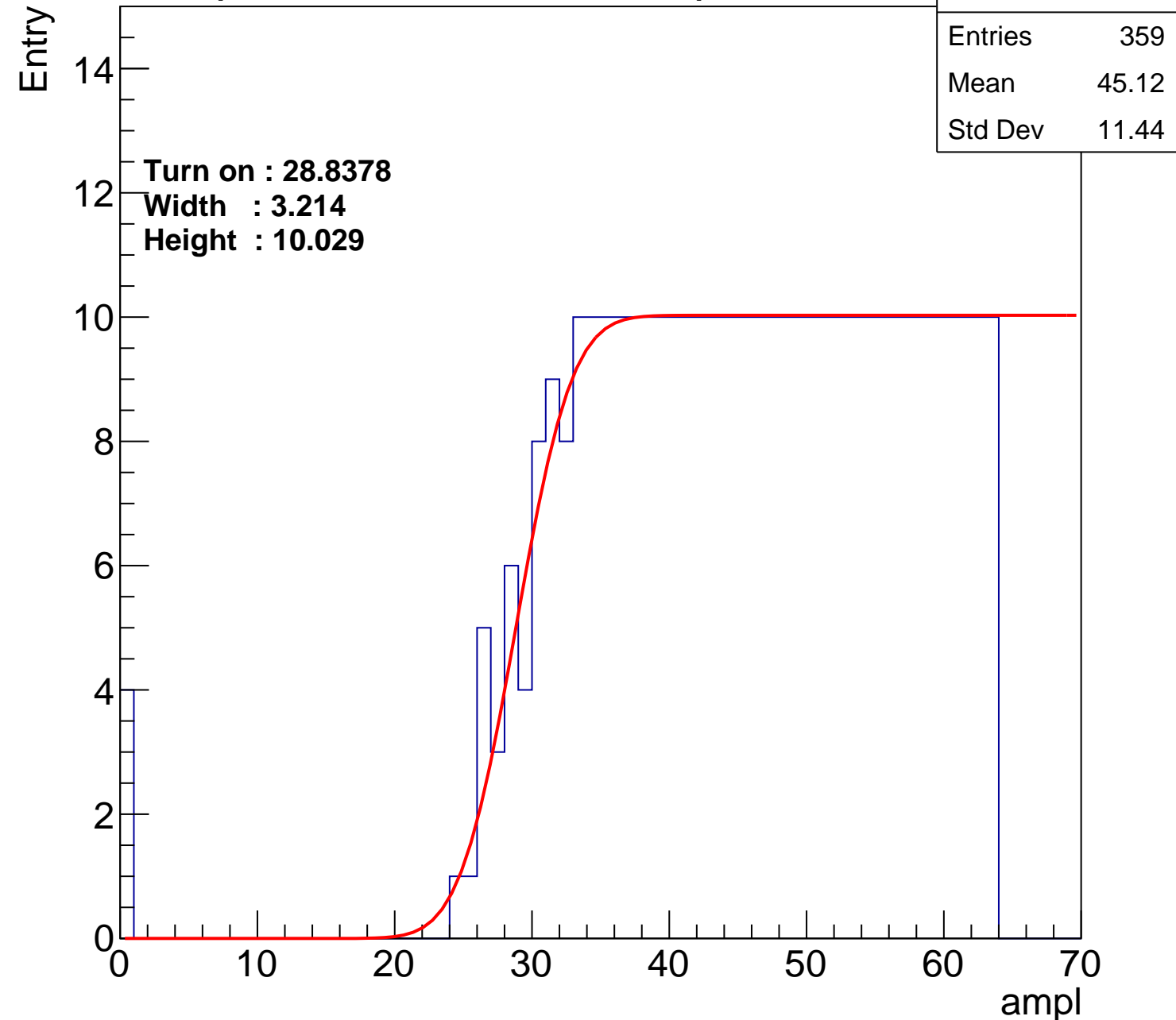
Width : 3.214

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch32

calib\_packv5\_042523\_0143.root, FC#5, port B1

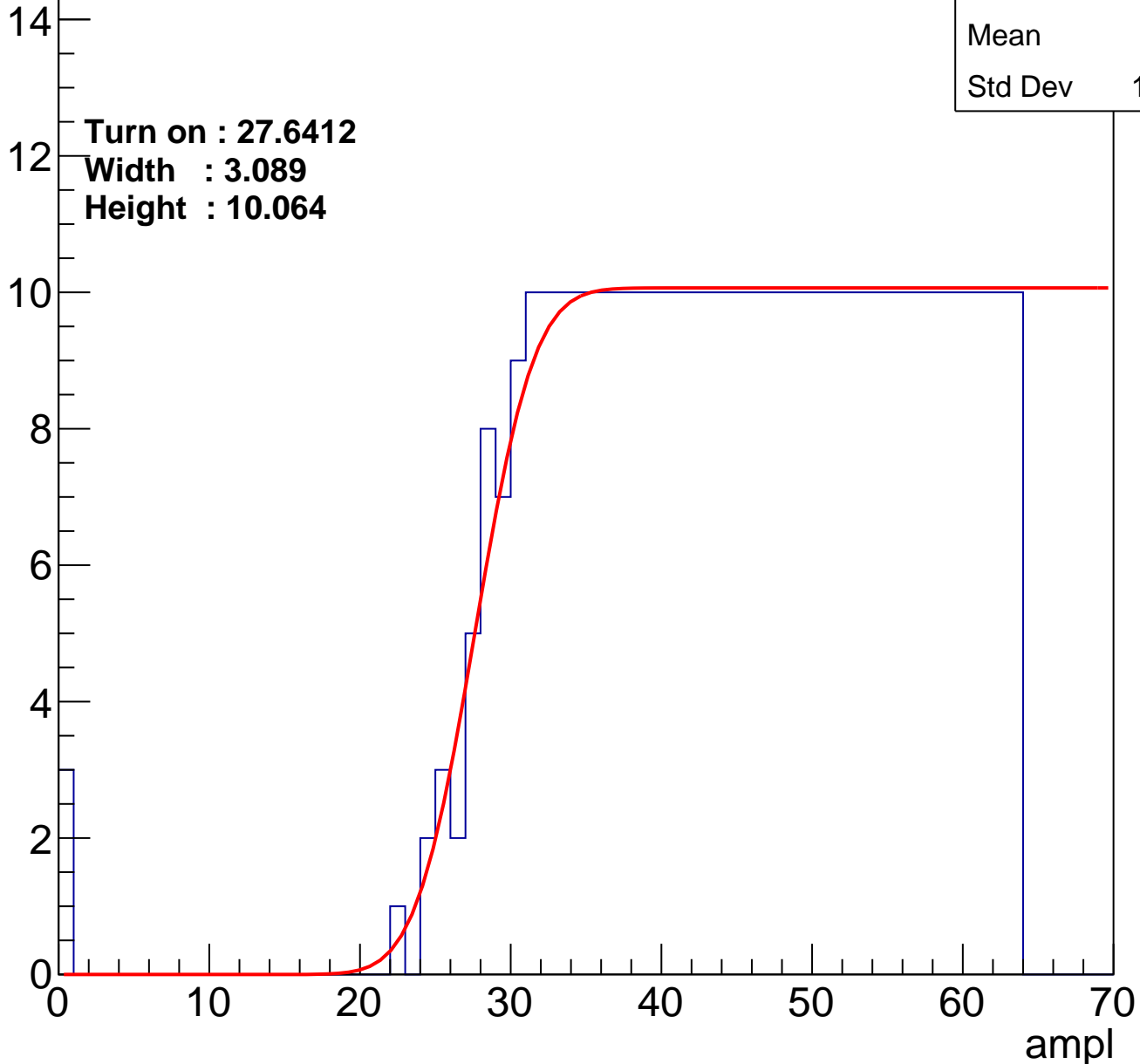
Entries	370
Mean	44.7
Std Dev	11.43

Turn on : 27.6412

Width : 3.089

Height : 10.064

Entry



# B0L000S, U20-ch33

calib\_packv5\_042523\_0143.root, FC#5, port B1

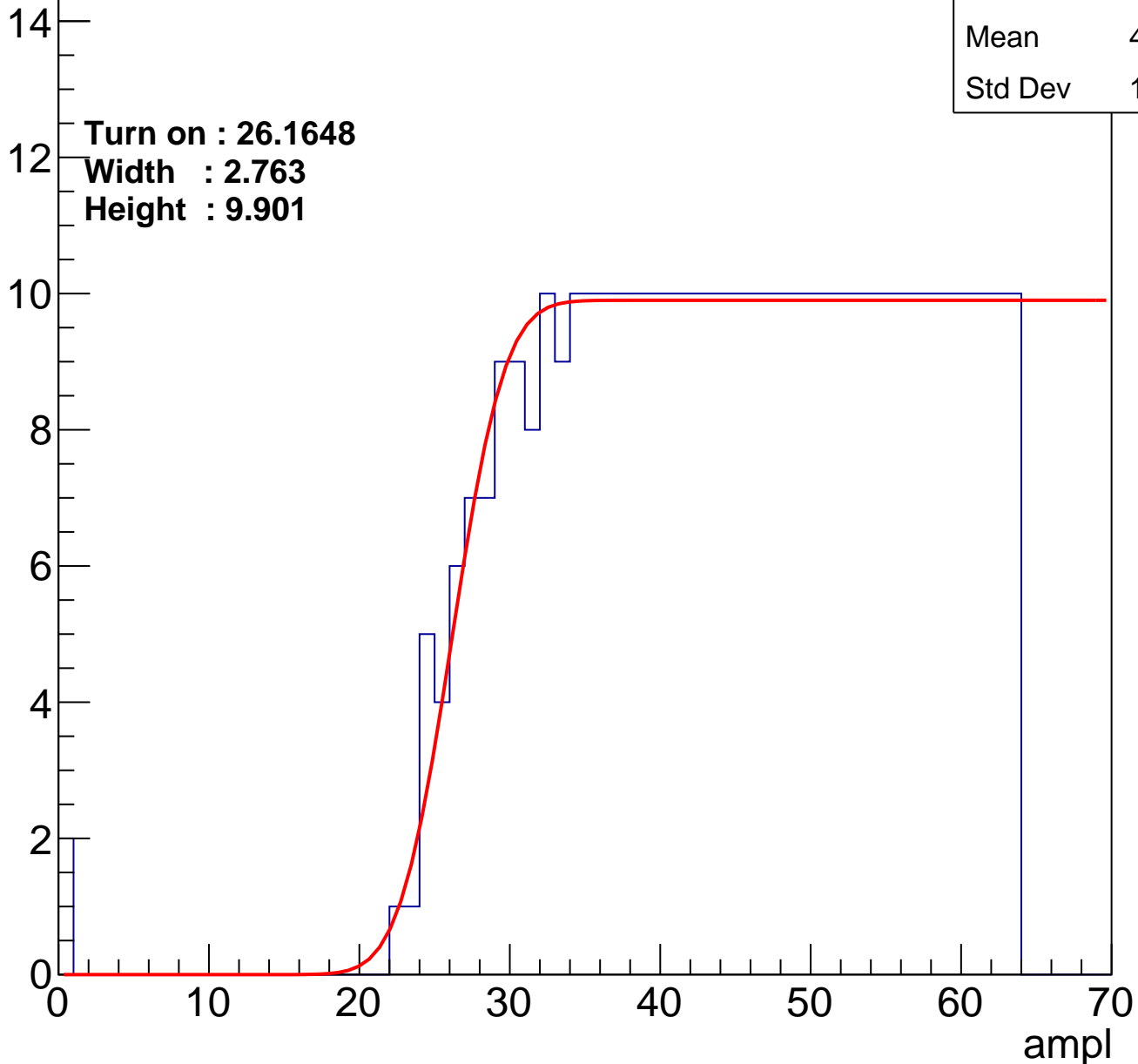
Entries	378
Mean	44.32
Std Dev	11.52

Turn on : 26.1648

Width : 2.763

Height : 9.901

Entry



# B0L000S, U20-ch34

calib\_packv5\_042523\_0143.root, FC#5, port B1

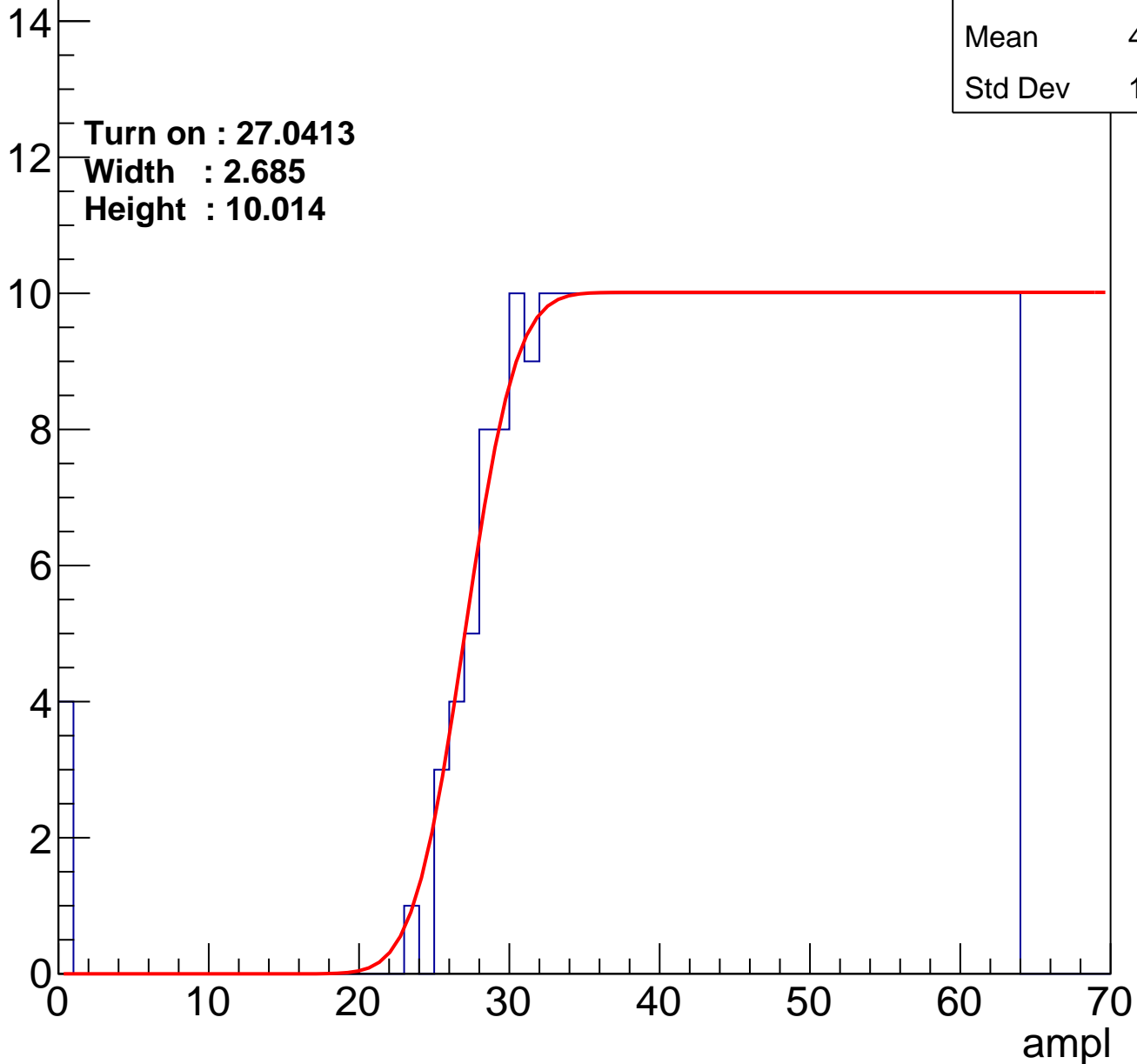
Entries	372
Mean	44.55
Std Dev	11.64

Turn on : 27.0413

Width : 2.685

Height : 10.014

Entry



# B0L000S, U20-ch35

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.8
Std Dev	11.85

Turn on : 25.3385

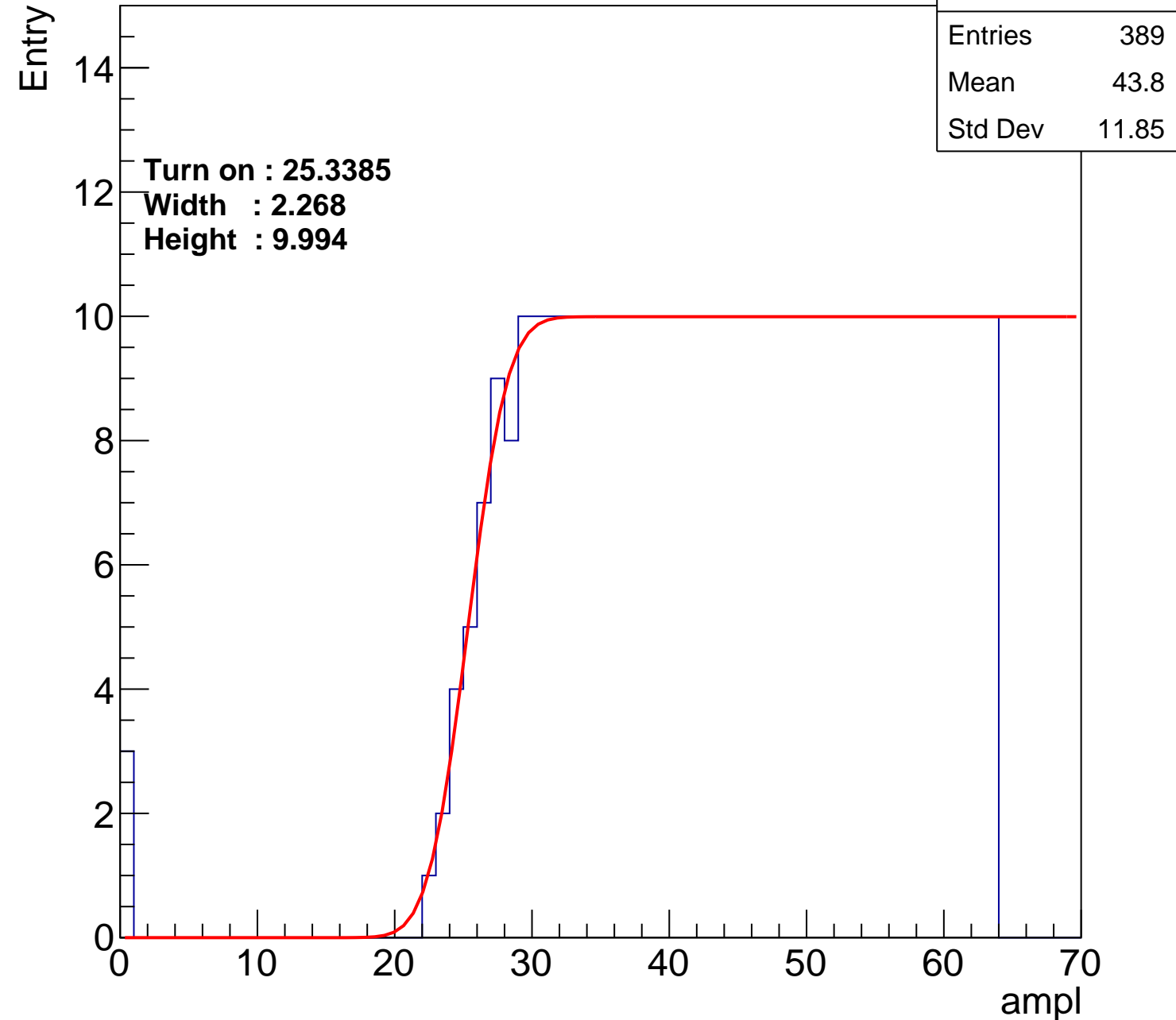
Width : 2.268

Height : 9.994

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch36

calib\_packv5\_042523\_0143.root, FC#5, port B1

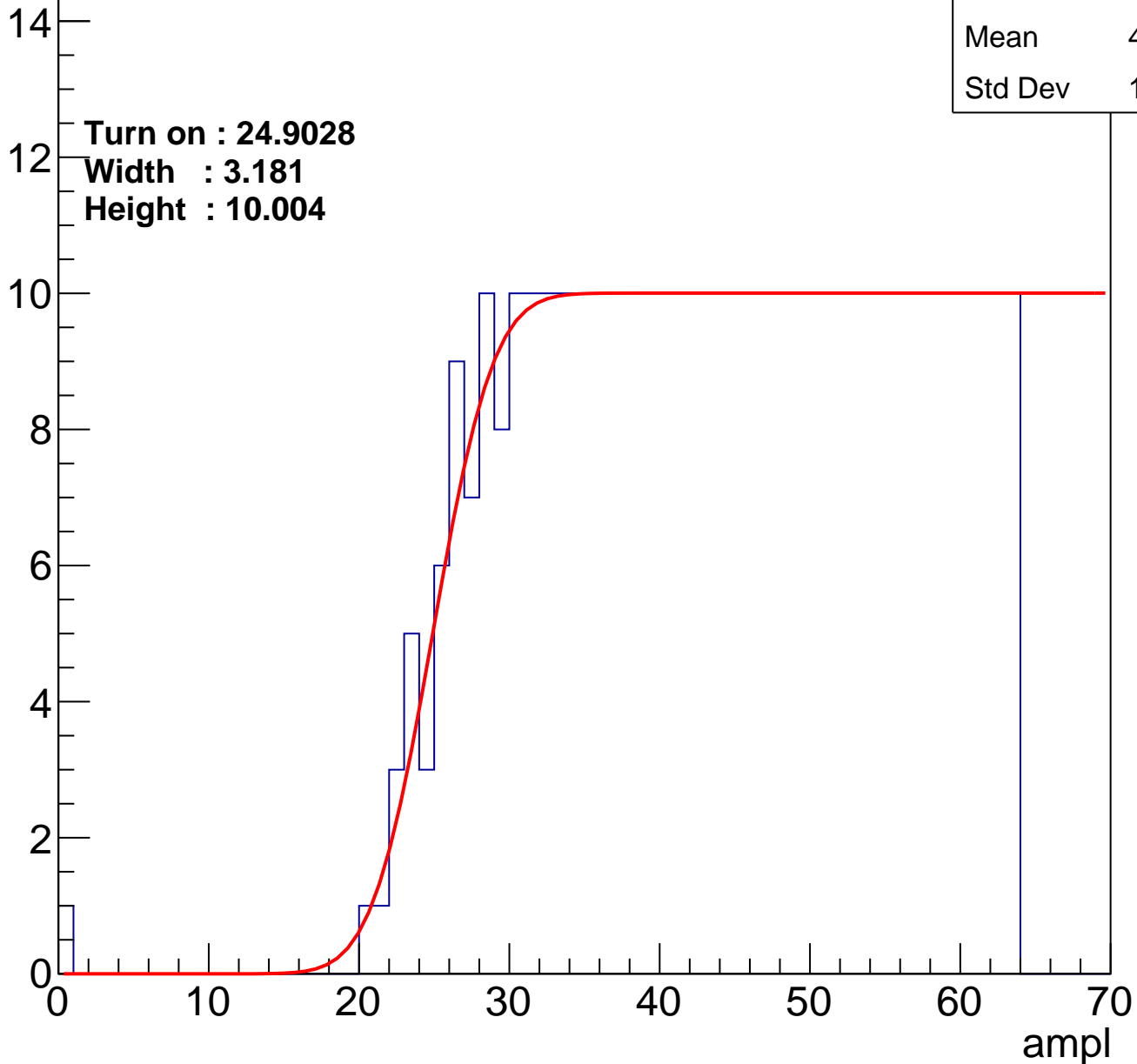
Entries	394
Mean	43.63
Std Dev	11.72

**Turn on : 24.9028**

**Width : 3.181**

**Height : 10.004**

Entry



# B0L000S, U20-ch37

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.23
Std Dev	11.52

Turn on : 26.7530

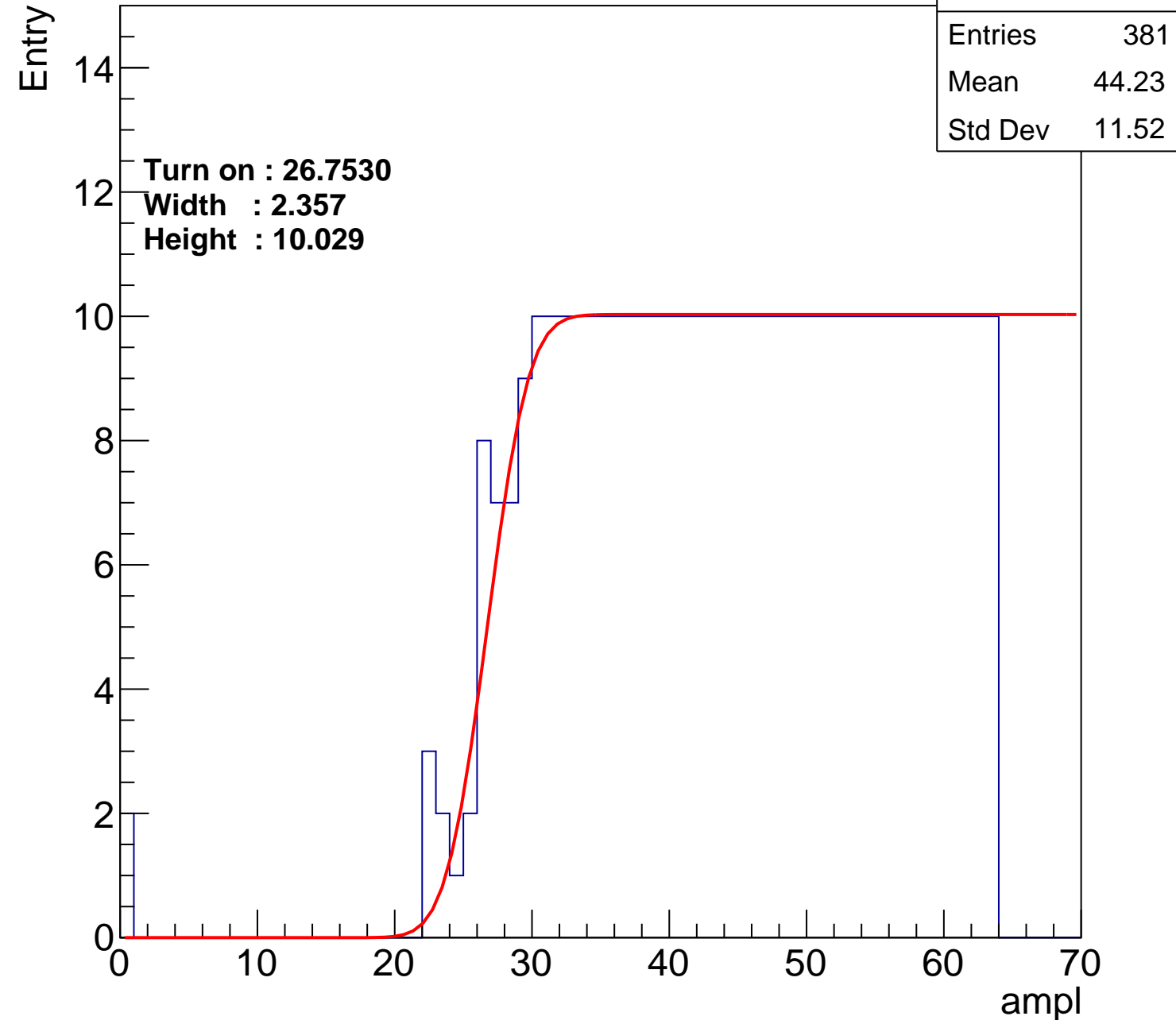
Width : 2.357

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch38

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.07
Std Dev	11.92

**Turn on : 26.8304**

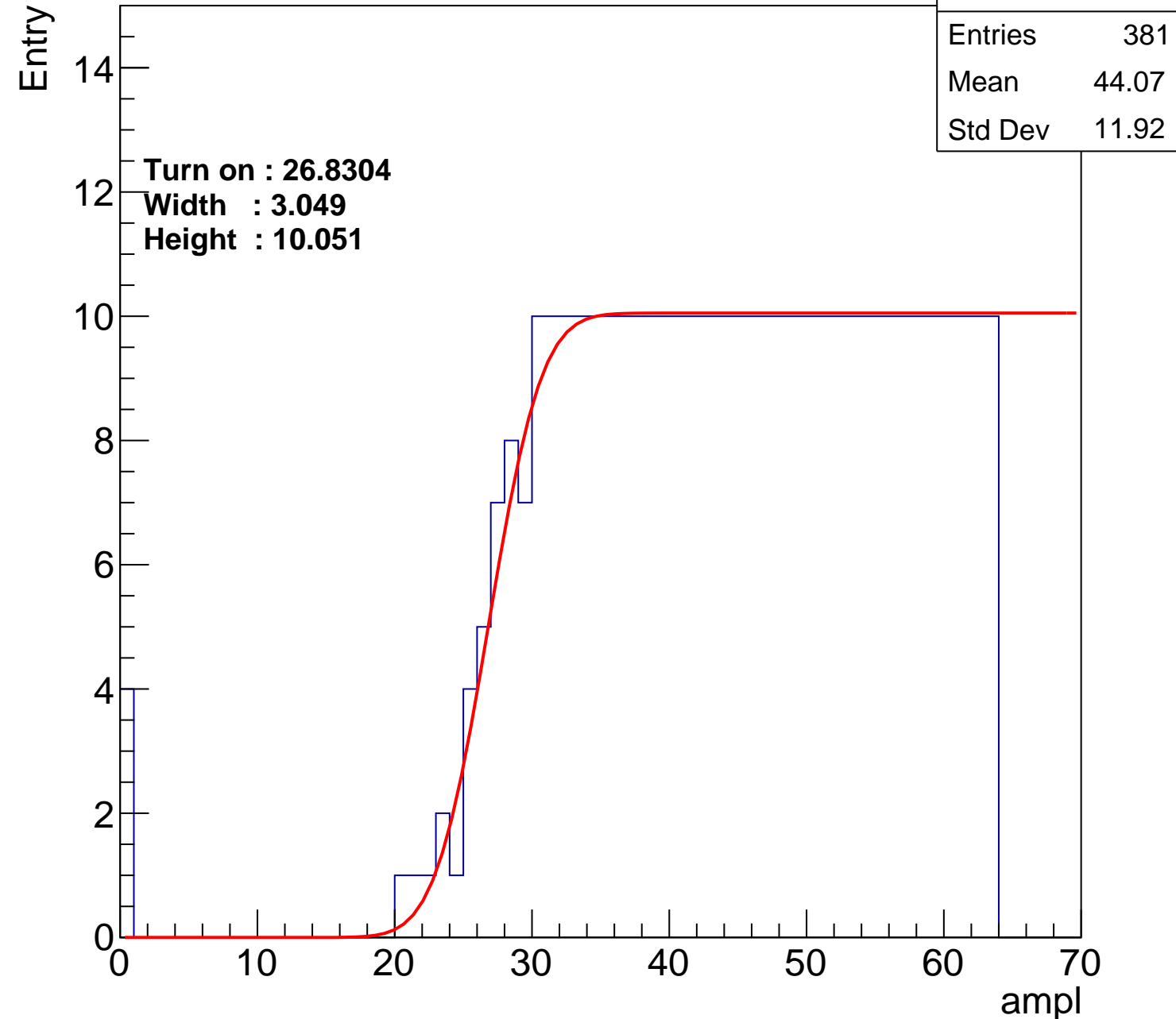
**Width : 3.049**

**Height : 10.051**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch39

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	45.01
Std Dev	11.26

**Turn on : 27.9177**

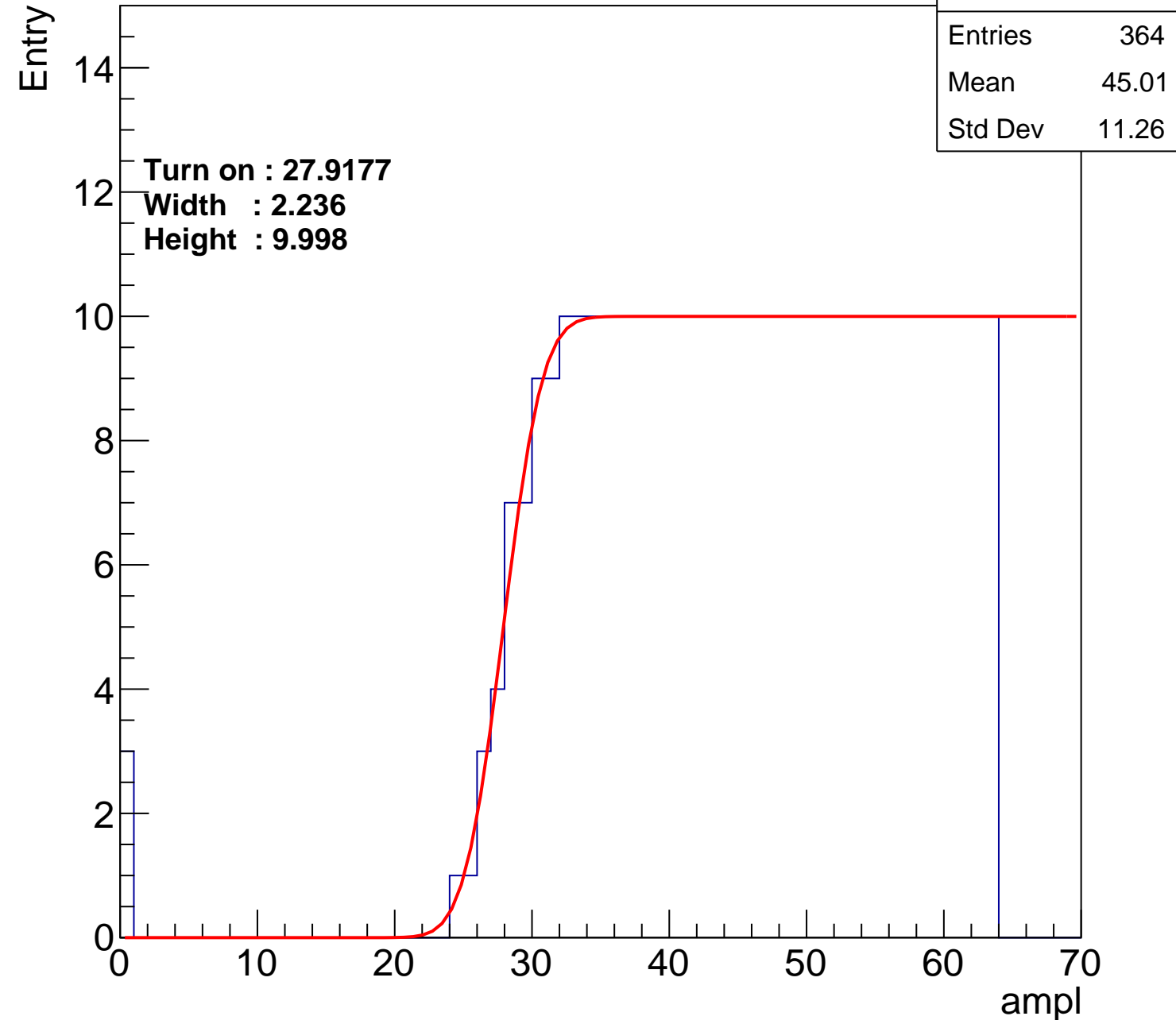
**Width : 2.236**

**Height : 9.998**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch40

calib\_packv5\_042523\_0143.root, FC#5, port B1

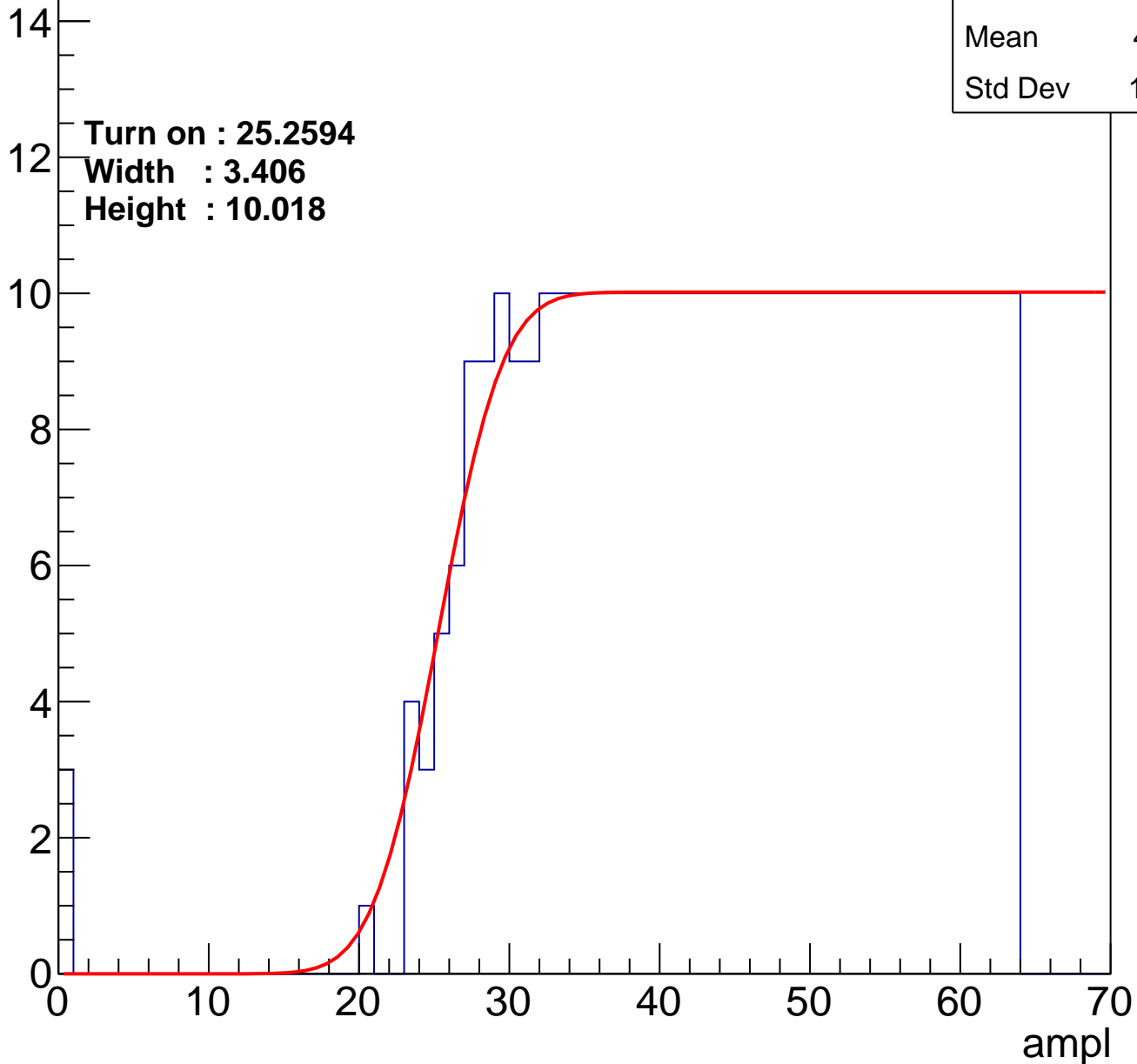
Entries	388
Mean	43.81
Std Dev	11.88

Turn on : 25.2594

Width : 3.406

Height : 10.018

Entry



# B0L000S, U20-ch41

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.87
Std Dev	11.04

Turn on : 27.2506

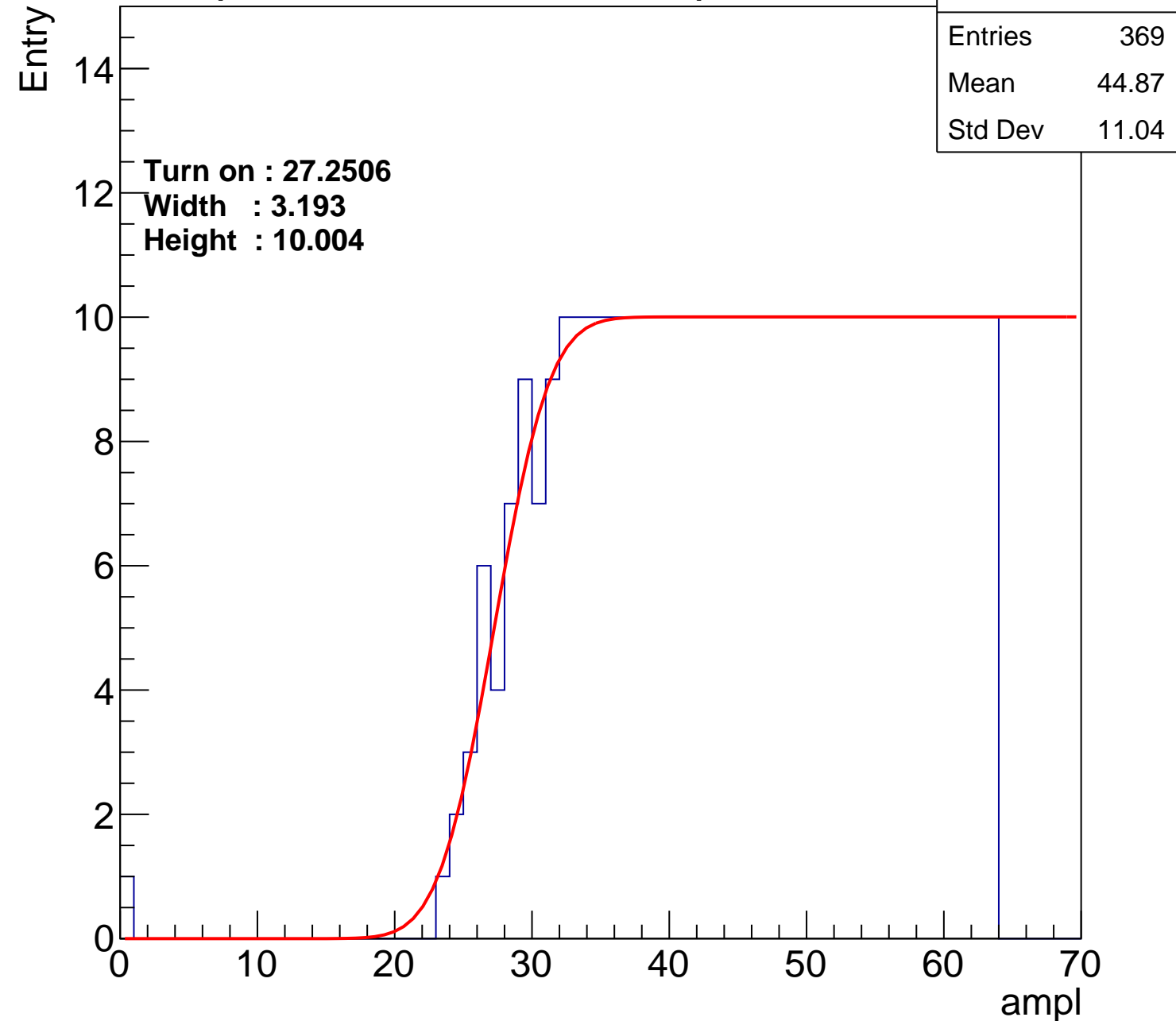
Width : 3.193

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch42

calib\_packv5\_042523\_0143.root, FC#5, port B1

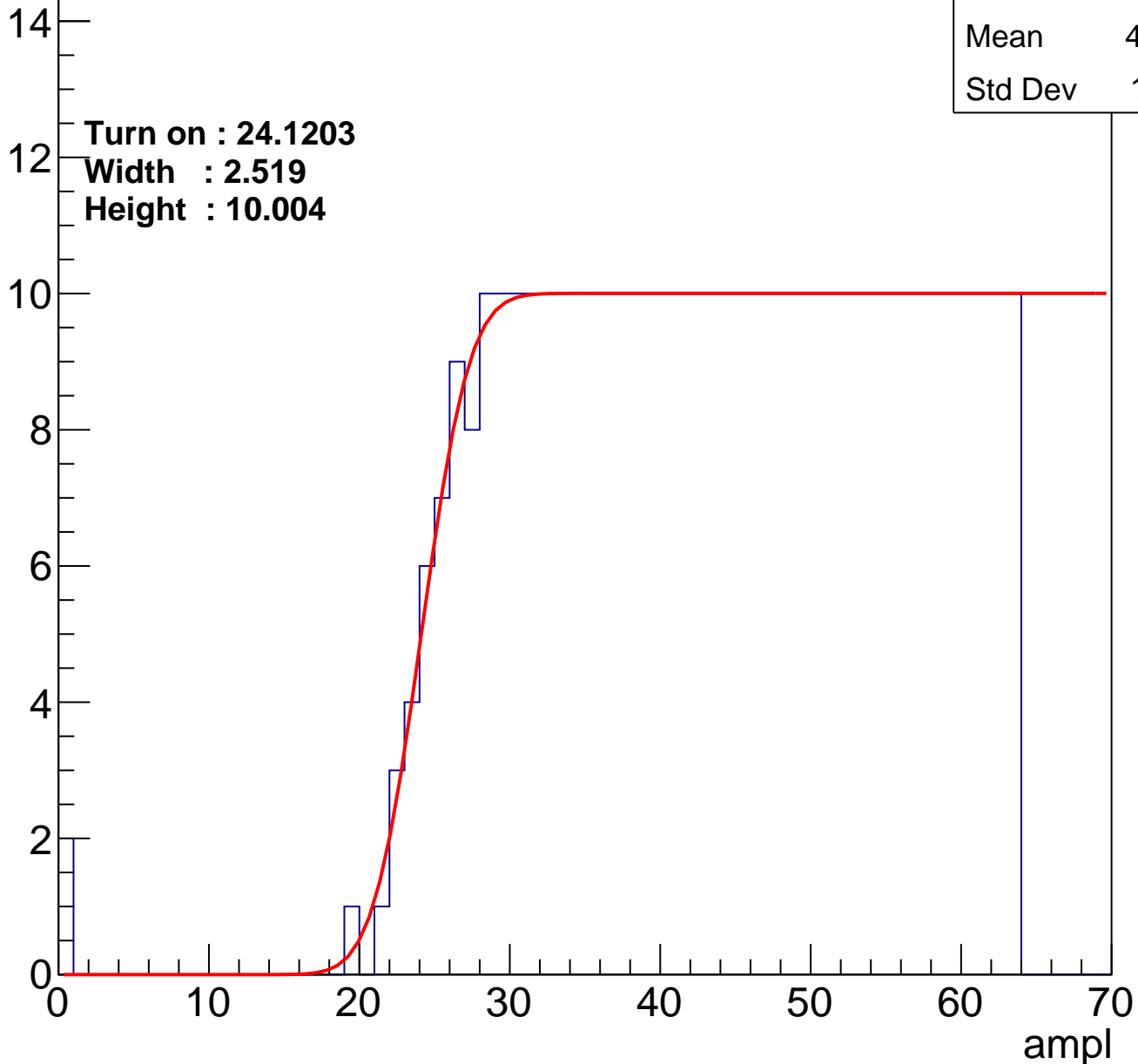
Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.1203

Width : 2.519

Height : 10.004

Entry



# B0L000S, U20-ch43

calib\_packv5\_042523\_0143.root, FC#5, port B1

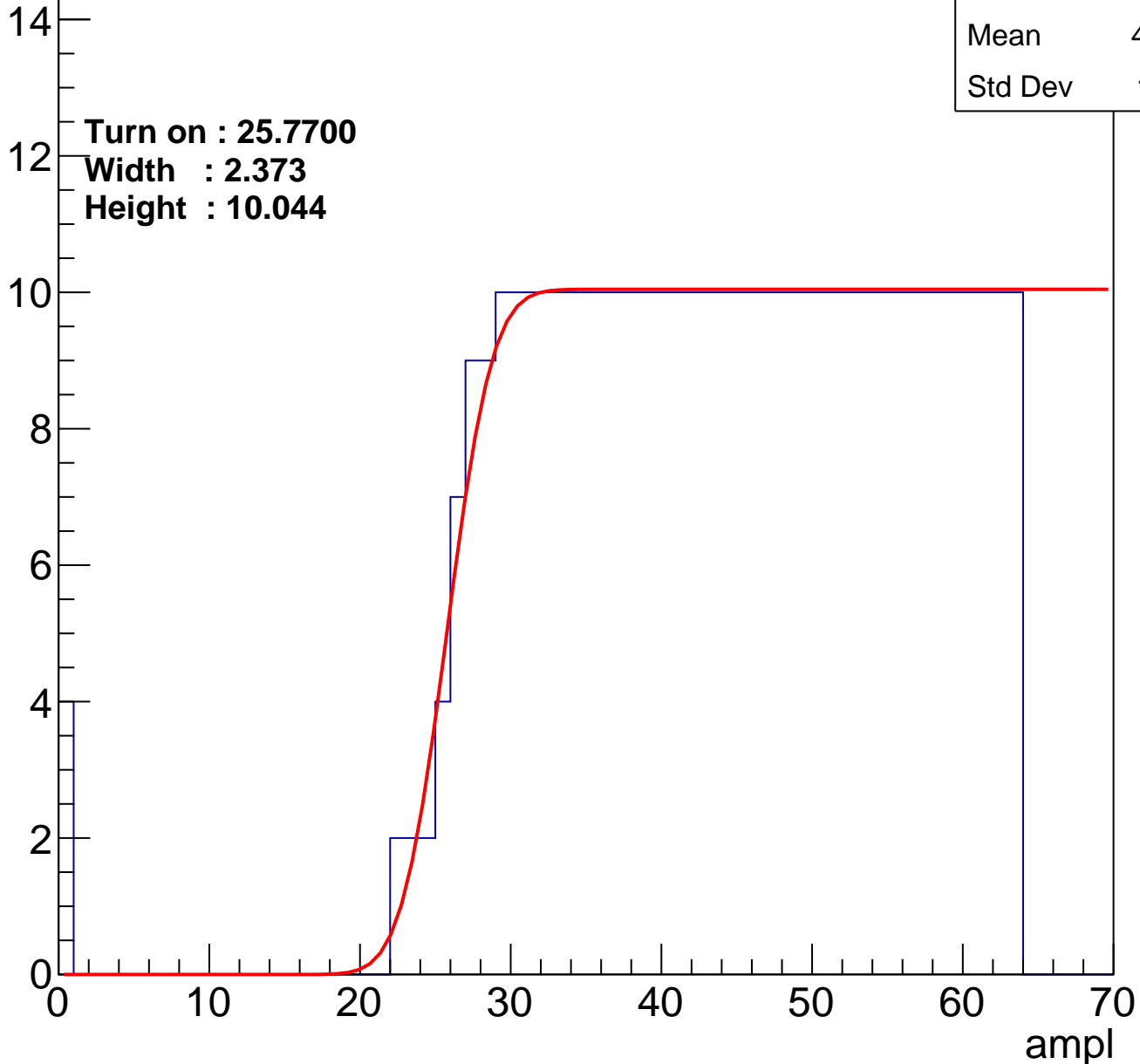
Entries	389
Mean	43.74
Std Dev	12.01

**Turn on : 25.7700**

**Width : 2.373**

**Height : 10.044**

Entry



# B0L000S, U20-ch44

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.49
Std Dev	11.55

**Turn on : 27.0127**

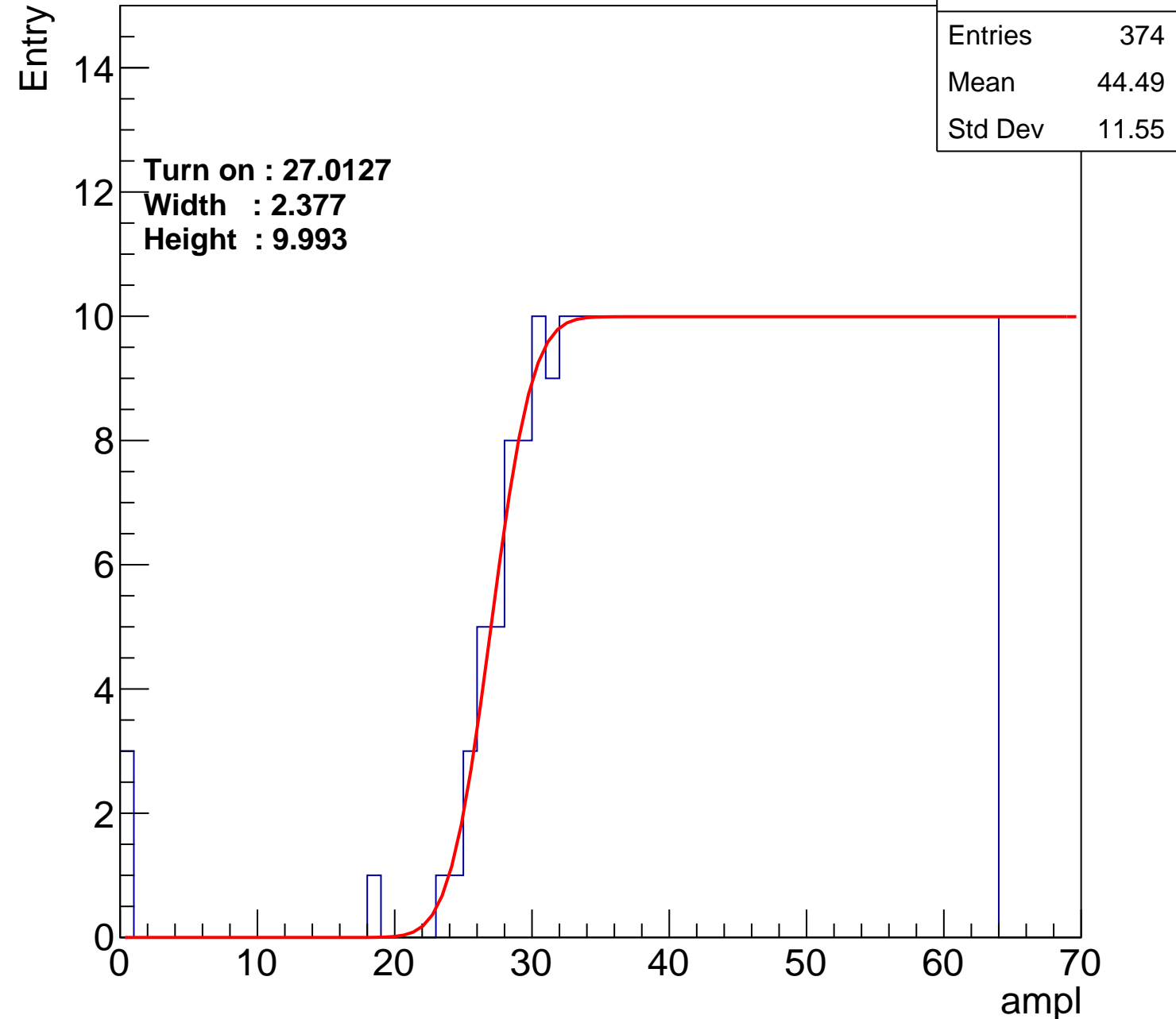
**Width : 2.377**

**Height : 9.993**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch45

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	358
Mean	45.45
Std Dev	10.69

**Turn on : 28.3750**

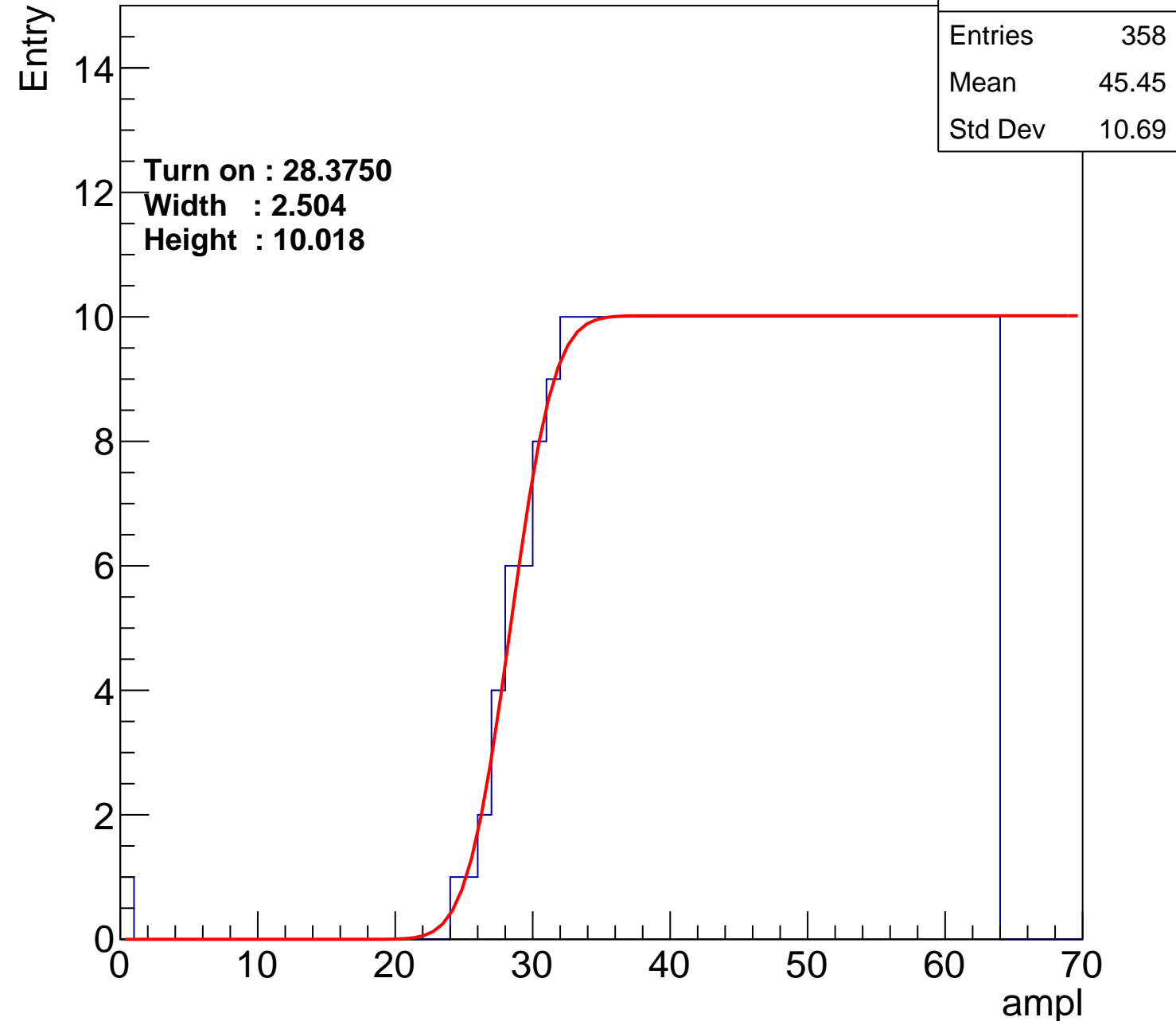
**Width : 2.504**

**Height : 10.018**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch46

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	364
Mean	44.9
Std Dev	11.53

**Turn on : 27.7903**

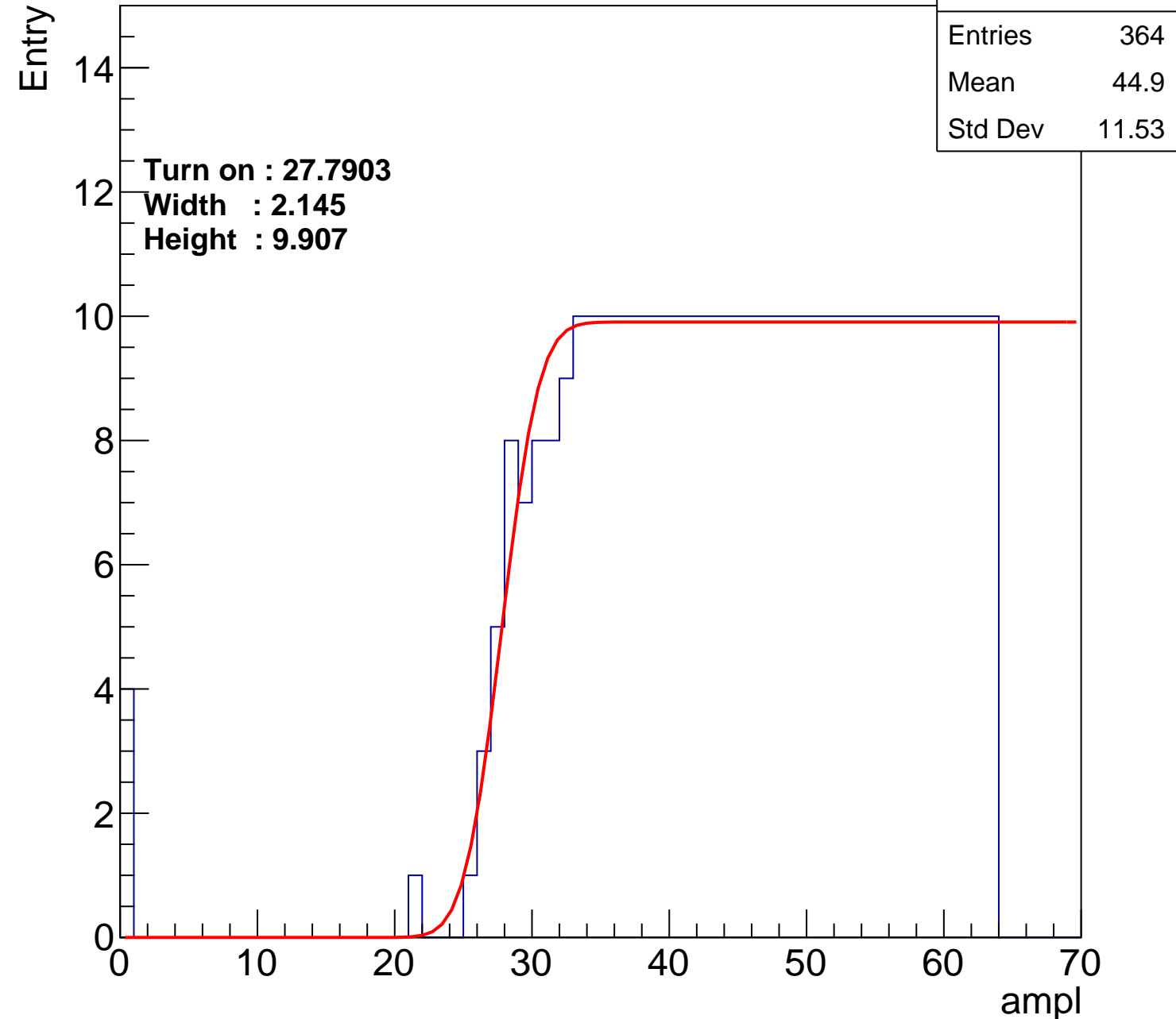
**Width : 2.145**

**Height : 9.907**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch47

calib\_packv5\_042523\_0143.root, FC#5, port B1

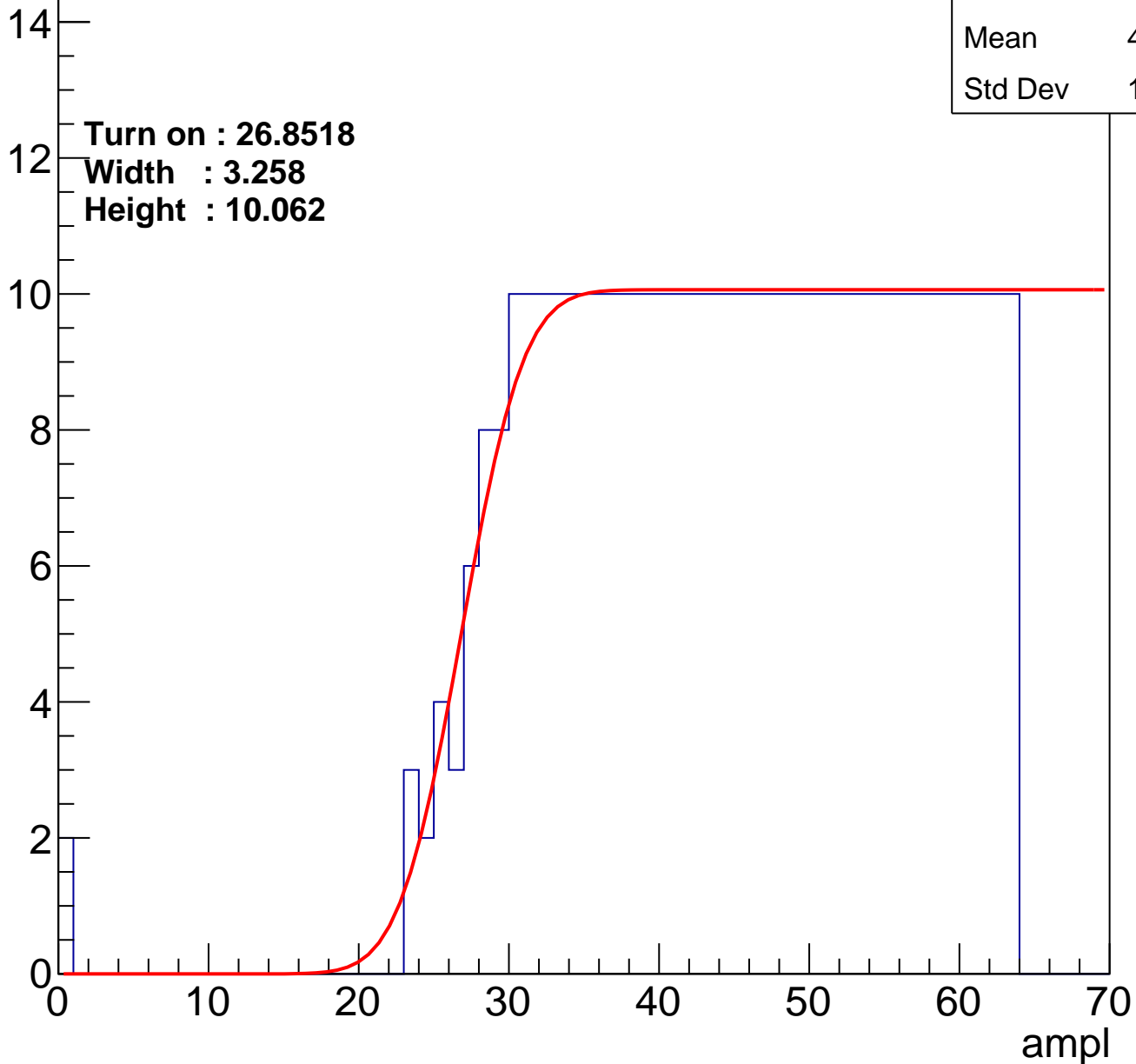
Entries	376
Mean	44.48
Std Dev	11.39

Turn on : 26.8518

Width : 3.258

Height : 10.062

Entry



# B0L000S, U20-ch48

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.67
Std Dev	11.48

Turn on : 27.3286

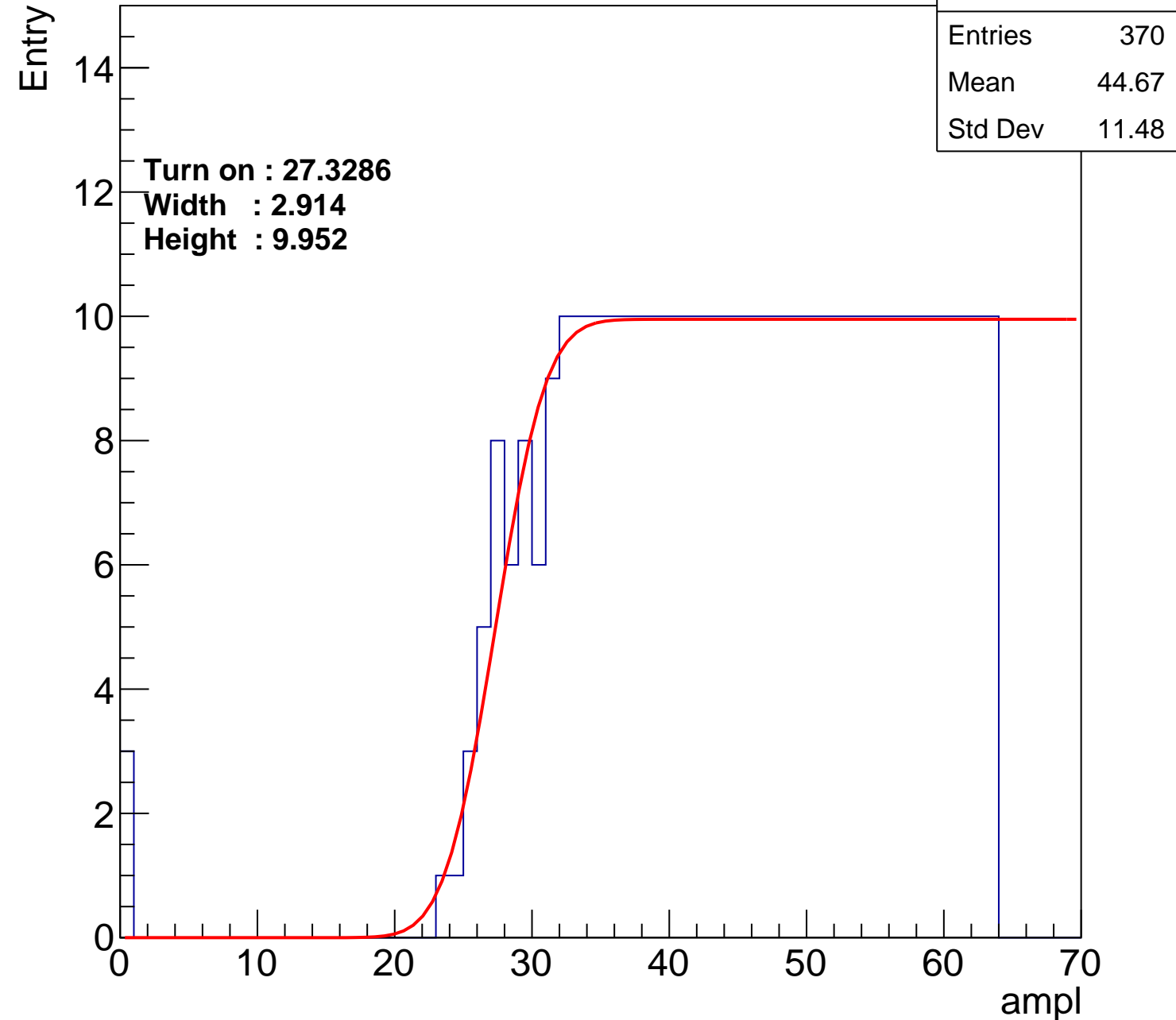
Width : 2.914

Height : 9.952

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch49

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.67
Std Dev	11.15

Turn on : 27.0069

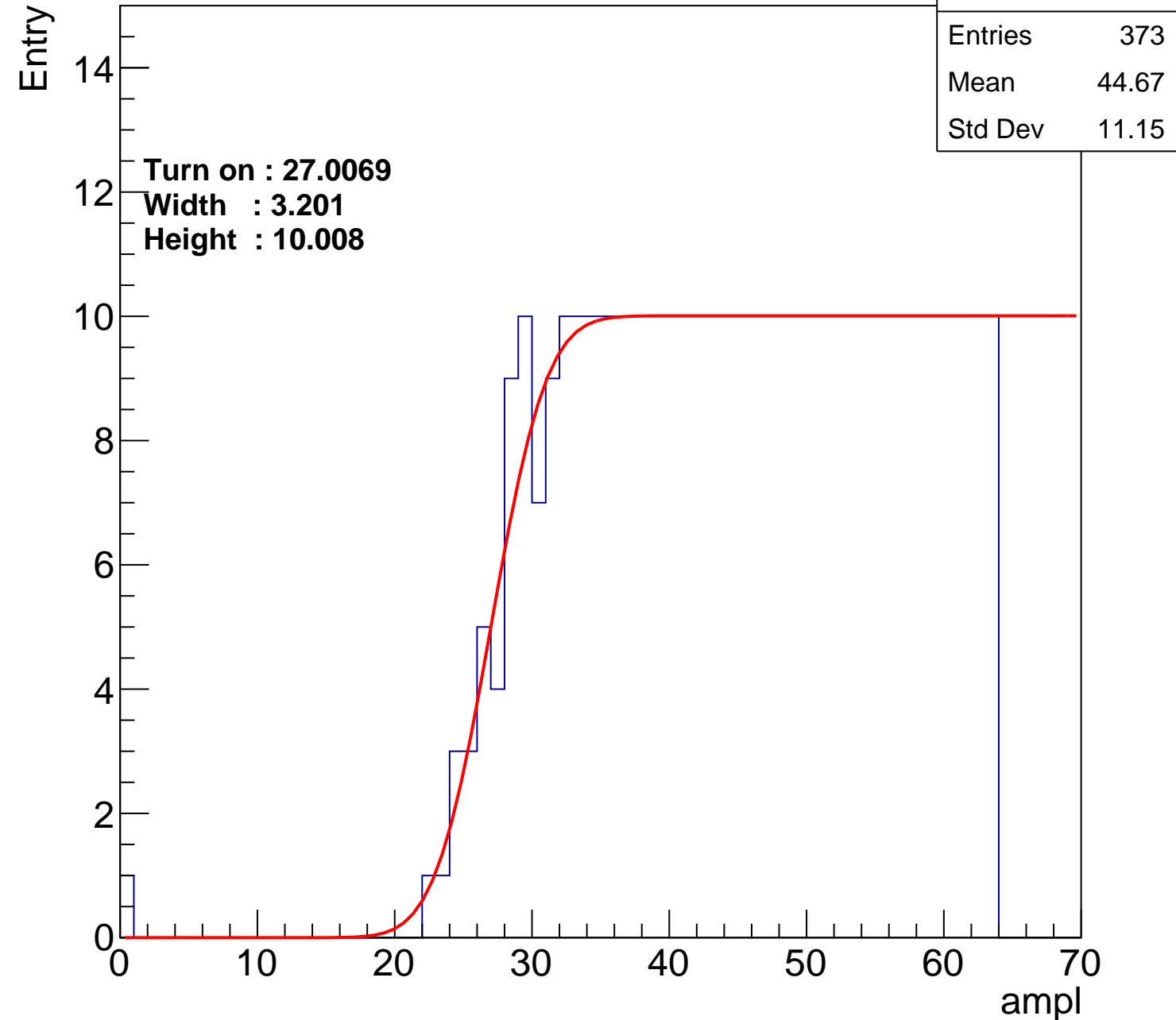
Width : 3.201

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch50

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.78
Std Dev	12.17

Turn on : 25.6099

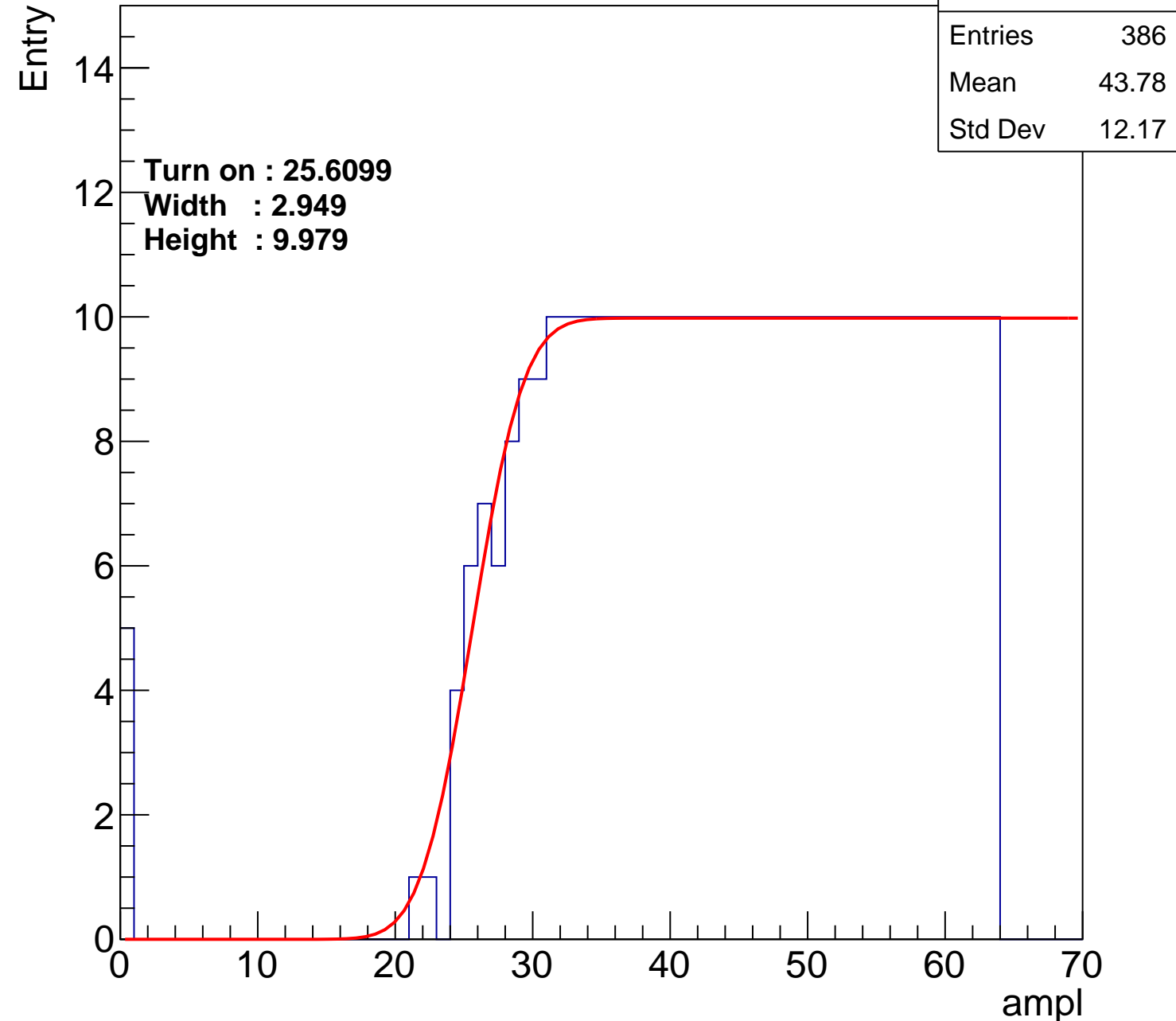
Width : 2.949

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch51

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.37
Std Dev	12.07

Turn on : 28.2063

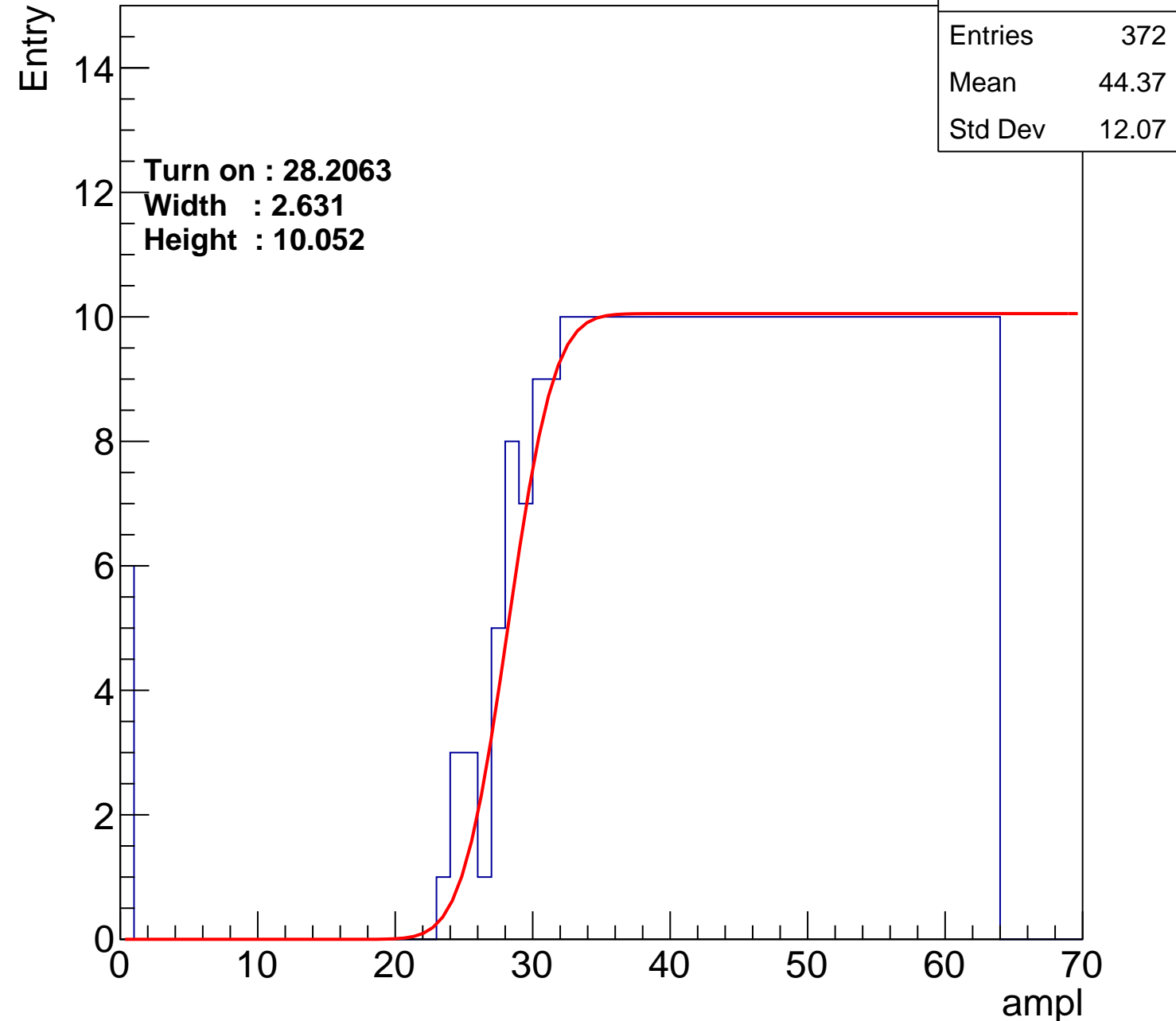
Width : 2.631

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch52

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.16
Std Dev	11.83

**Turn on : 26.5570**

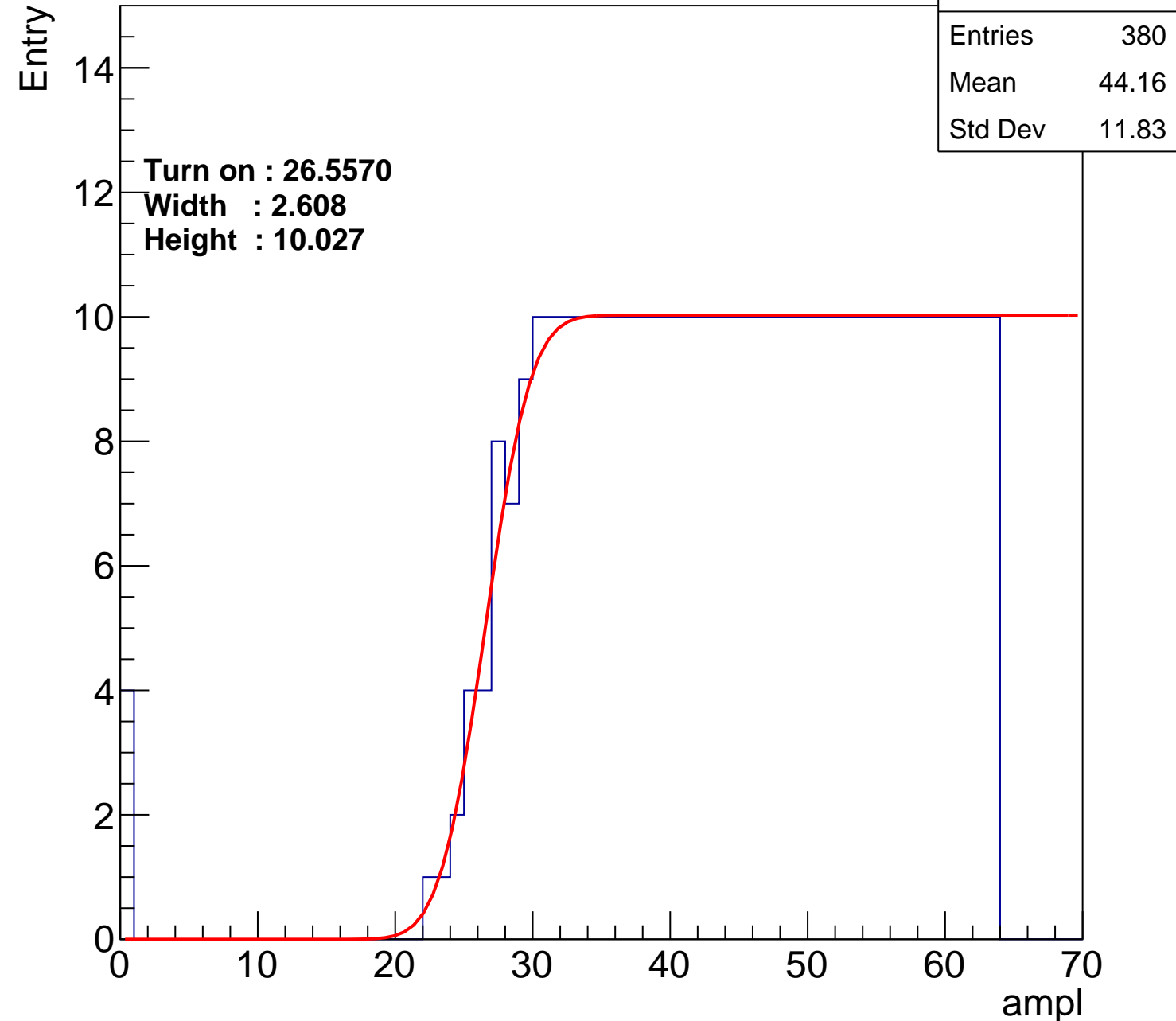
**Width : 2.608**

**Height : 10.027**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch53

calib\_packv5\_042523\_0143.root, FC#5, port B1

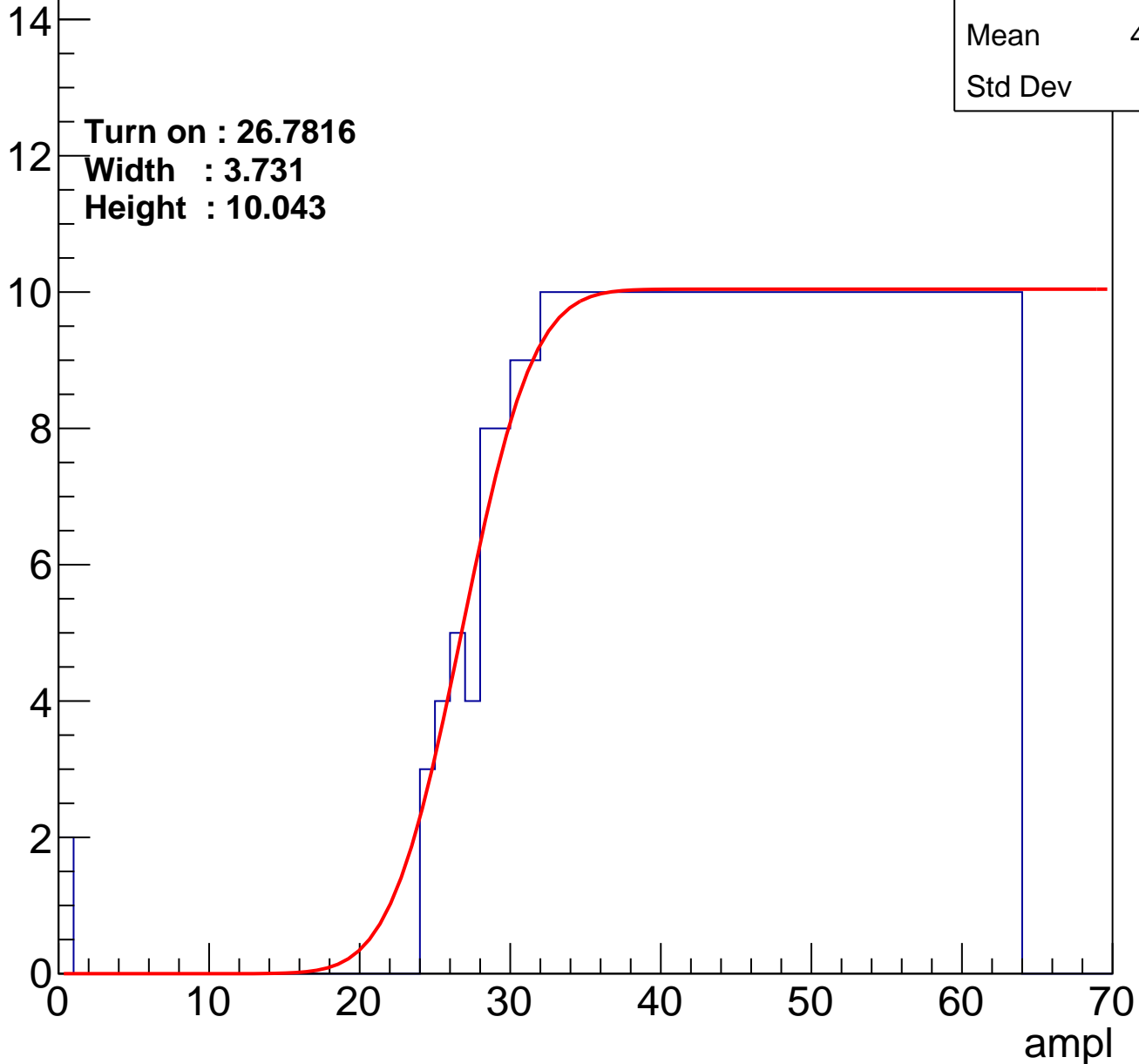
Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 26.7816

Width : 3.731

Height : 10.043

Entry



# B0L000S, U20-ch54

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.8
Std Dev	11.05

Turn on : 27.1734

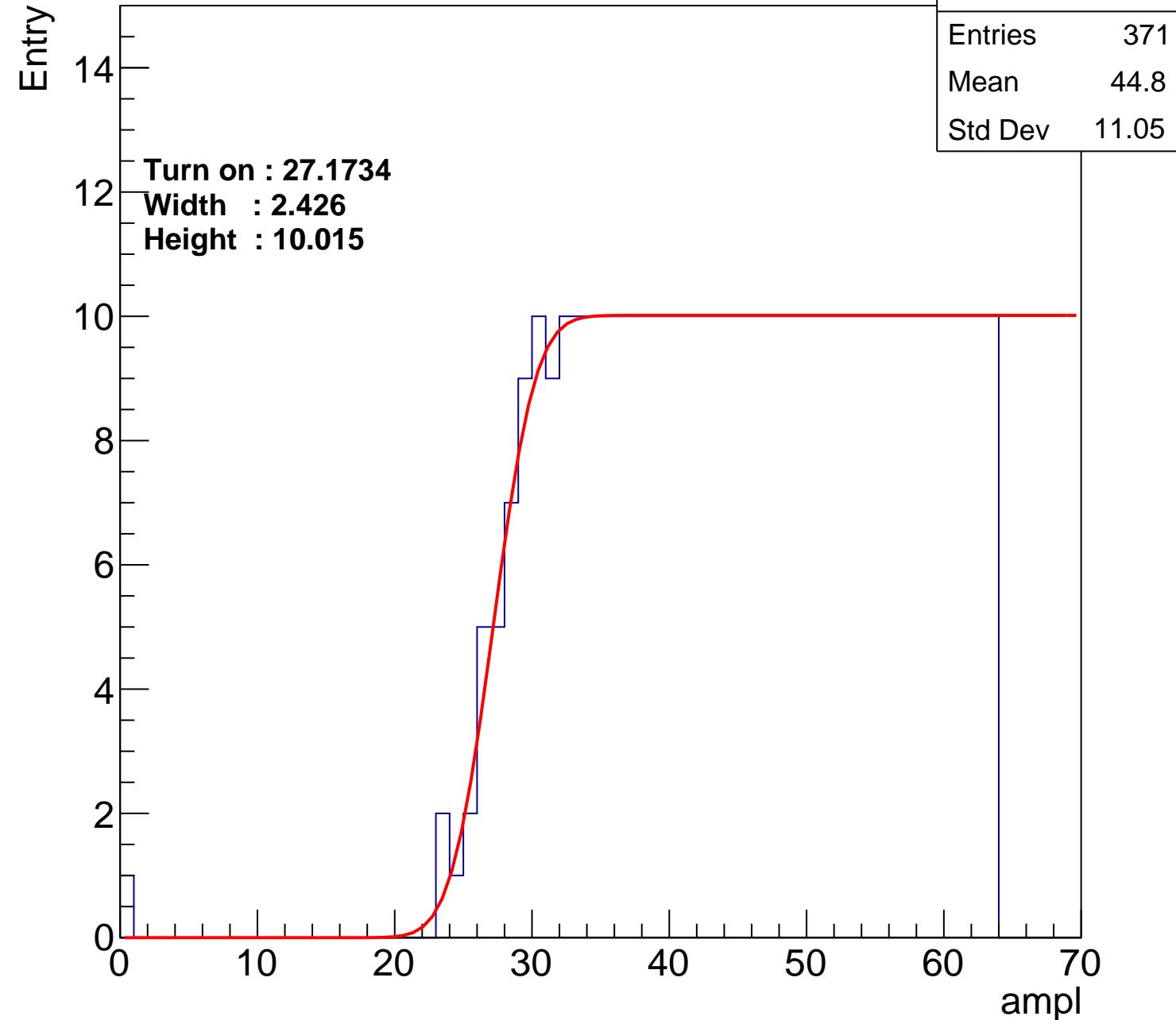
Width : 2.426

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch55

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	352
Mean	45.71
Std Dev	10.59

**Turn on : 29.1739**

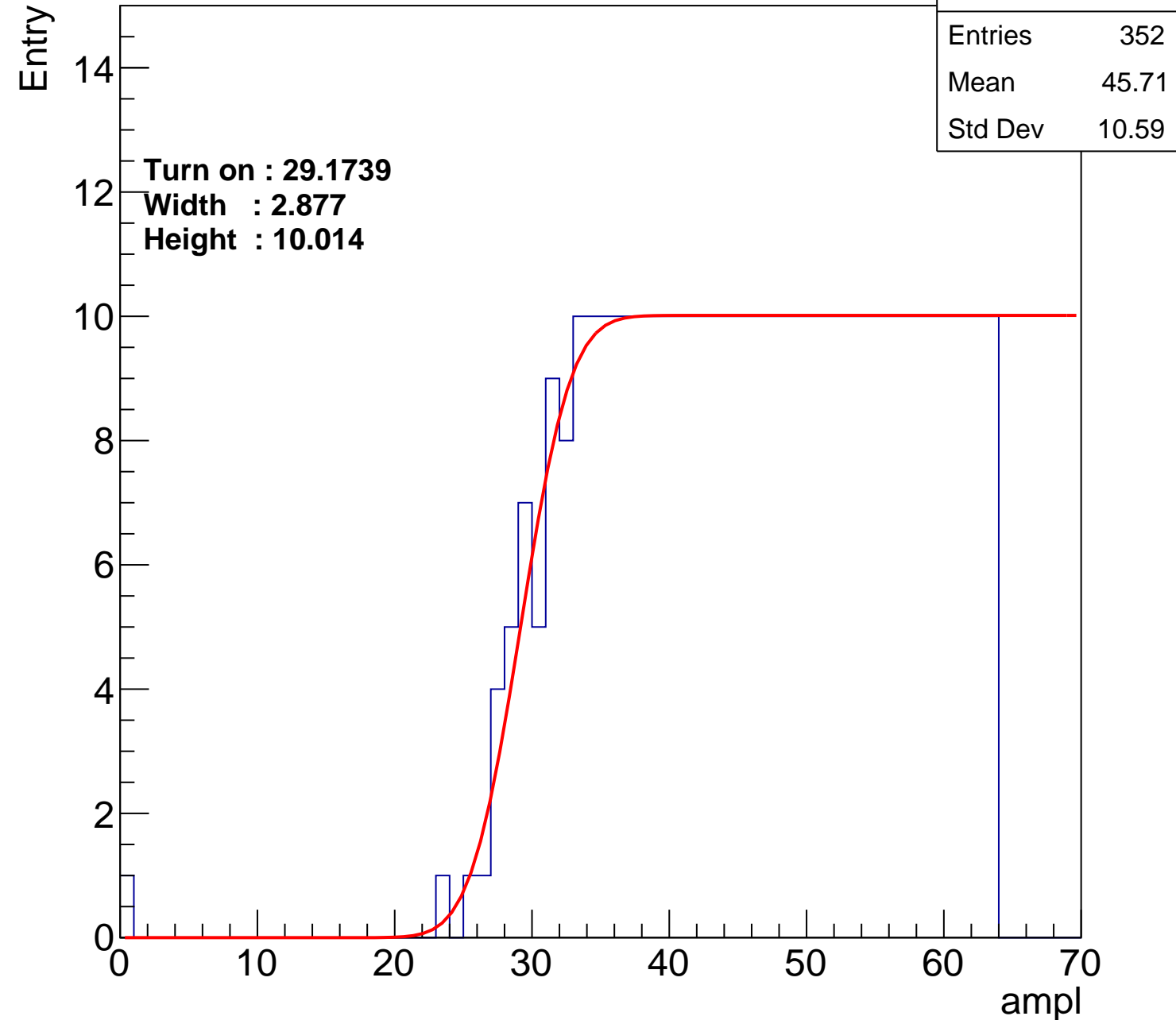
**Width : 2.877**

**Height : 10.014**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch56

calib\_packv5\_042523\_0143.root, FC#5, port B1

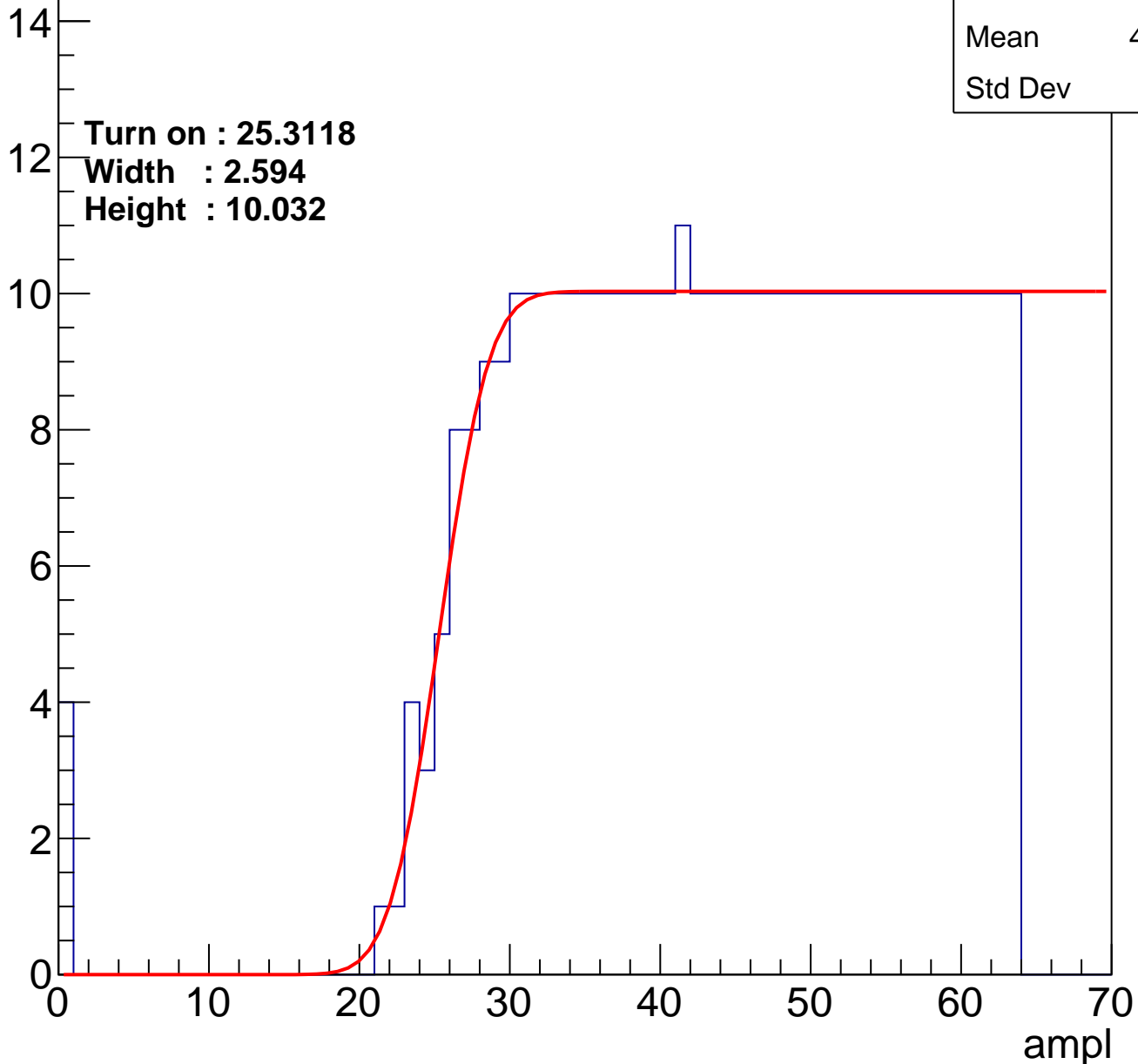
Entries	393
Mean	43.56
Std Dev	12.1

Turn on : 25.3118

Width : 2.594

Height : 10.032

Entry



# B0L000S, U20-ch57

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.47
Std Dev	11.86

Turn on : 27.5805

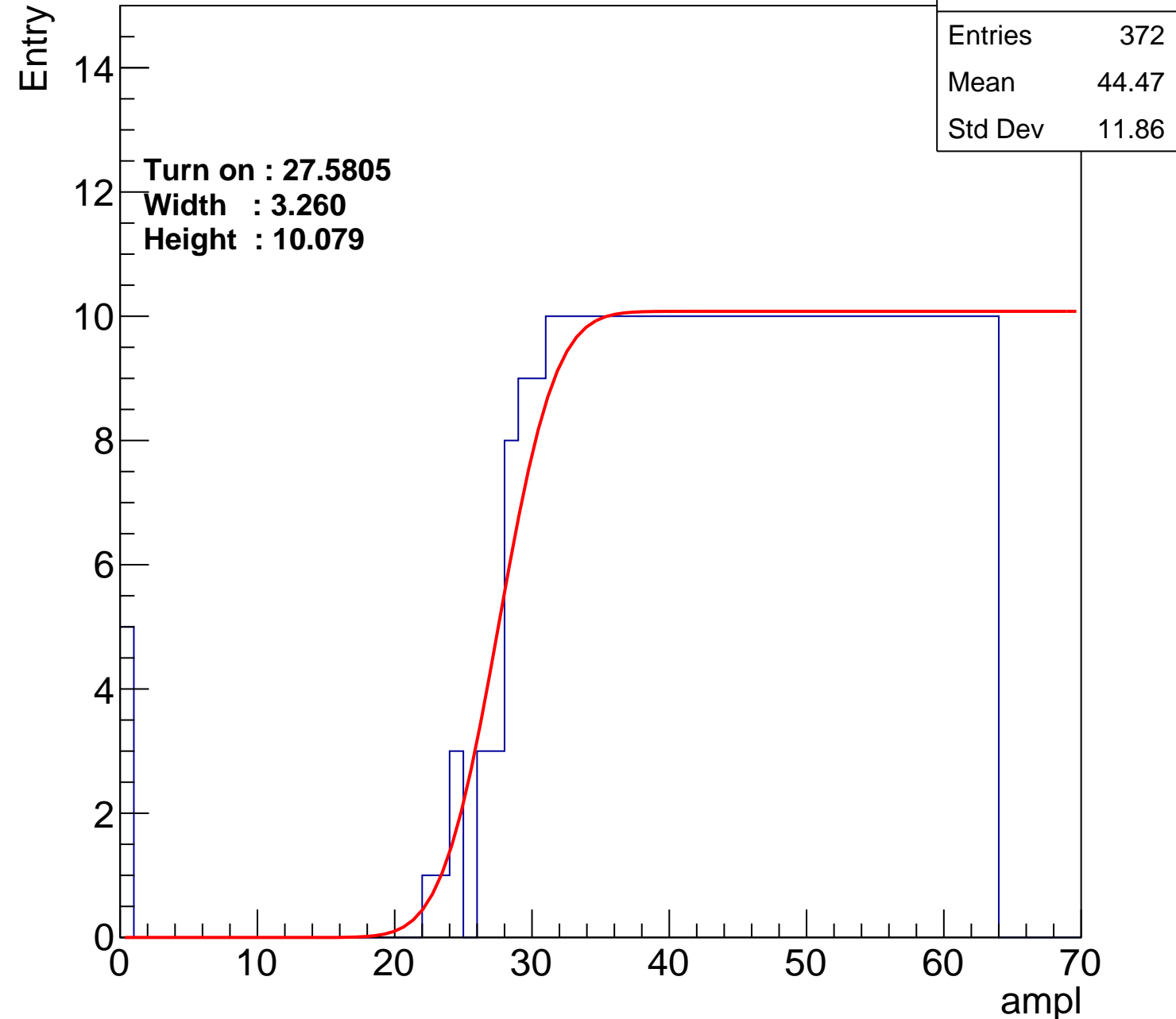
Width : 3.260

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch58

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.55
Std Dev	11.19

Turn on : 26.4642

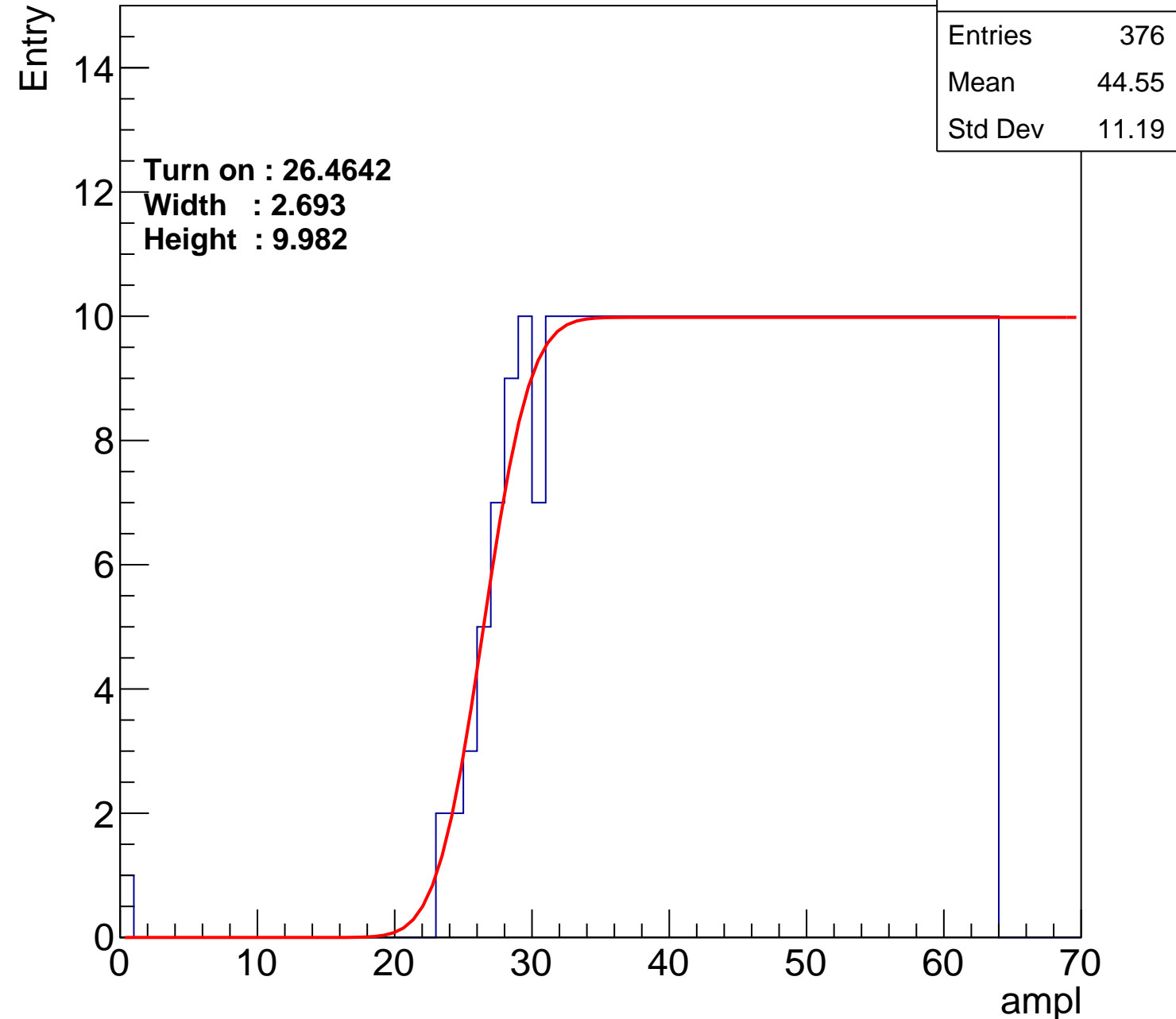
Width : 2.693

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch59

calib\_packv5\_042523\_0143.root, FC#5, port B1

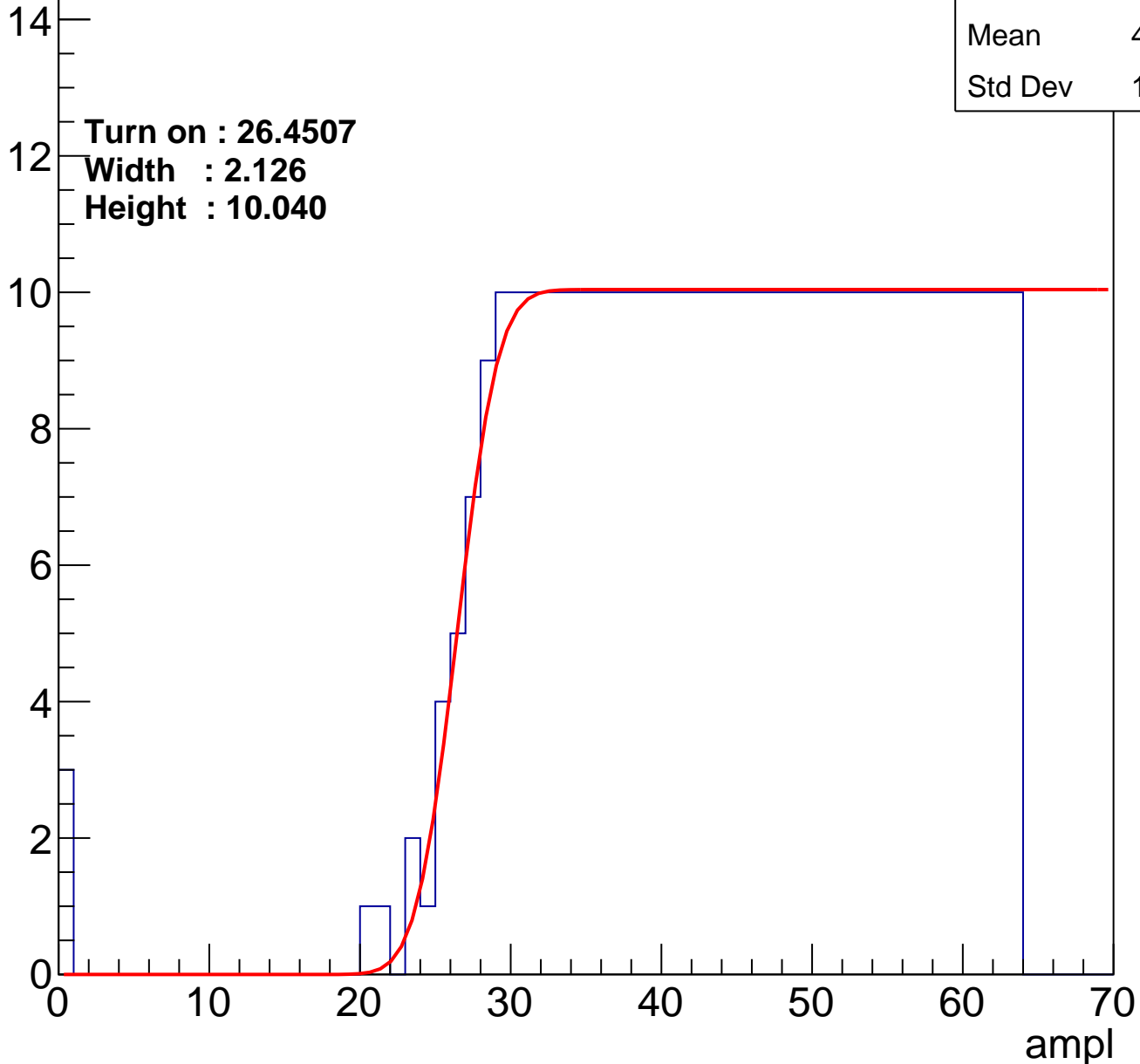
Entries	383
Mean	44.08
Std Dev	11.73

**Turn on : 26.4507**

**Width : 2.126**

**Height : 10.040**

Entry



# B0L000S, U20-ch60

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 26.9197

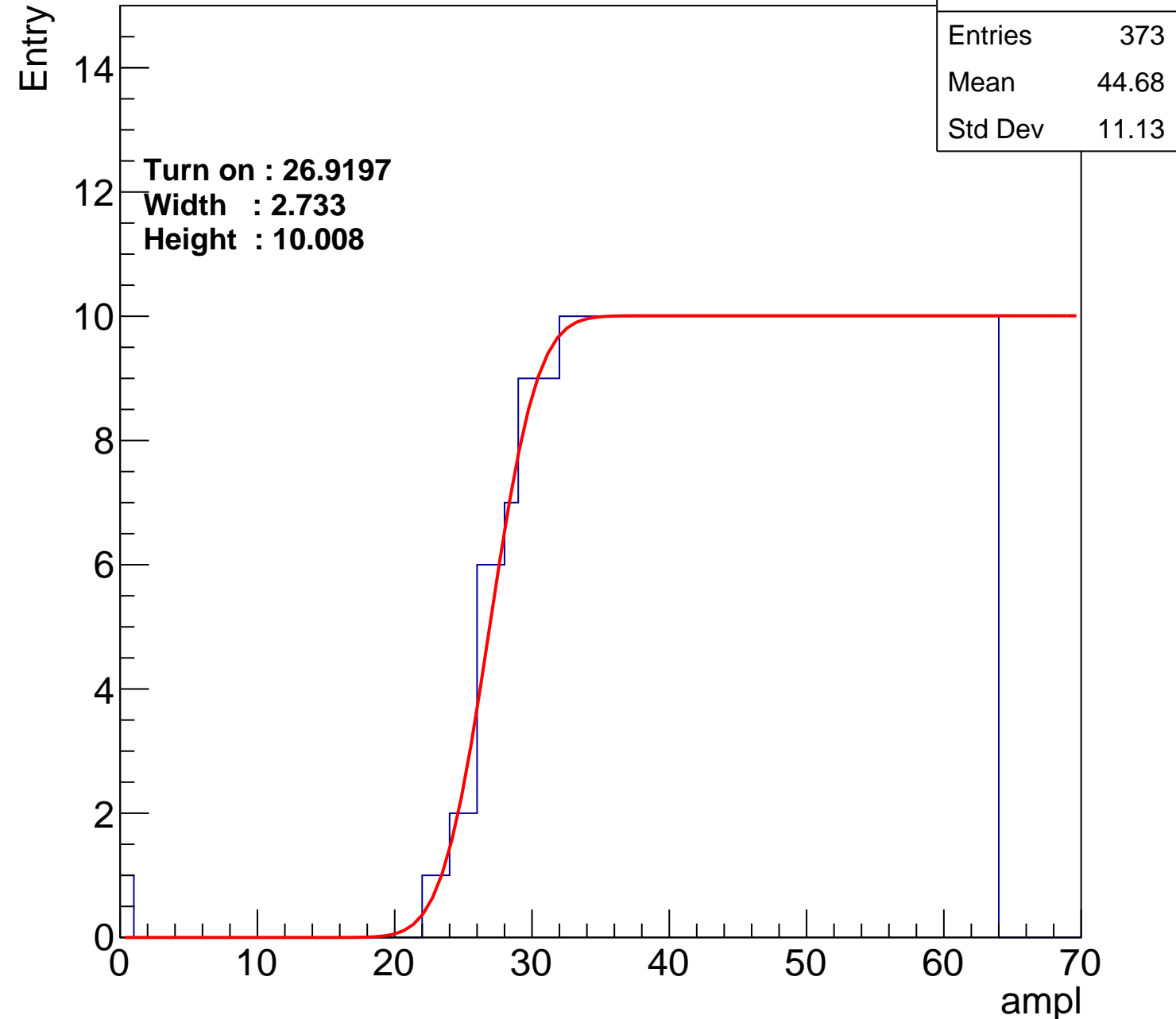
Width : 2.733

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch61

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.2806

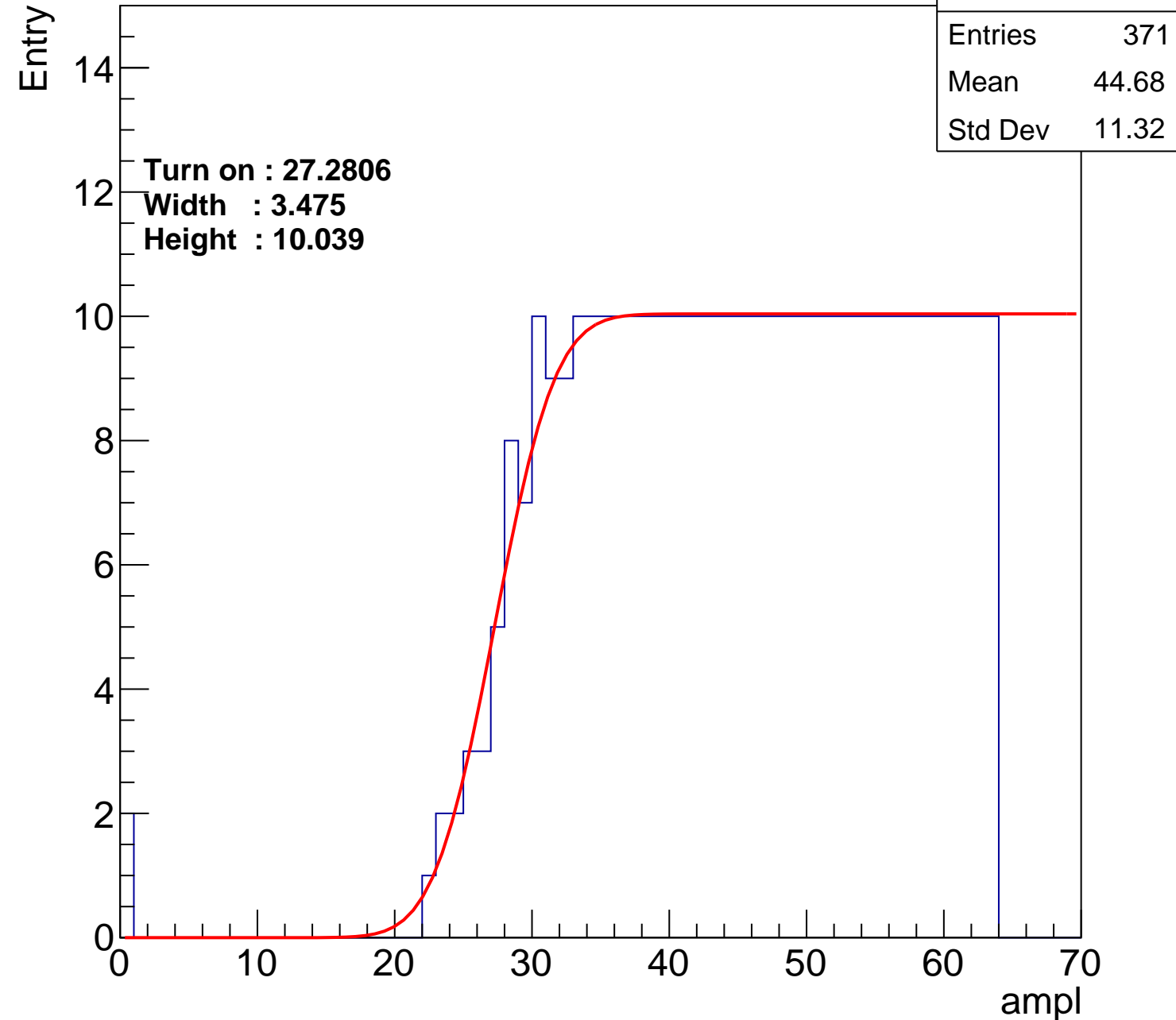
Width : 3.475

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch62

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.91

Turn on : 26.3520

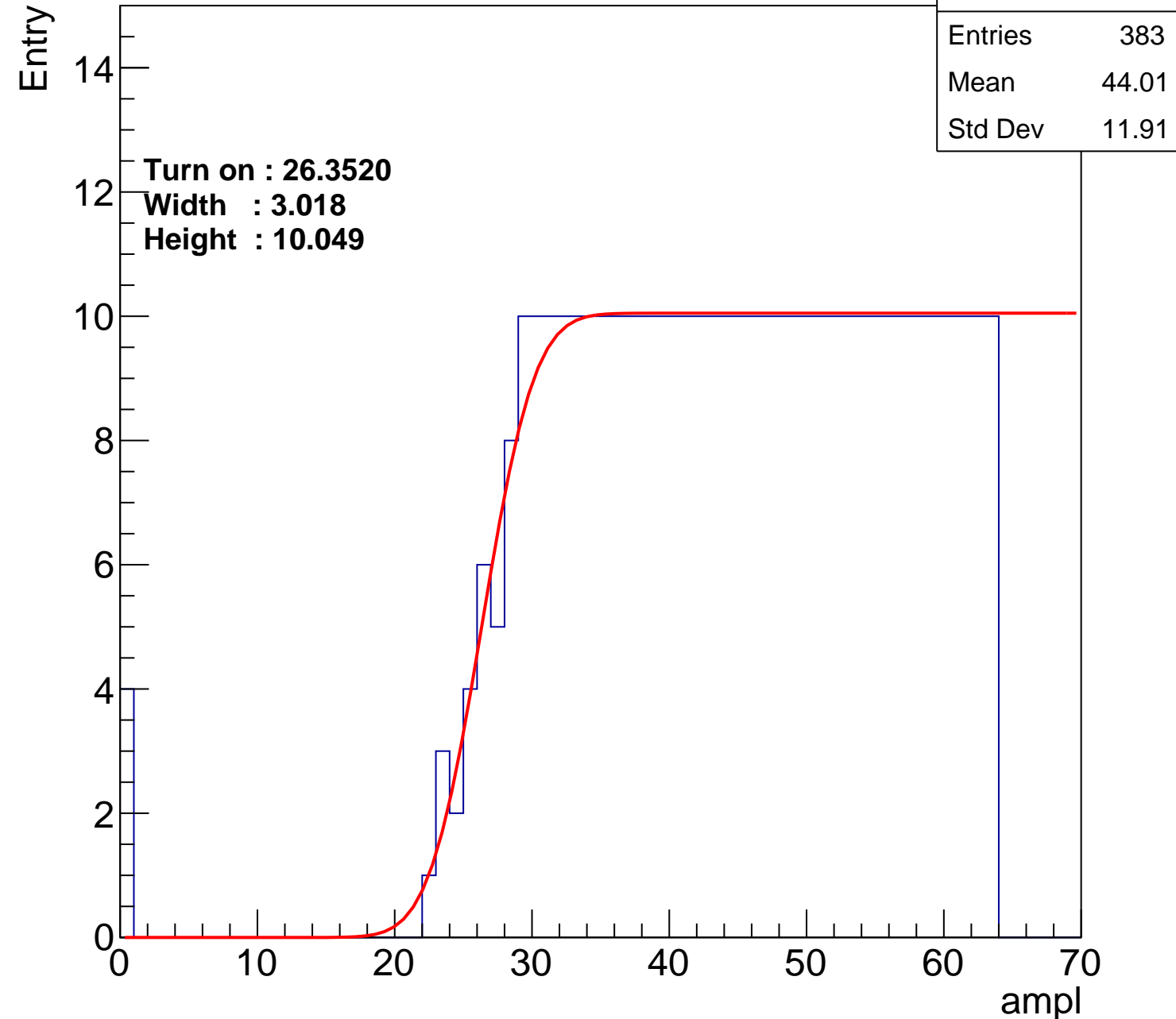
Width : 3.018

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch63

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.6
Std Dev	11.16

**Turn on : 26.9489**

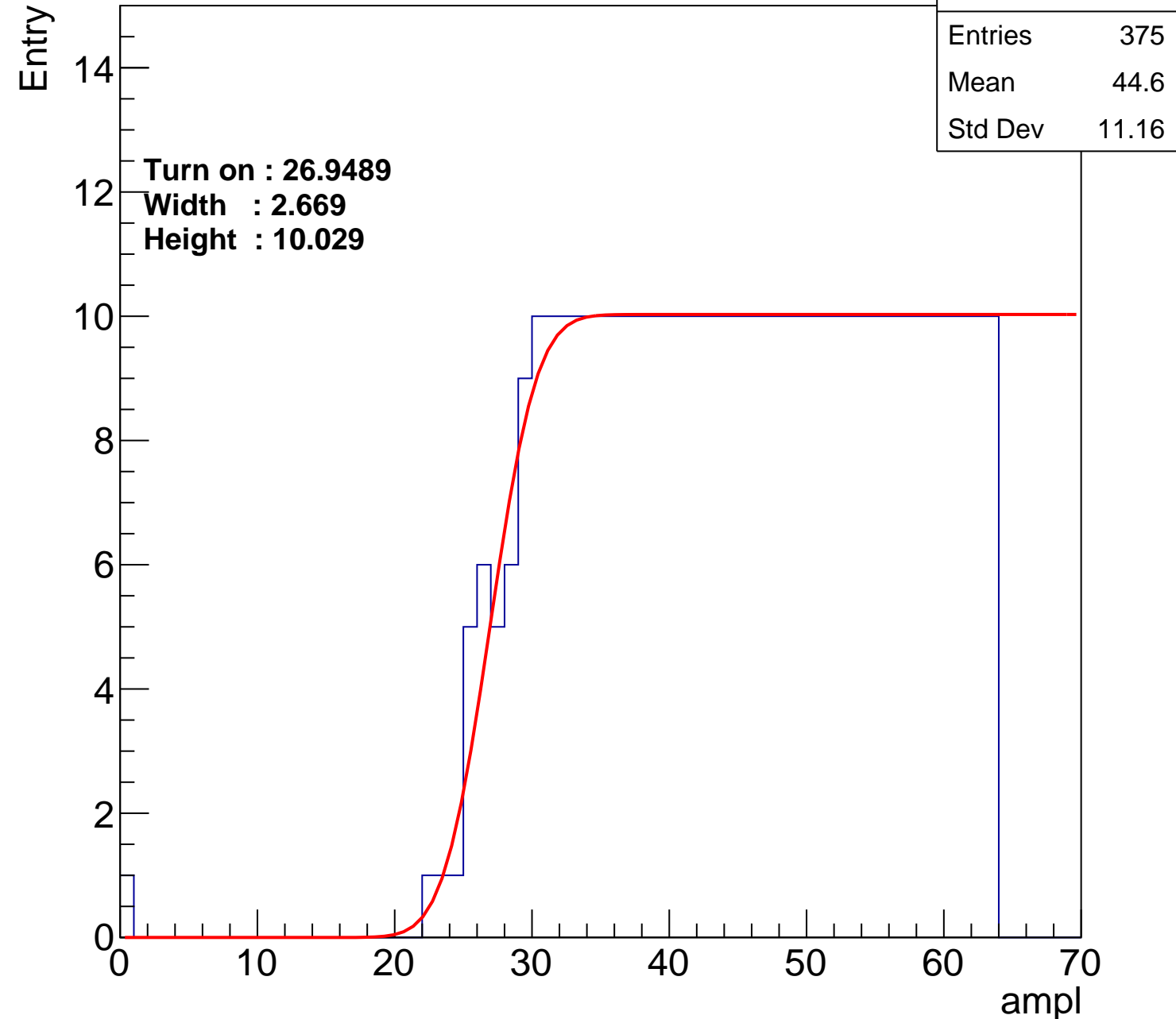
**Width : 2.669**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch64

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.37
Std Dev	11.45

**Turn on : 26.8097**

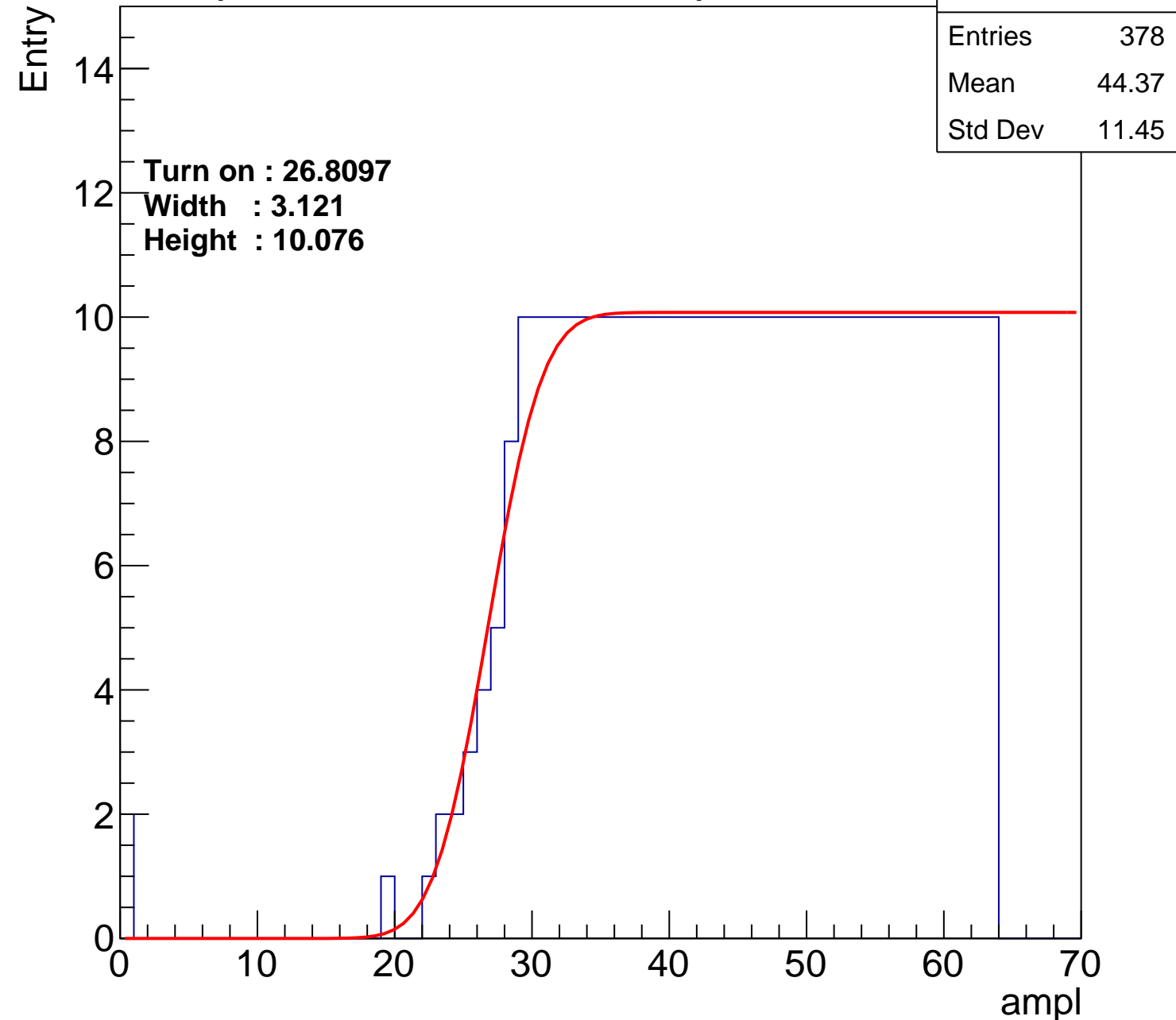
**Width : 3.121**

**Height : 10.076**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch65

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	354
Mean	45.46
Std Dev	11.08

**Turn on : 29.3402**

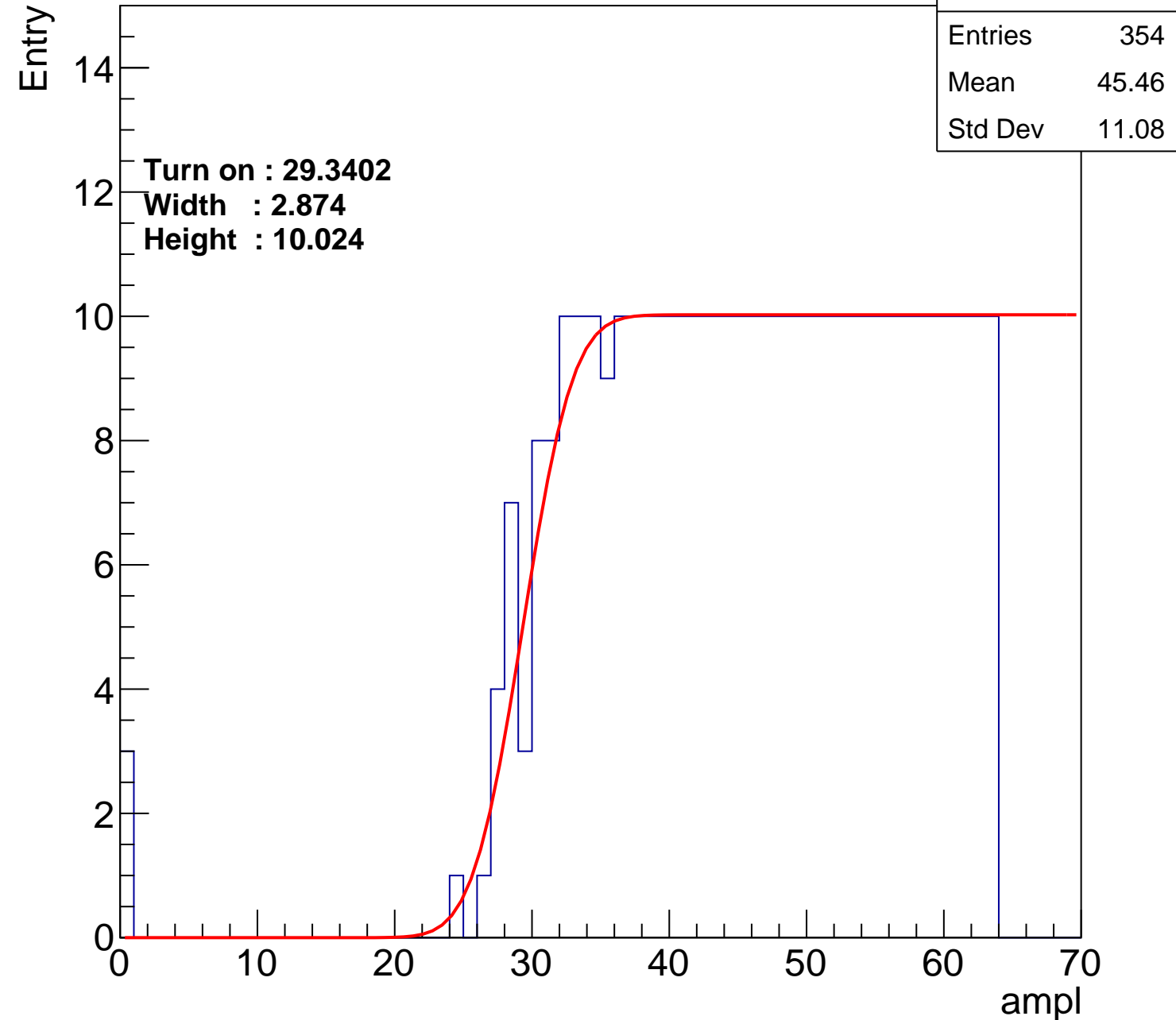
**Width : 2.874**

**Height : 10.024**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch66

calib\_packv5\_042523\_0143.root, FC#5, port B1

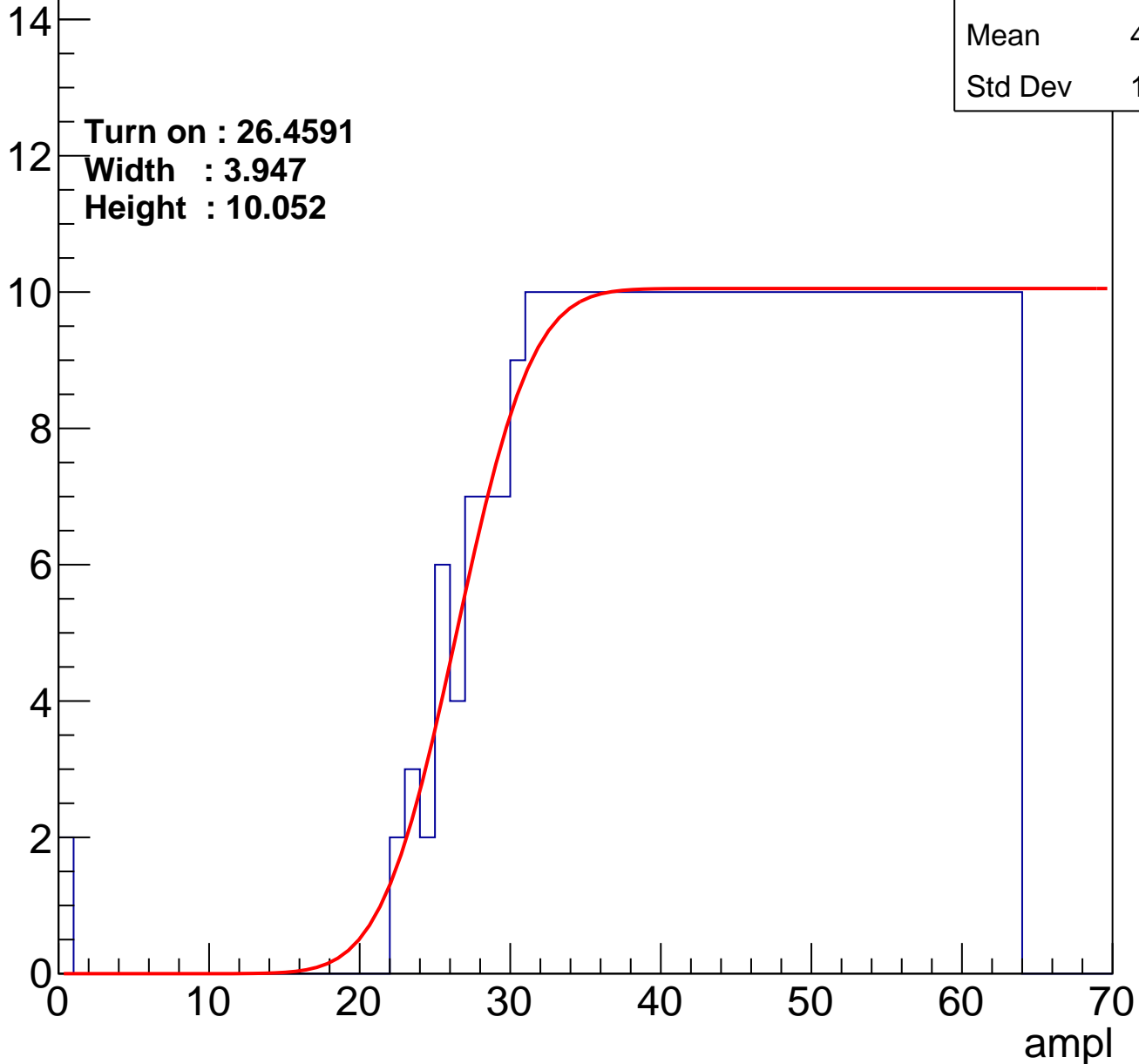
Entries	379
Mean	44.28
Std Dev	11.54

Turn on : 26.4591

Width : 3.947

Height : 10.052

Entry



# B0L000S, U20-ch67

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	405
Mean	42.96
Std Dev	12.33

Turn on : 24.2202

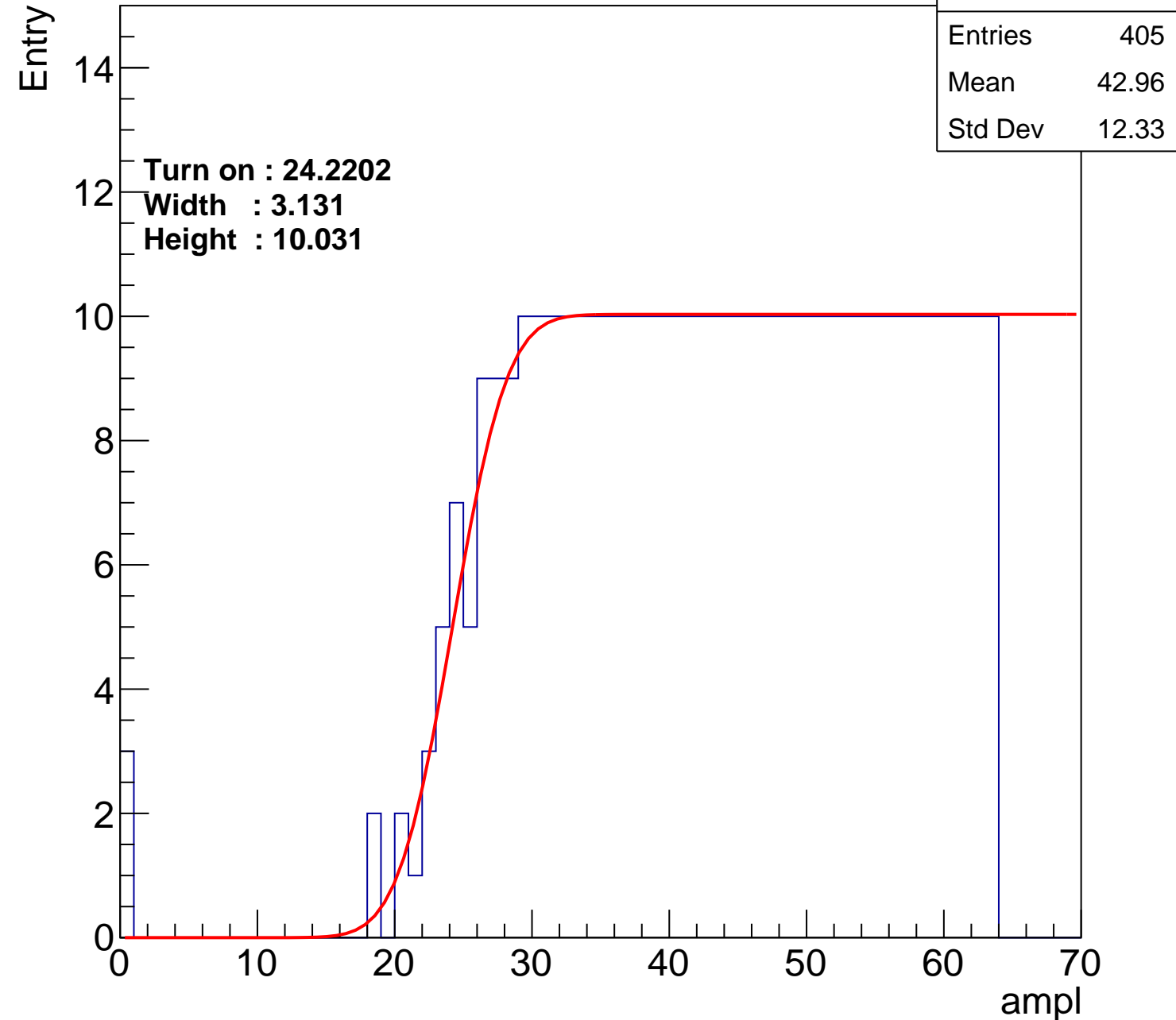
Width : 3.131

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch68

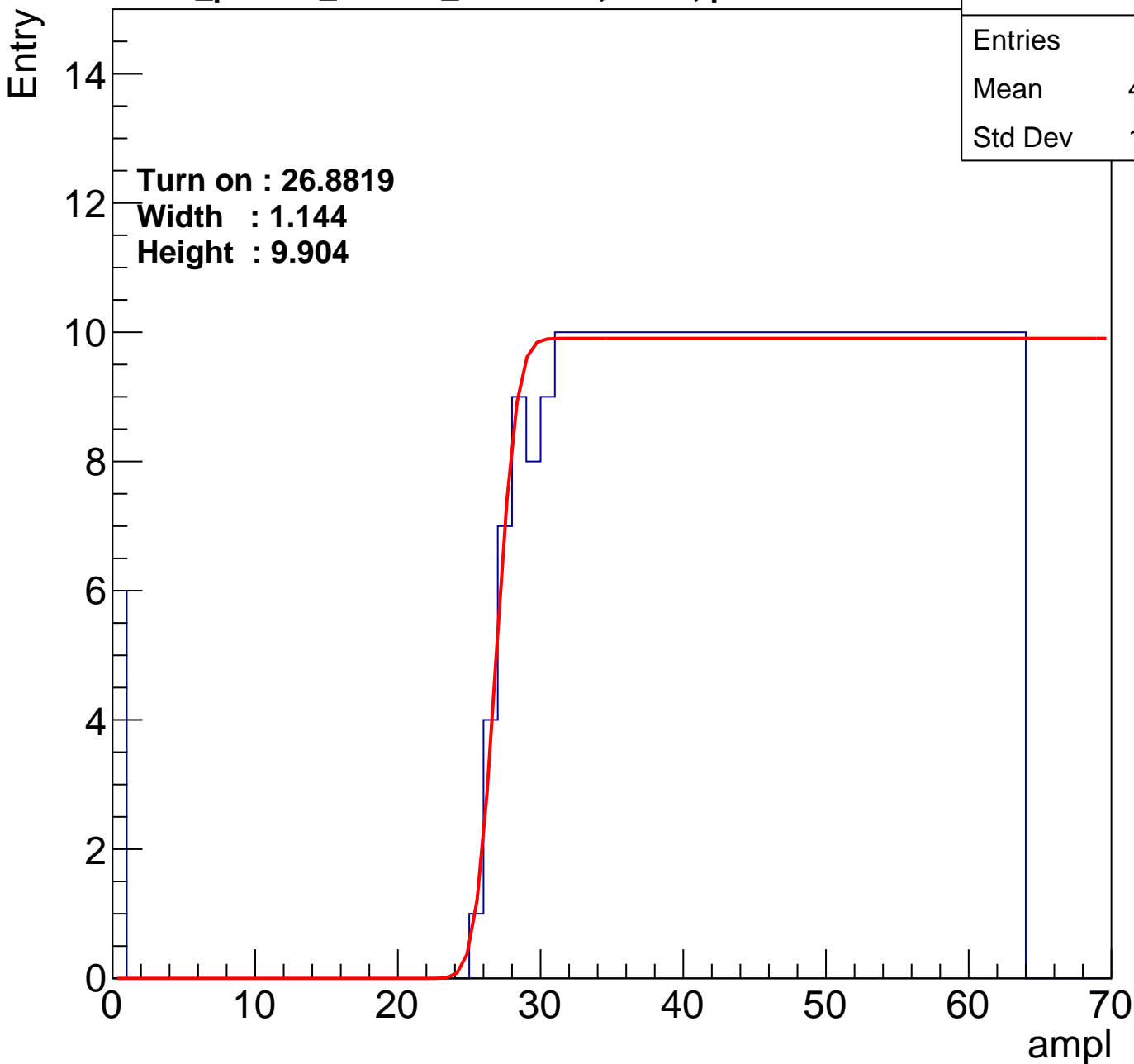
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.34
Std Dev	12.02

Turn on : 26.8819

Width : 1.144

Height : 9.904



# B0L000S, U20-ch69

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 26.5231

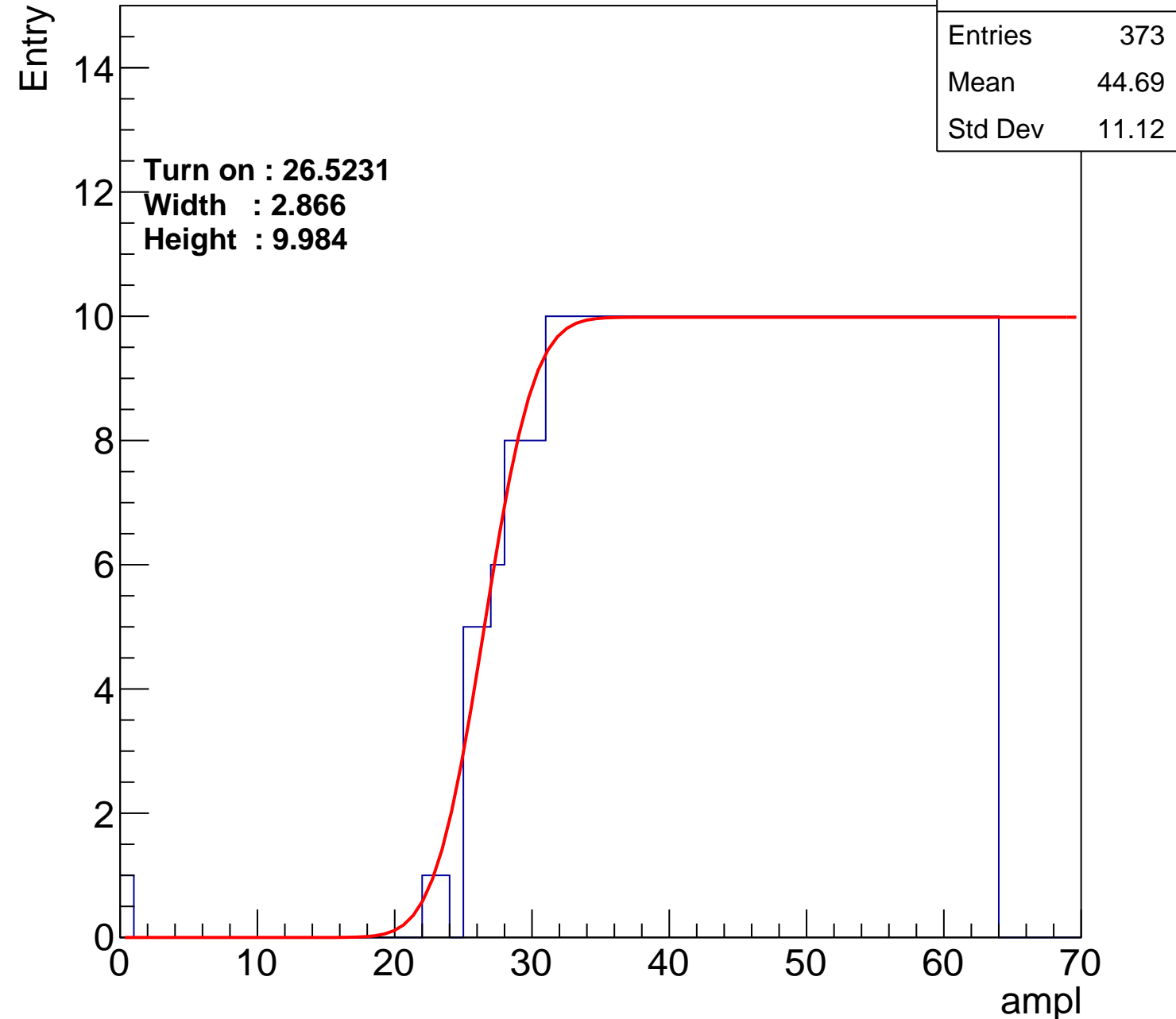
Width : 2.866

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch70

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.94
Std Dev	11.67

**Turn on : 25.5194**

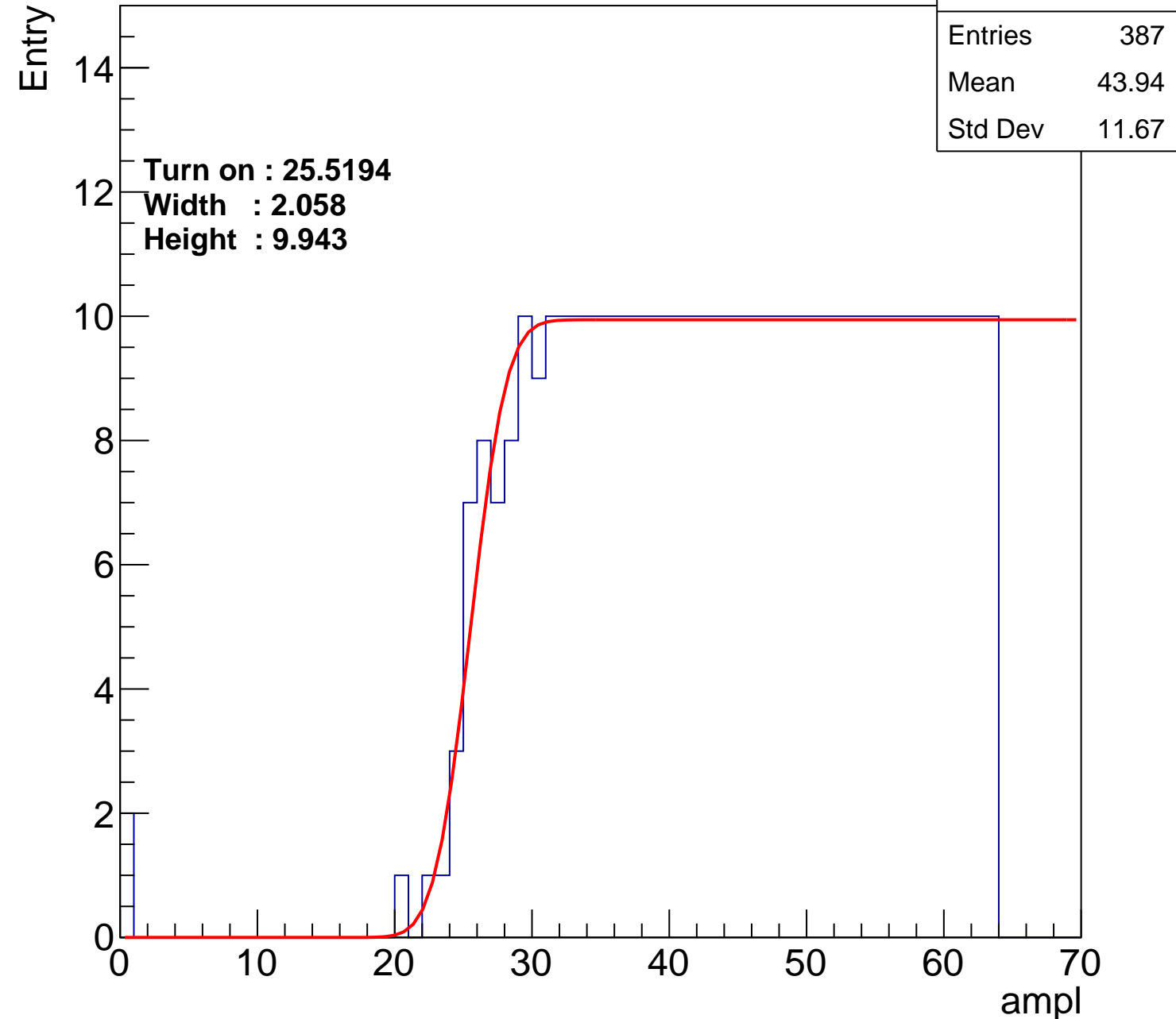
**Width : 2.058**

**Height : 9.943**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch71

calib\_packv5\_042523\_0143.root, FC#5, port B1

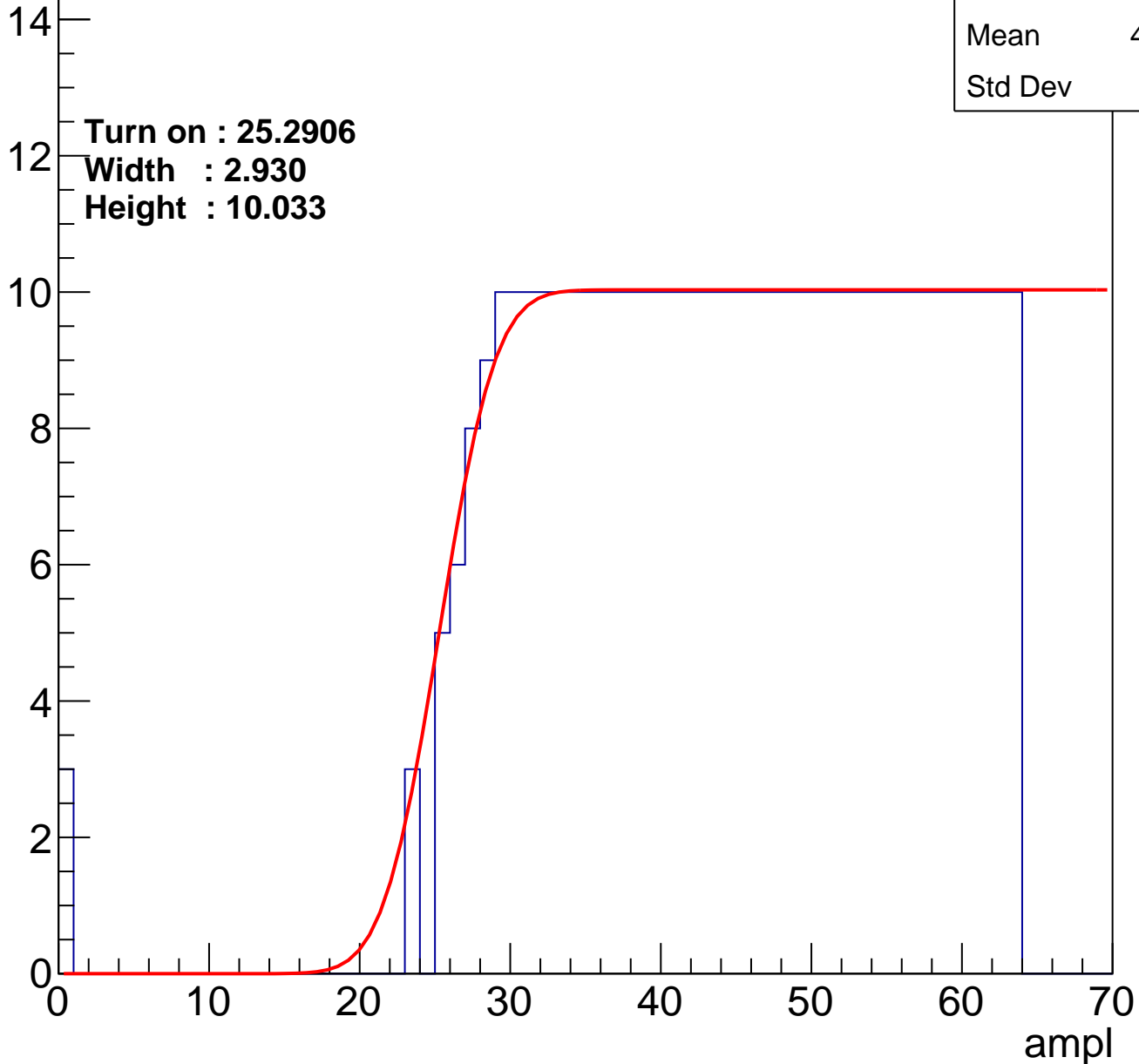
Entries	384
Mean	44.06
Std Dev	11.7

Turn on : 25.2906

Width : 2.930

Height : 10.033

Entry



# B0L000S, U20-ch72

calib\_packv5\_042523\_0143.root, FC#5, port B1

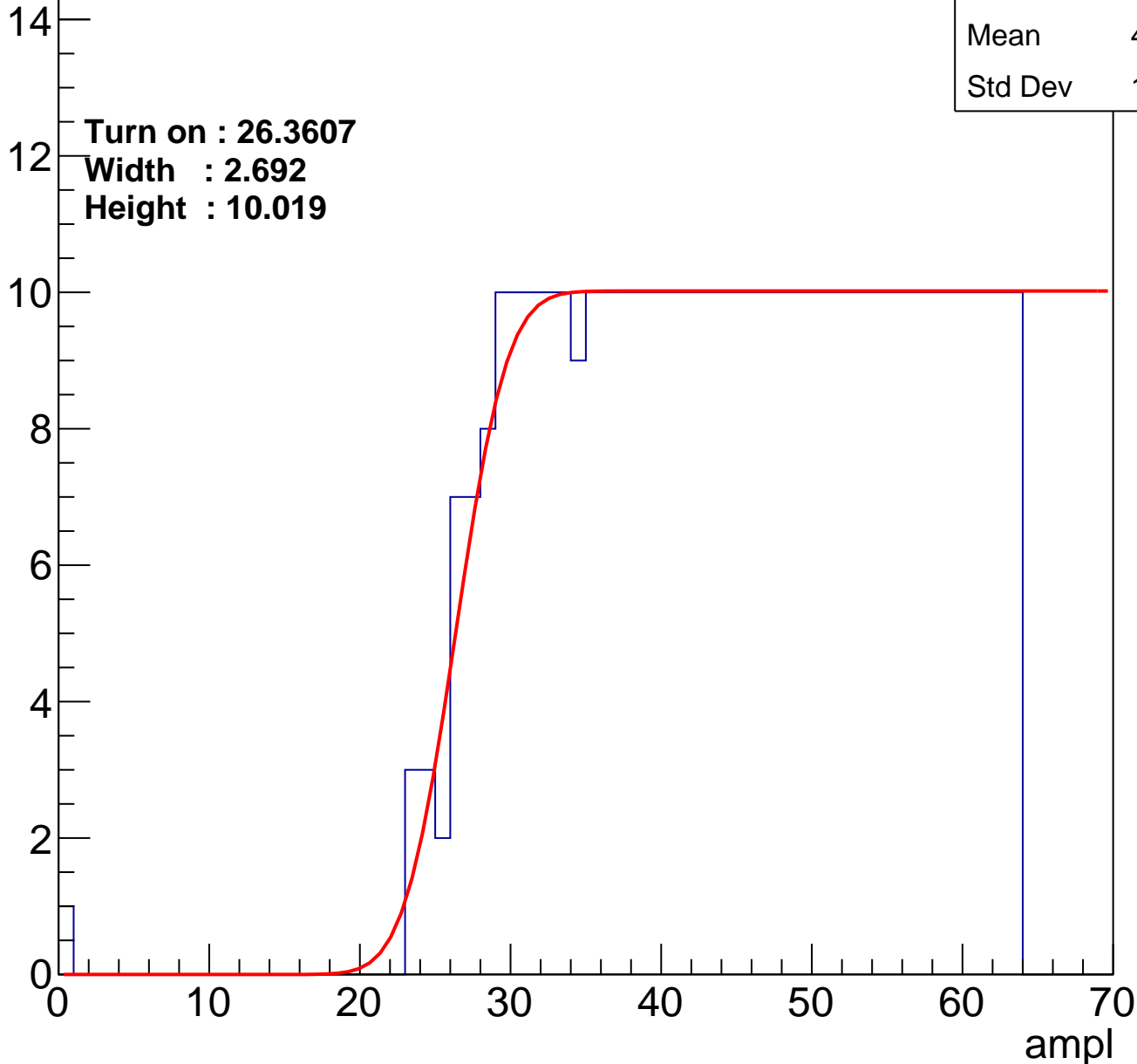
Entries	380
Mean	44.35
Std Dev	11.29

**Turn on : 26.3607**

**Width : 2.692**

**Height : 10.019**

Entry



# B0L000S, U20-ch73

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	44.05
Std Dev	11.51

Turn on : 25.7792

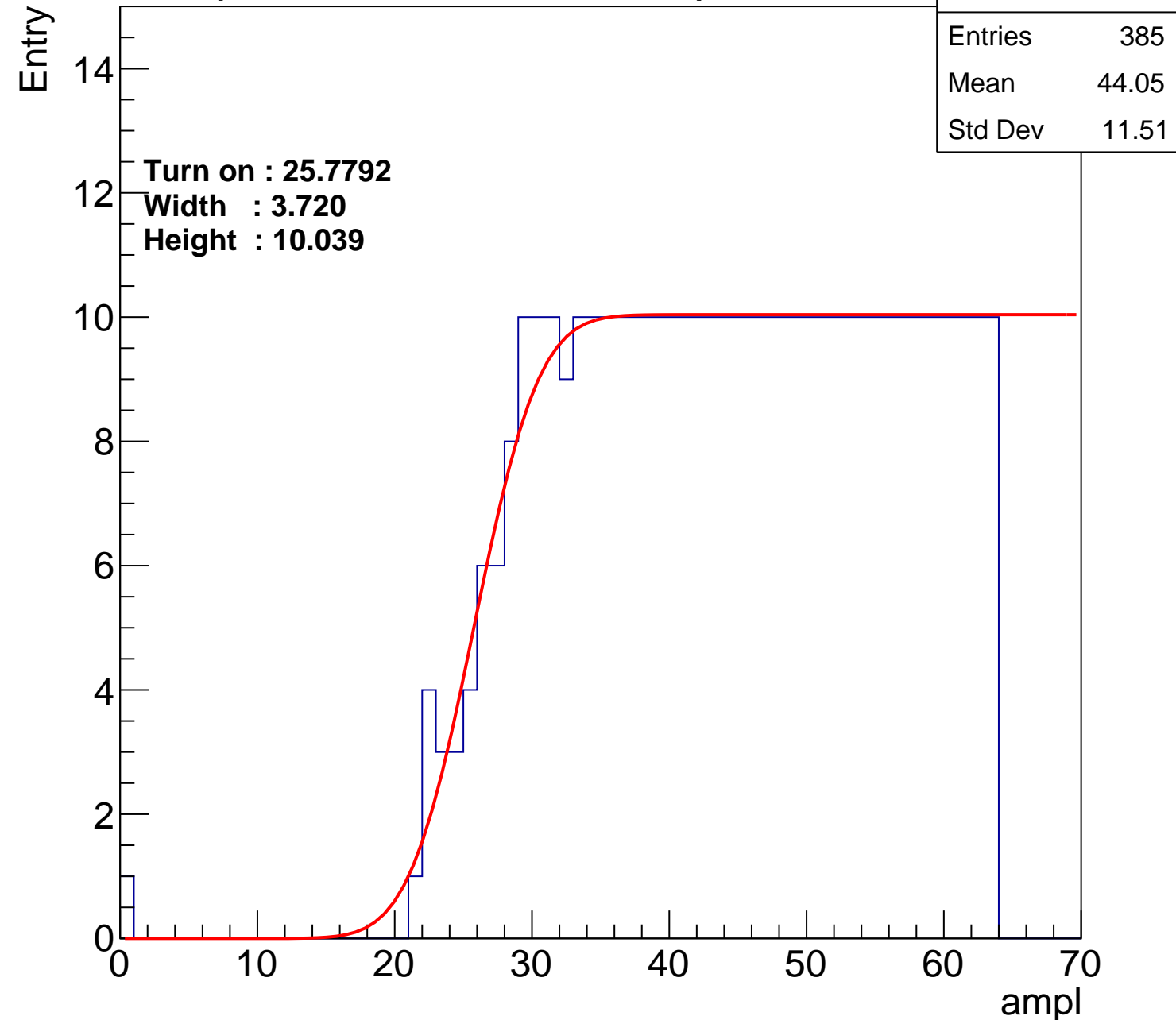
Width : 3.720

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

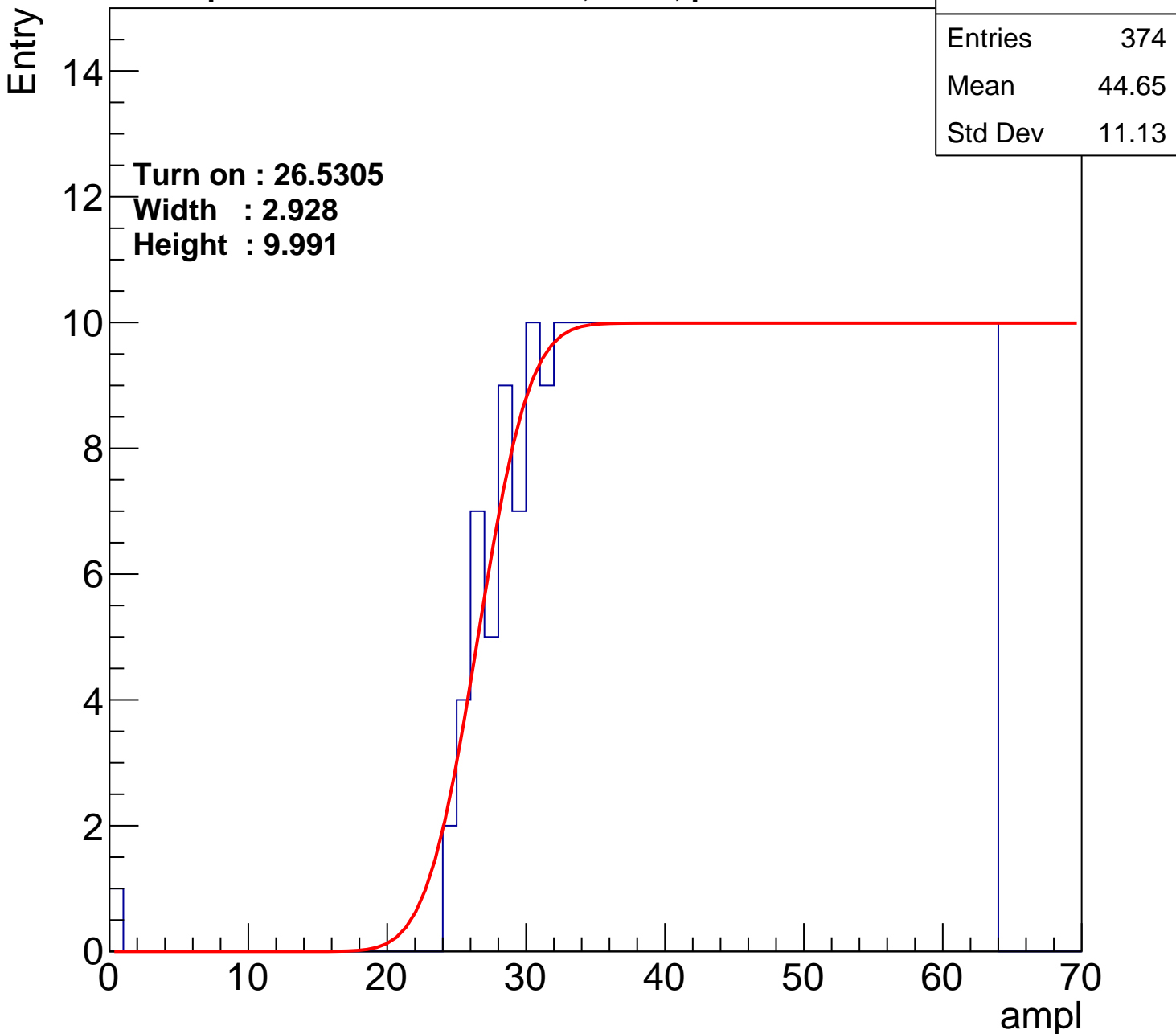
ampl



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	374
Mean	44.65
Std Dev	11.13

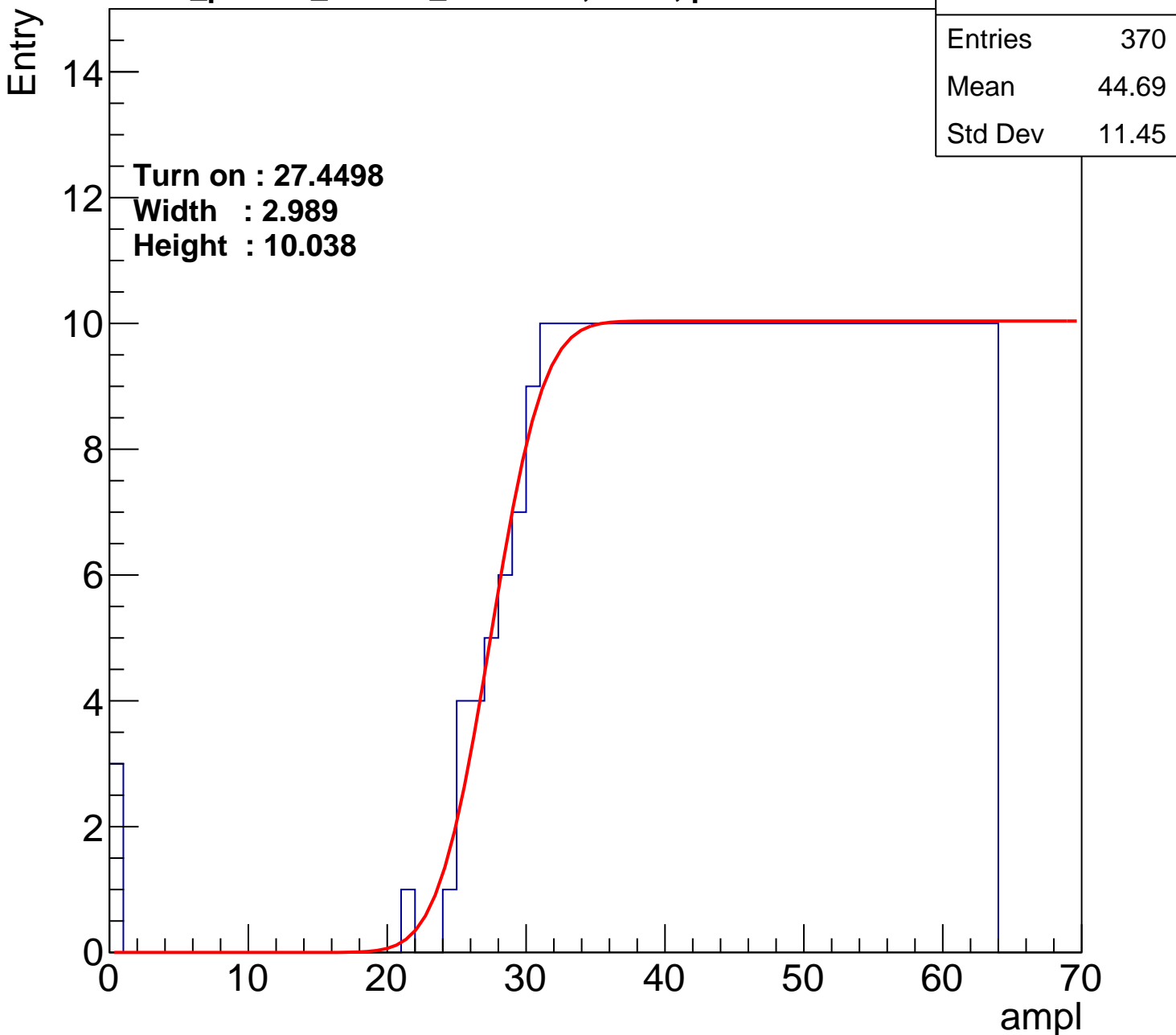
**Height : 9.991**



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Turn on : 27.4498  
Width : 2.989  
Height : 10.038



# B0L000S, U20-ch76

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.1946

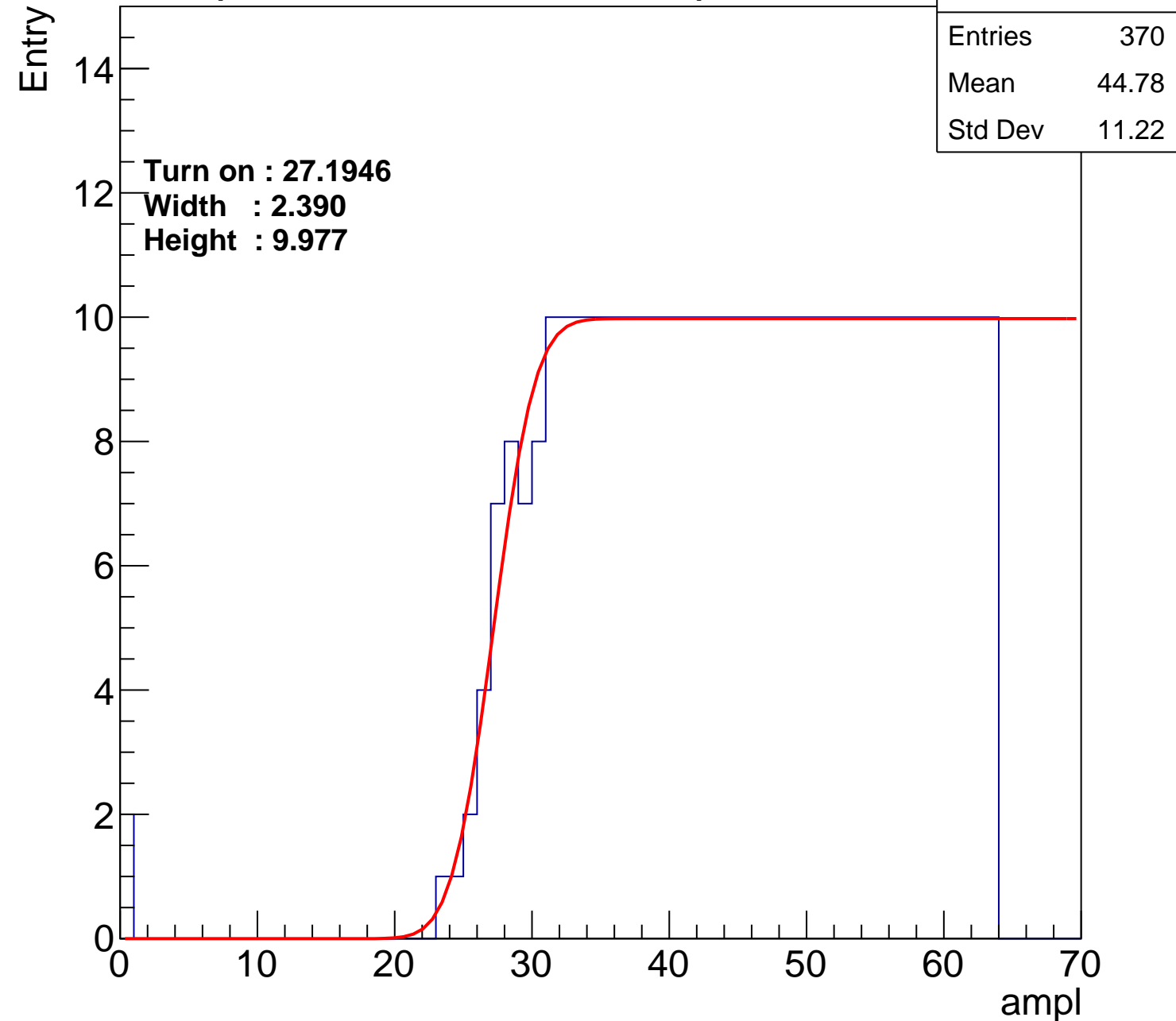
Width : 2.390

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch77

calib\_packv5\_042523\_0143.root, FC#5, port B1

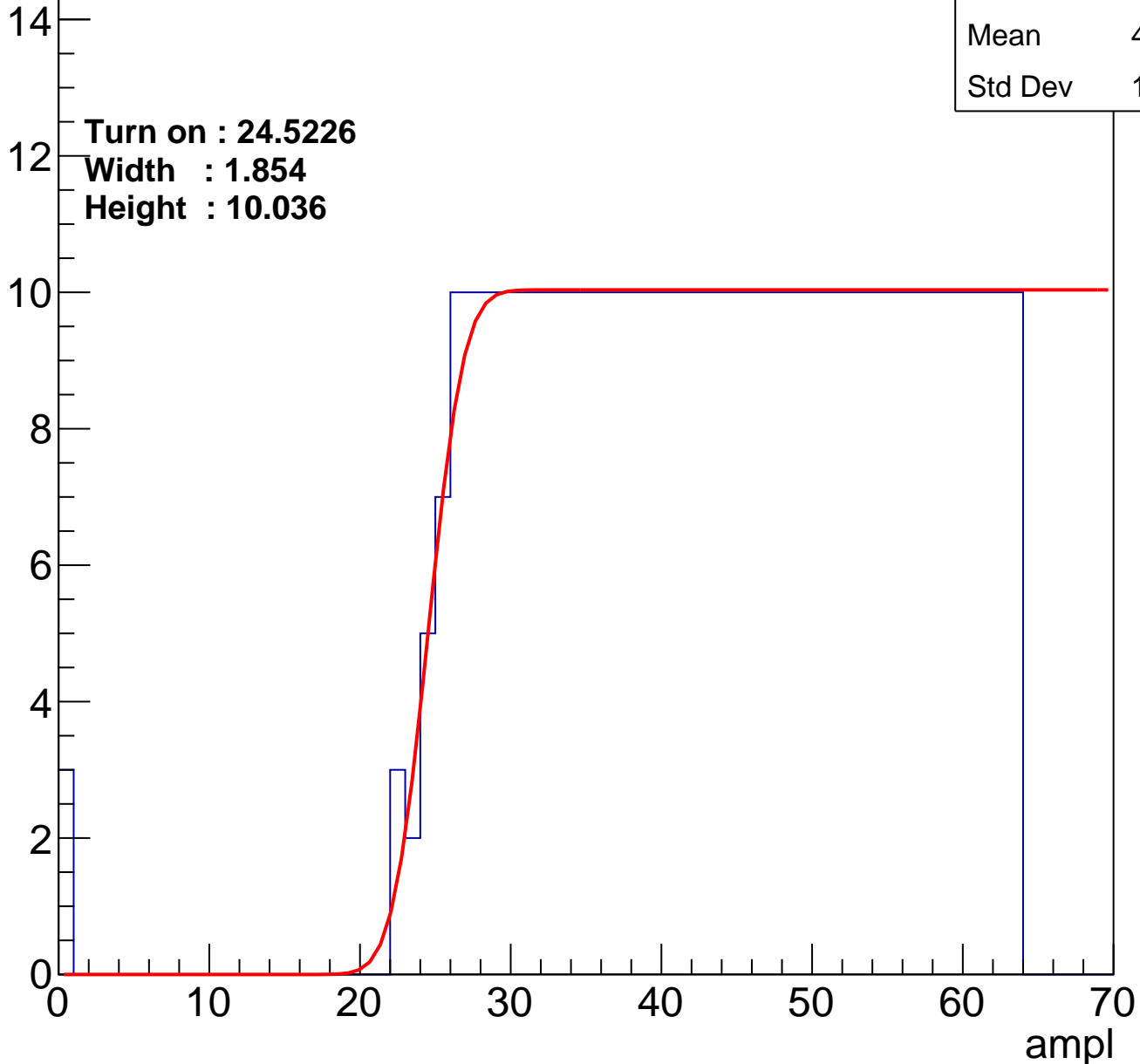
Entries	400
Mean	43.29
Std Dev	12.07

Turn on : 24.5226

Width : 1.854

Height : 10.036

Entry



# B0L000S, U20-ch78

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	382
Mean	44.03
Std Dev	12.02

Turn on : 26.5689

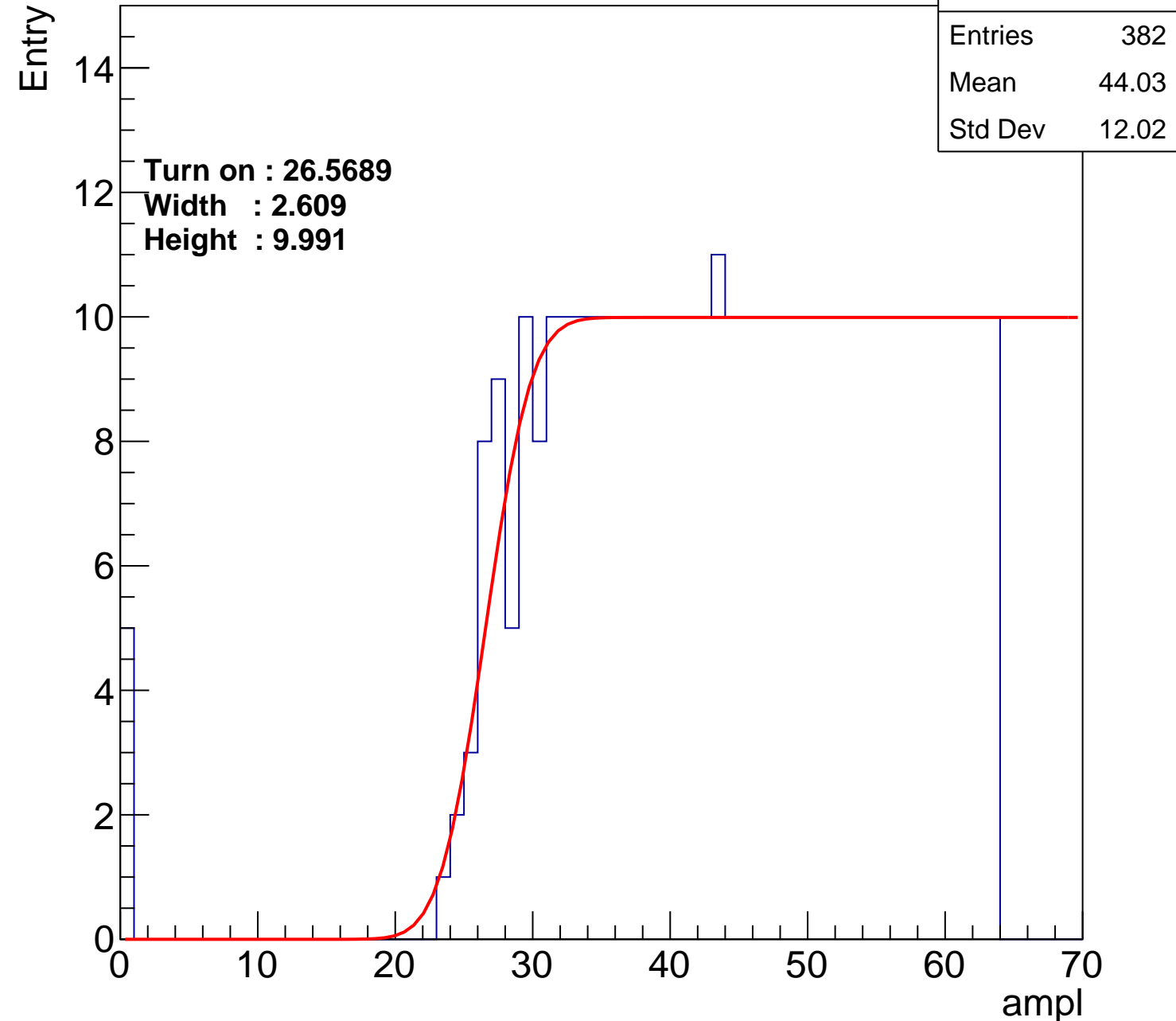
Width : 2.609

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch79

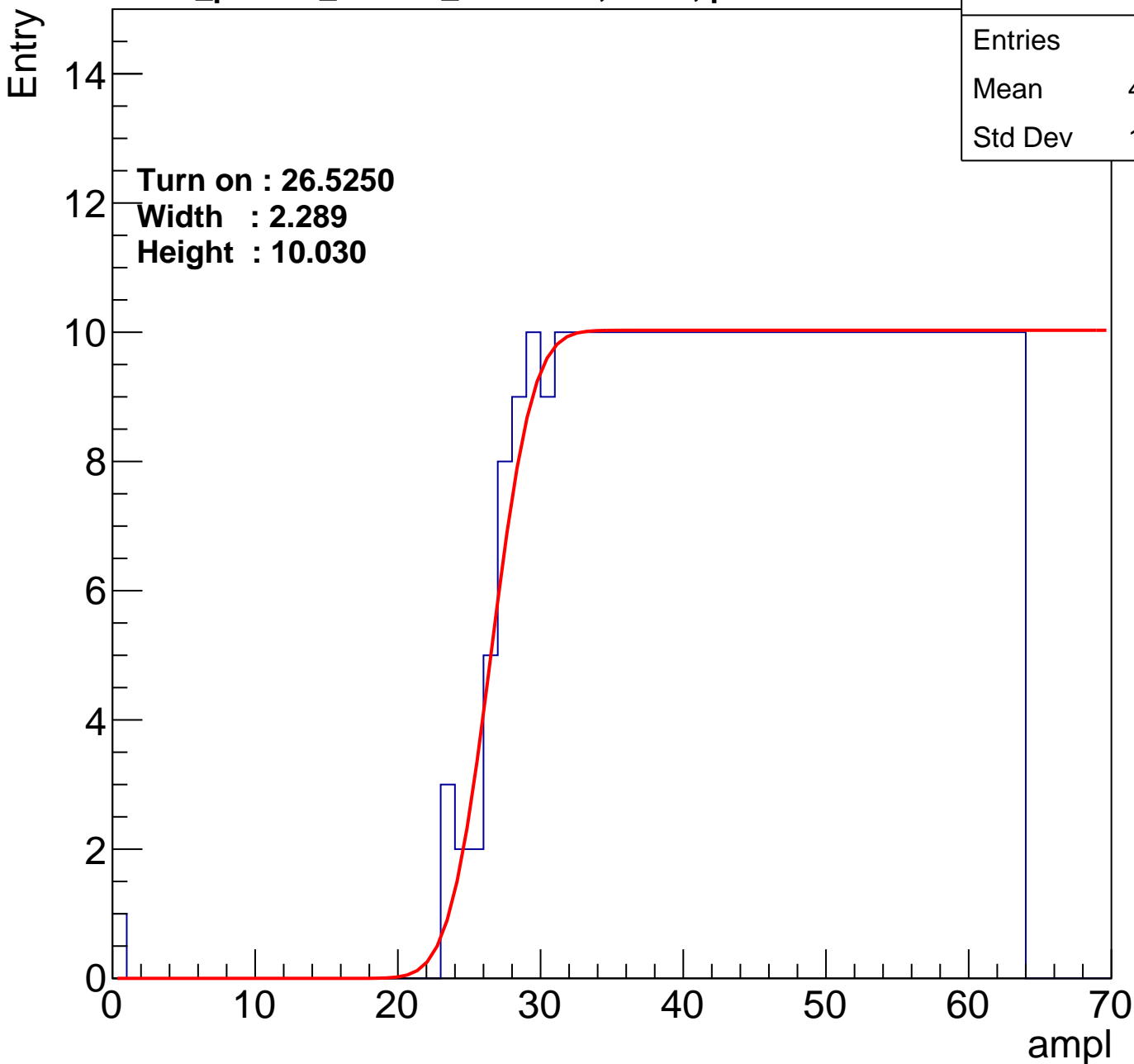
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.5250

Width : 2.289

Height : 10.030



**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	375
Mean	44.35
Std Dev	11.79

Mean	44.35
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Std Dev	11.79
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**Width : 3.549**

**Height : 10.076**



# B0L000S, U20-ch81

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	394
Mean	43.46
Std Dev	12.17

Turn on : 25.1413

Width : 2.922

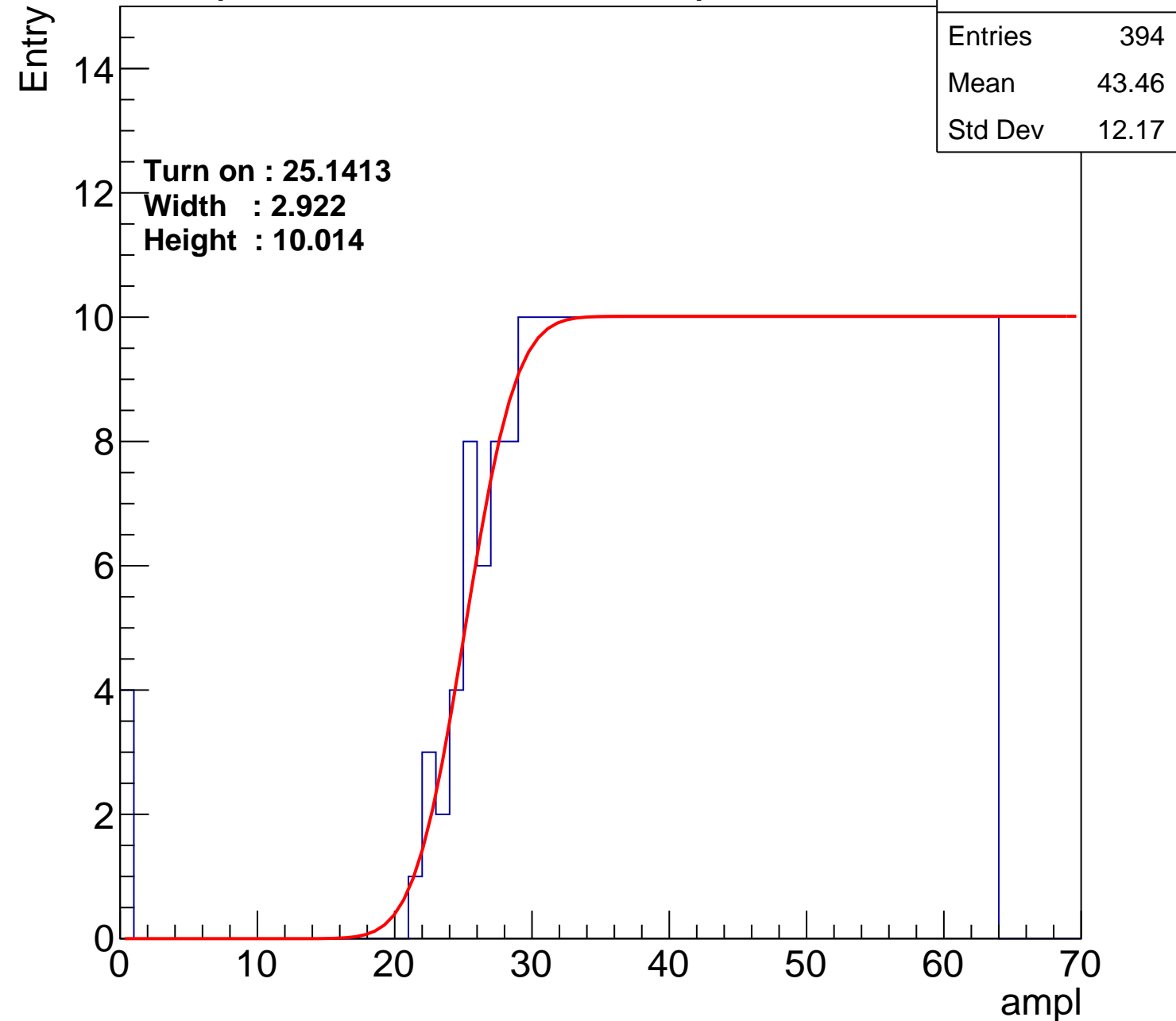
Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U20-ch82

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	385
Mean	43.82
Std Dev	12.16

Turn on : 26.2519

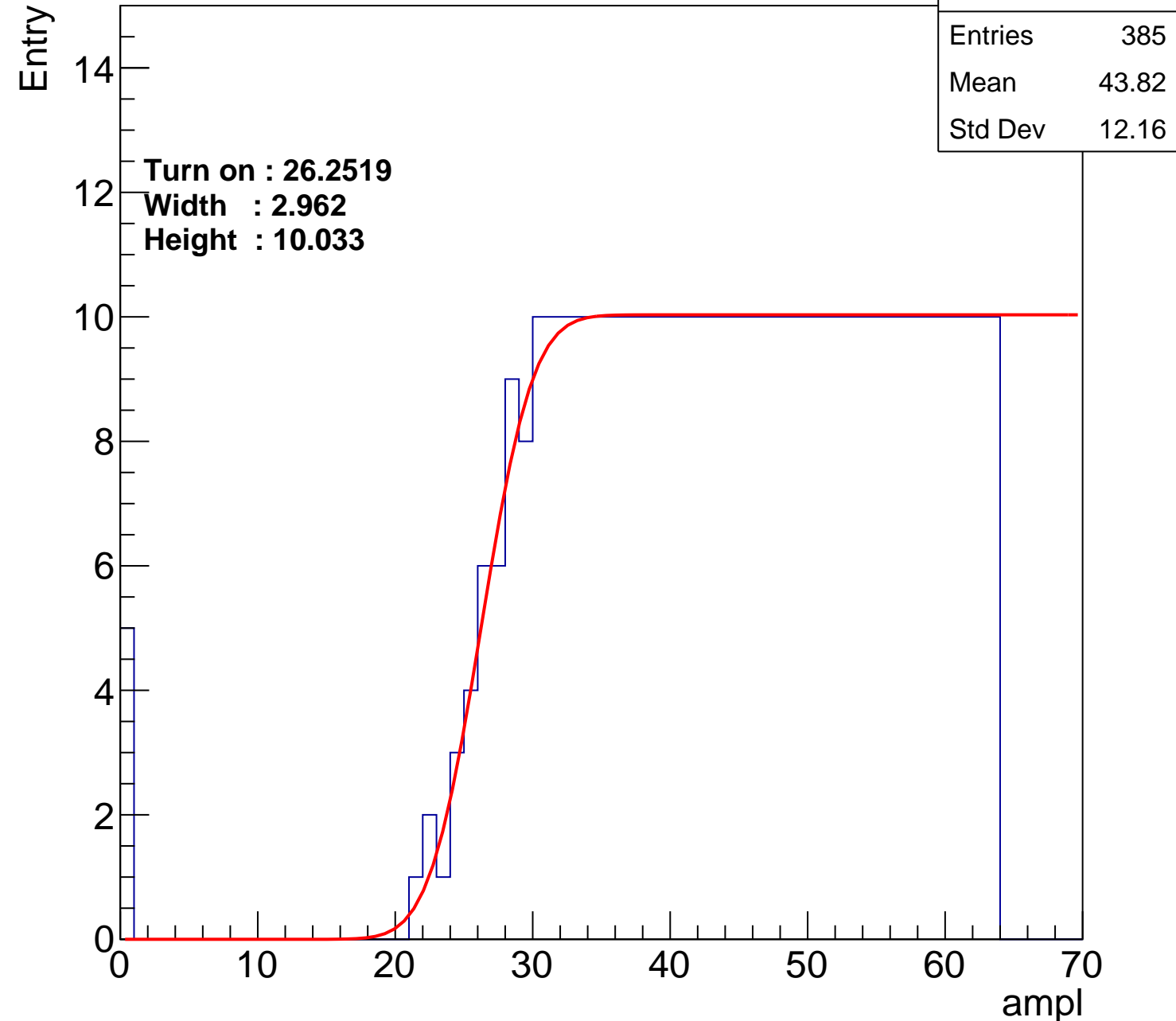
Width : 2.962

Height : 10.033

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch83

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 27.4345

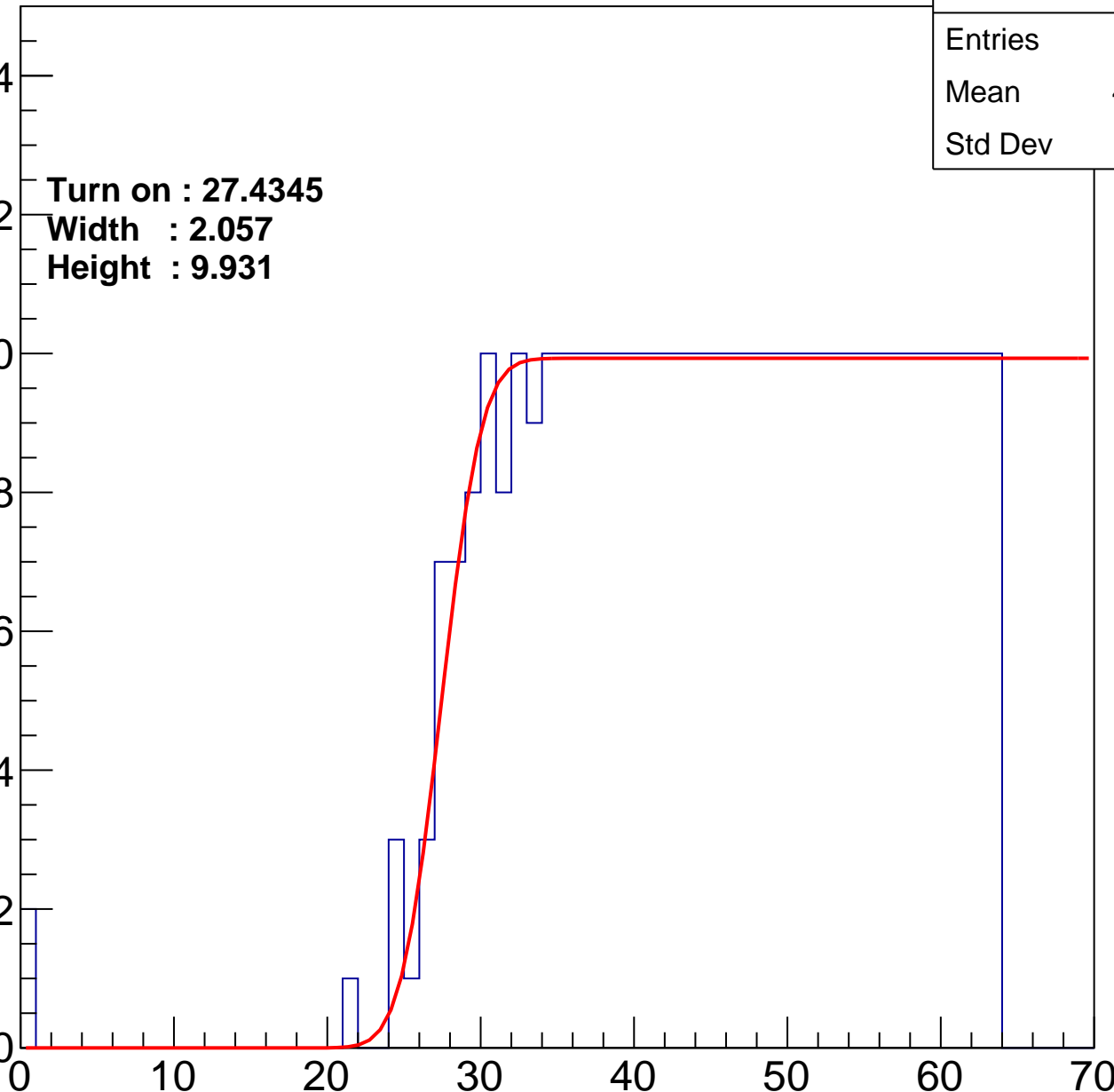
Width : 2.057

Height : 9.931

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch84

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	383
Mean	44.01
Std Dev	11.9

Turn on : 26.0065

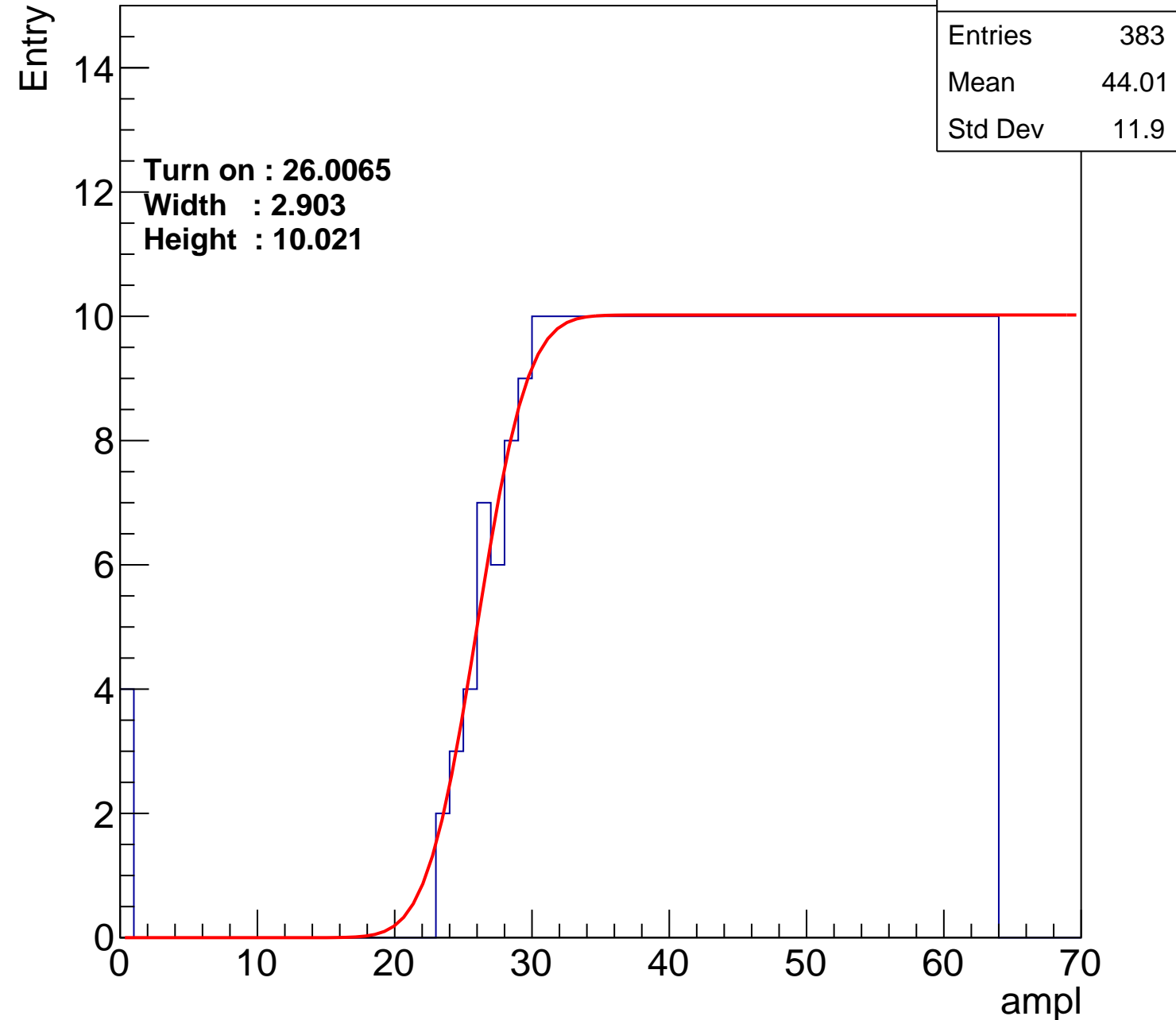
Width : 2.903

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch85

calib\_packv5\_042523\_0143.root, FC#5, port B1

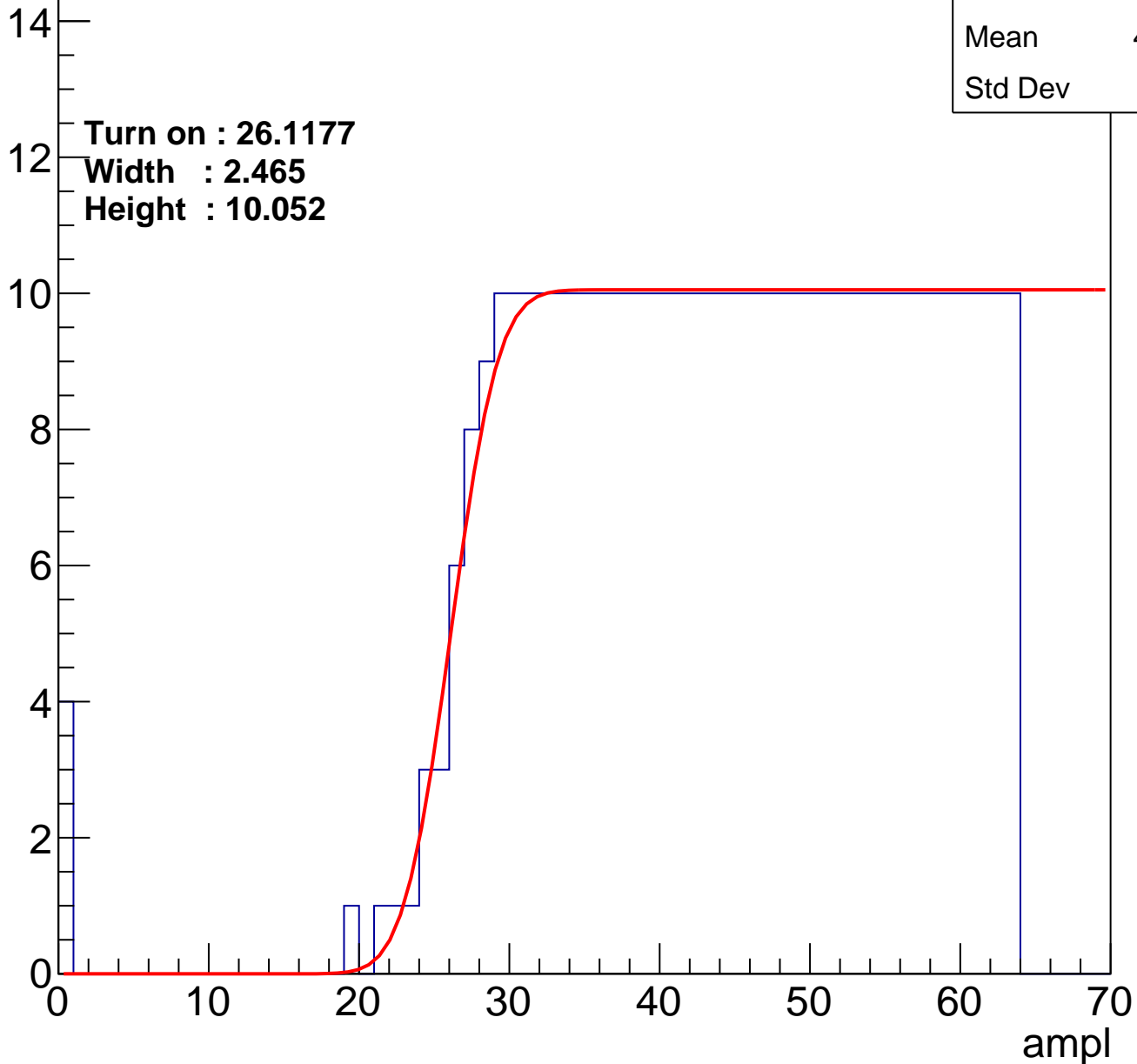
Entries	387
Mean	43.81
Std Dev	12

Turn on : 26.1177

Width : 2.465

Height : 10.052

Entry



# B0L000S, U20-ch86

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	379
Mean	44.36
Std Dev	11.34

**Turn on : 26.0935**

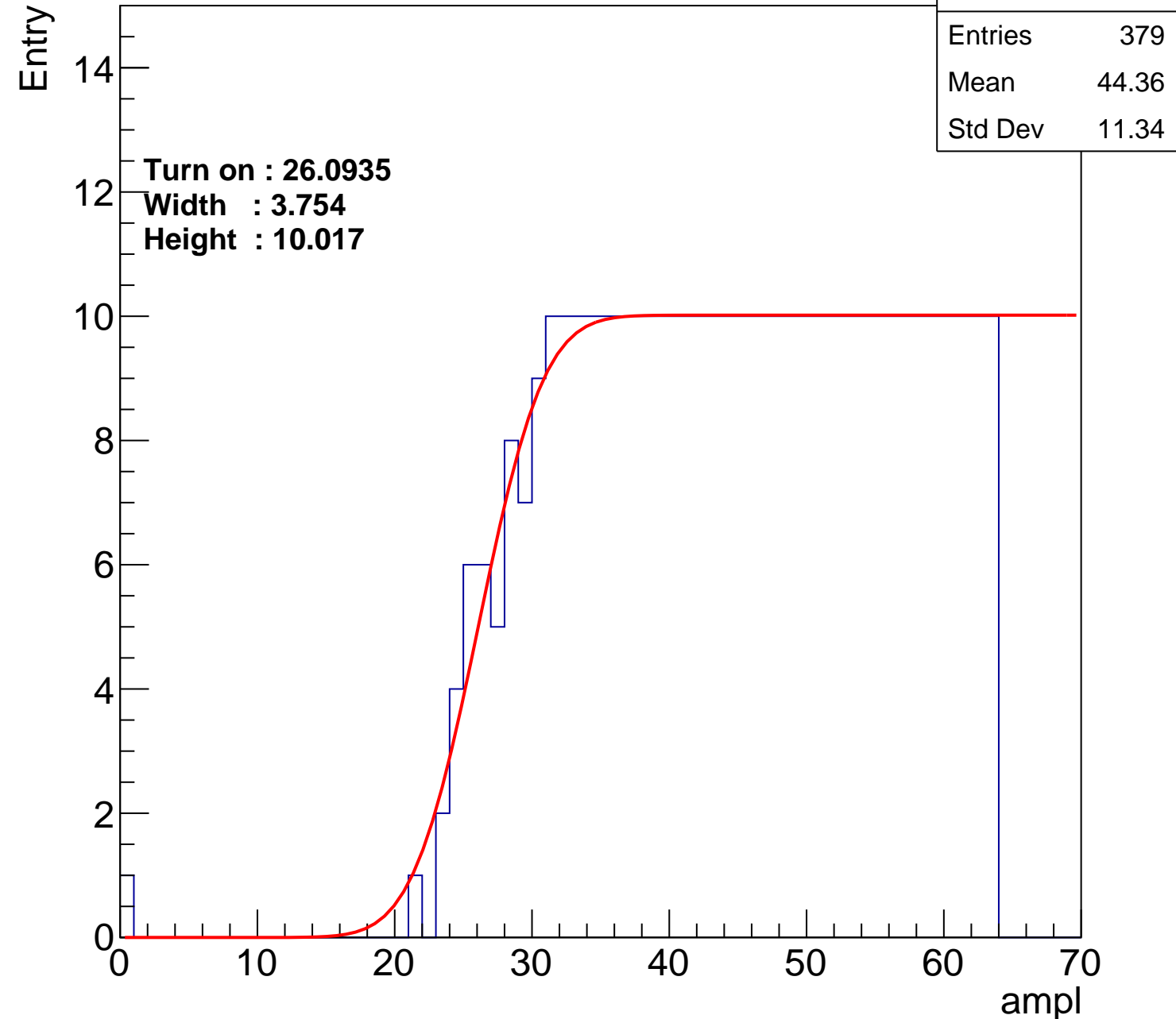
**Width : 3.754**

**Height : 10.017**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch87

calib\_packv5\_042523\_0143.root, FC#5, port B1

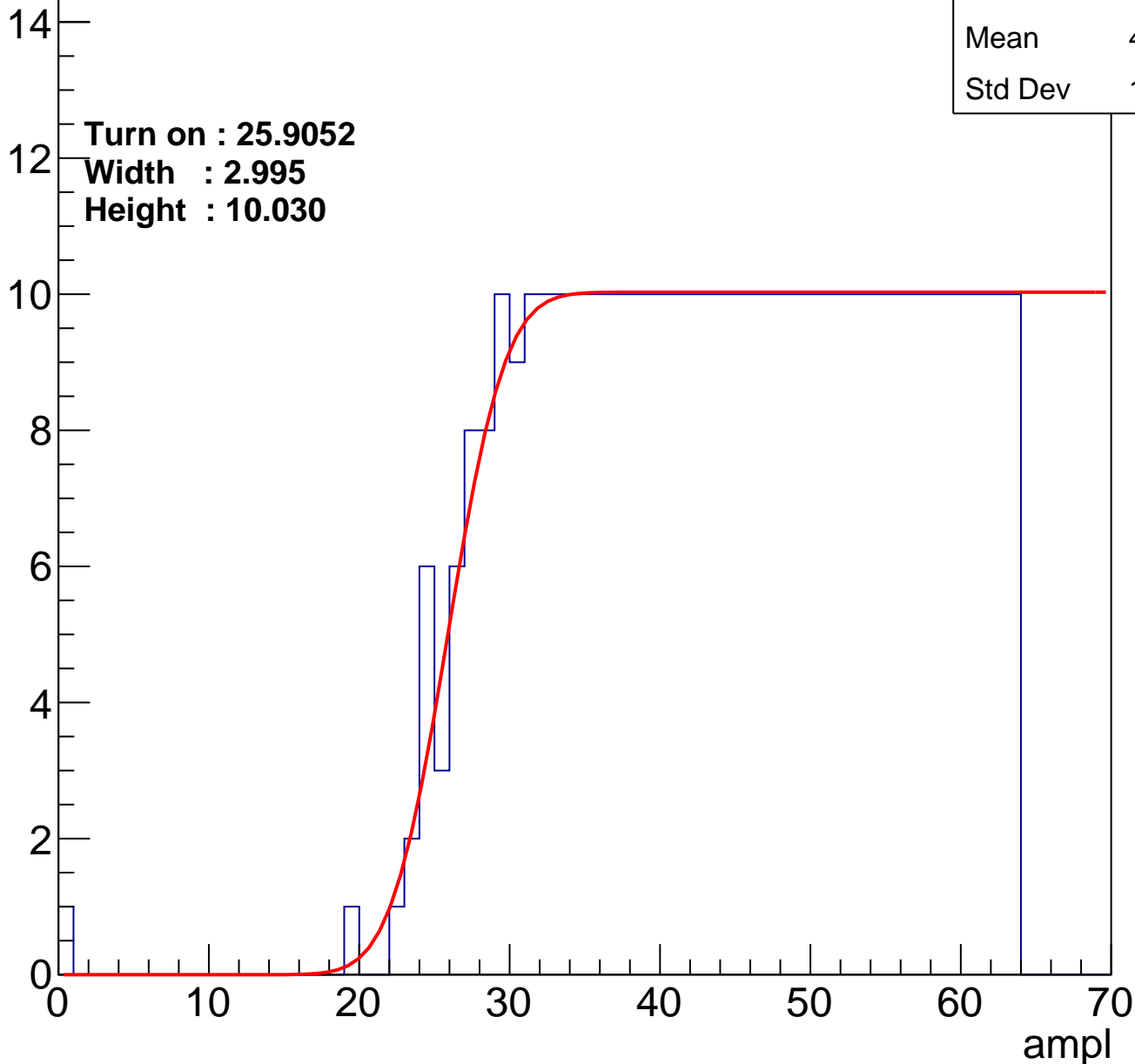
Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.9052

Width : 2.995

Height : 10.030

Entry



# B0L000S, U20-ch88

calib\_packv5\_042523\_0143.root, FC#5, port B1

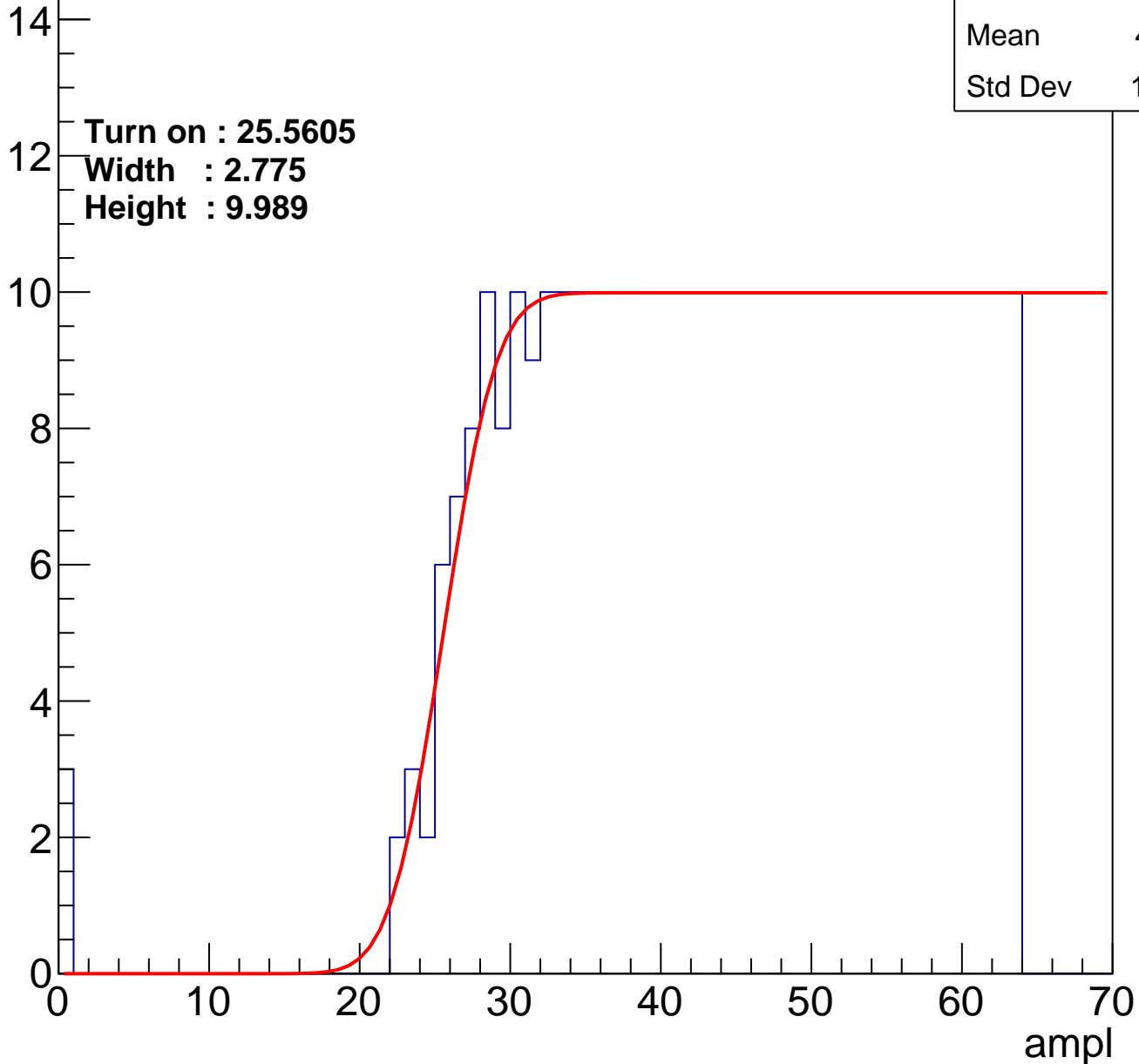
Entries	388
Mean	43.81
Std Dev	11.87

Turn on : 25.5605

Width : 2.775

Height : 9.989

Entry



# B0L000S, U20-ch89

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.63
Std Dev	11.49

Turn on : 27.9631

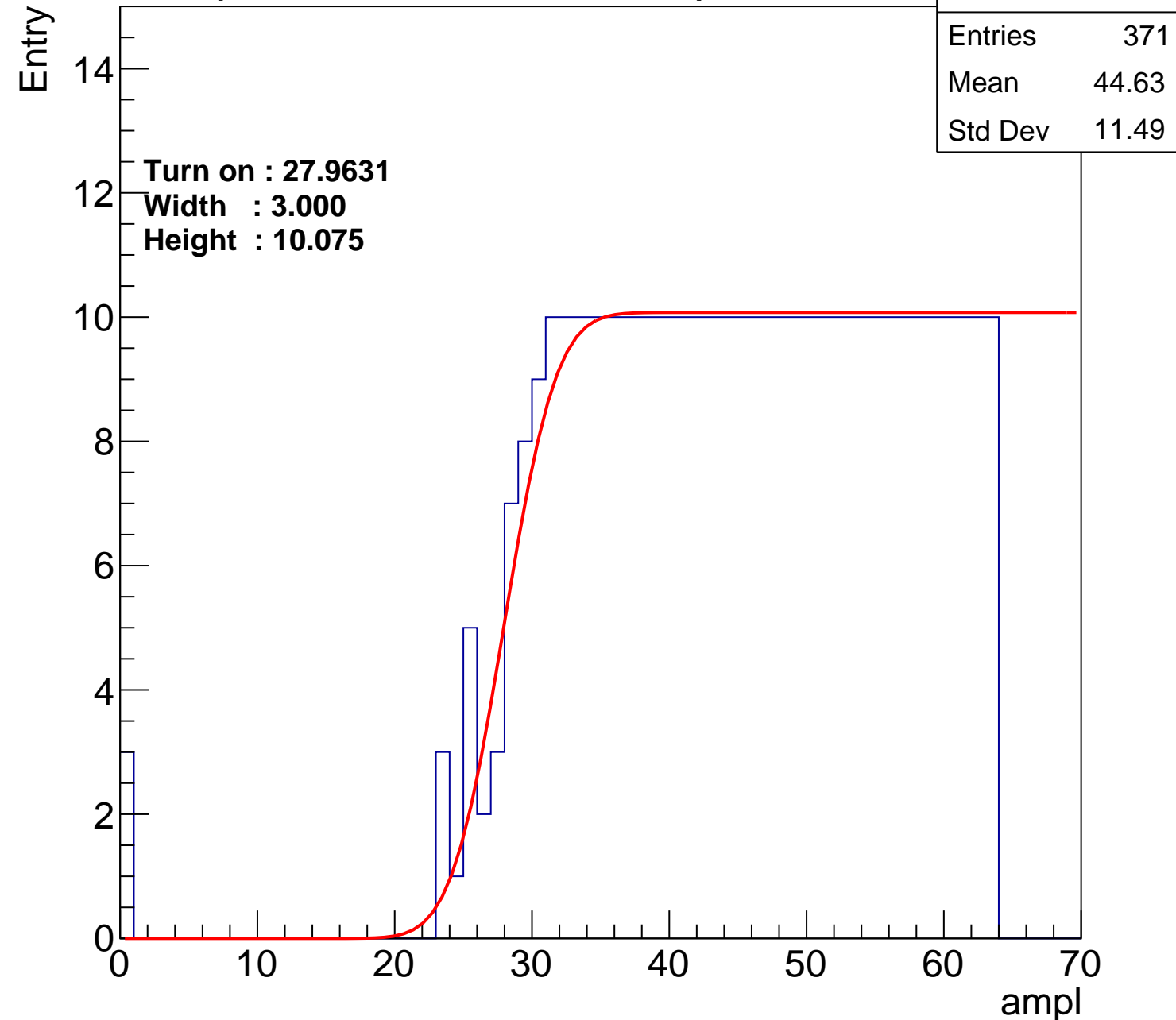
Width : 3.000

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch90

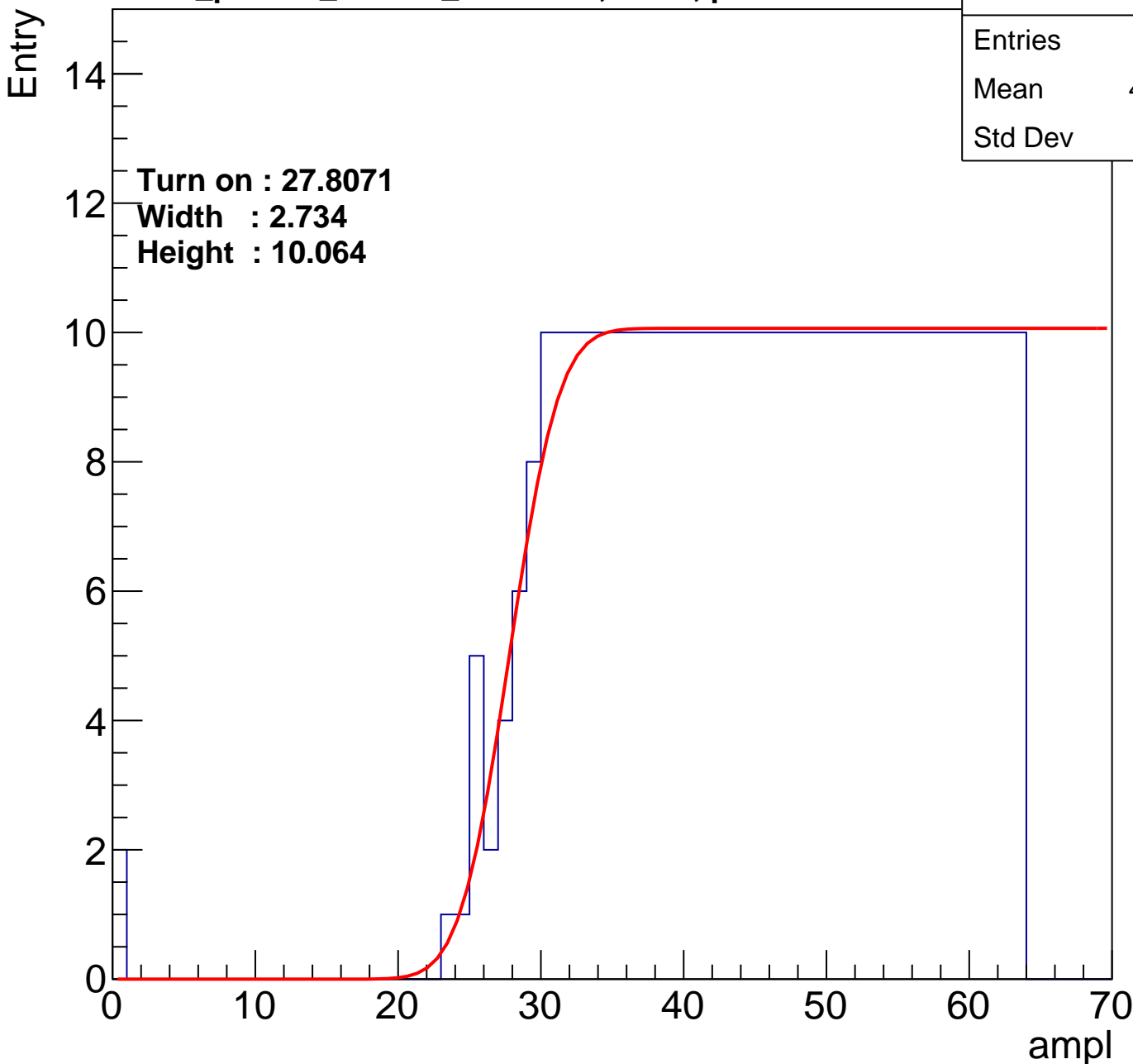
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	369
Mean	44.83
Std Dev	11.2

**Turn on : 27.8071**

**Width : 2.734**

**Height : 10.064**



# B0L000S, U20-ch91

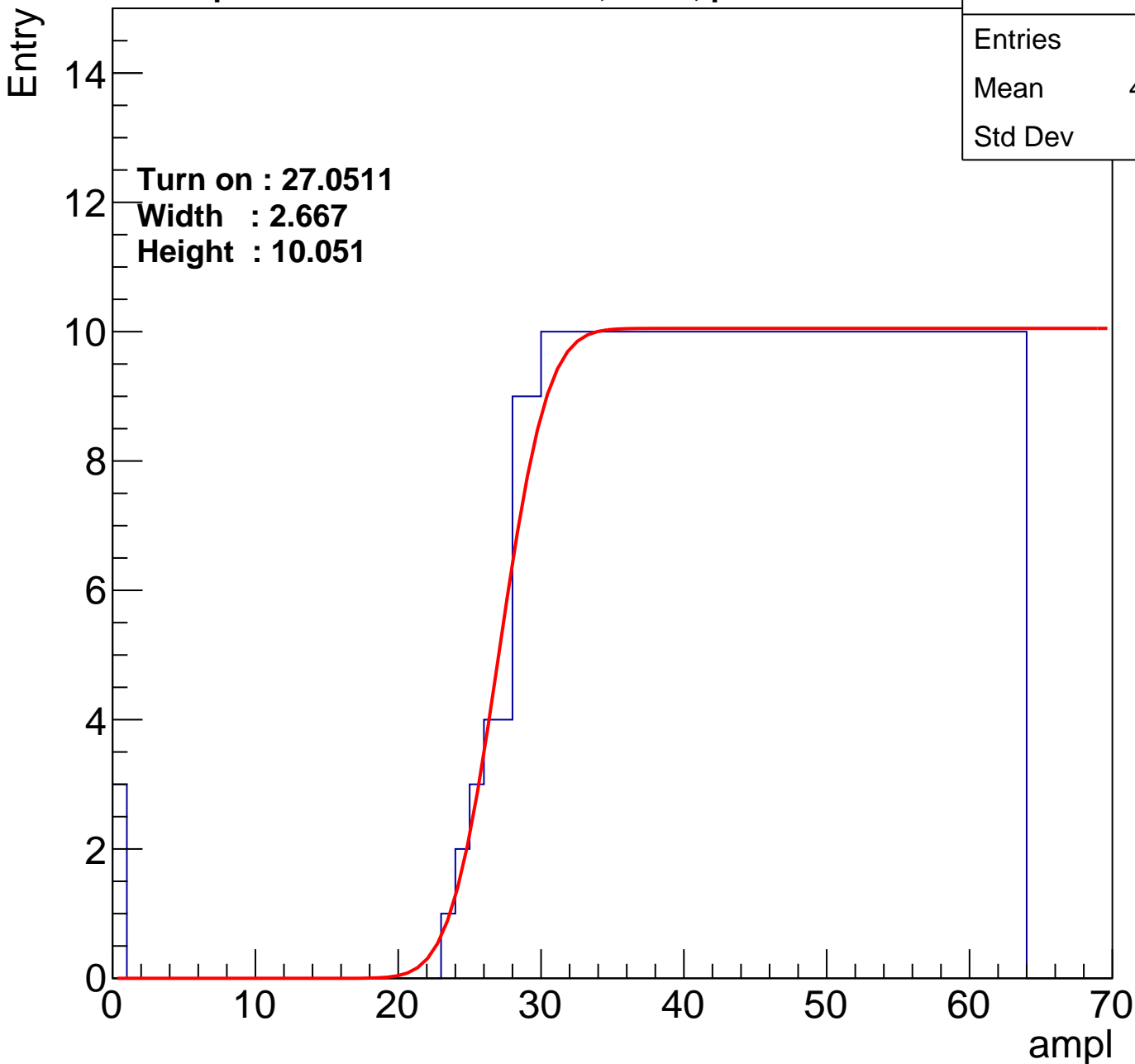
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	375
Mean	44.48
Std Dev	11.51

Turn on : 27.0511

Width : 2.667

Height : 10.051



# B0L000S, U20-ch92

calib\_packv5\_042523\_0143.root, FC#5, port B1

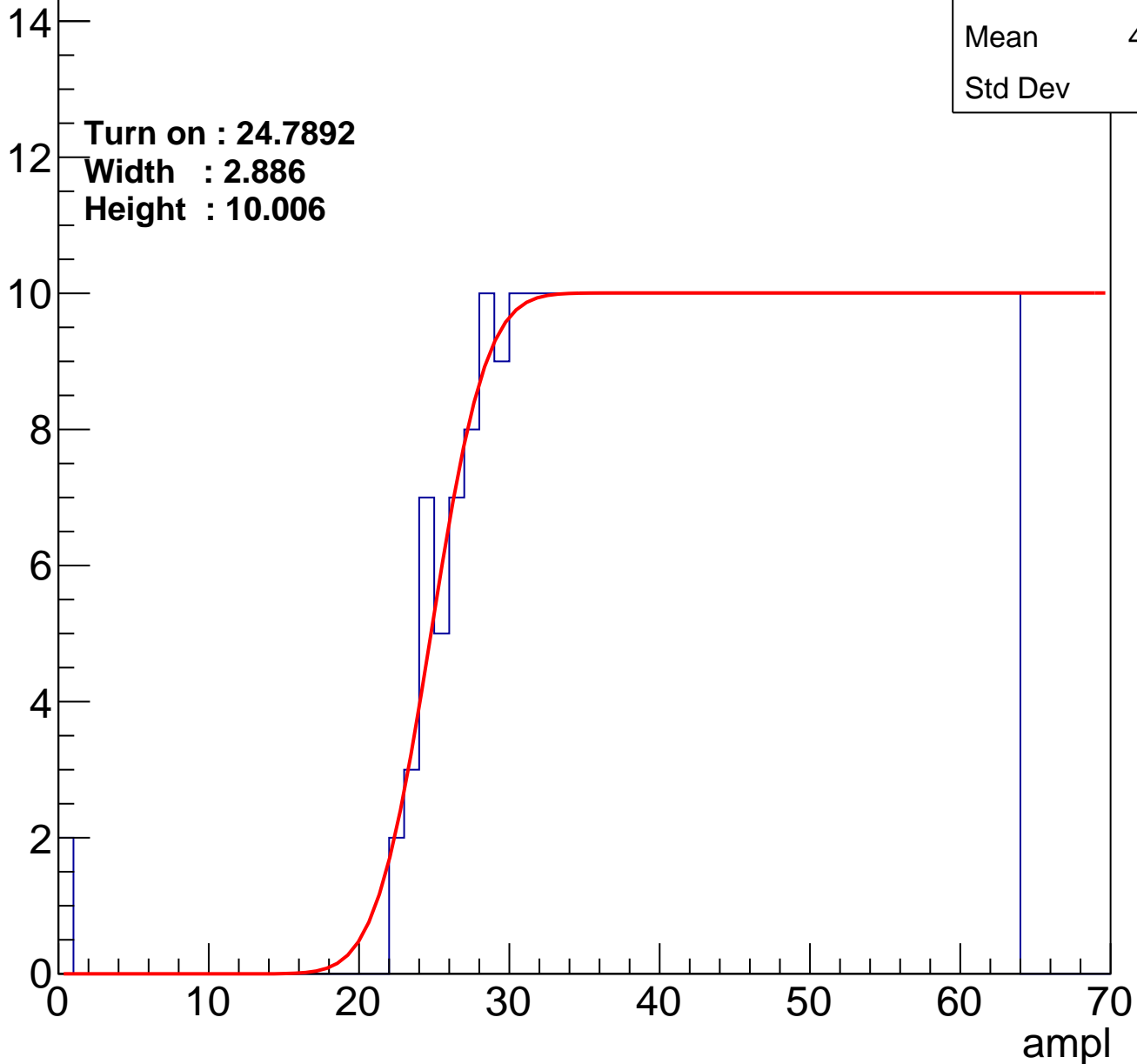
Entries	393
Mean	43.65
Std Dev	11.8

Turn on : 24.7892

Width : 2.886

Height : 10.006

Entry



# B0L000S, U20-ch93

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	404
Mean	42.77
Std Dev	12.89

Turn on : 24.8380

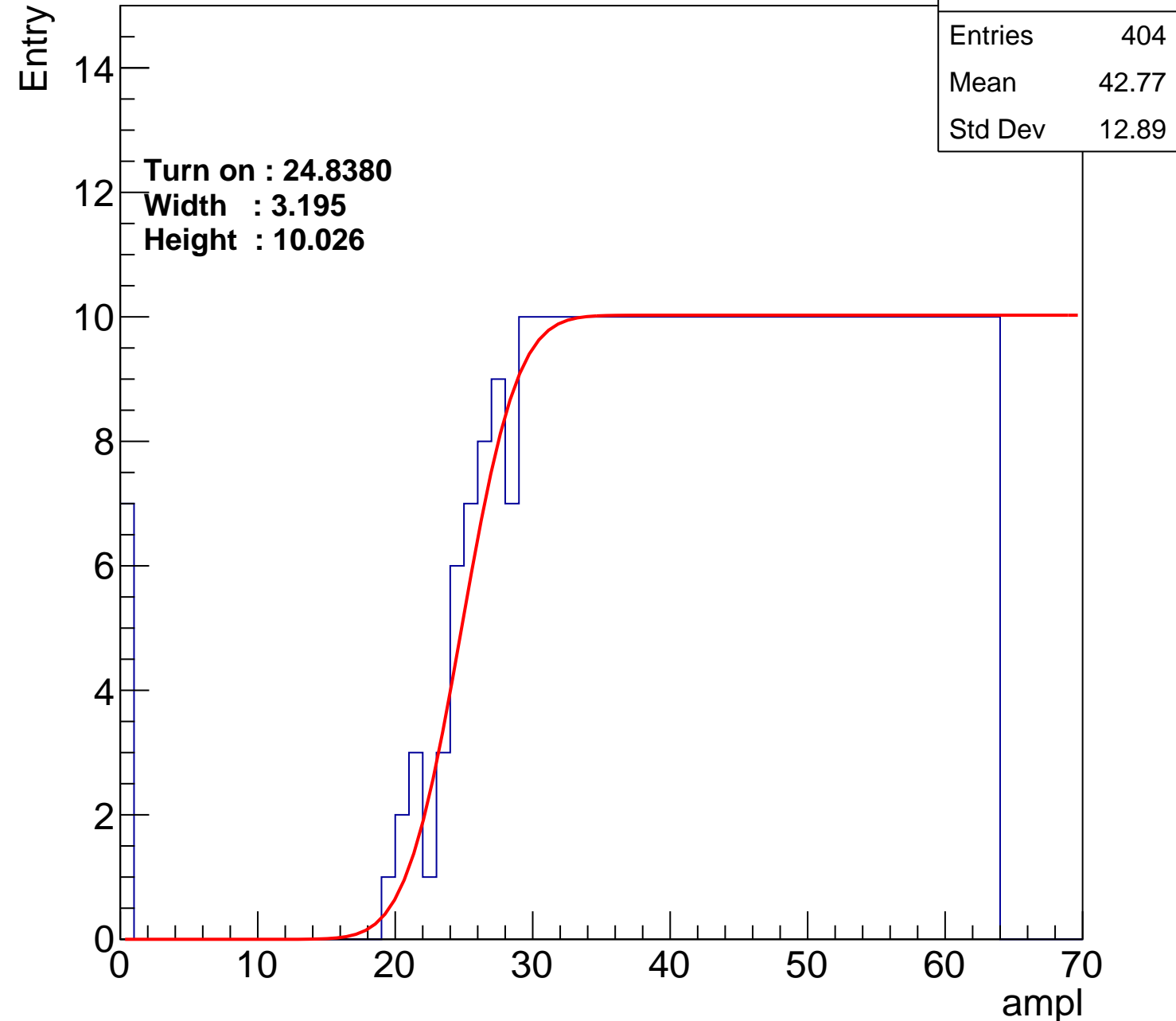
Width : 3.195

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch94

calib\_packv5\_042523\_0143.root, FC#5, port B1

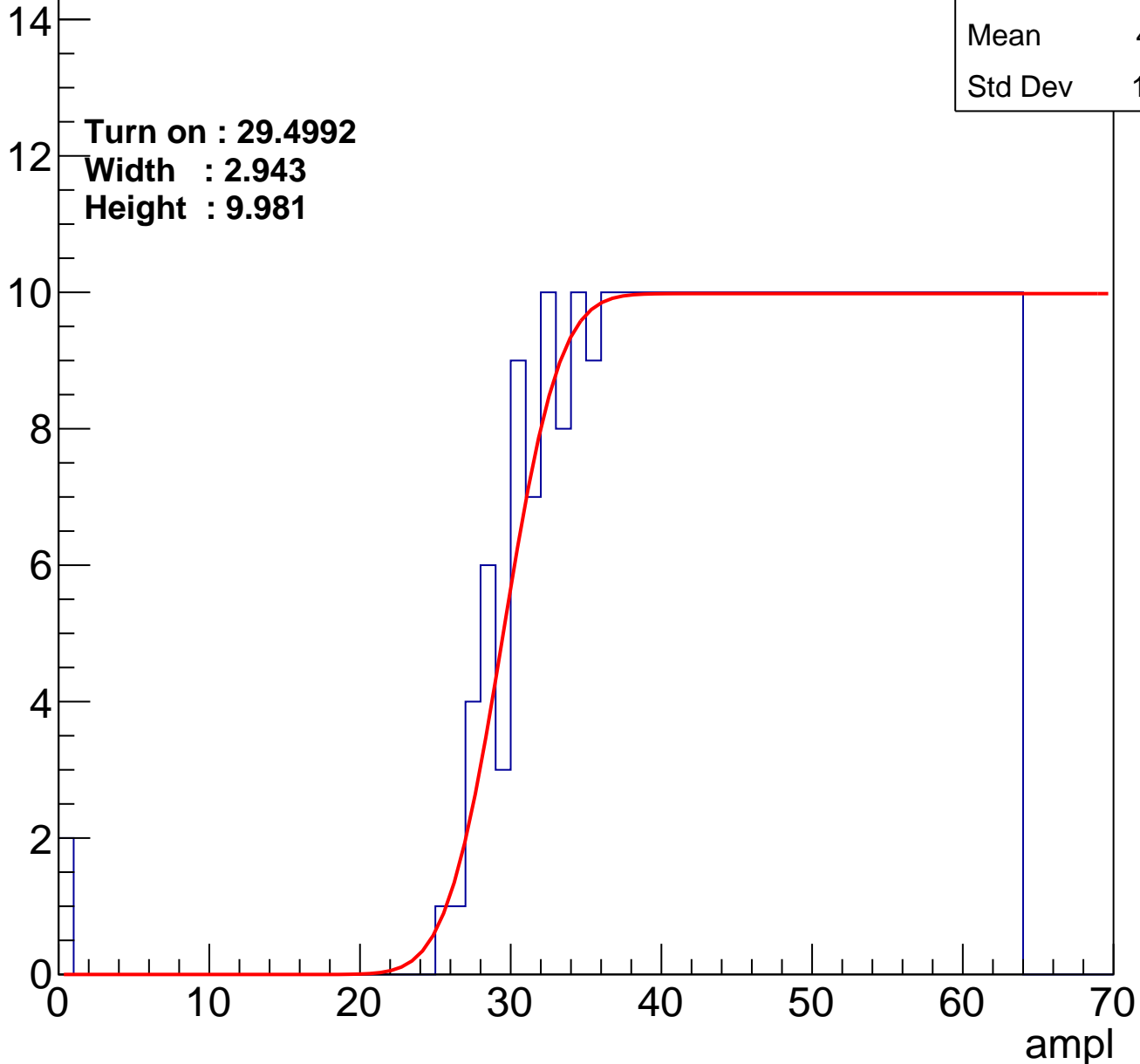
Entries	350
Mean	45.71
Std Dev	10.79

Turn on : 29.4992

Width : 2.943

Height : 9.981

Entry





# B0L000S, U20-ch95

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	389
Mean	43.7
Std Dev	12.07

**Turn on : 26.3758**

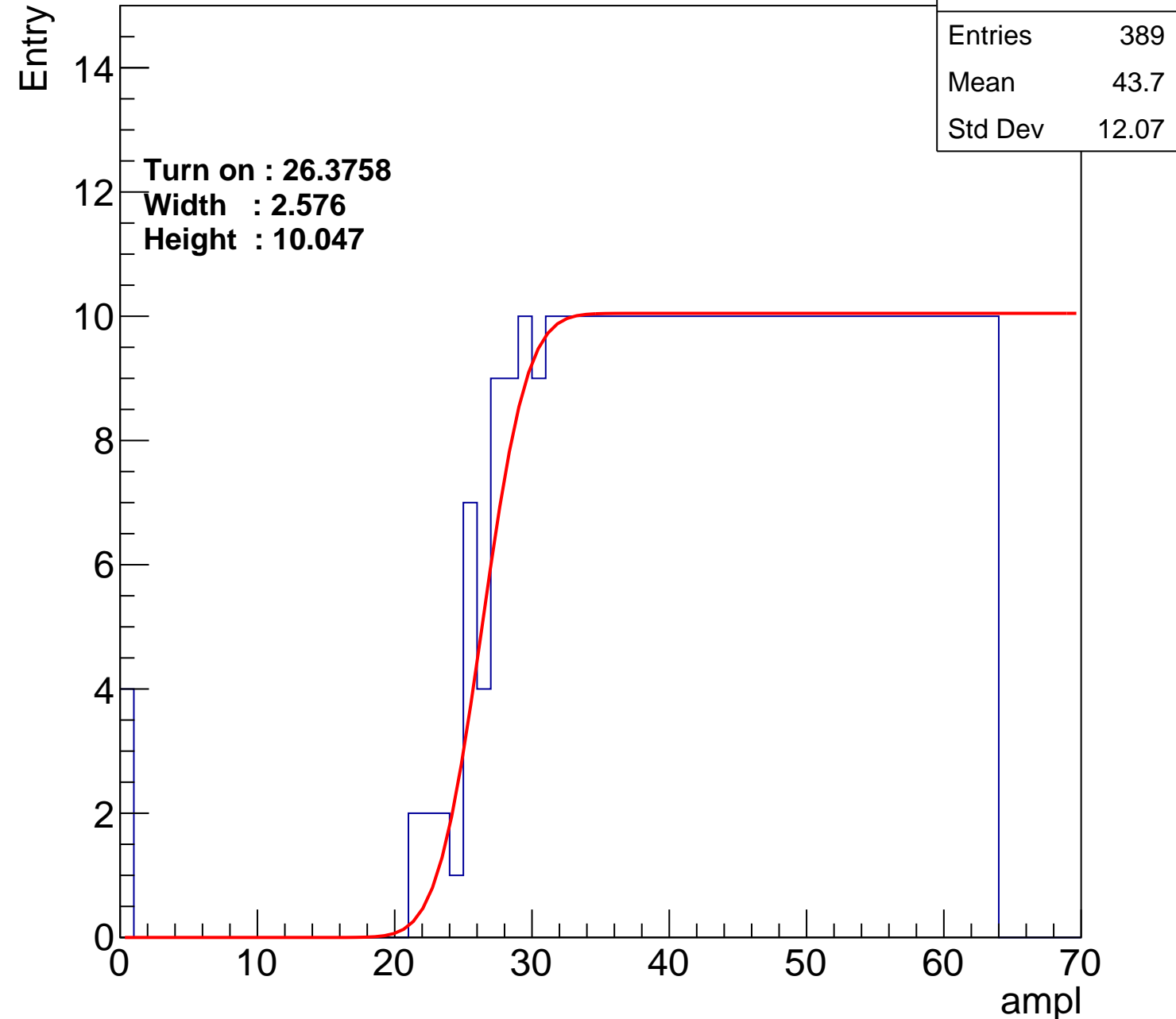
**Width : 2.576**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch96

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	386
Mean	43.91
Std Dev	11.84

**Turn on : 25.9297**

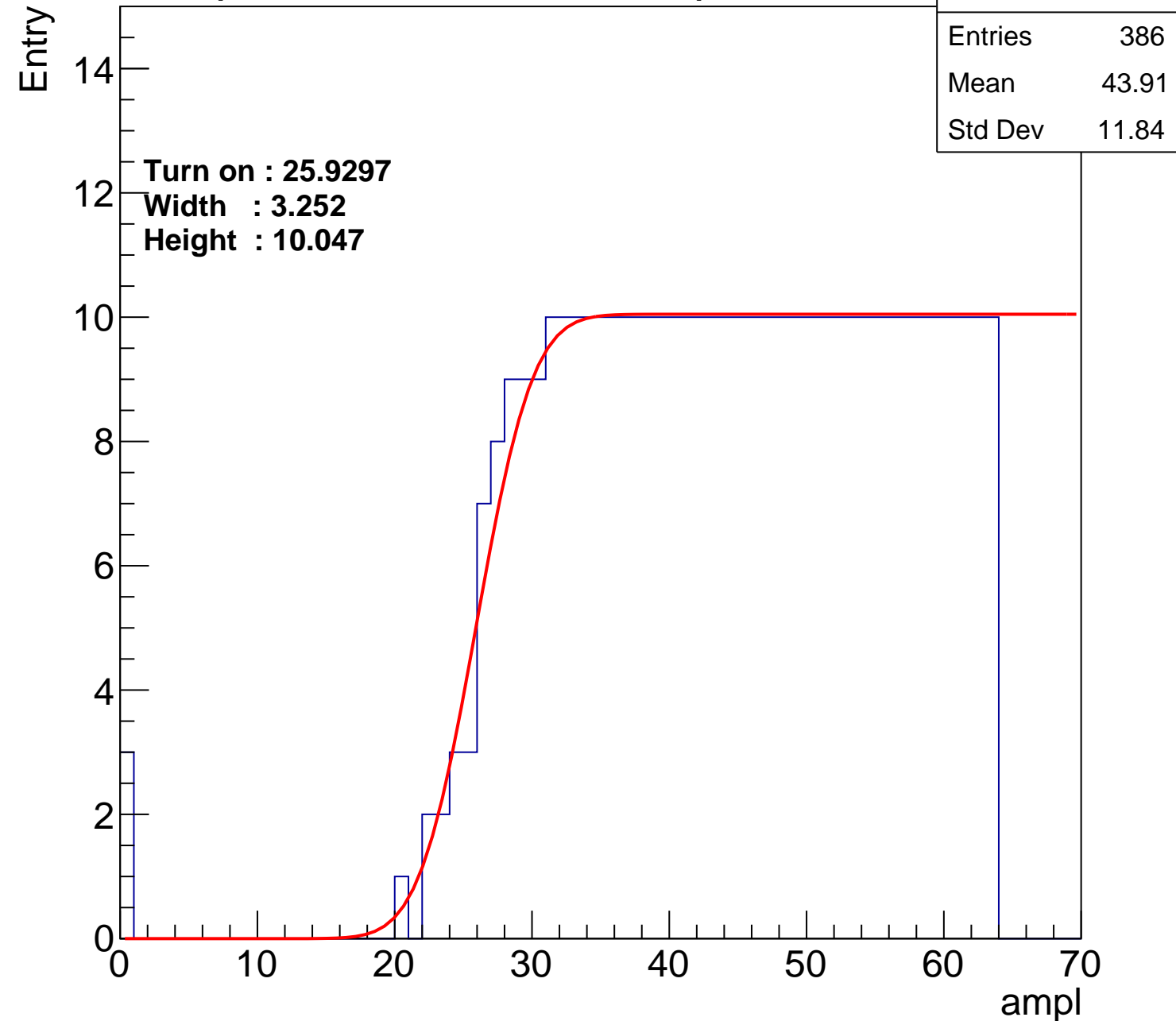
**Width : 3.252**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch97

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.7623

Width : 1.906

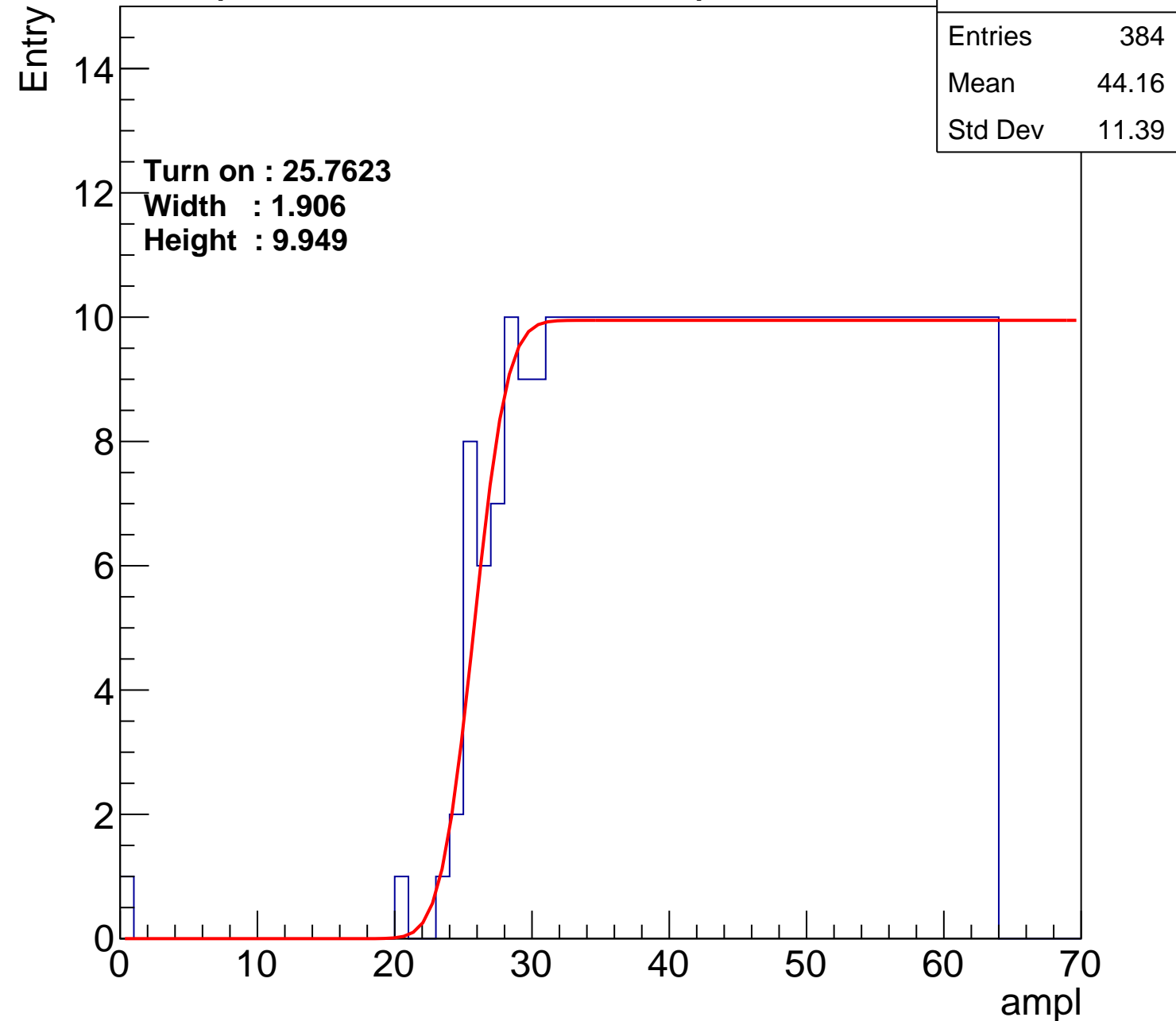
Height : 9.949

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U20-ch98

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	373
Mean	44.48
Std Dev	11.7

**Turn on : 27.2485**

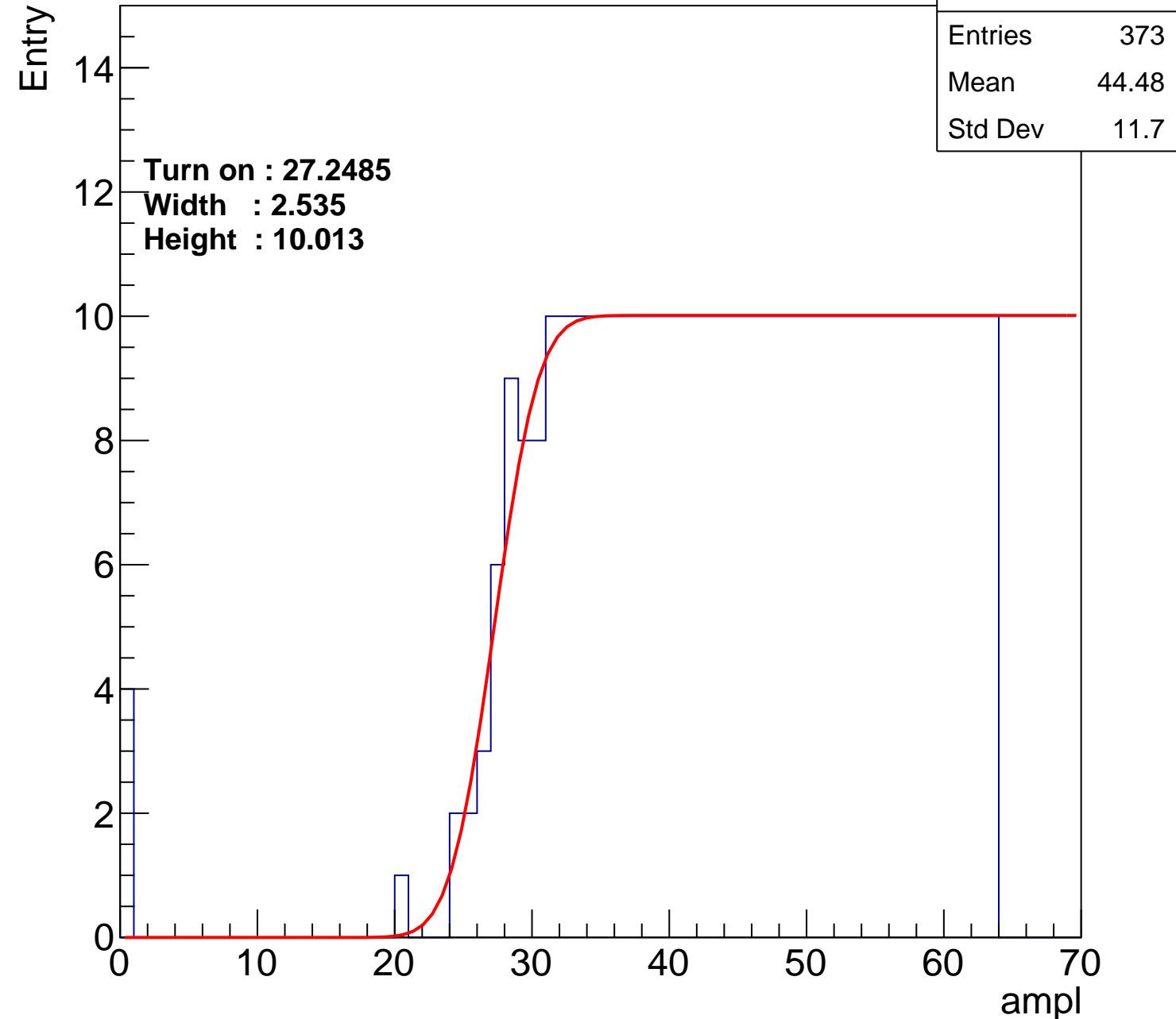
**Width : 2.535**

**Height : 10.013**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch99

calib\_packv5\_042523\_0143.root, FC#5, port B1

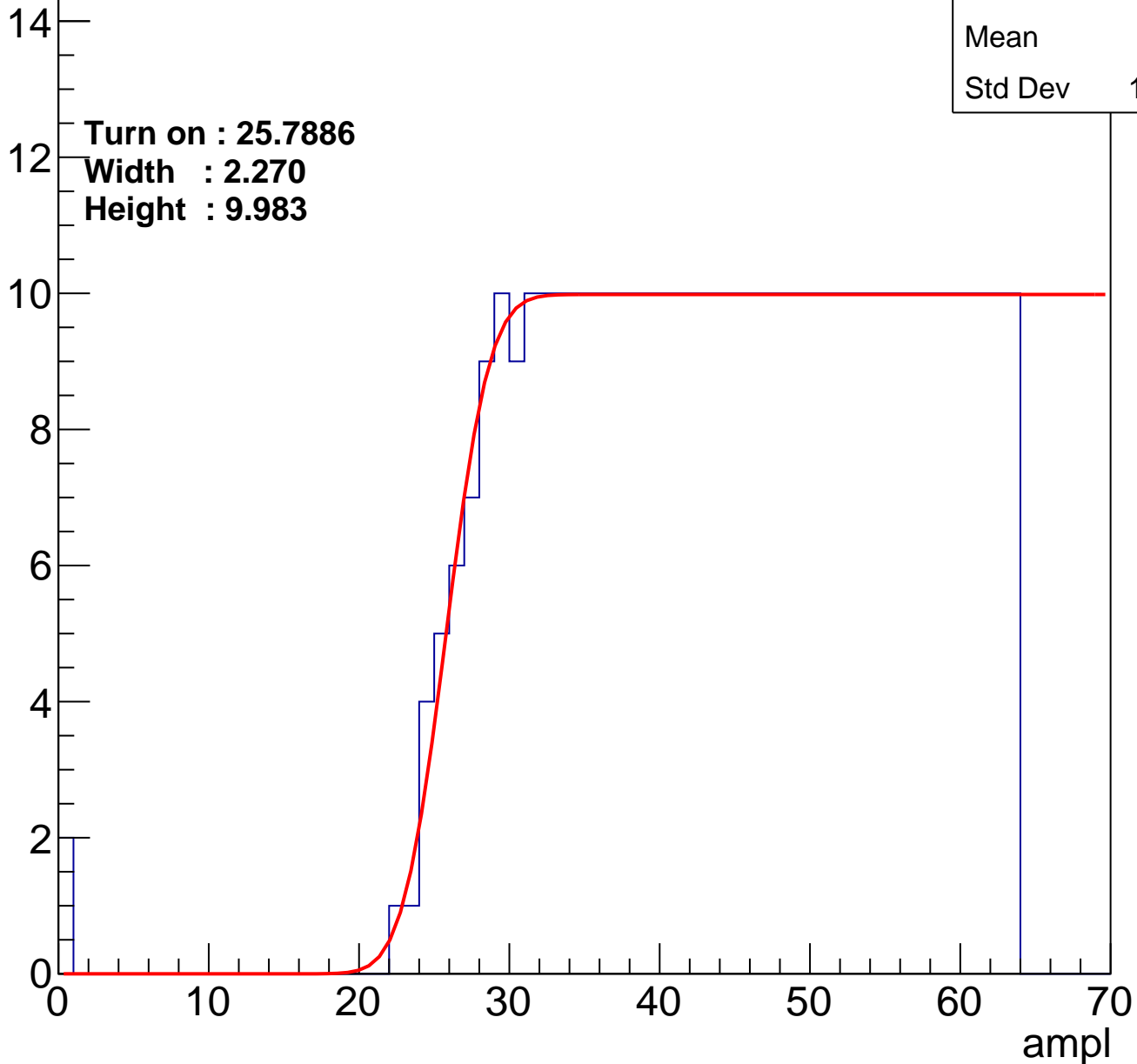
Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.7886

Width : 2.270

Height : 9.983

Entry



# B0L000S, U20-ch100

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	380
Mean	44.23
Std Dev	11.65

**Turn on : 26.6267**

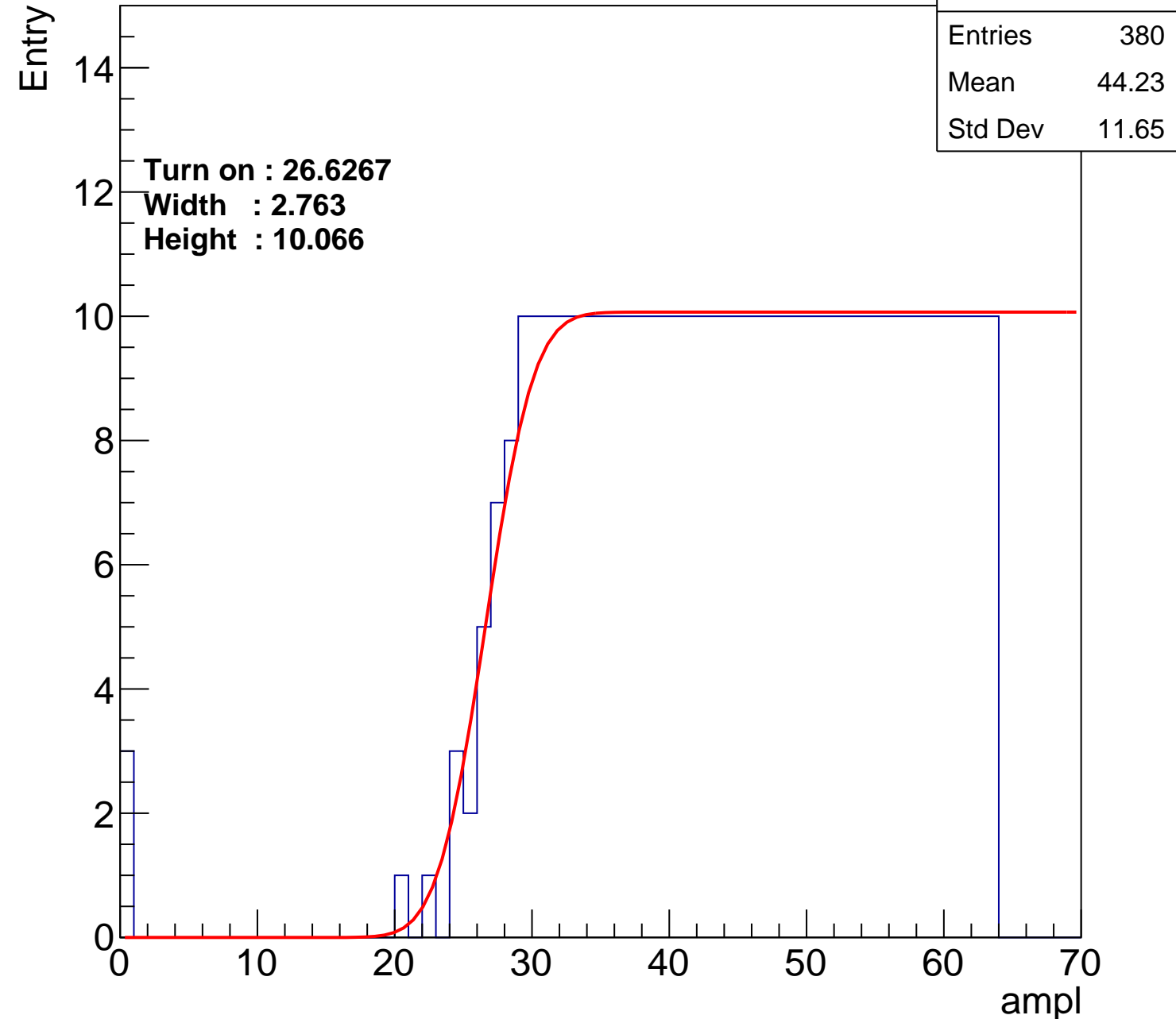
**Width : 2.763**

**Height : 10.066**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# BOL000S, U20-ch101

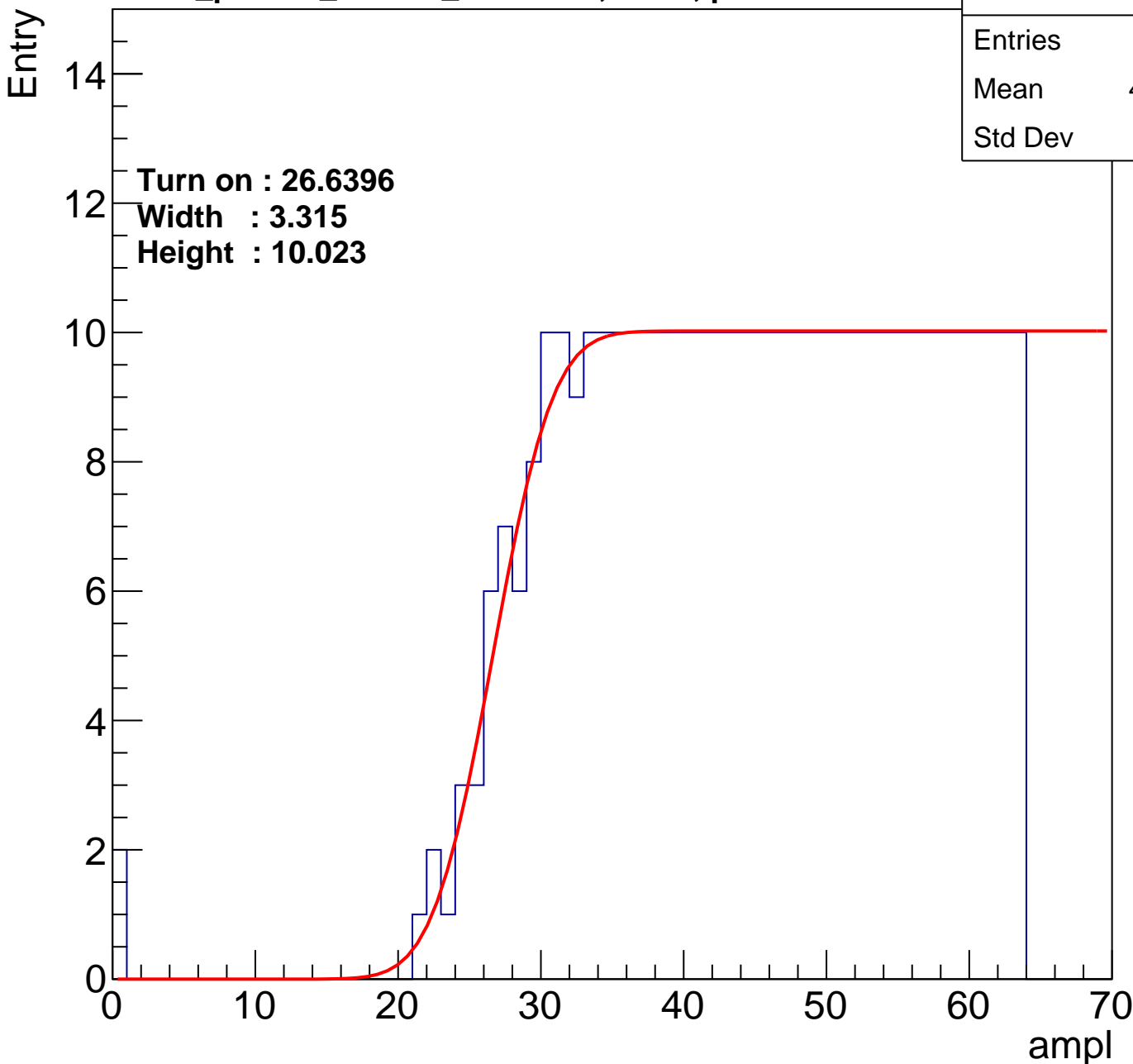
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

**Turn on : 26.6396**

**Width : 3.315**

**Height : 10.023**

Entries	378
Mean	44.33
Std Dev	11.51



# B0L000S, U20-ch102

calib\_packv5\_042523\_0143.root, FC#5, port B1

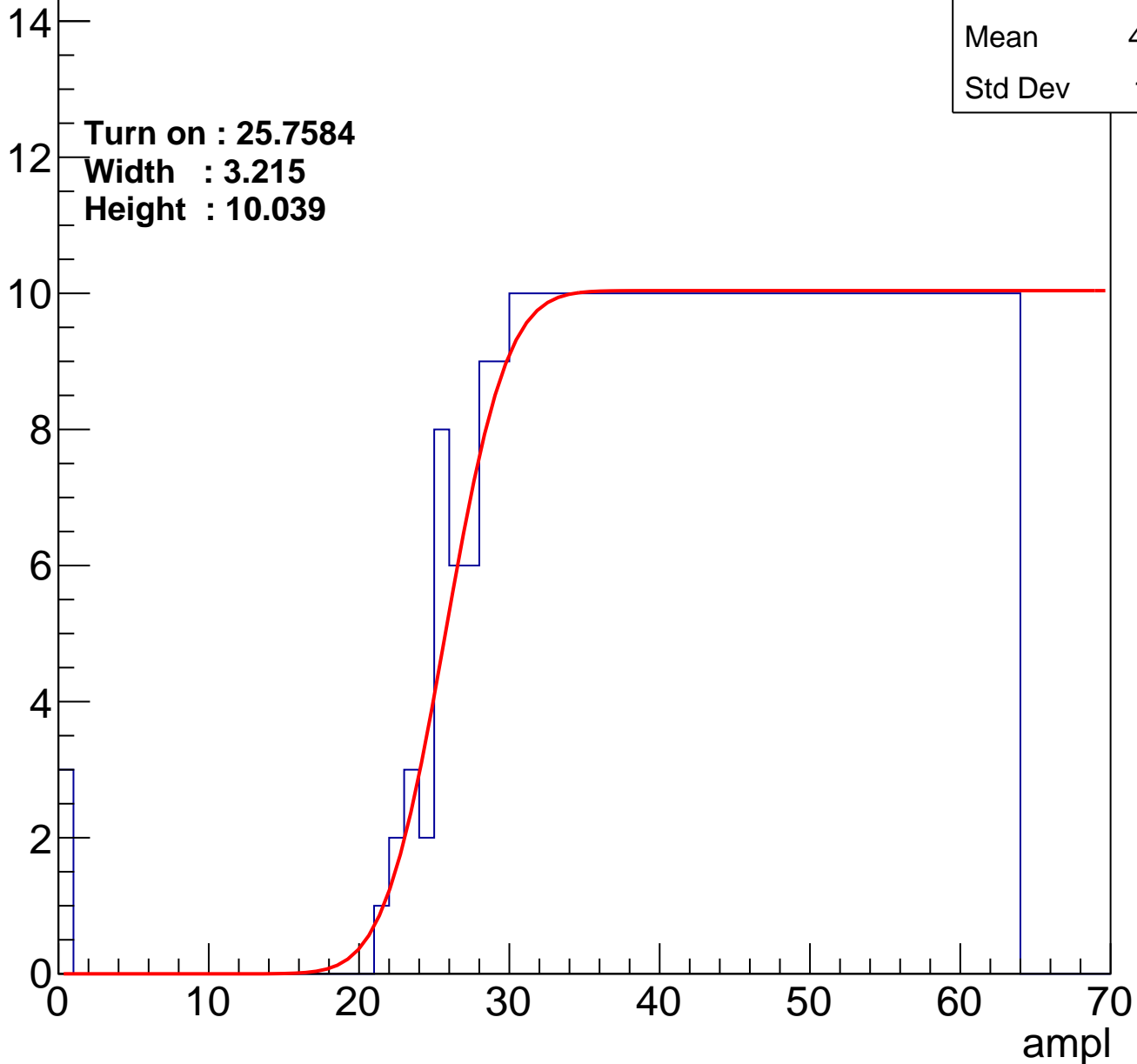
Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.7584

Width : 3.215

Height : 10.039

Entry





# B0L000S, U20-ch103

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	392
Mean	43.52
Std Dev	12.25

Turn on : 25.3198

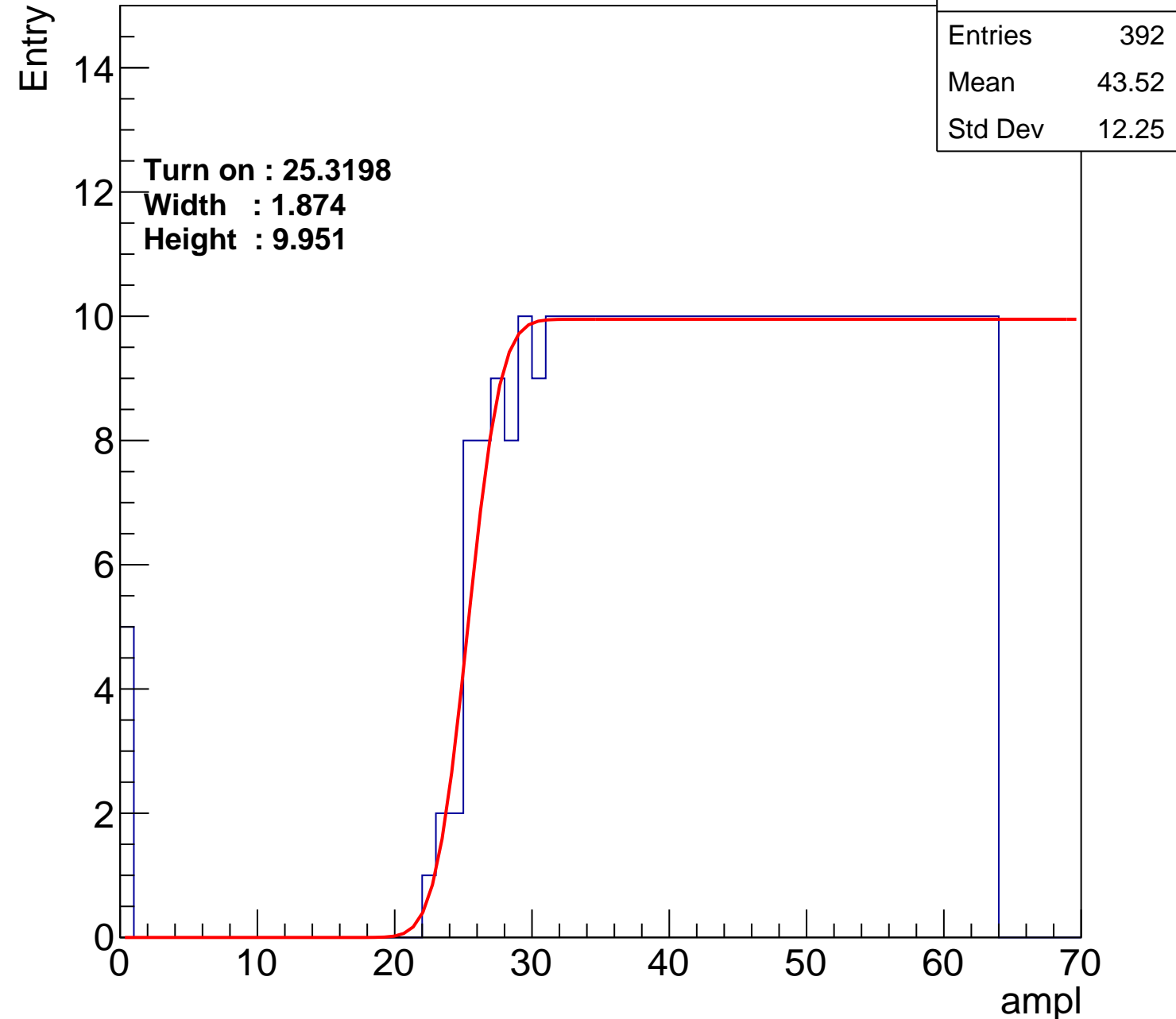
Width : 1.874

Height : 9.951

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch104

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.26
Std Dev	11.46

**Turn on : 25.8736**

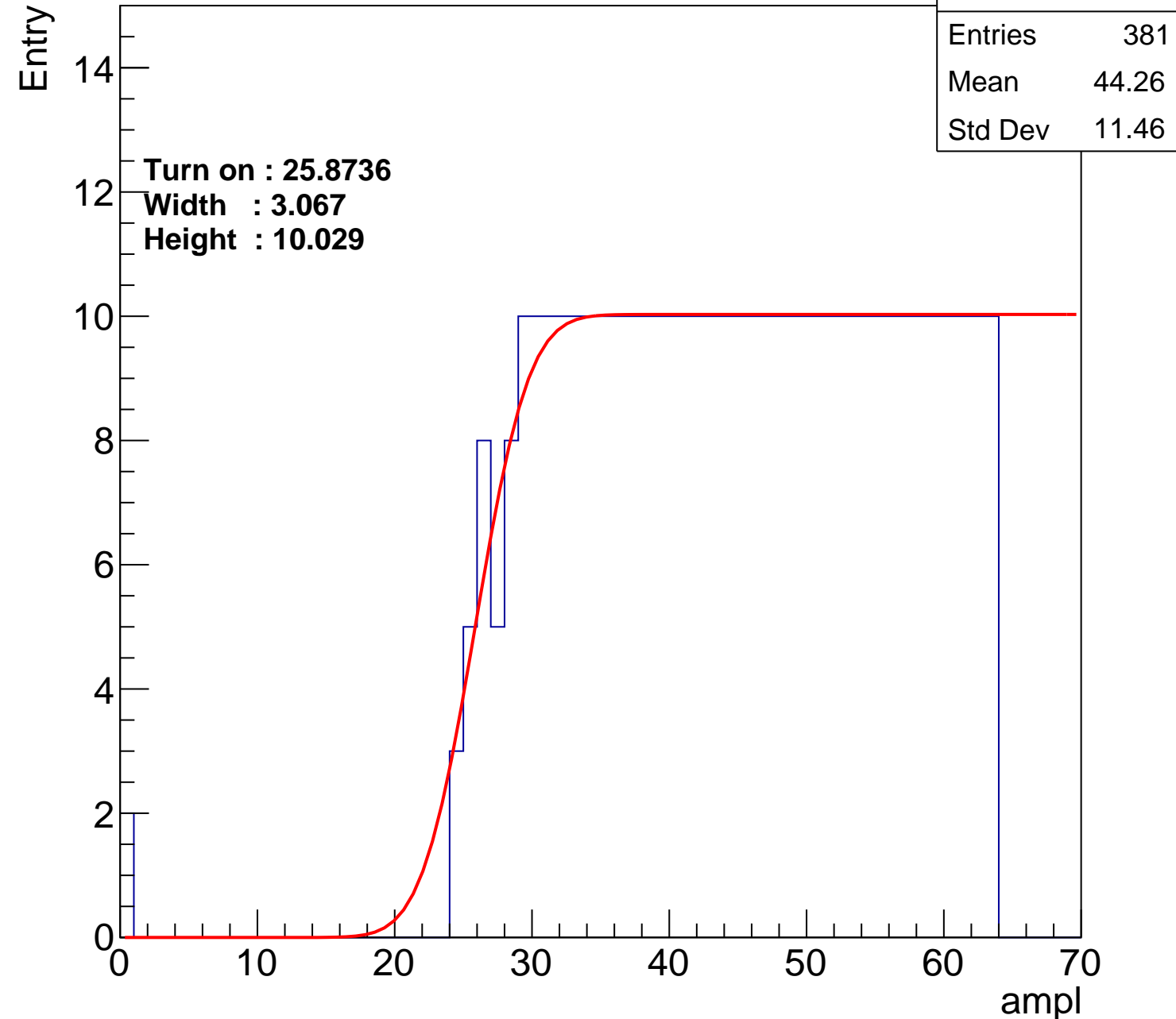
**Width : 3.067**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch105

**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	367
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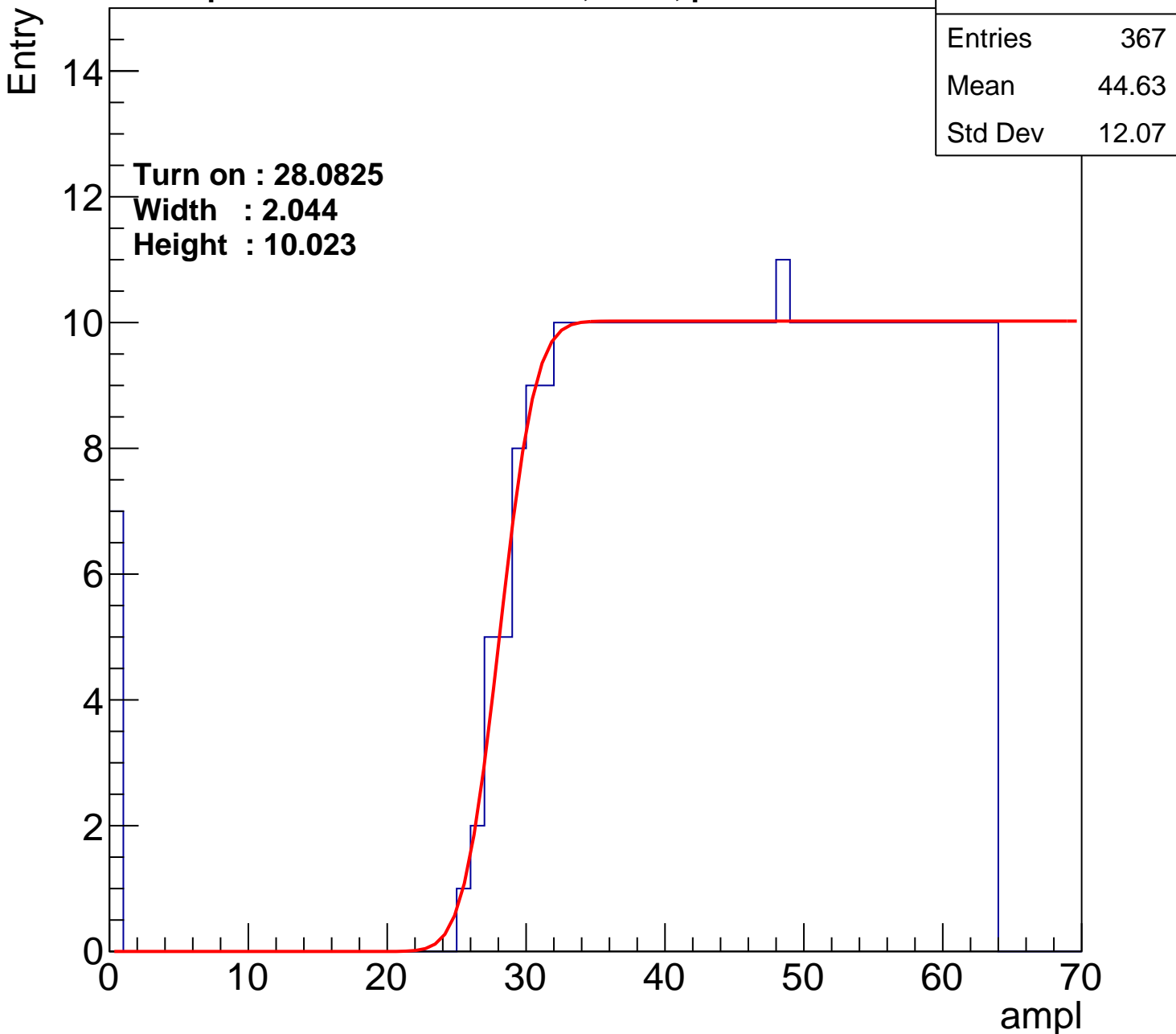
Mean	44.63
------	-------

Std Dev	12.07
---------	-------

**Turn on : 28.0825**

**Width : 2.044**

**Height : 10.023**



# B0L000S, U20-ch106

calib\_packv5\_042523\_0143.root, FC#5, port B1

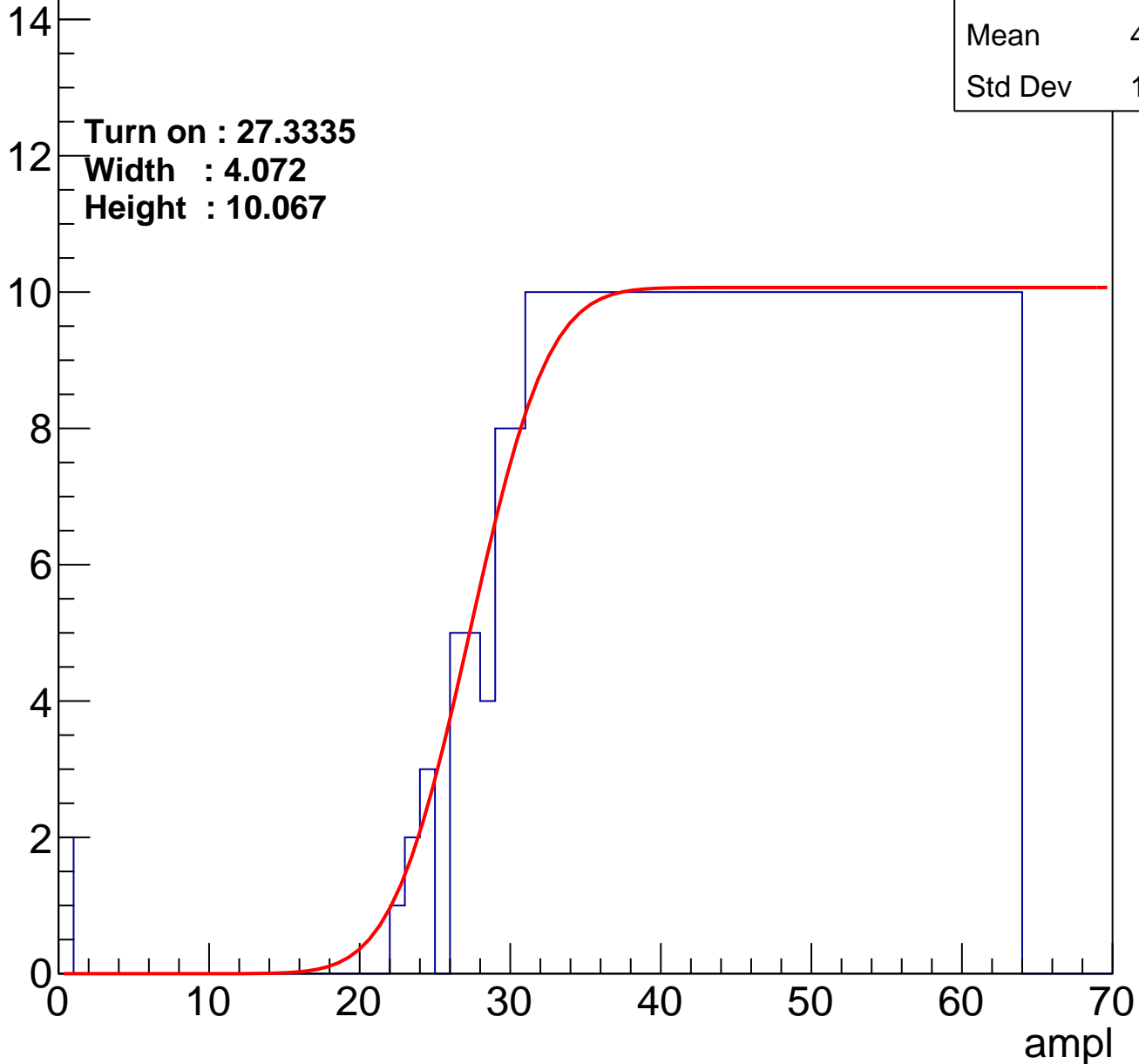
Entries	368
Mean	44.83
Std Dev	11.24

**Turn on : 27.3335**

**Width : 4.072**

**Height : 10.067**

Entry



# B0L000S, U20-ch107

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	376
Mean	44.3
Std Dev	11.81

**Turn on : 26.9875**

**Width : 2.816**

**Height : 9.998**

Entry

14

12

10

8

6

4

2

0

0

10

20

30

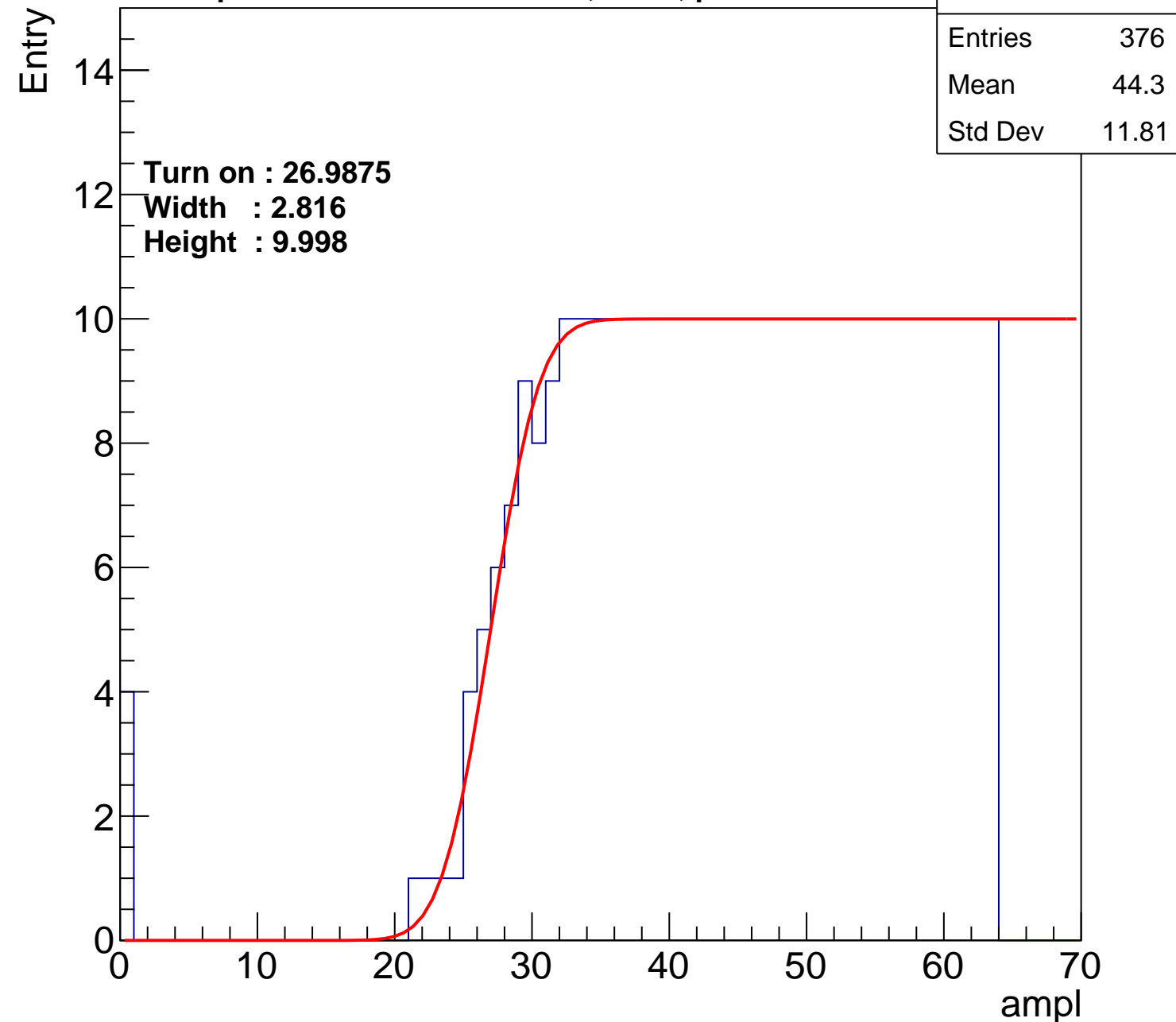
40

50

60

70

ampl



# B0L000S, U20-ch108

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.7319

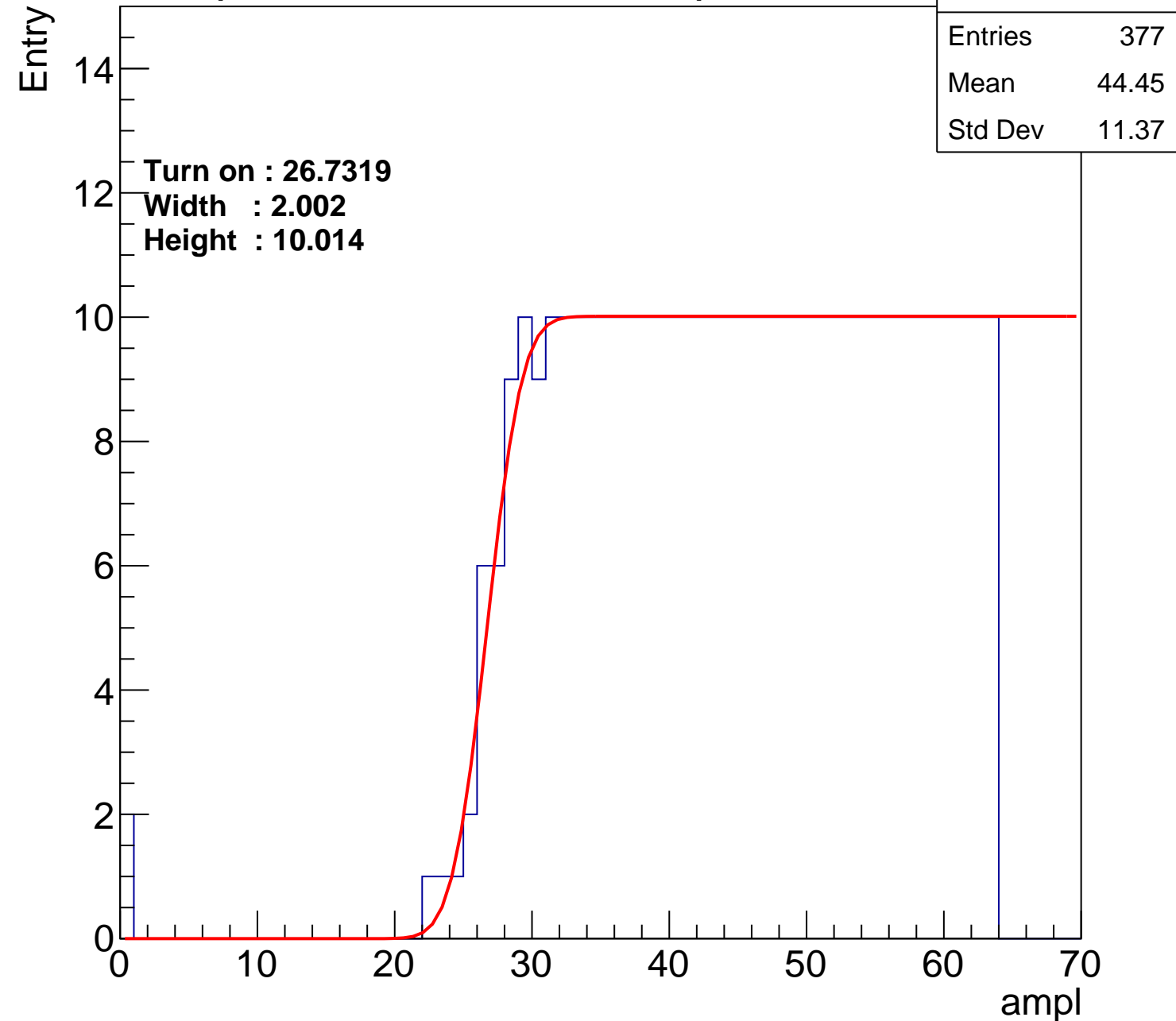
Width : 2.002

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch109

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.09
Std Dev	11.57

**Turn on : 25.5540**

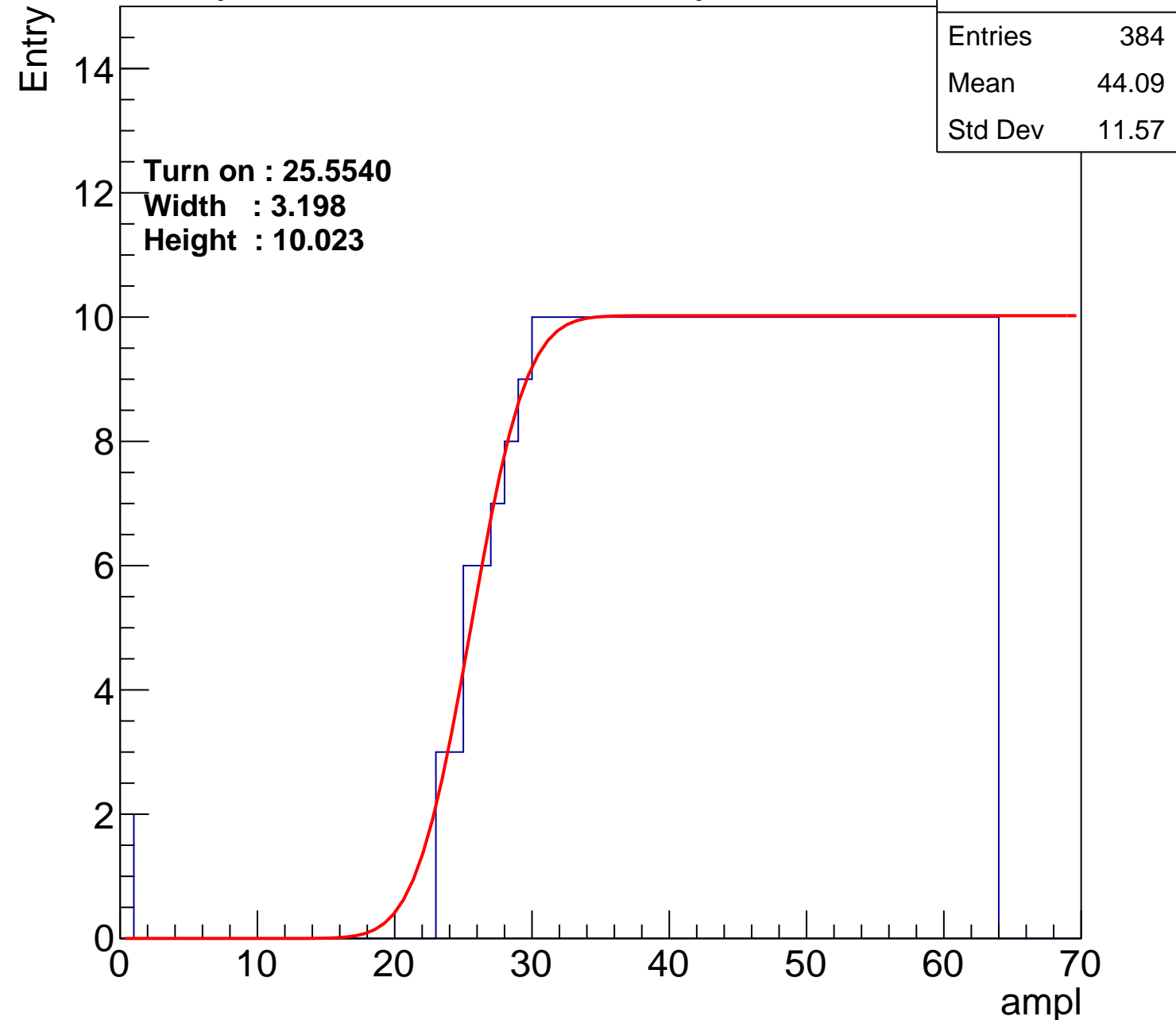
**Width : 3.198**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch110

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	387
Mean	43.94
Std Dev	11.65

Turn on : 25.6989

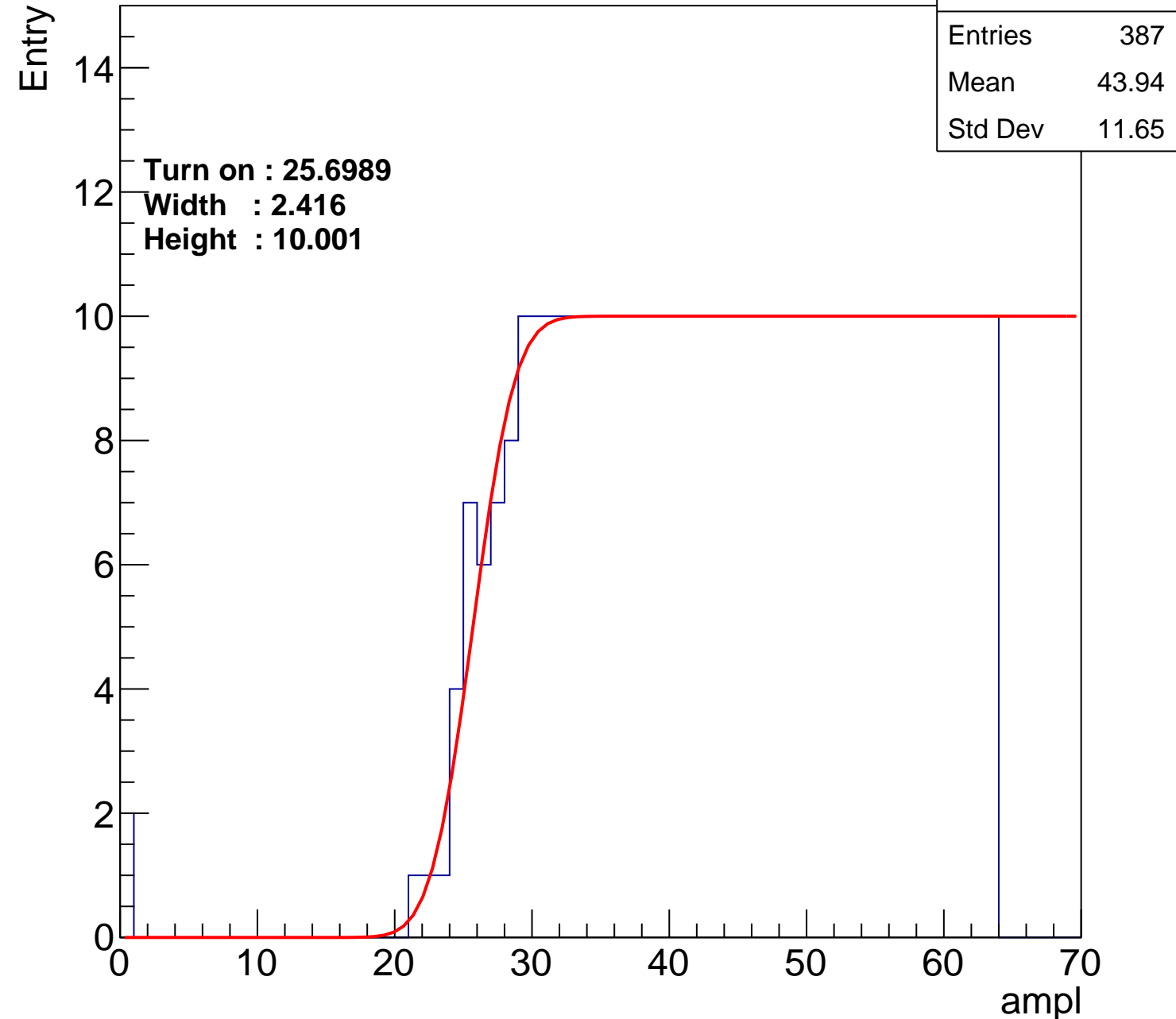
Width : 2.416

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch111

calib\_packv5\_042523\_0143.root, FC#5, port B1

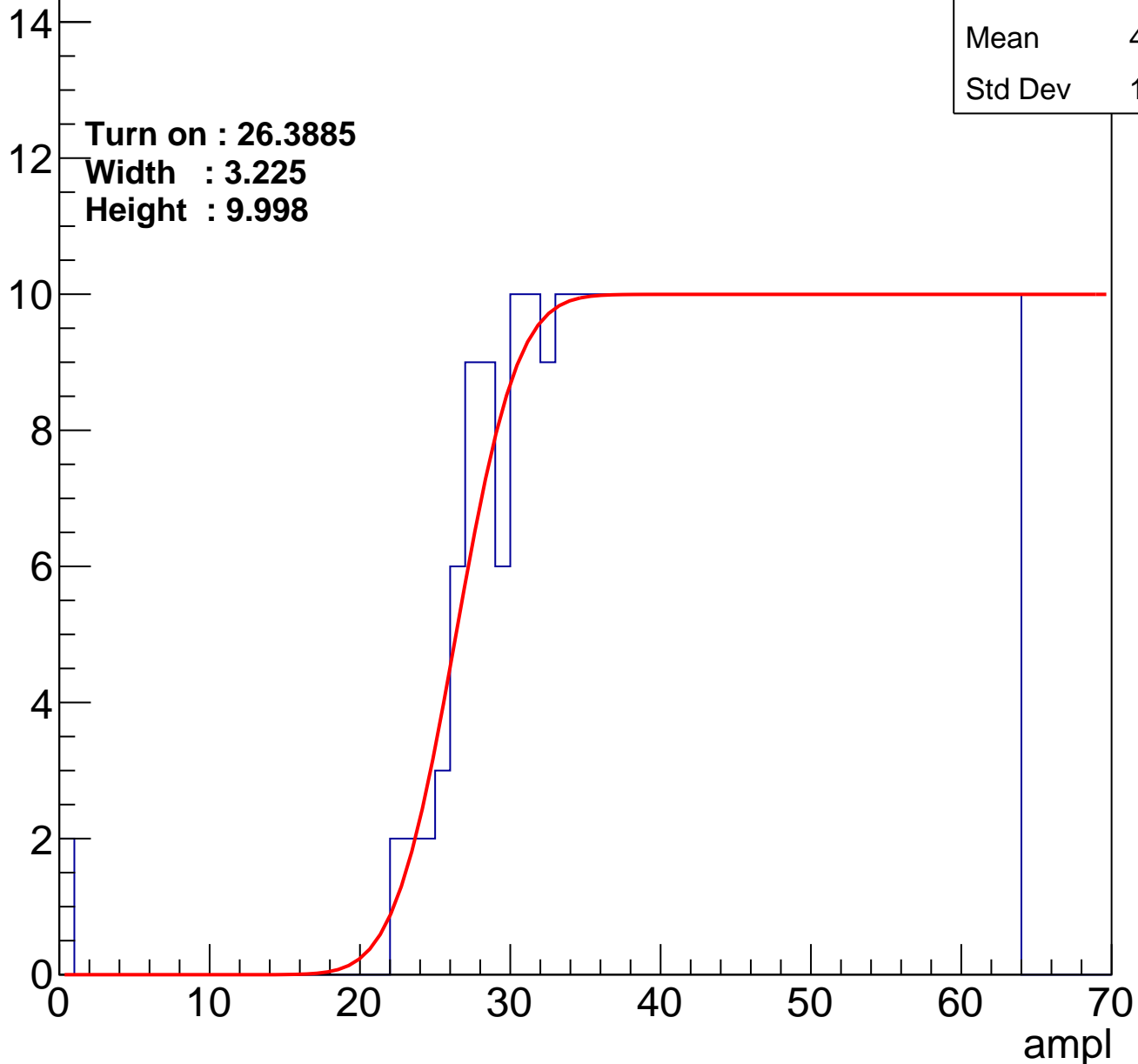
Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.3885

Width : 3.225

Height : 9.998

Entry



# B0L000S, U20-ch112

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	361
Mean	45.2
Std Dev	11.02

**Turn on : 28.2679**

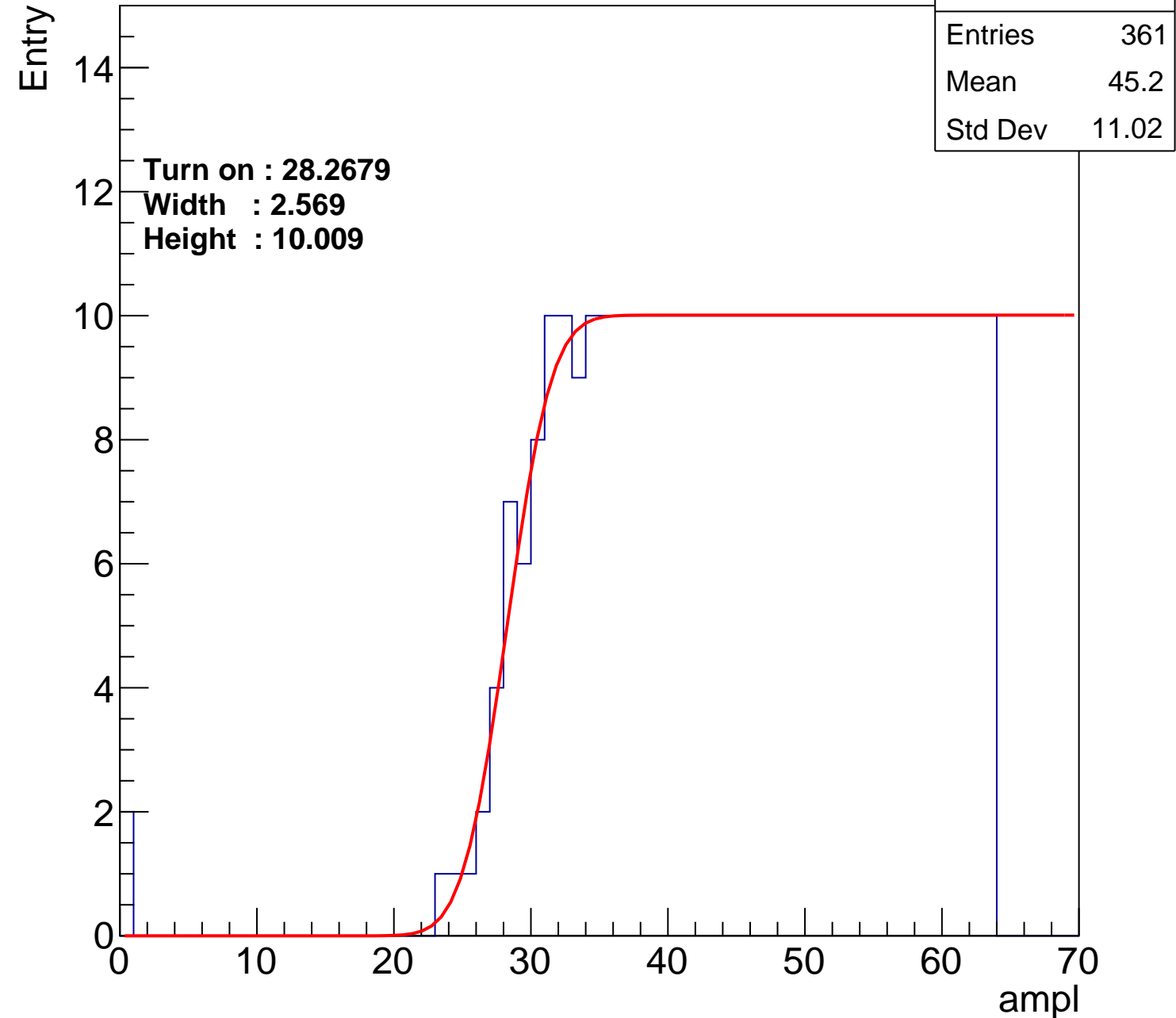
**Width : 2.569**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch113

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	374
Mean	44.61
Std Dev	11.18

**Turn on : 26.9567**

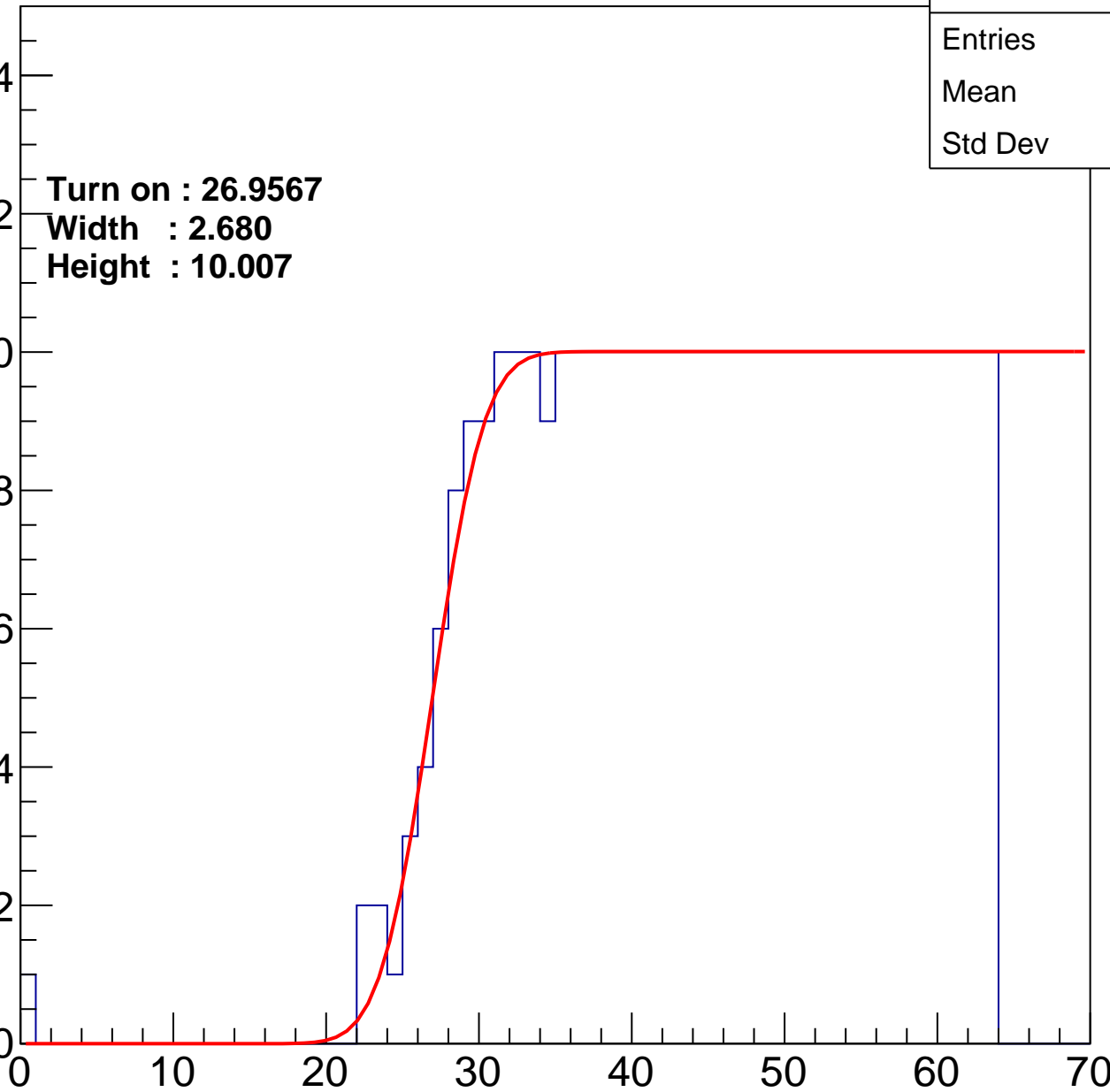
**Width : 2.680**

**Height : 10.007**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch114

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	371
Mean	44.65
Std Dev	11.45

**Turn on : 27.0376**

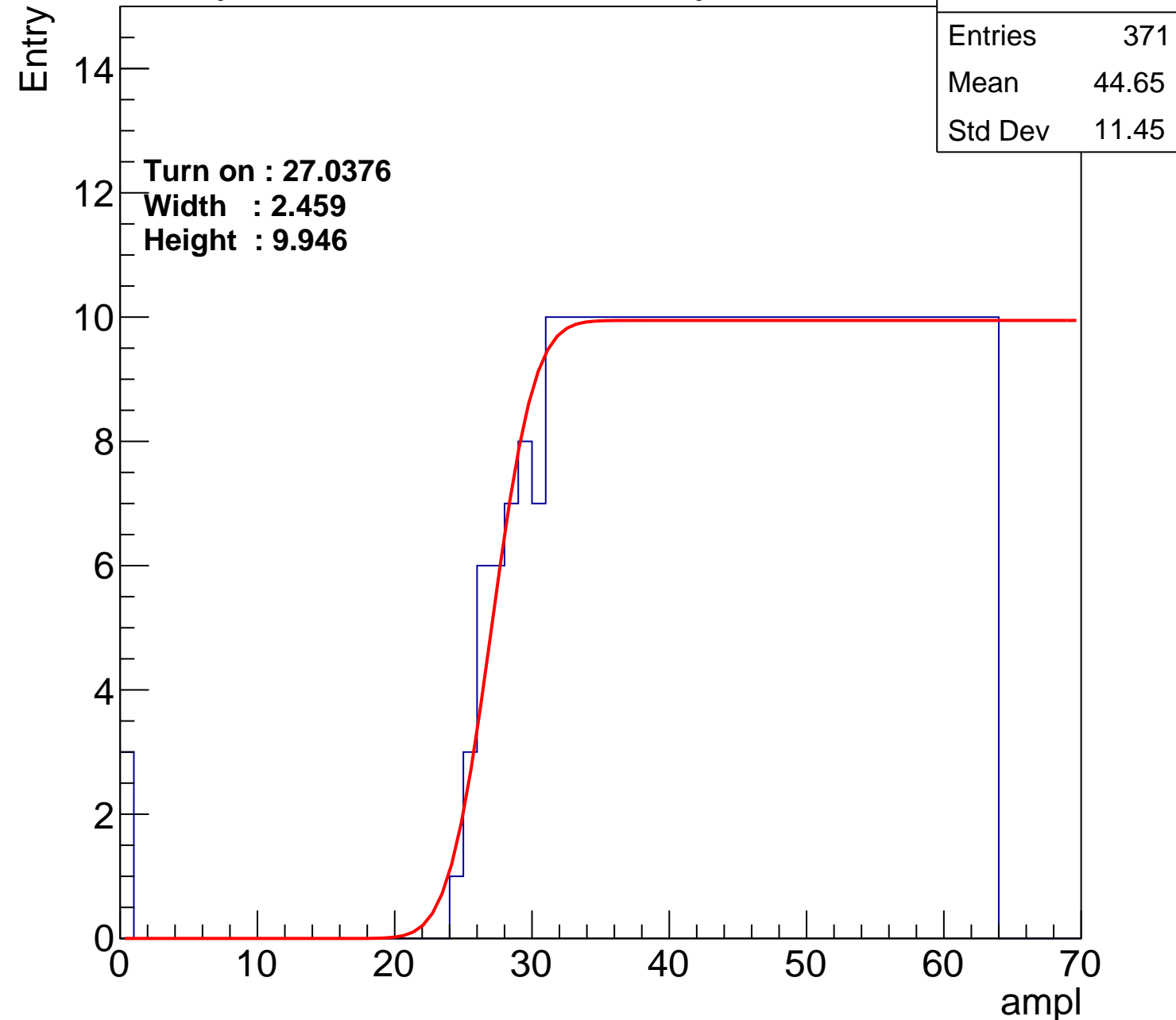
**Width : 2.459**

**Height : 9.946**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch115

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	392
Mean	43.56
Std Dev	12.13

Turn on : 26.0130

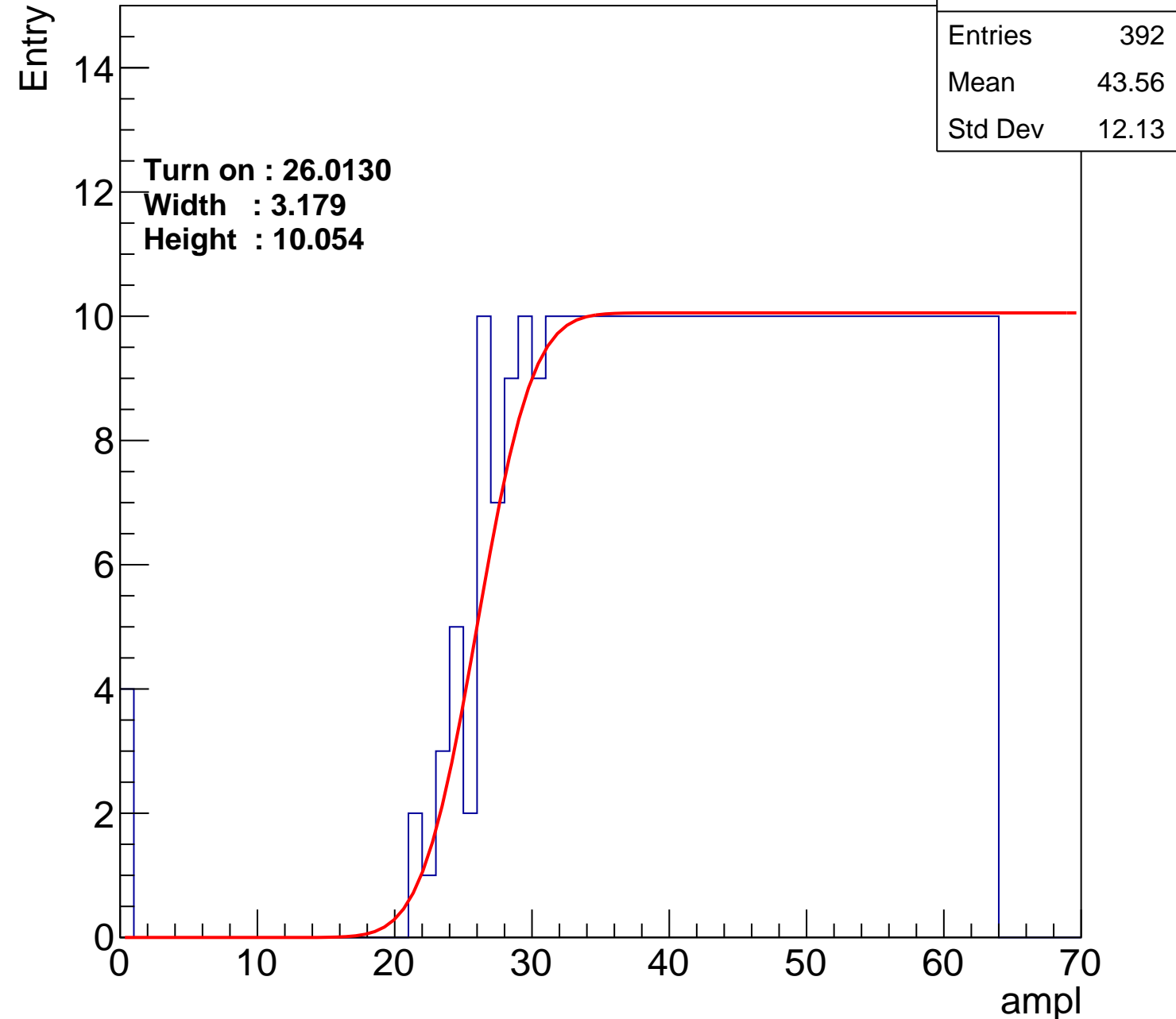
Width : 3.179

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch116

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	354
Mean	45.43
Std Dev	11.15

**Turn on : 29.3898**

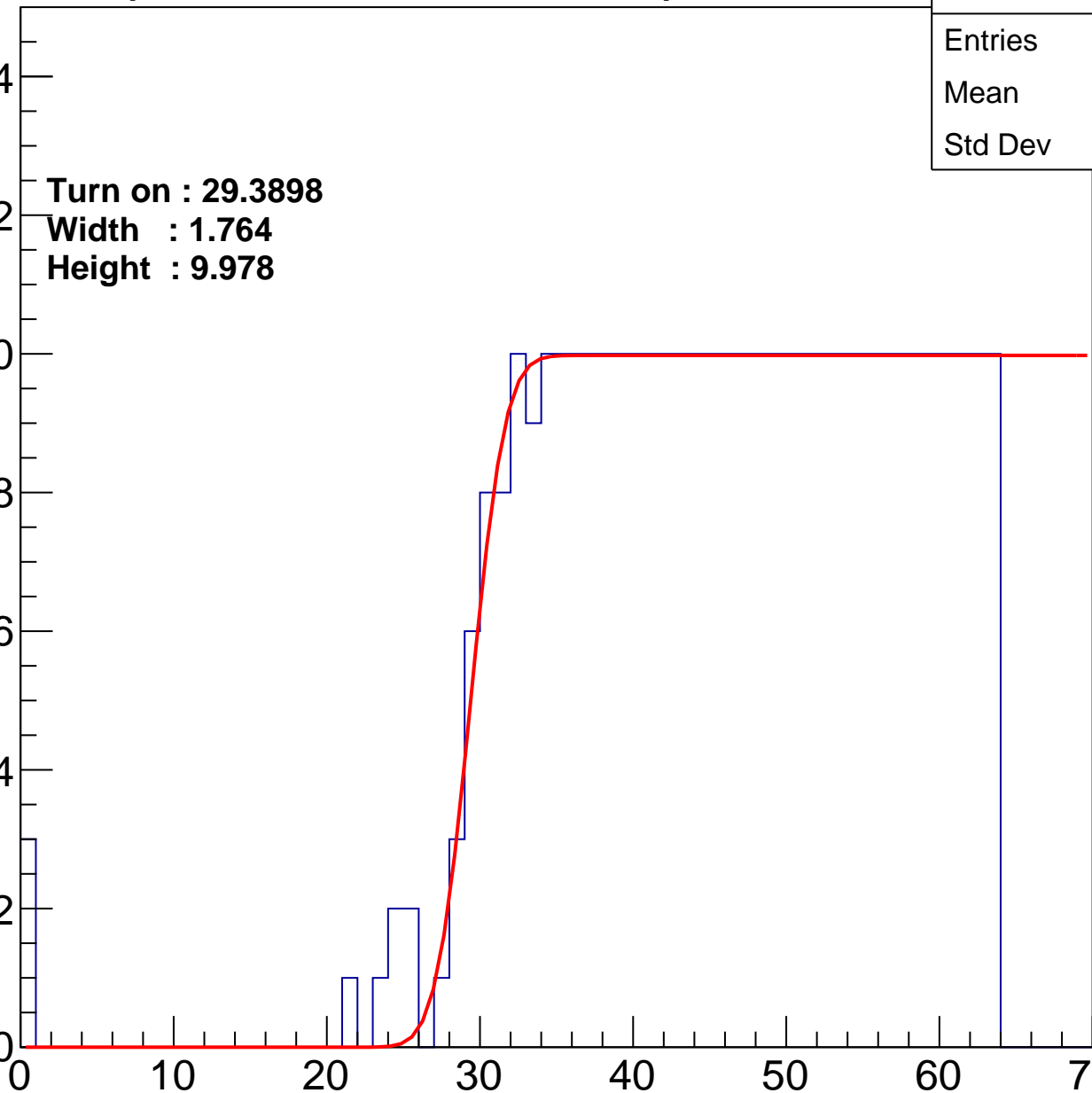
**Width : 1.764**

**Height : 9.978**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch117

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	378
Mean	44.45
Std Dev	11.25

**Turn on : 26.2949**

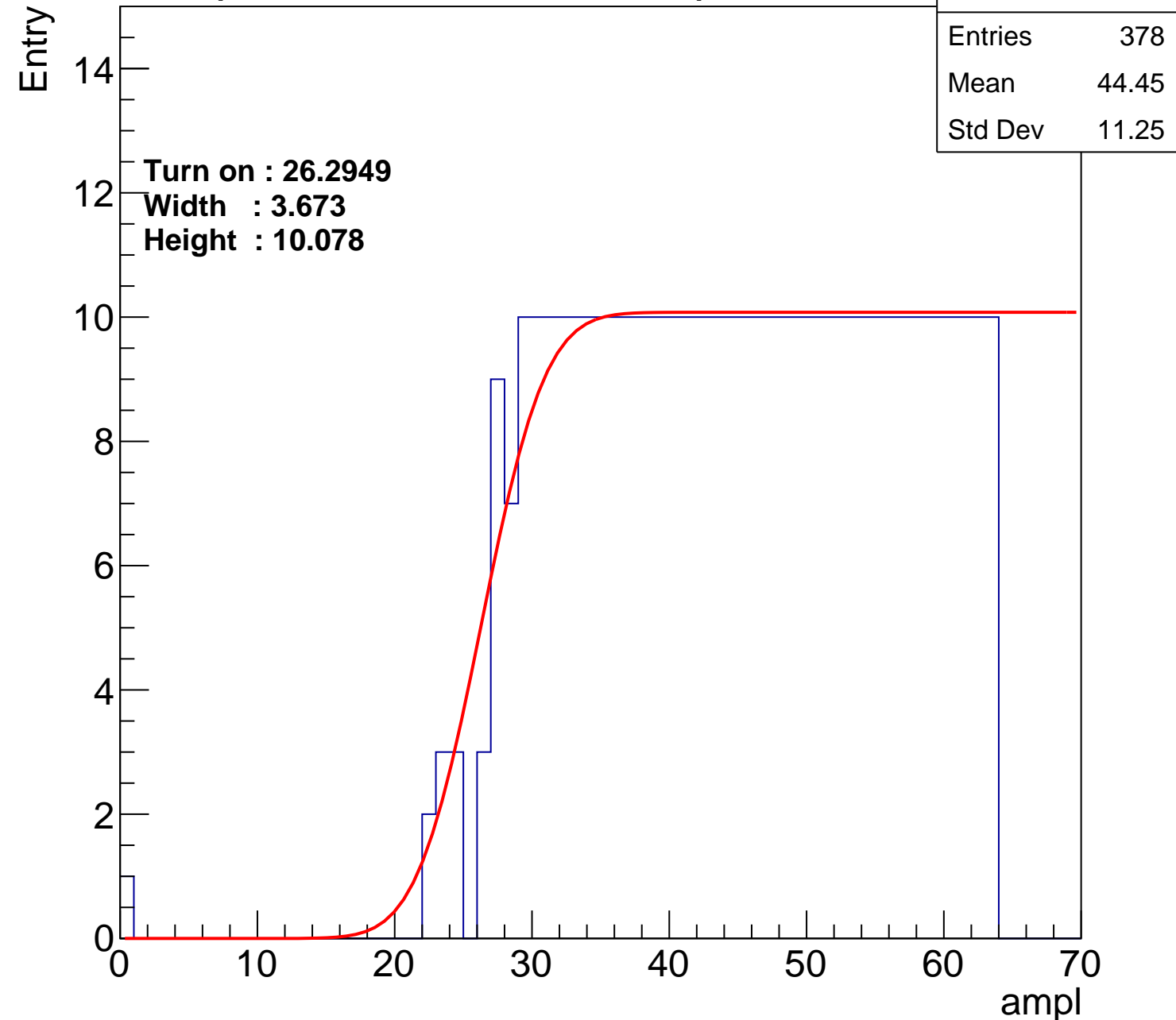
**Width : 3.673**

**Height : 10.078**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch118

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	370
Mean	44.73
Std Dev	11.29

**Turn on : 27.5976**

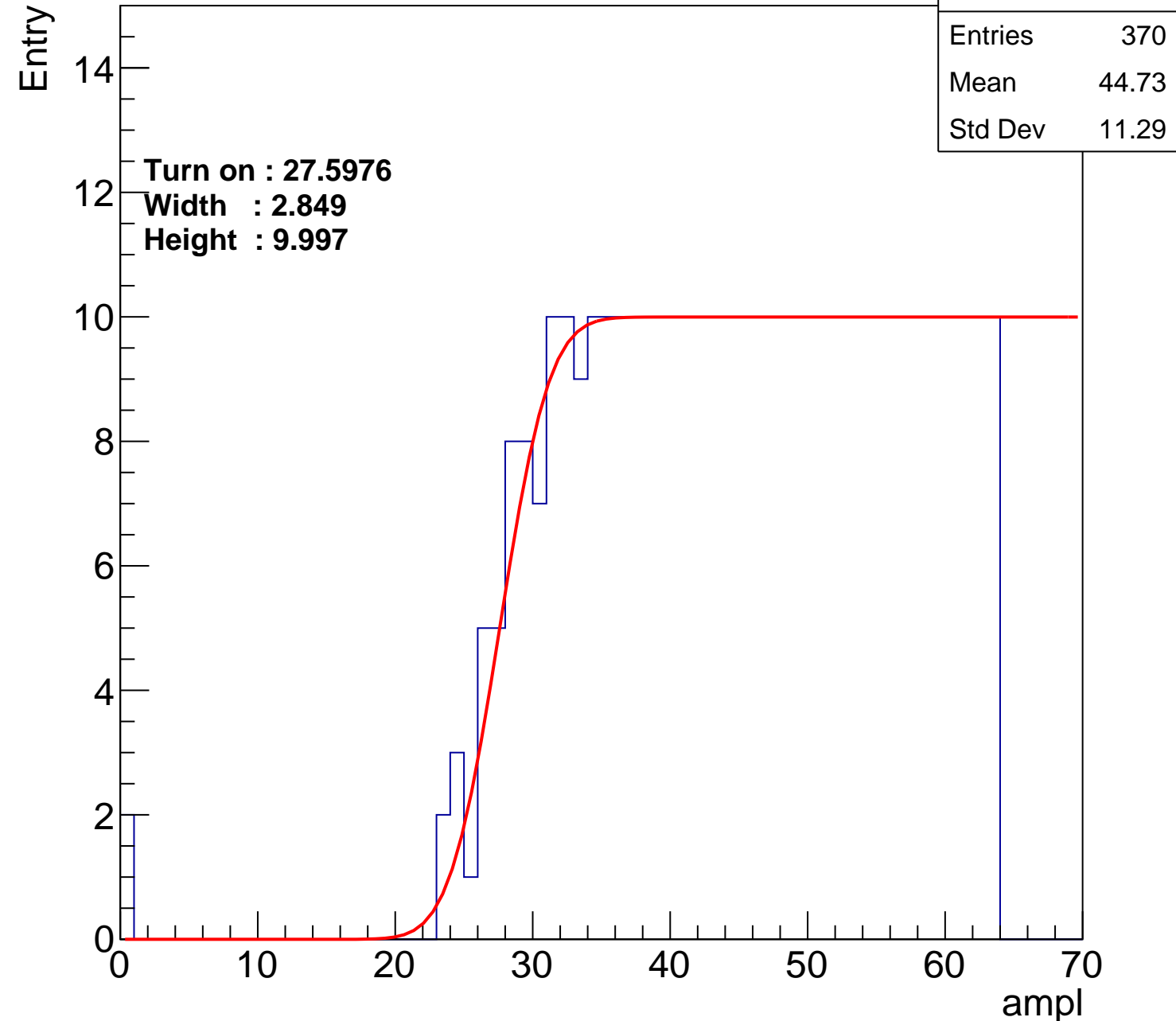
**Width : 2.849**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L000S, U20-ch119

calib\_packv5\_042523\_0143.root, FC#5, port B1

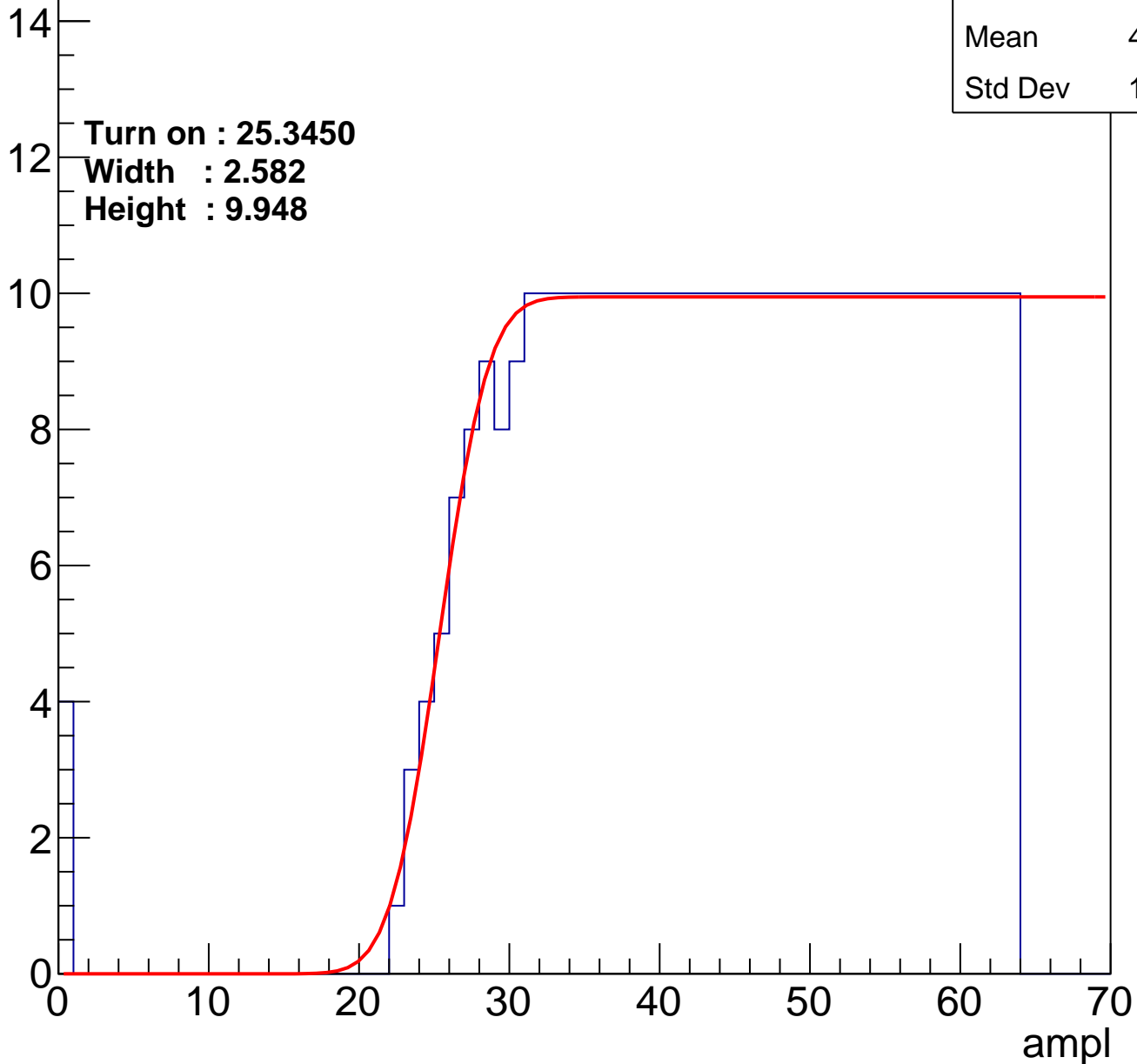
Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.3450

Width : 2.582

Height : 9.948

Entry



# B0L000S, U20-ch120

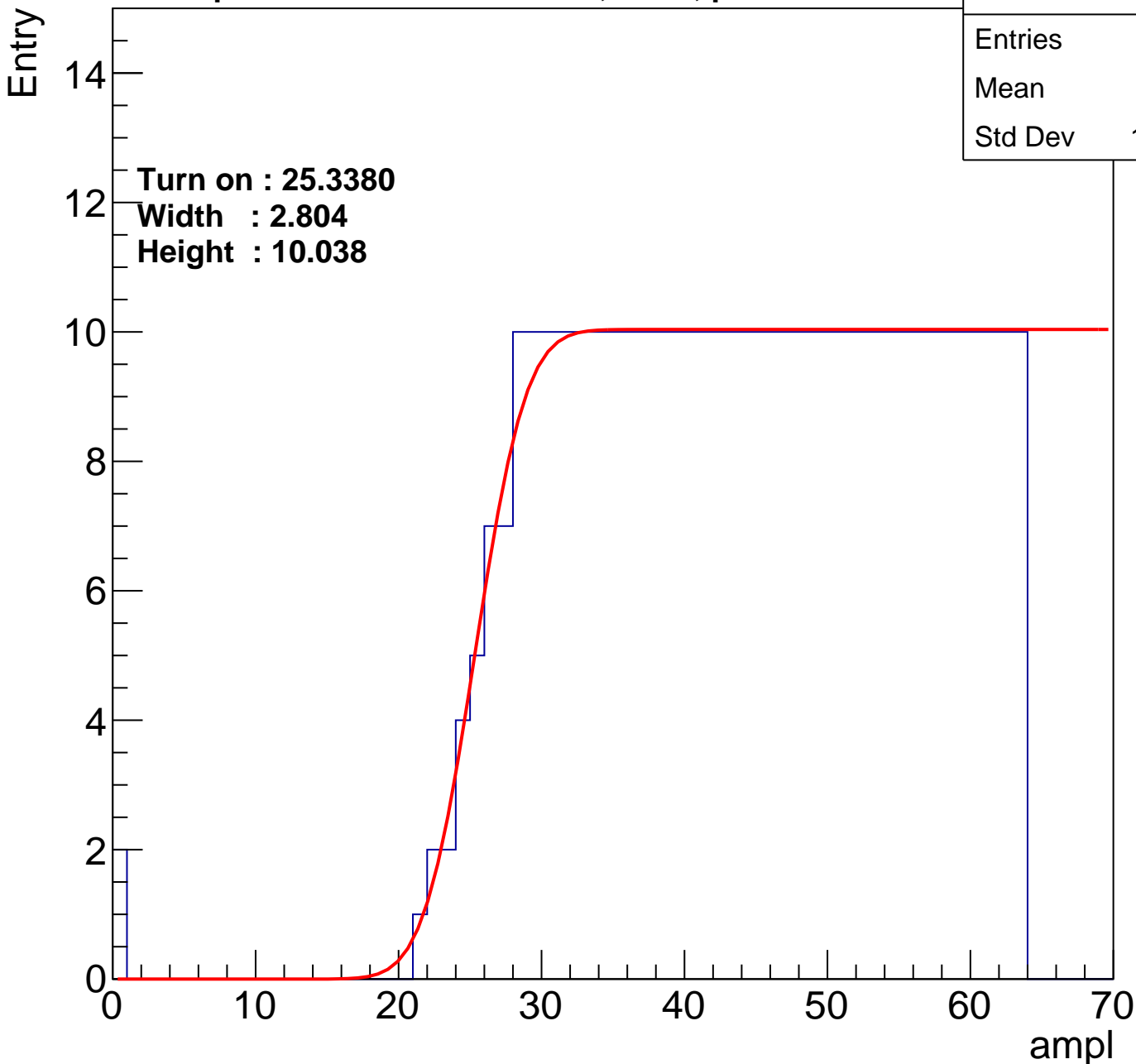
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	390
Mean	43.8
Std Dev	11.72

**Turn on : 25.3380**

**Width : 2.804**

**Height : 10.038**



# B0L000S, U20-ch121

calib\_packv5\_042523\_0143.root, FC#5, port B1

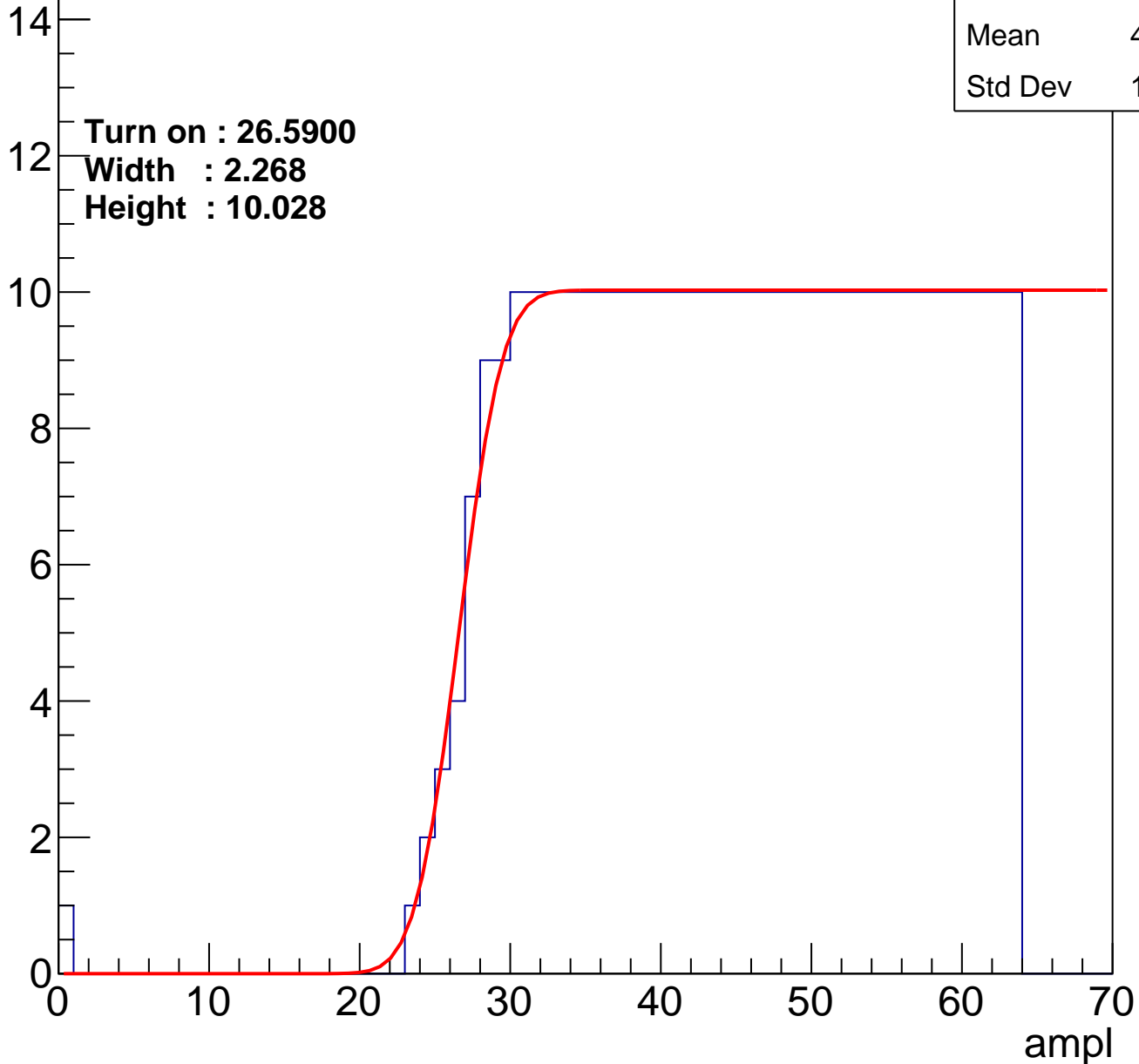
Entries	376
Mean	44.58
Std Dev	11.14

**Turn on : 26.5900**

**Width : 2.268**

**Height : 10.028**

Entry



# B0L000S, U20-ch122

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	372
Mean	44.54
Std Dev	11.65

Turn on : 27.4622

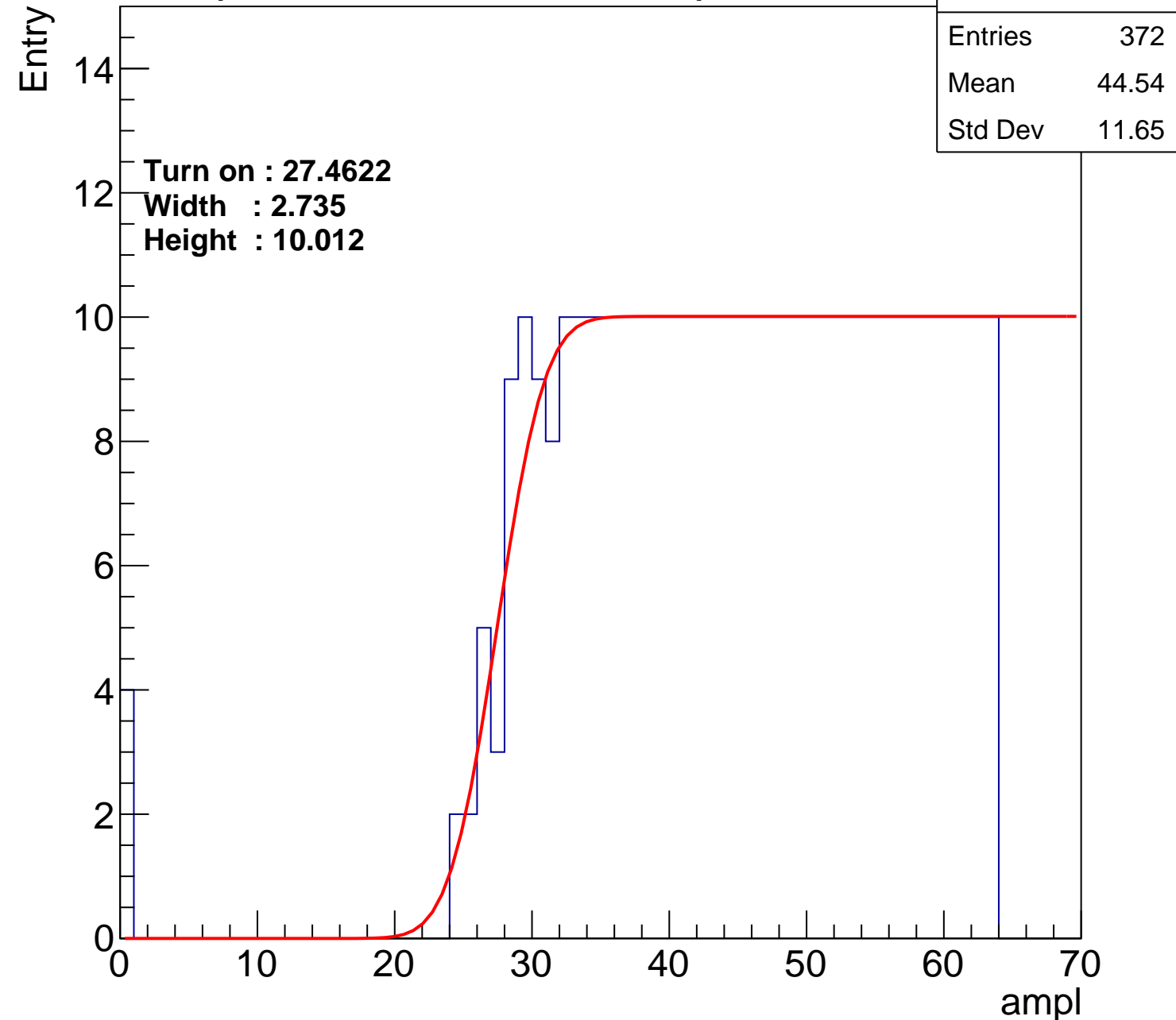
Width : 2.735

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch123

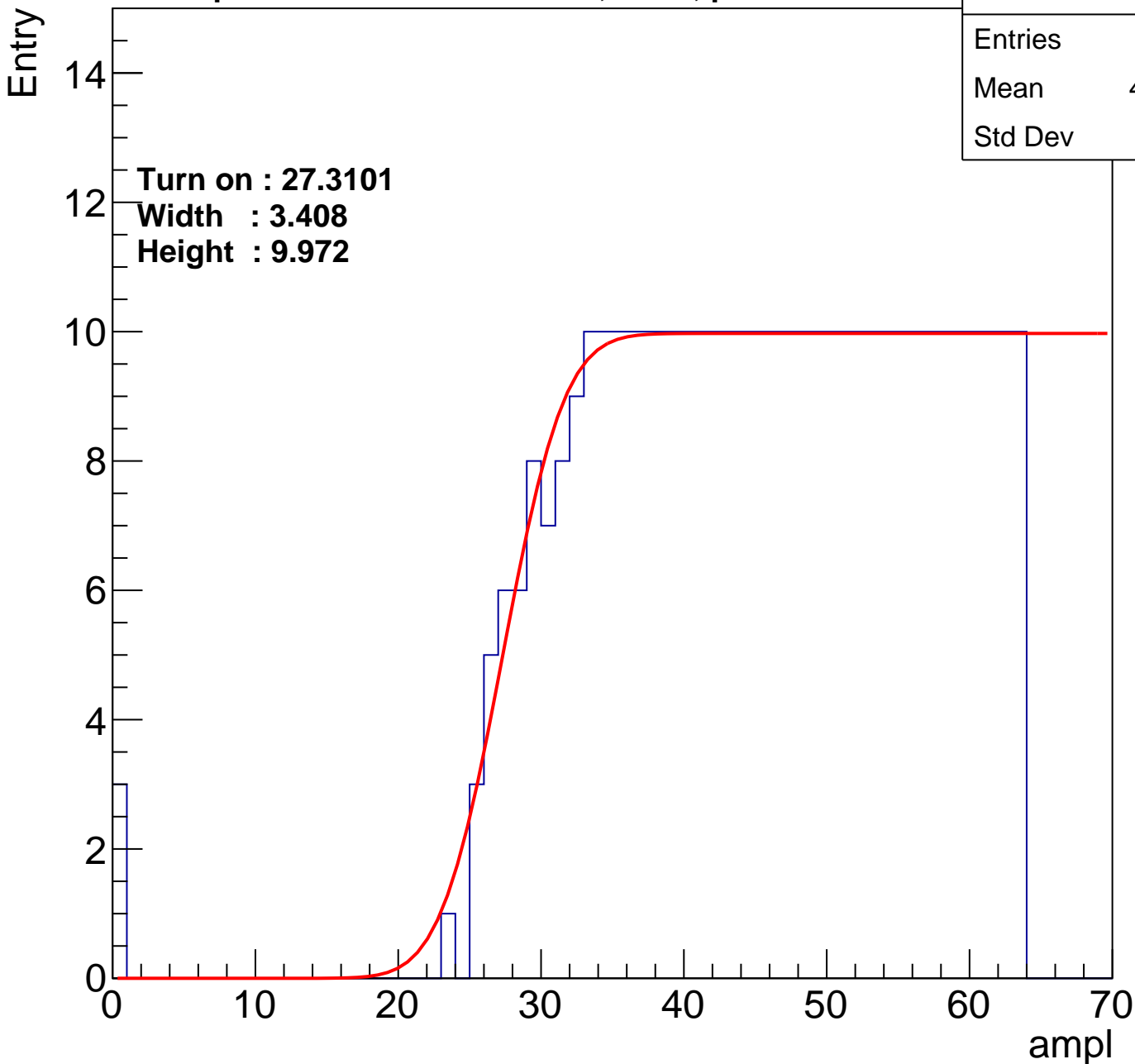
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	366
Mean	44.85
Std Dev	11.4

**Turn on : 27.3101**

**Width : 3.408**

**Height : 9.972**



# B0L000S, U20-ch124

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	384
Mean	44.05
Std Dev	11.65

Turn on : 26.1224

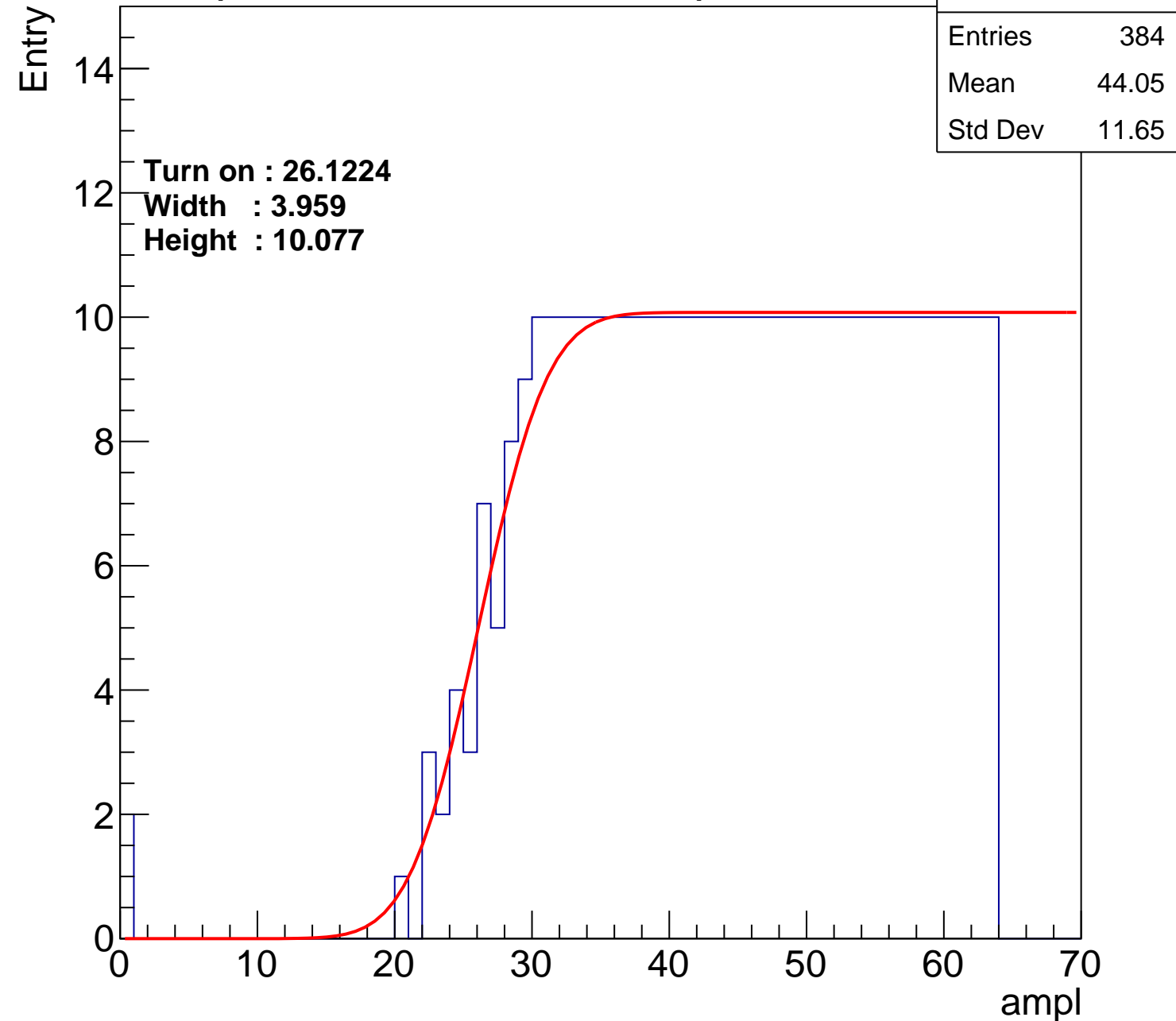
Width : 3.959

Height : 10.077

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L000S, U20-ch125

calib\_packv5\_042523\_0143.root, FC#5, port B1

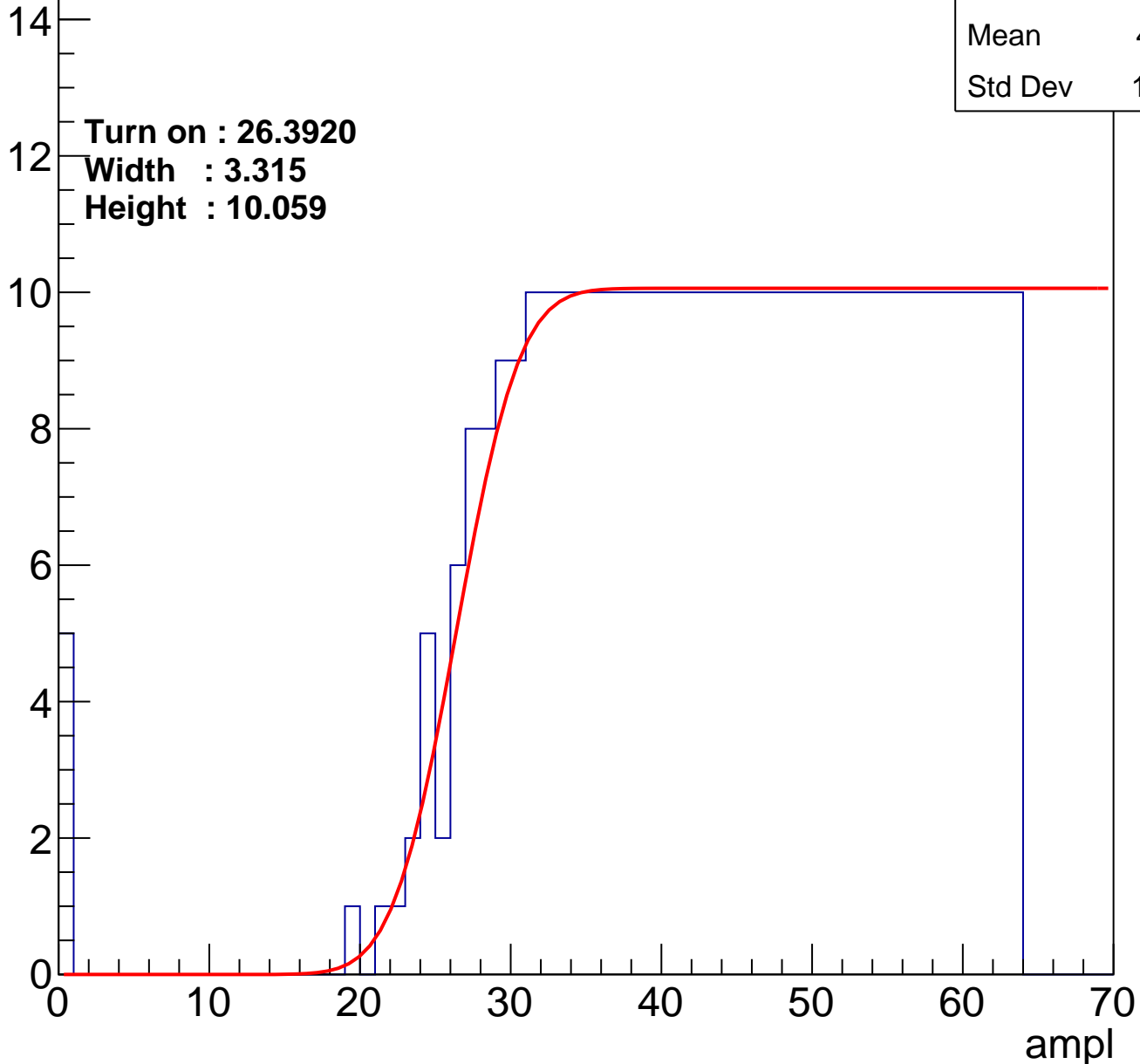
Entries	387
Mean	43.71
Std Dev	12.23

**Turn on : 26.3920**

**Width : 3.315**

**Height : 10.059**

Entry



# B0L000S, U20-ch126

calib\_packv5\_042523\_0143.root, FC#5, port B1

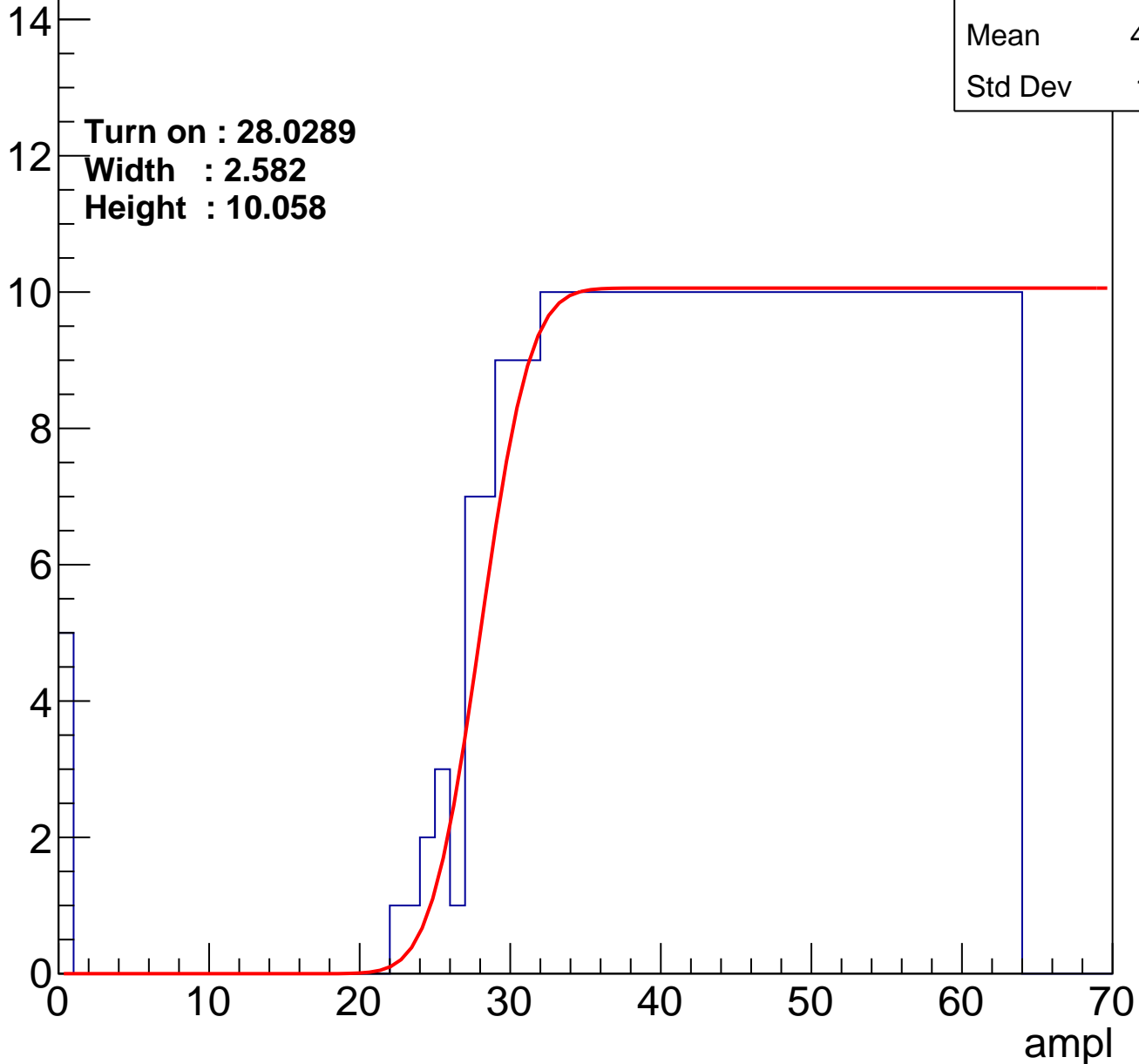
Entries	374
Mean	44.36
Std Dev	11.91

**Turn on : 28.0289**

**Width : 2.582**

**Height : 10.058**

Entry





# BOL000S, U20-ch127

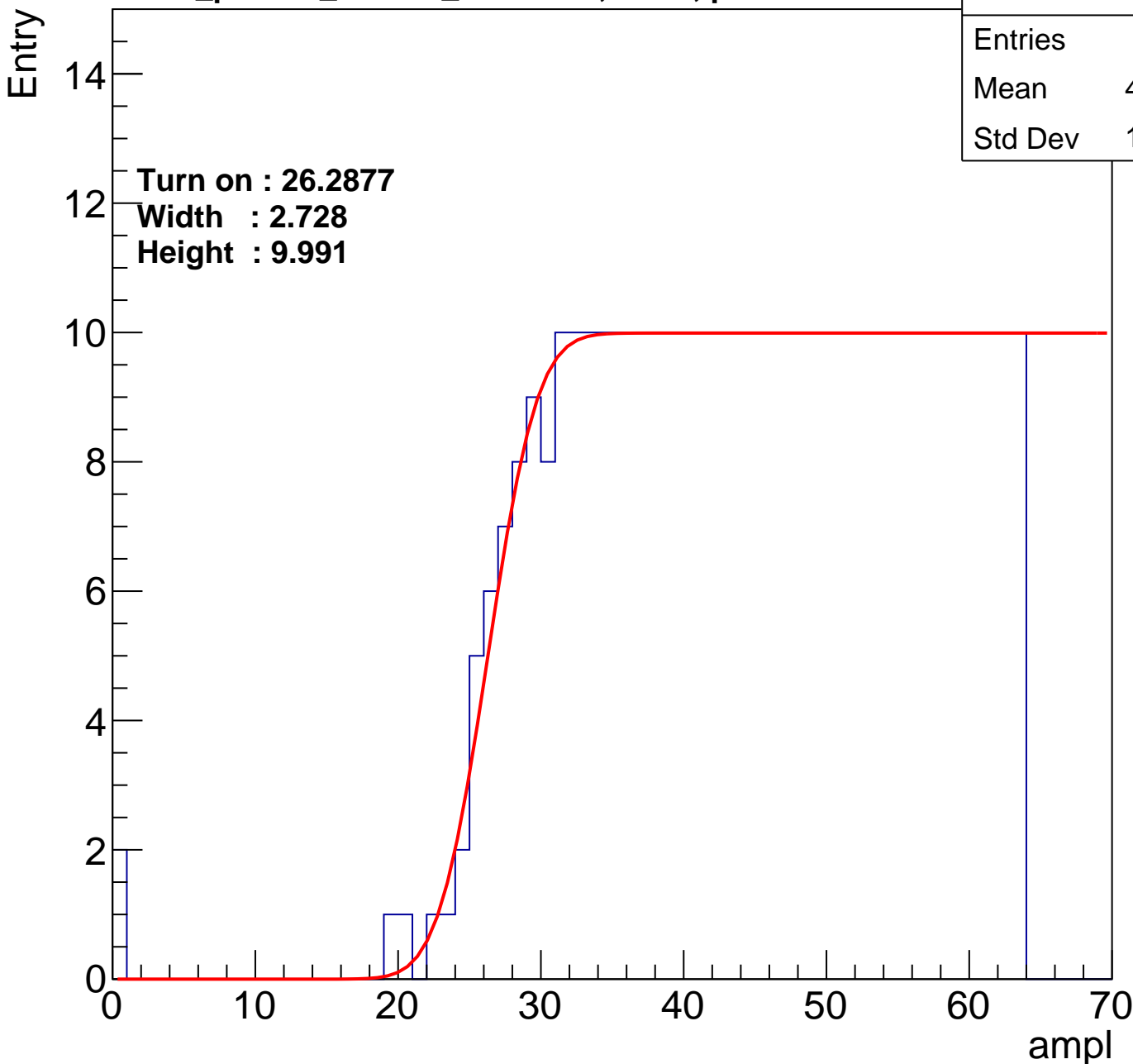
**calib\_packv5\_042523\_0143.root, FC#5, port B1**

Entries	381
Mean	44.19
Std Dev	11.58

**Turn on : 26.2877**

**Width : 2.728**

**Height : 9.991**



# B0L000S, U20-ch127

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	381
Mean	44.19
Std Dev	11.58

**Turn on : 26.2877**

**Width : 2.728**

**Height : 9.991**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

