

B1L002S, U16-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.31
Std Dev	11.87

Turn on : 24.5179

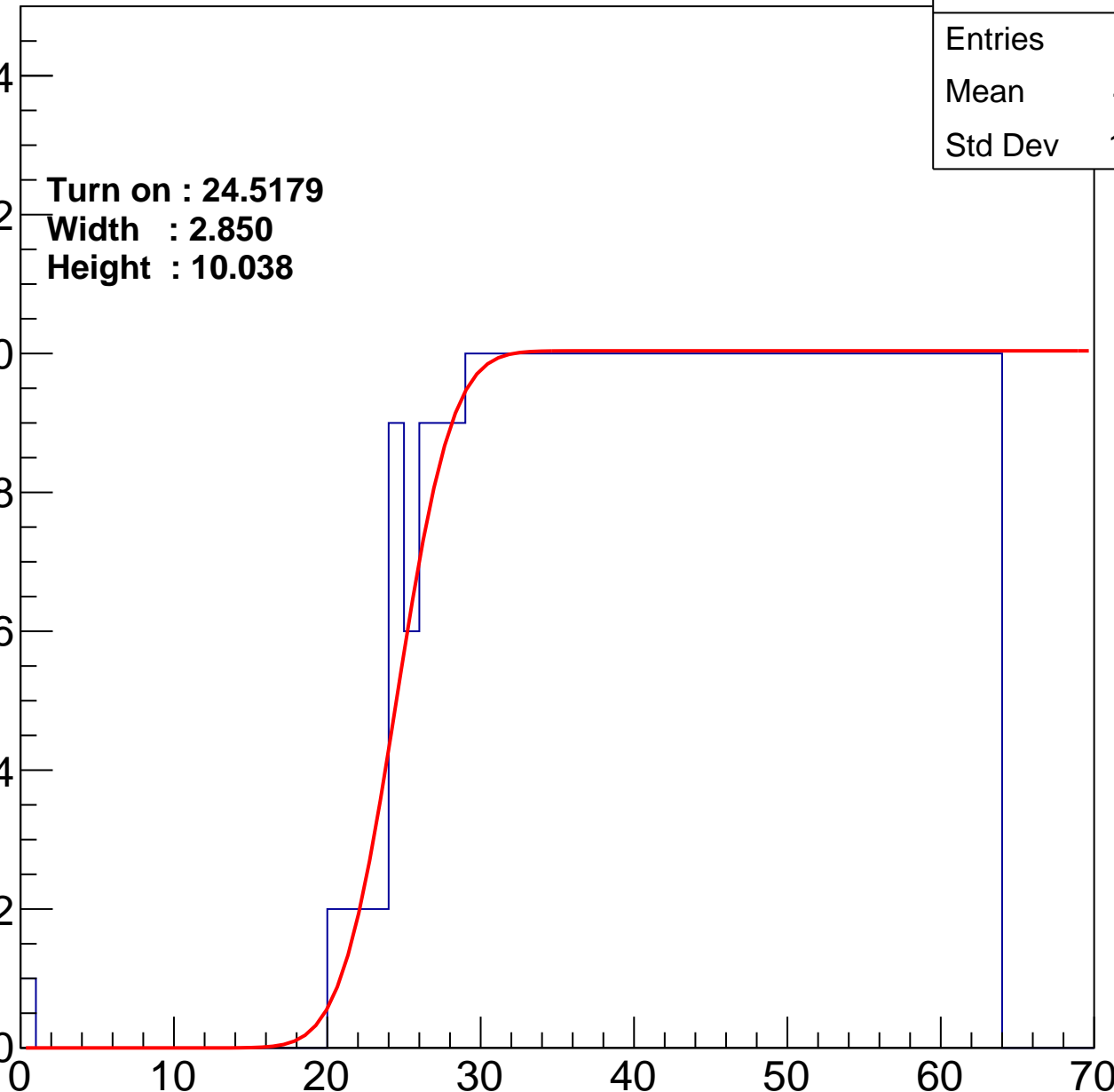
Width : 2.850

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.74
Std Dev	11.78

Turn on : 25.2549

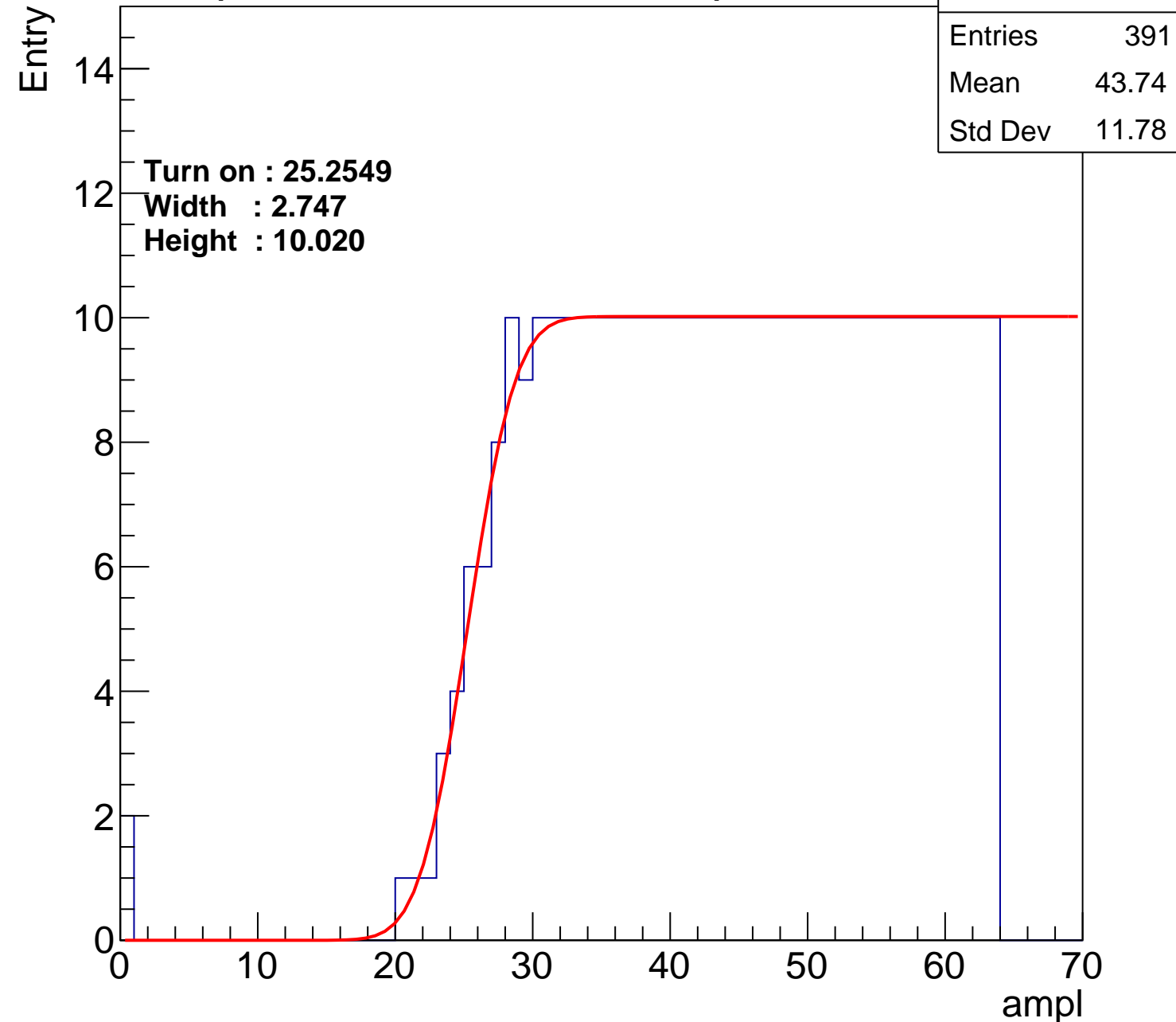
Width : 2.747

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch2

calib_packv5_042523_0143.root, FC#10, port B3

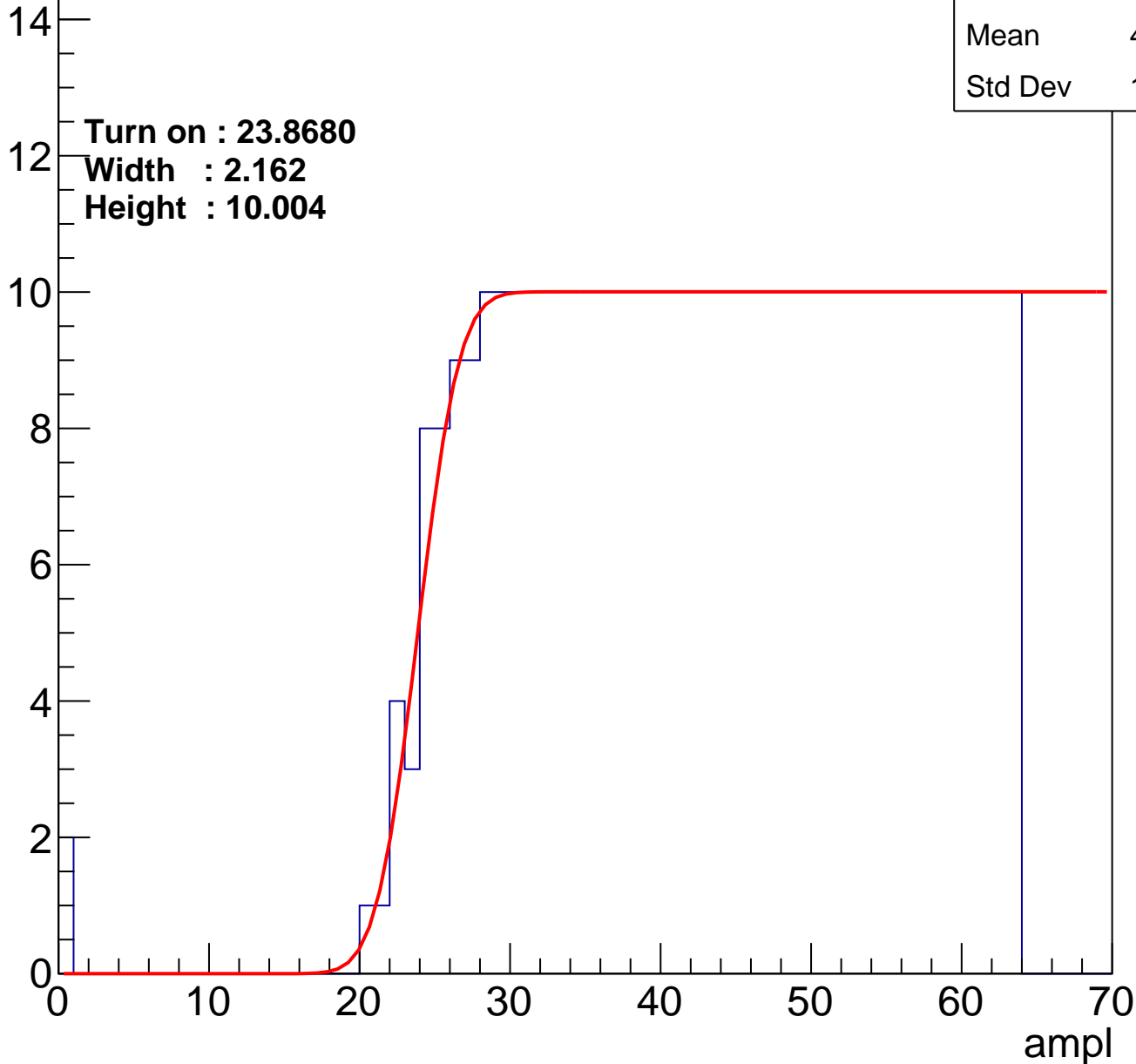
Entry

Entries	405
Mean	43.08
Std Dev	12.09

Turn on : 23.8680

Width : 2.162

Height : 10.004



B1L002S, U16-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.51
Std Dev	11.87

Turn on : 24.5256

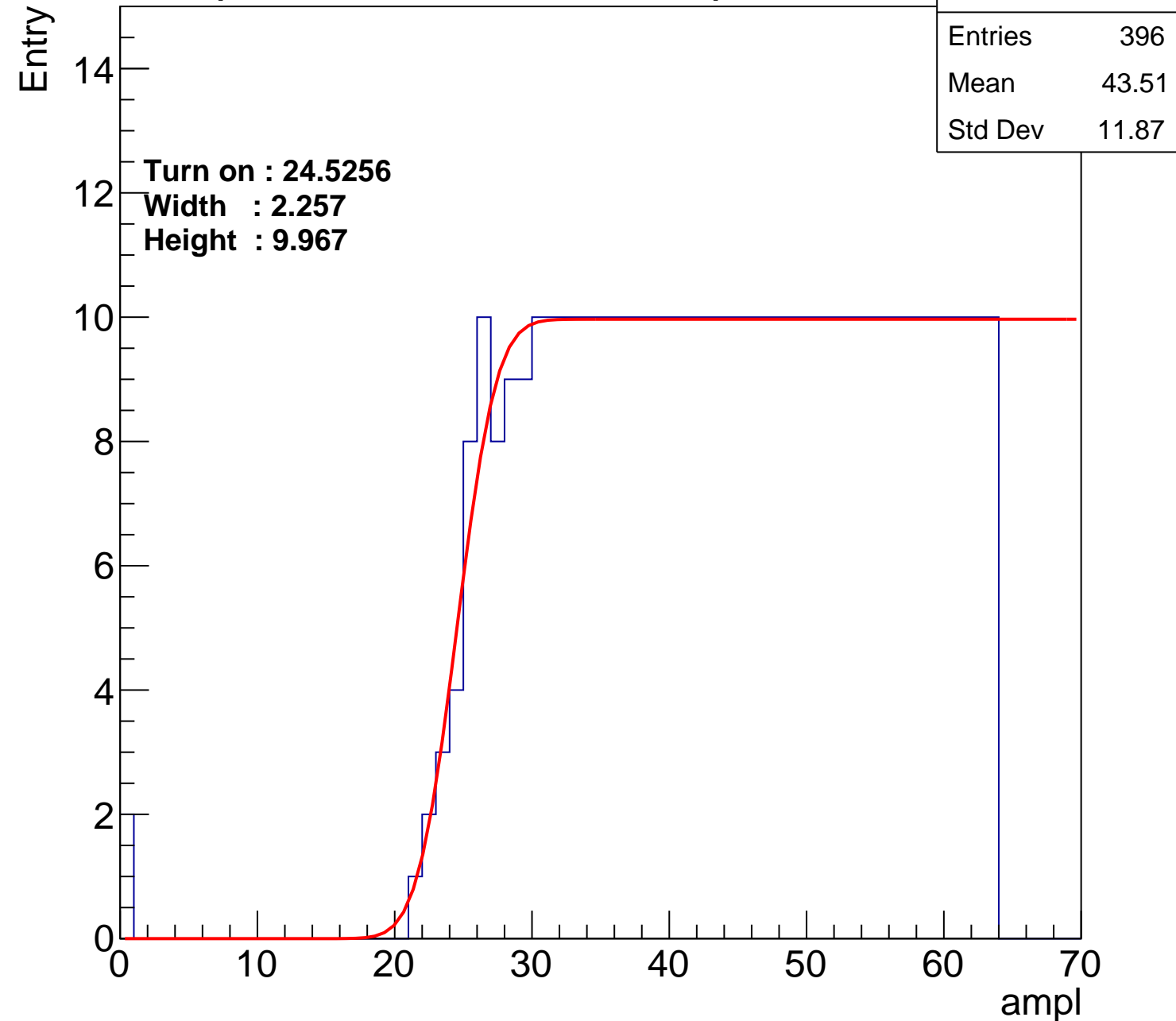
Width : 2.257

Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.32
Std Dev	11.29

Turn on : 26.3896

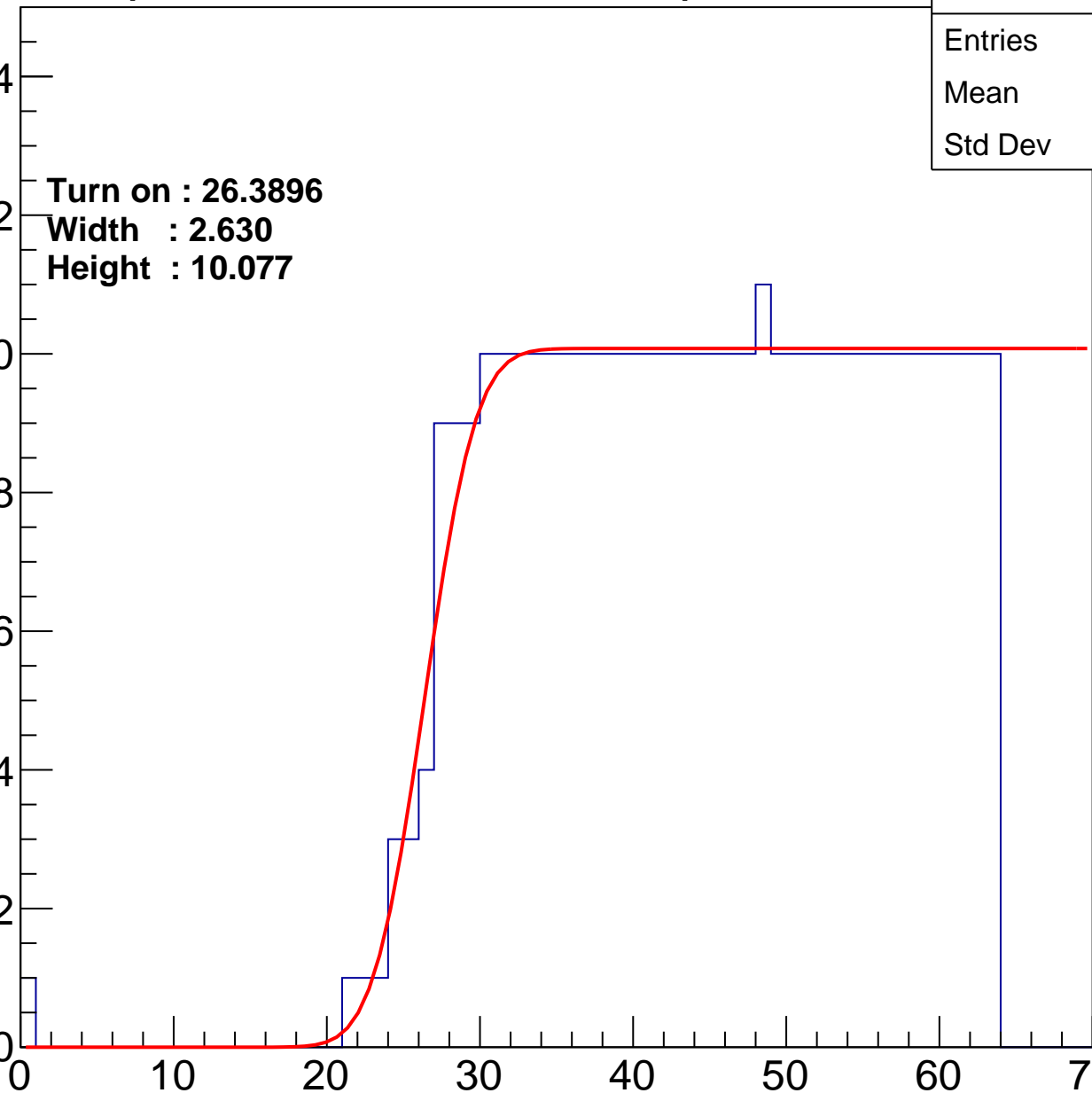
Width : 2.630

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.94
Std Dev	11.53

Turn on : 25.2593

Width : 2.534

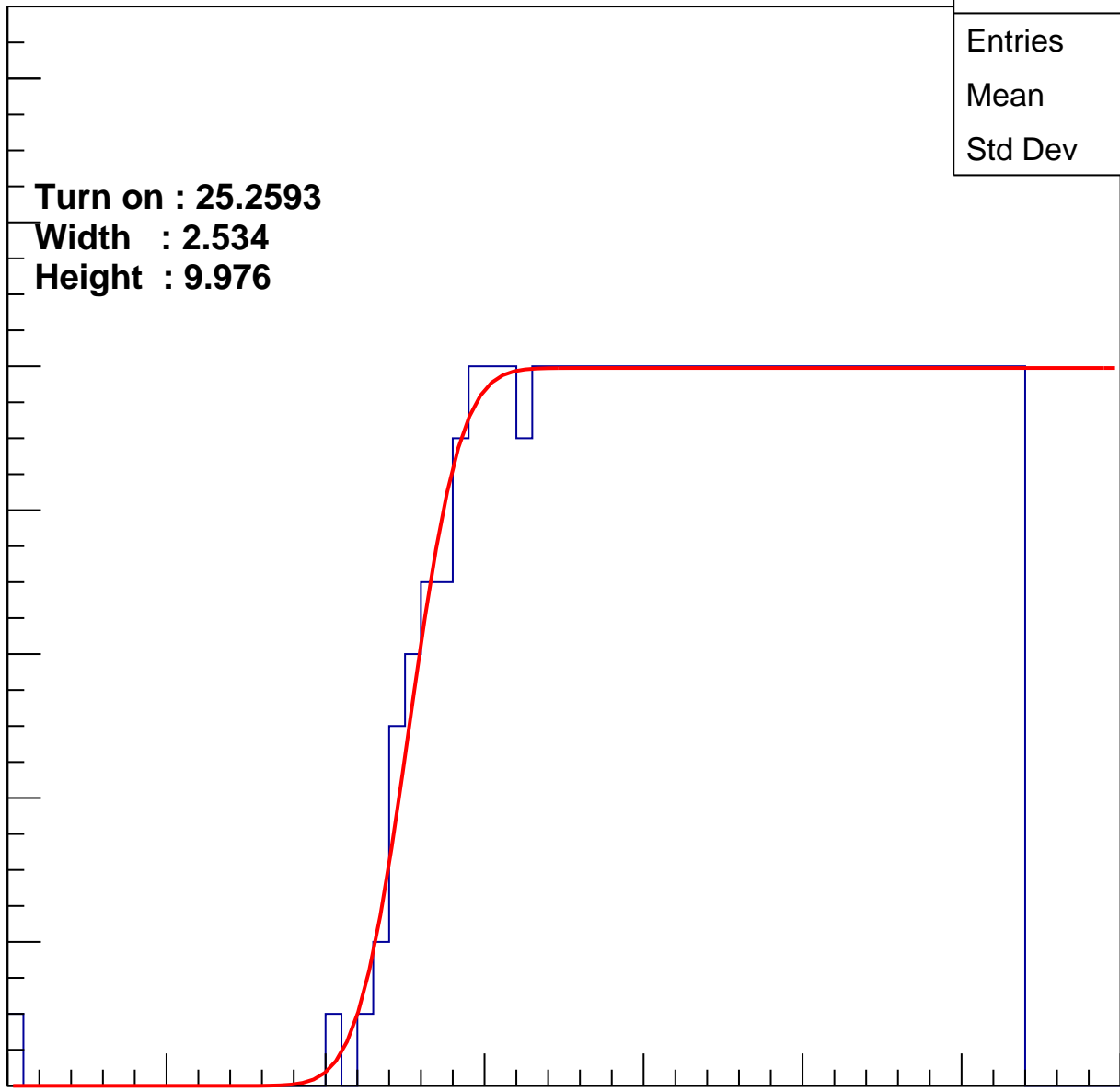
Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U16-ch6

calib_packv5_042523_0143.root, FC#10, port B3

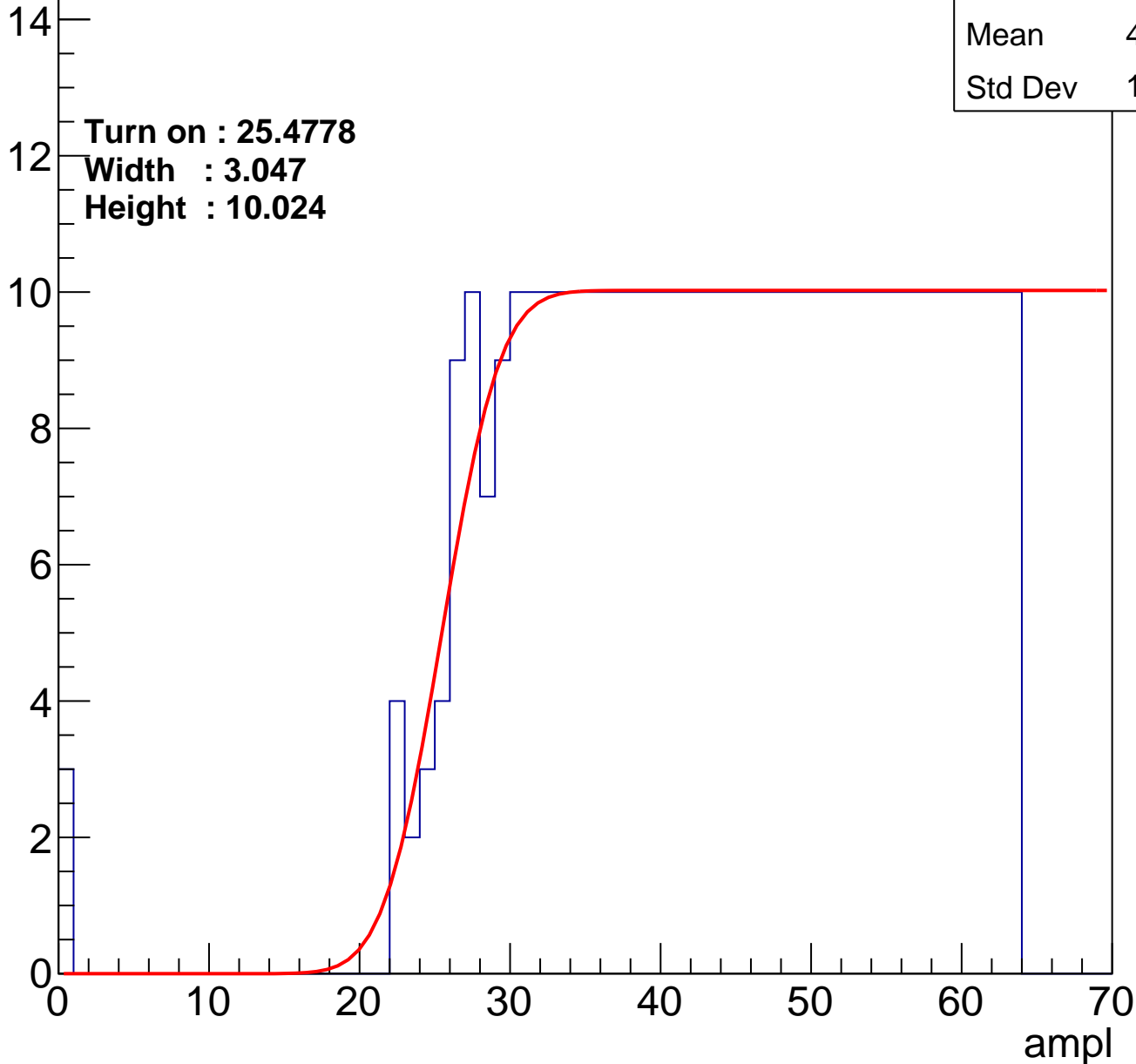
Entry

Entries	391
Mean	43.68
Std Dev	11.94

Turn on : 25.4778

Width : 3.047

Height : 10.024



B1L002S, U16-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.47
Std Dev	11.53

Turn on : 26.9182

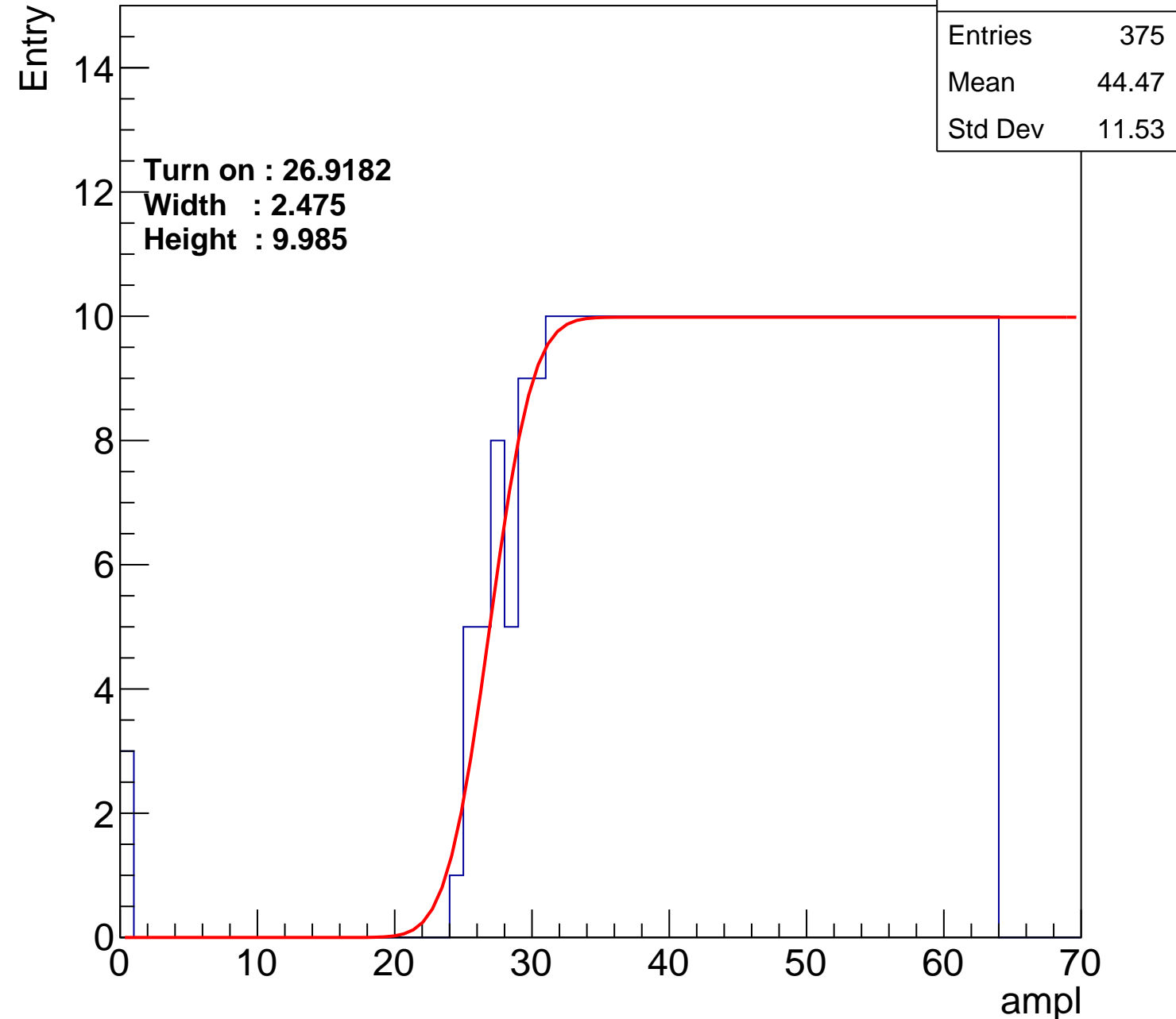
Width : 2.475

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch8

calib_packv5_042523_0143.root, FC#10, port B3

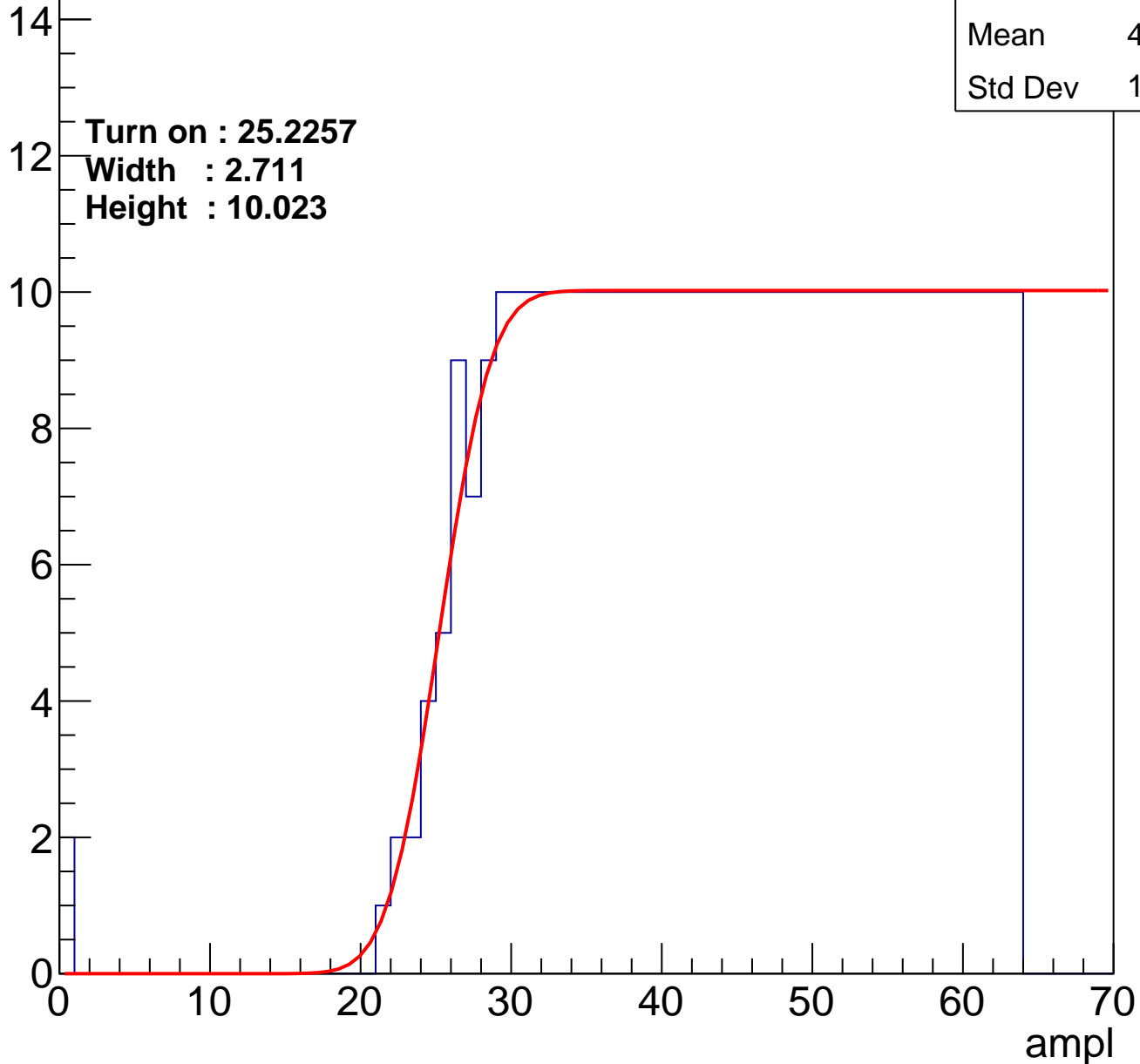
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.2257

Width : 2.711

Height : 10.023

Entry



B1L002S, U16-ch9

calib_packv5_042523_0143.root, FC#10, port B3

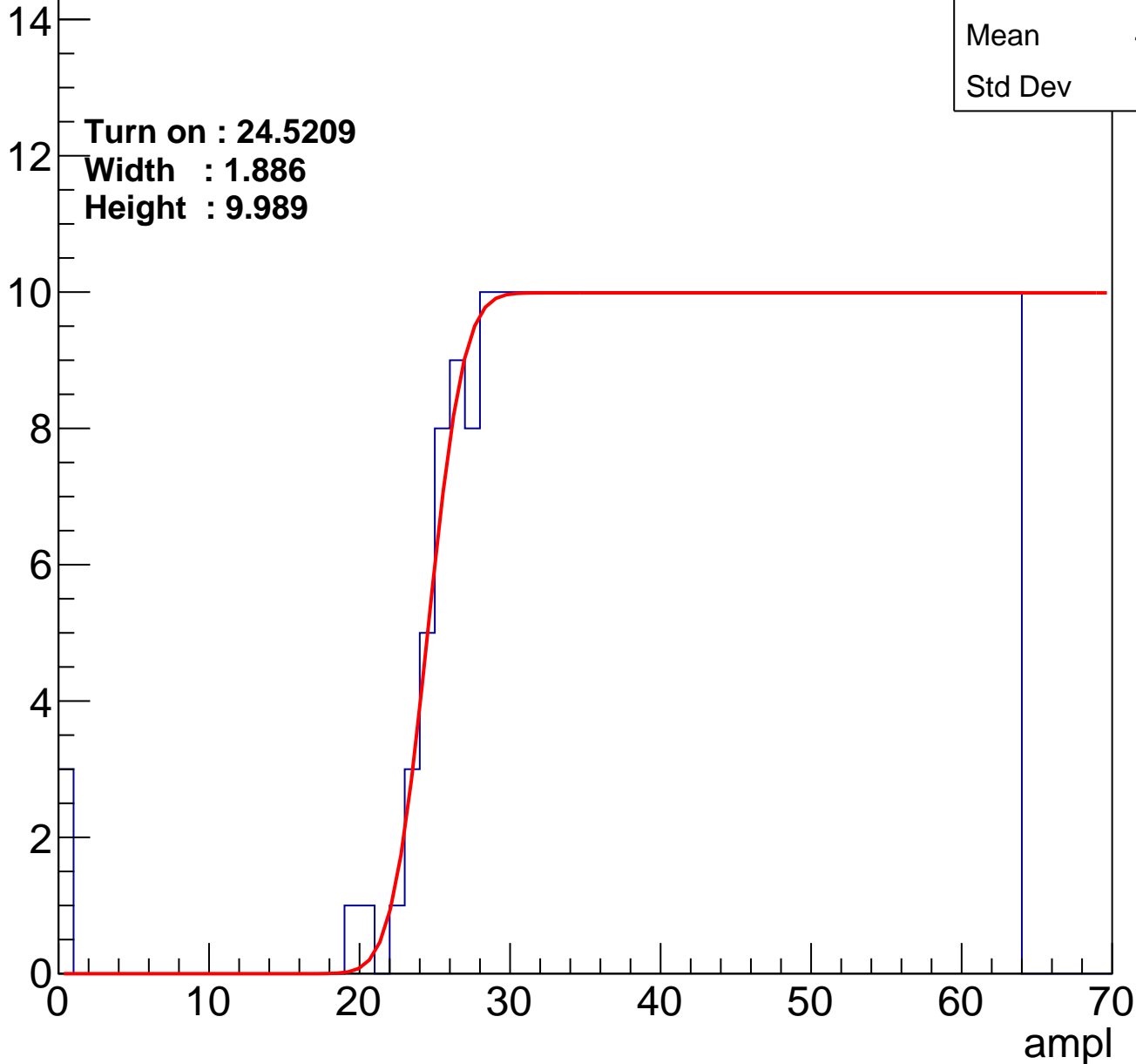
Entry

Entries	399
Mean	43.31
Std Dev	12.1

Turn on : 24.5209

Width : 1.886

Height : 9.989



B1L002S, U16-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	413
Mean	42.61
Std Dev	12.46

Turn on : 23.0172

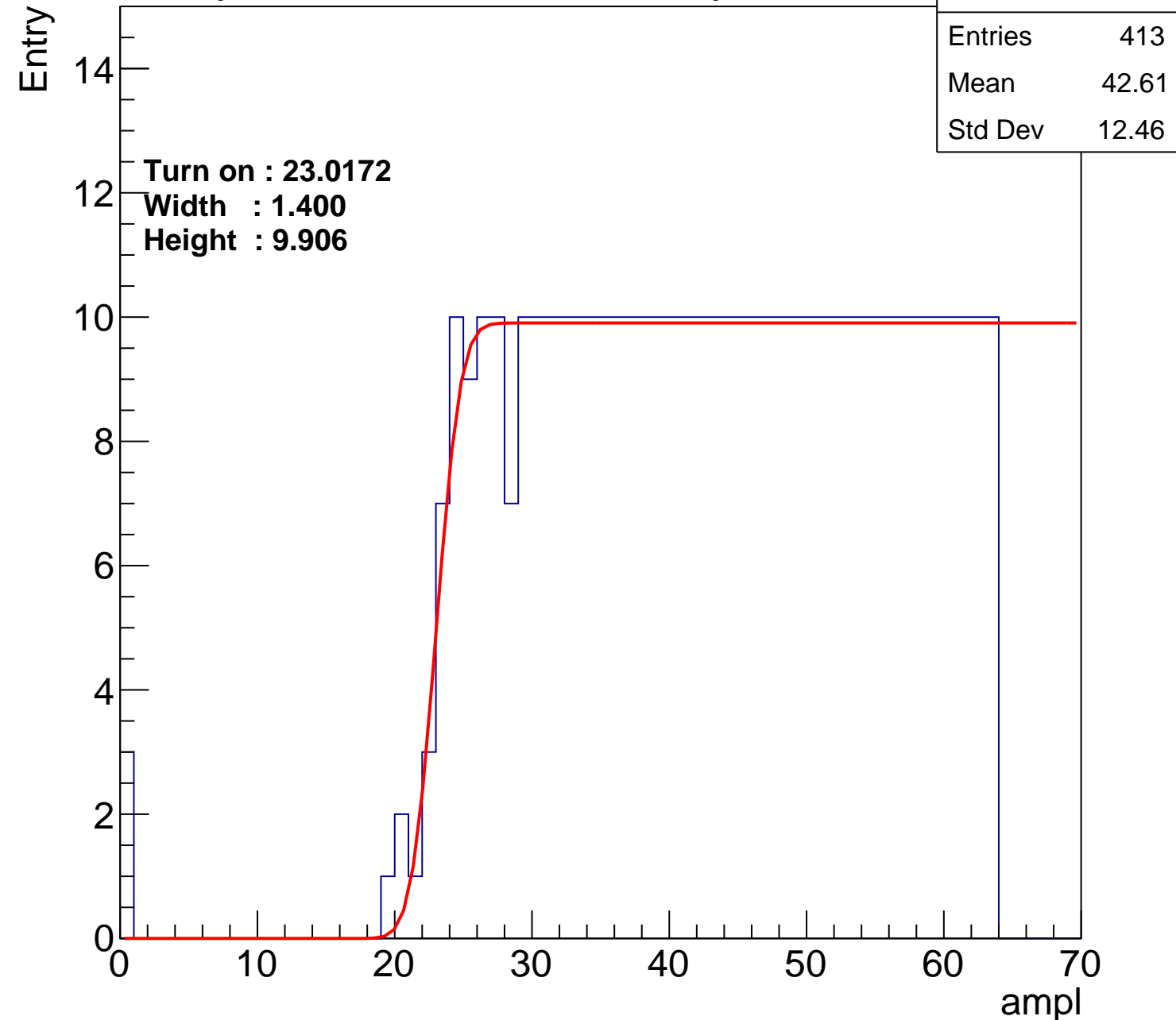
Width : 1.400

Height : 9.906

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch11

calib_packv5_042523_0143.root, FC#10, port B3

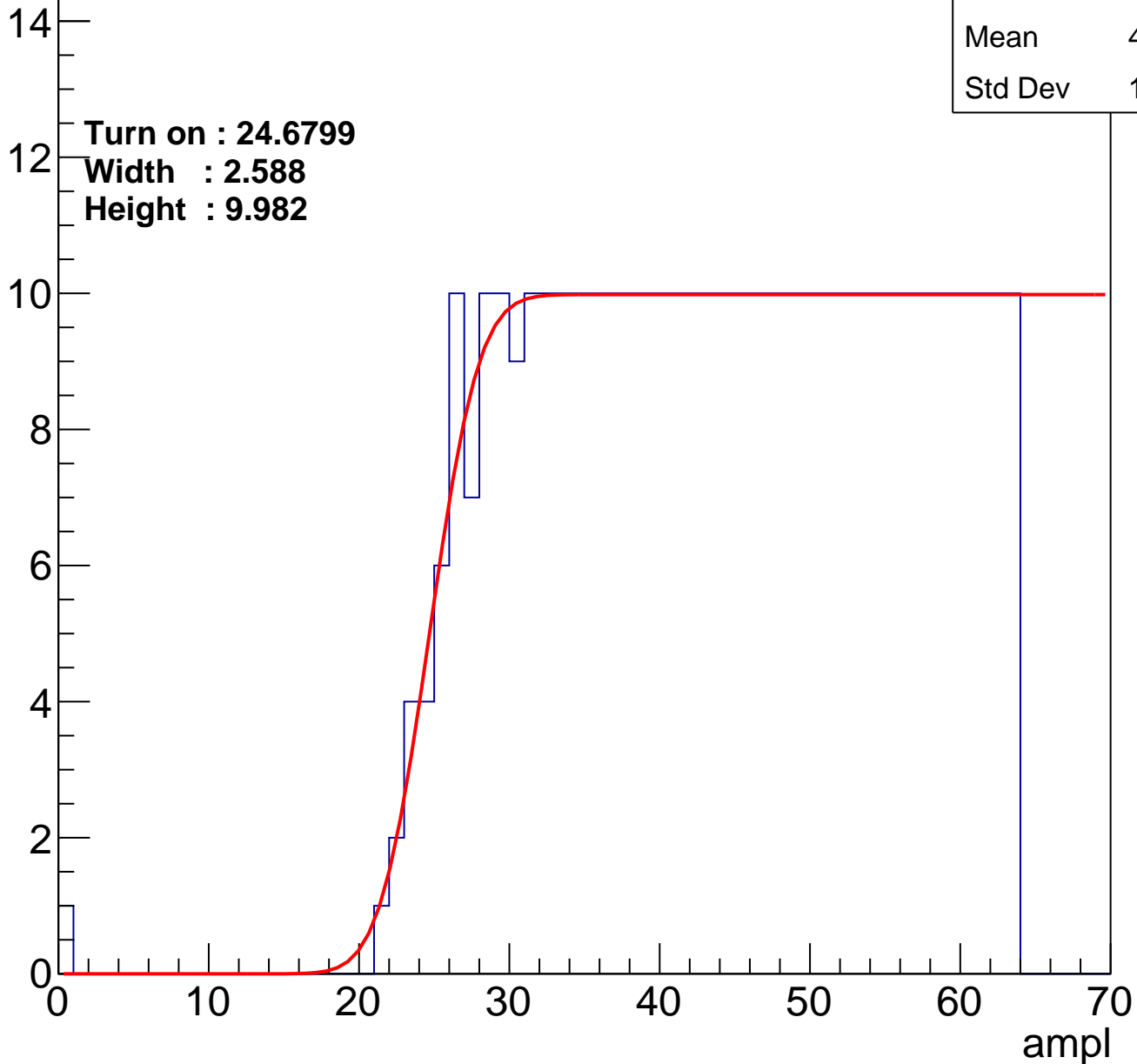
Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.6799

Width : 2.588

Height : 9.982

Entry



B1L002S, U16-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.13
Std Dev	11.95

Turn on : 23.5779

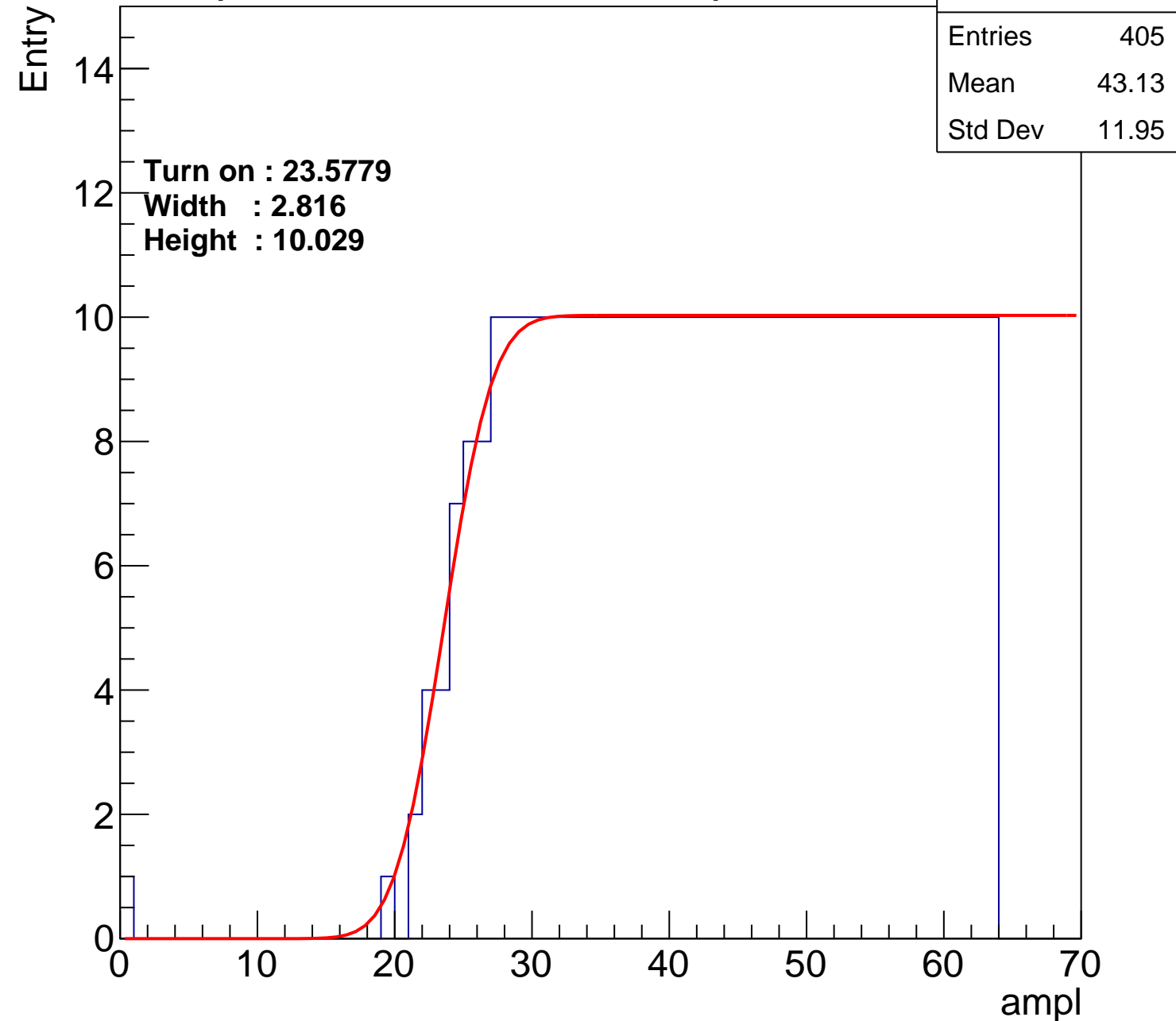
Width : 2.816

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.06
Std Dev	11.87

Turn on : 26.2482

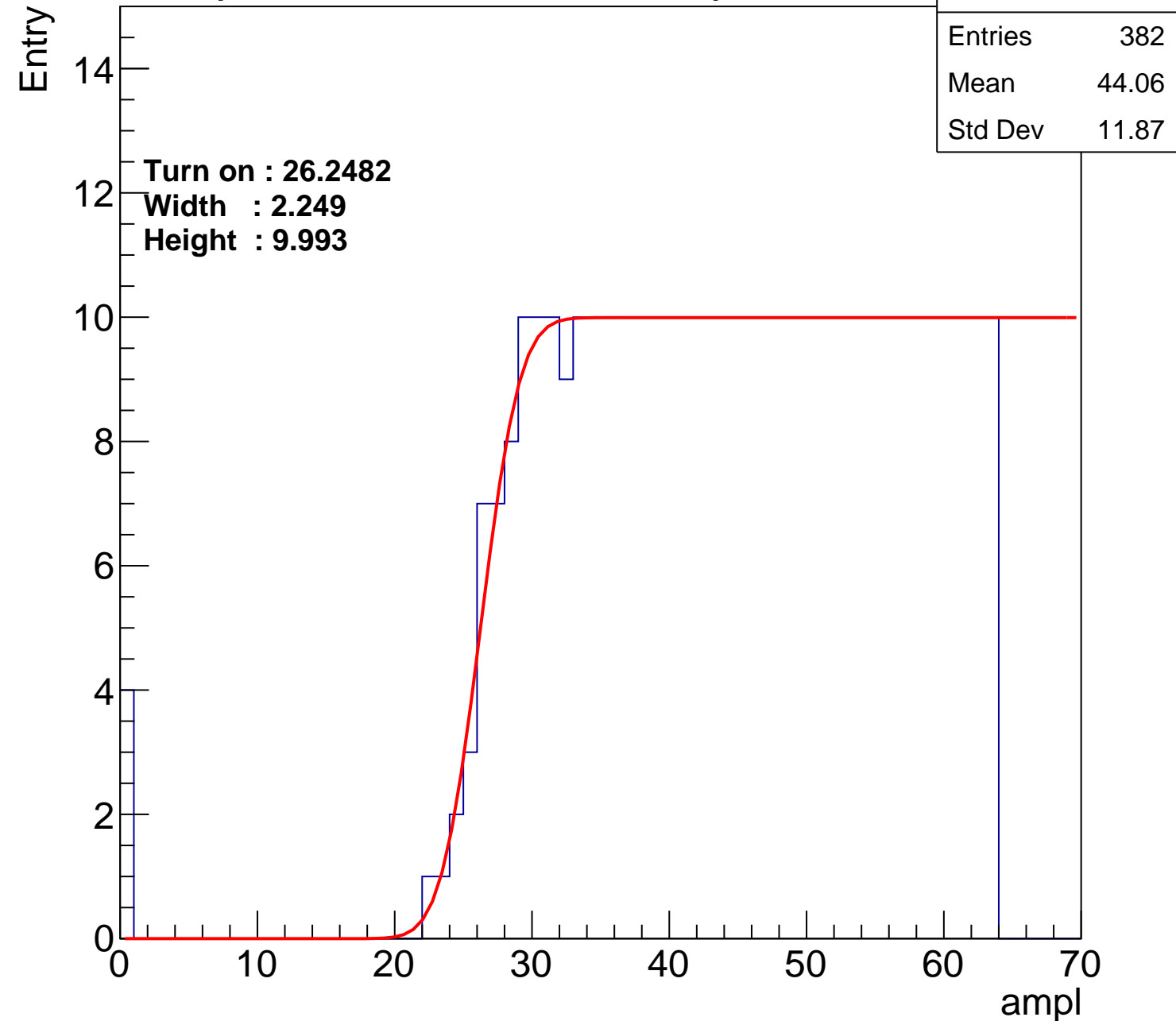
Width : 2.249

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.62
Std Dev	12.21

Turn on : 26.1650

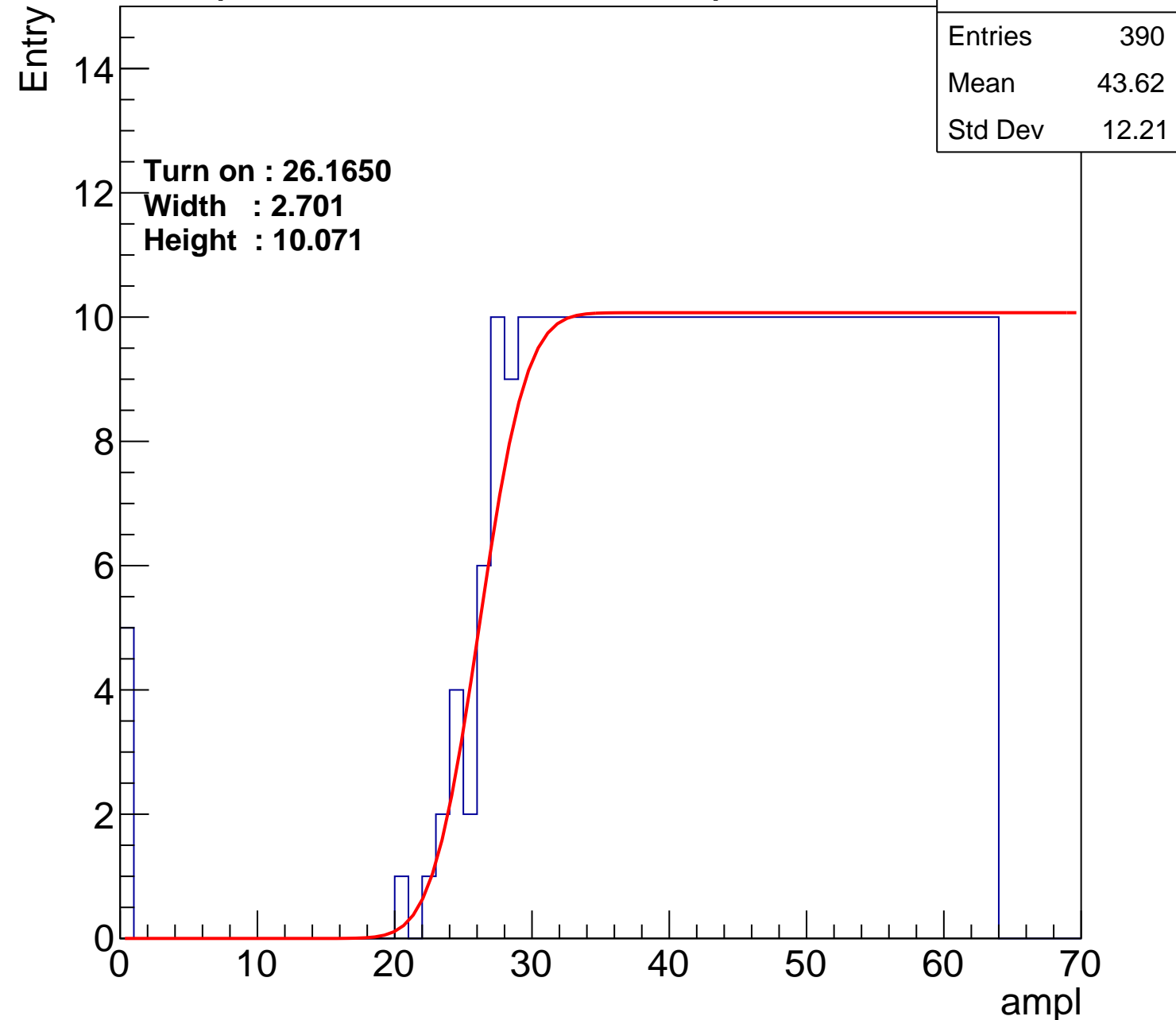
Width : 2.701

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.02
Std Dev	12.29

Turn on : 24.1042

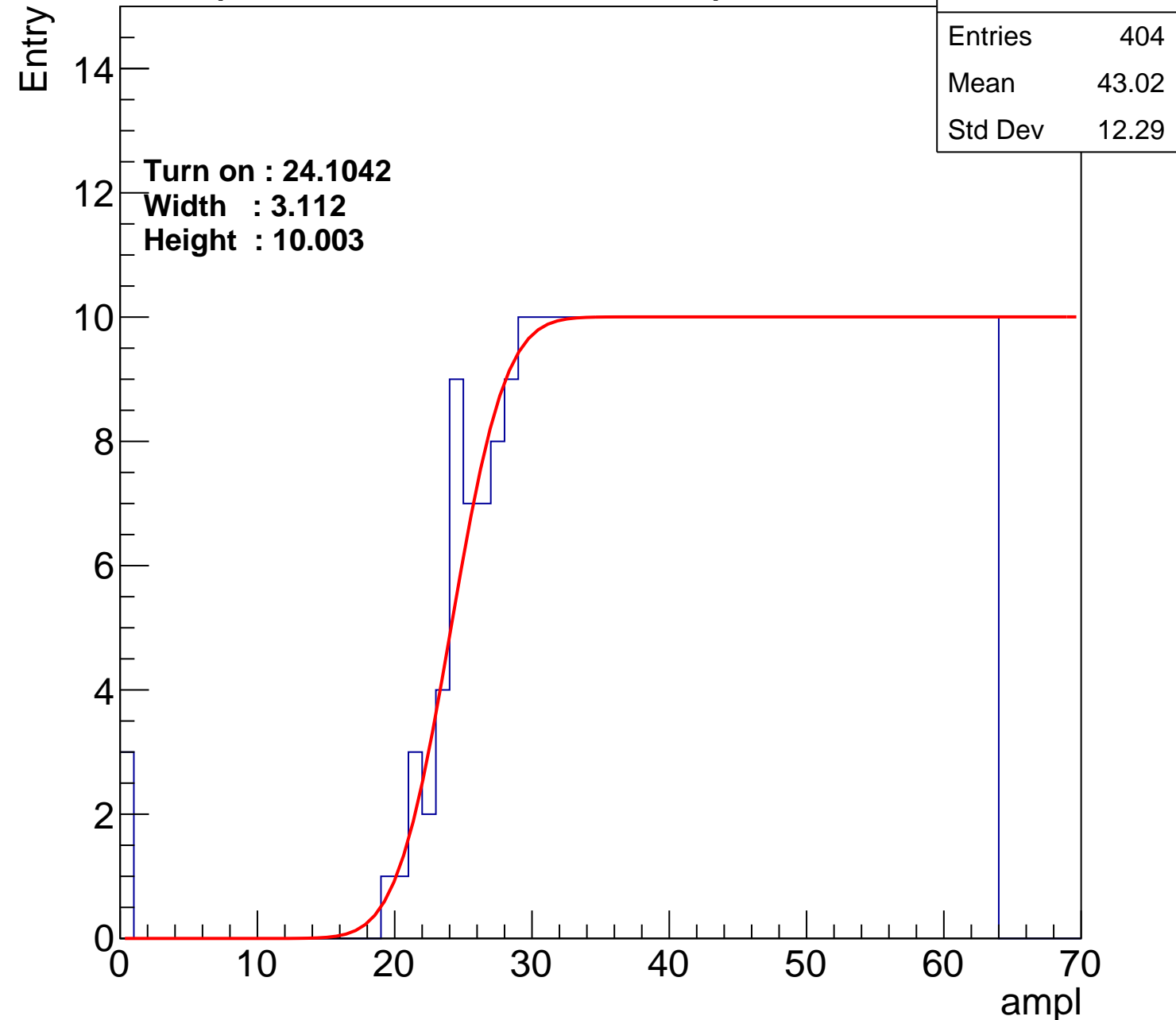
Width : 3.112

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.16
Std Dev	11.52

Turn on : 25.9151

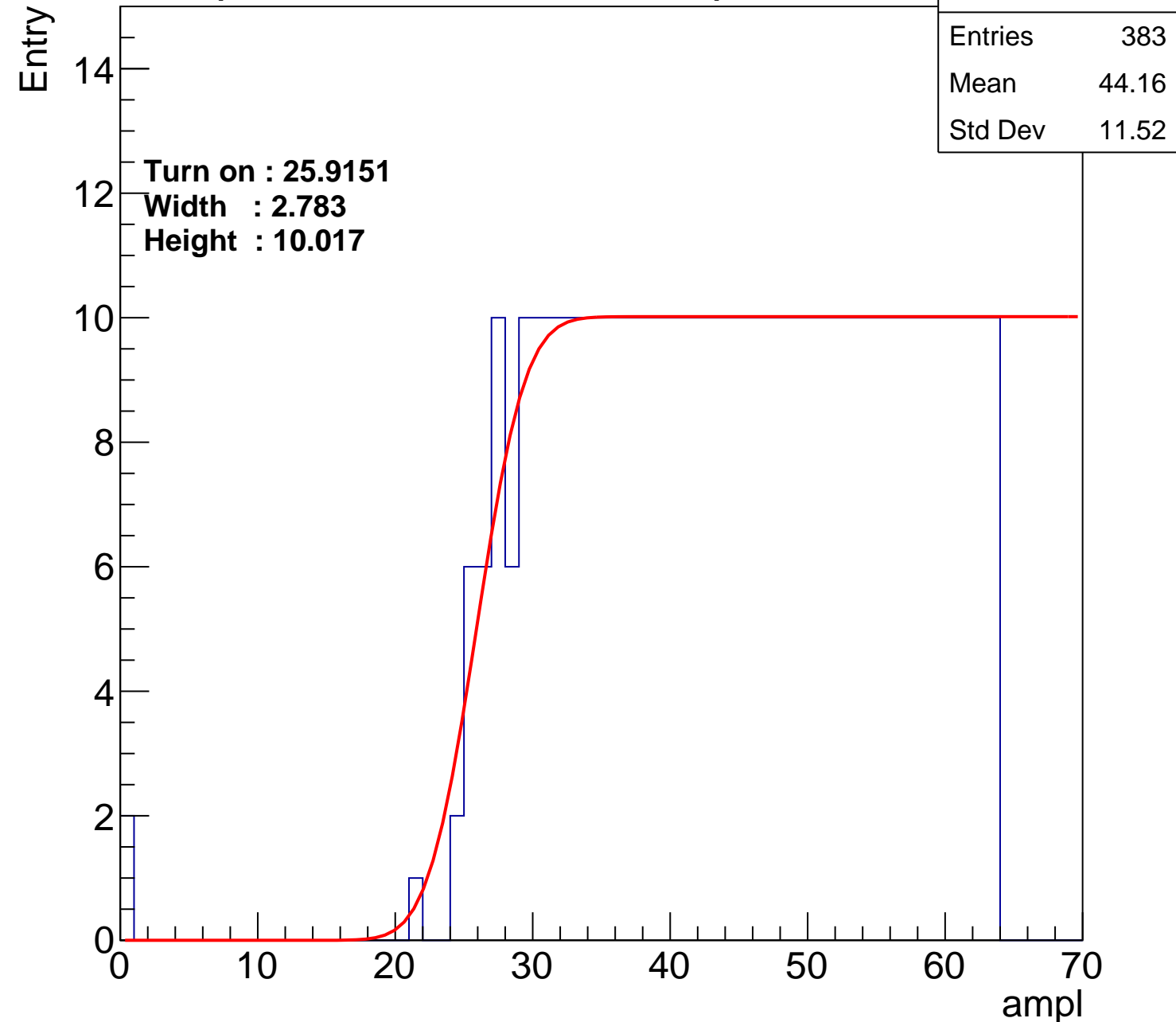
Width : 2.783

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.15
Std Dev	11.98

Turn on : 24.1114

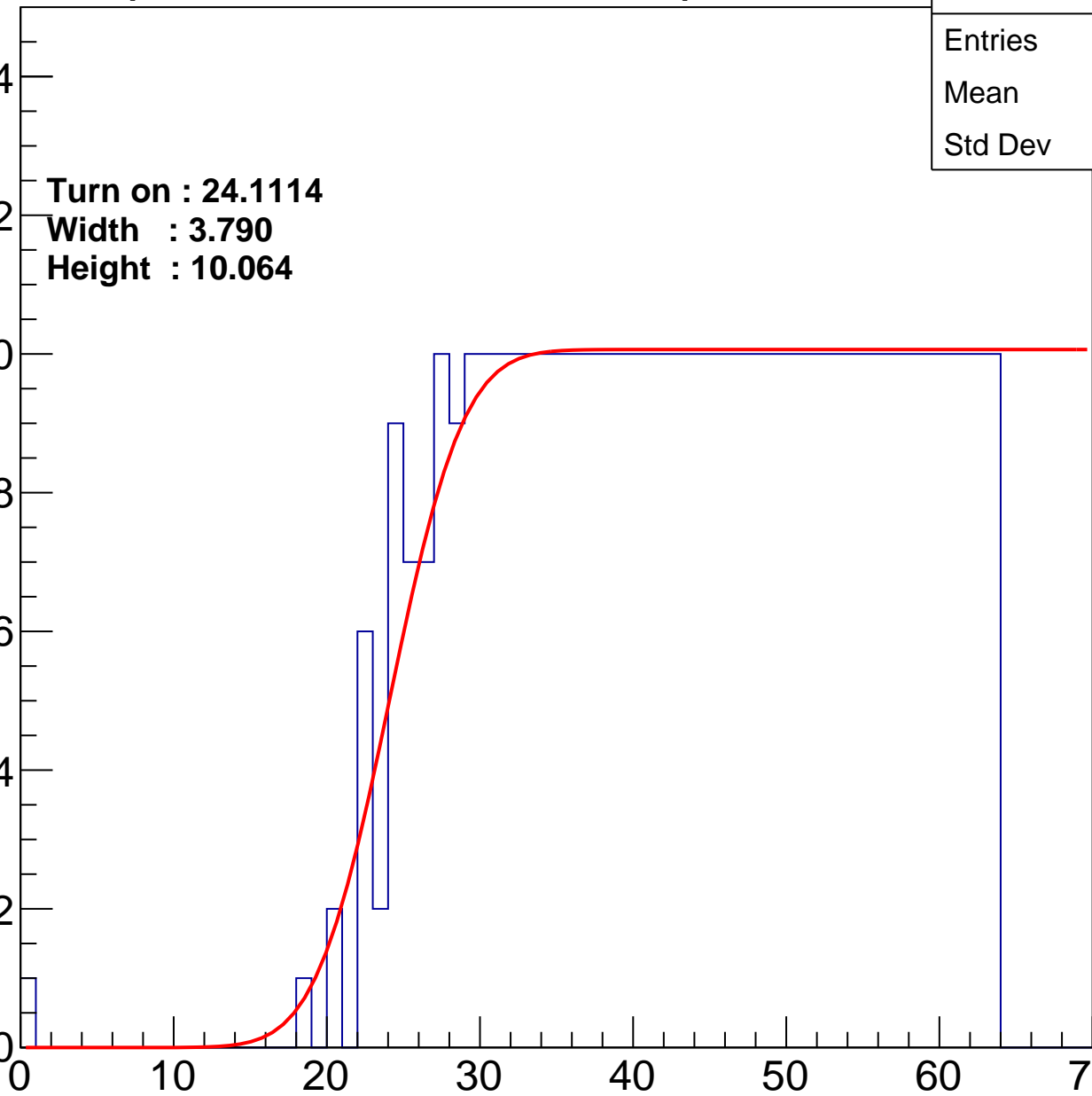
Width : 3.790

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.11
Std Dev	12.58

Turn on : 24.5654

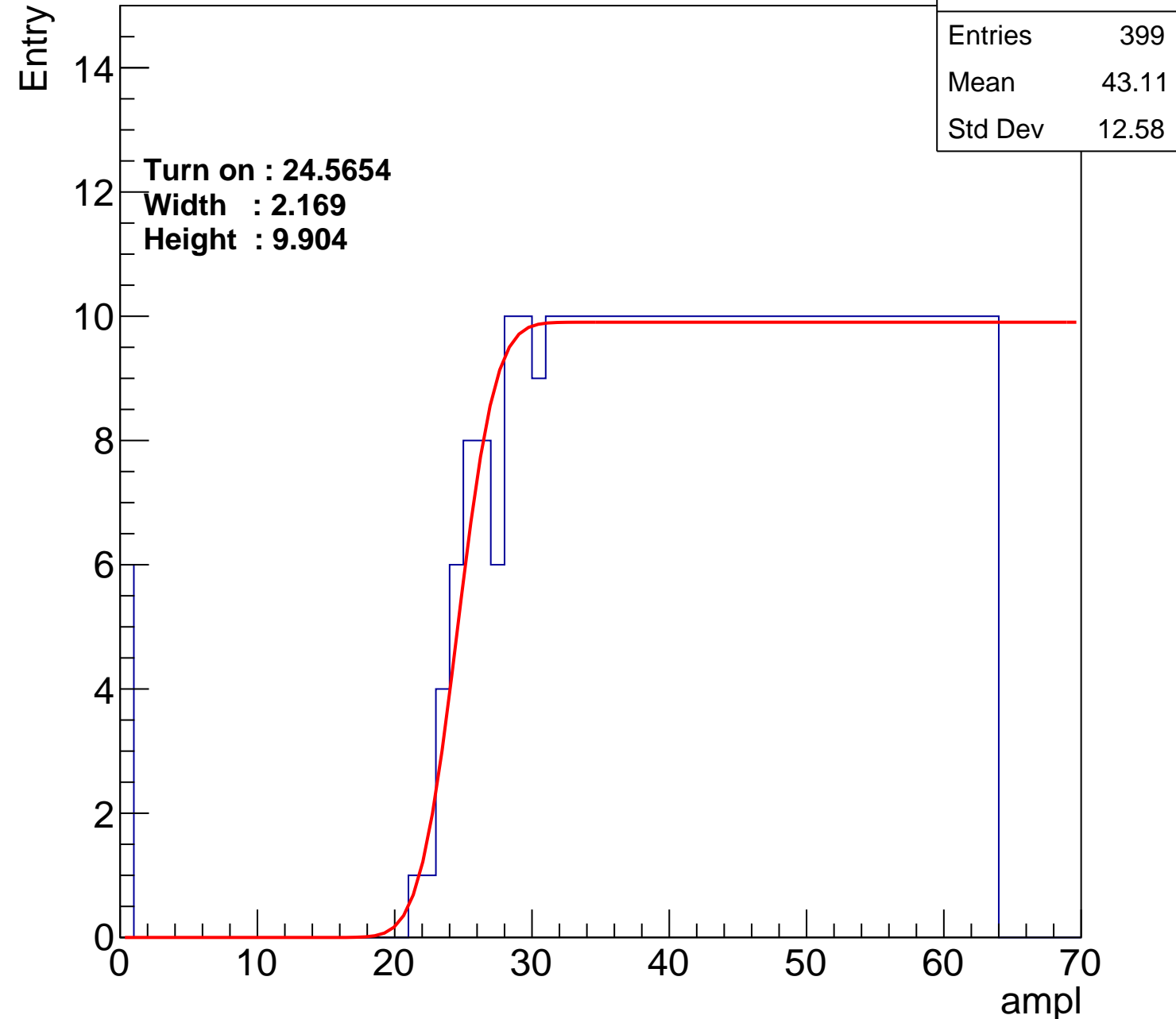
Width : 2.169

Height : 9.904

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch19

calib_packv5_042523_0143.root, FC#10, port B3

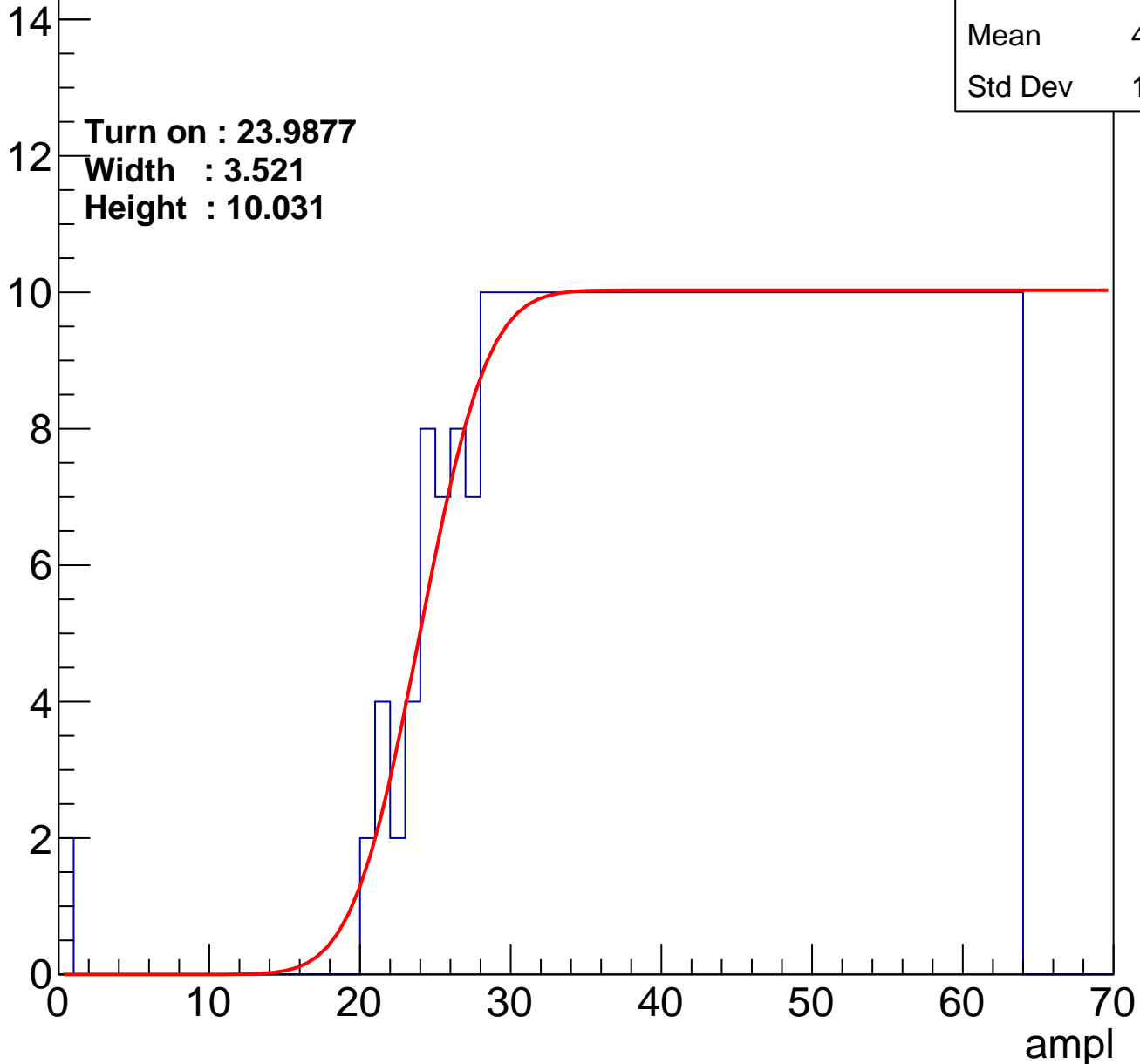
Entries	404
Mean	43.08
Std Dev	12.14

Turn on : 23.9877

Width : 3.521

Height : 10.031

Entry



B1L002S, U16-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	11.96

Turn on : 24.7845

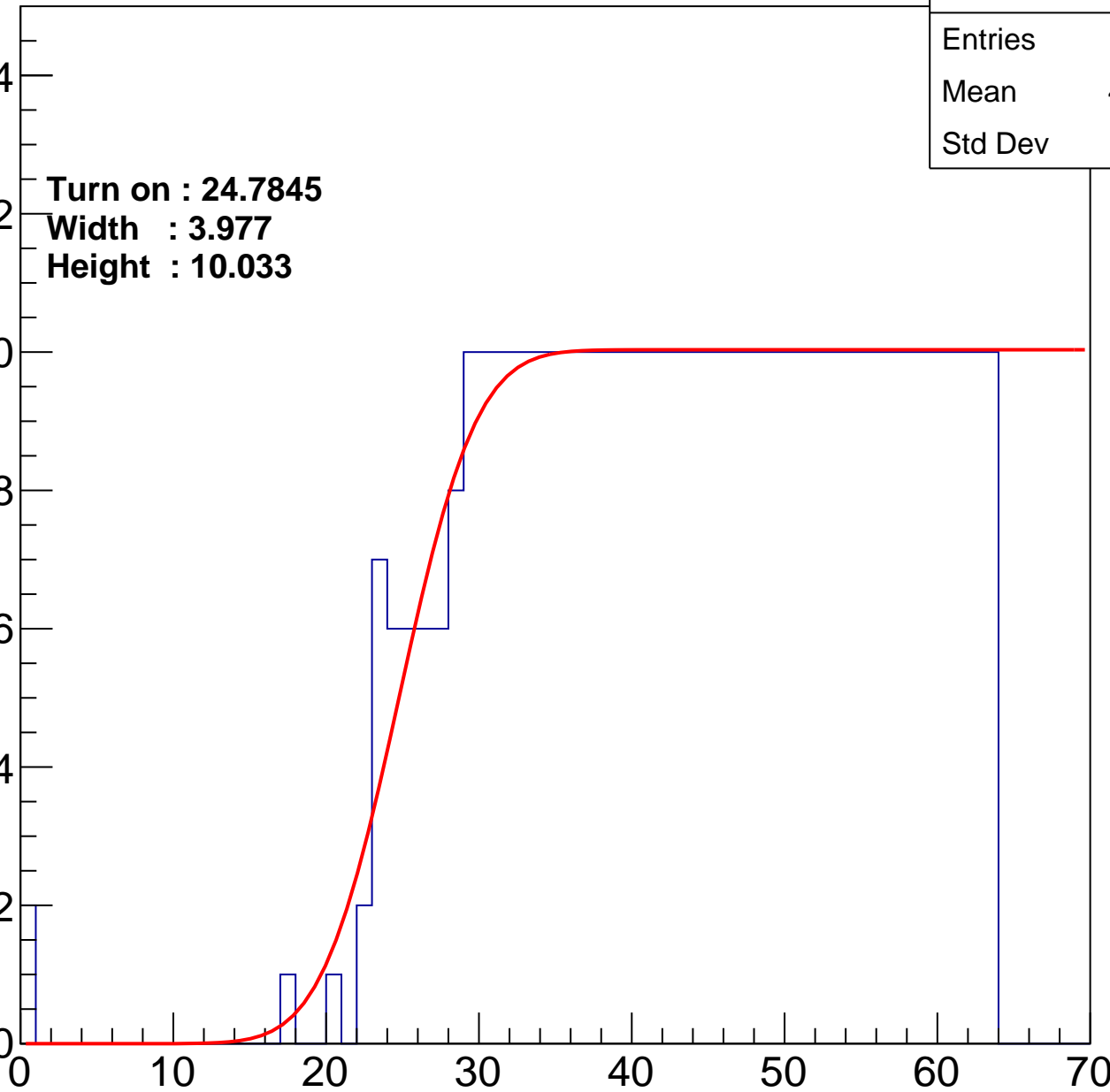
Width : 3.977

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.5712

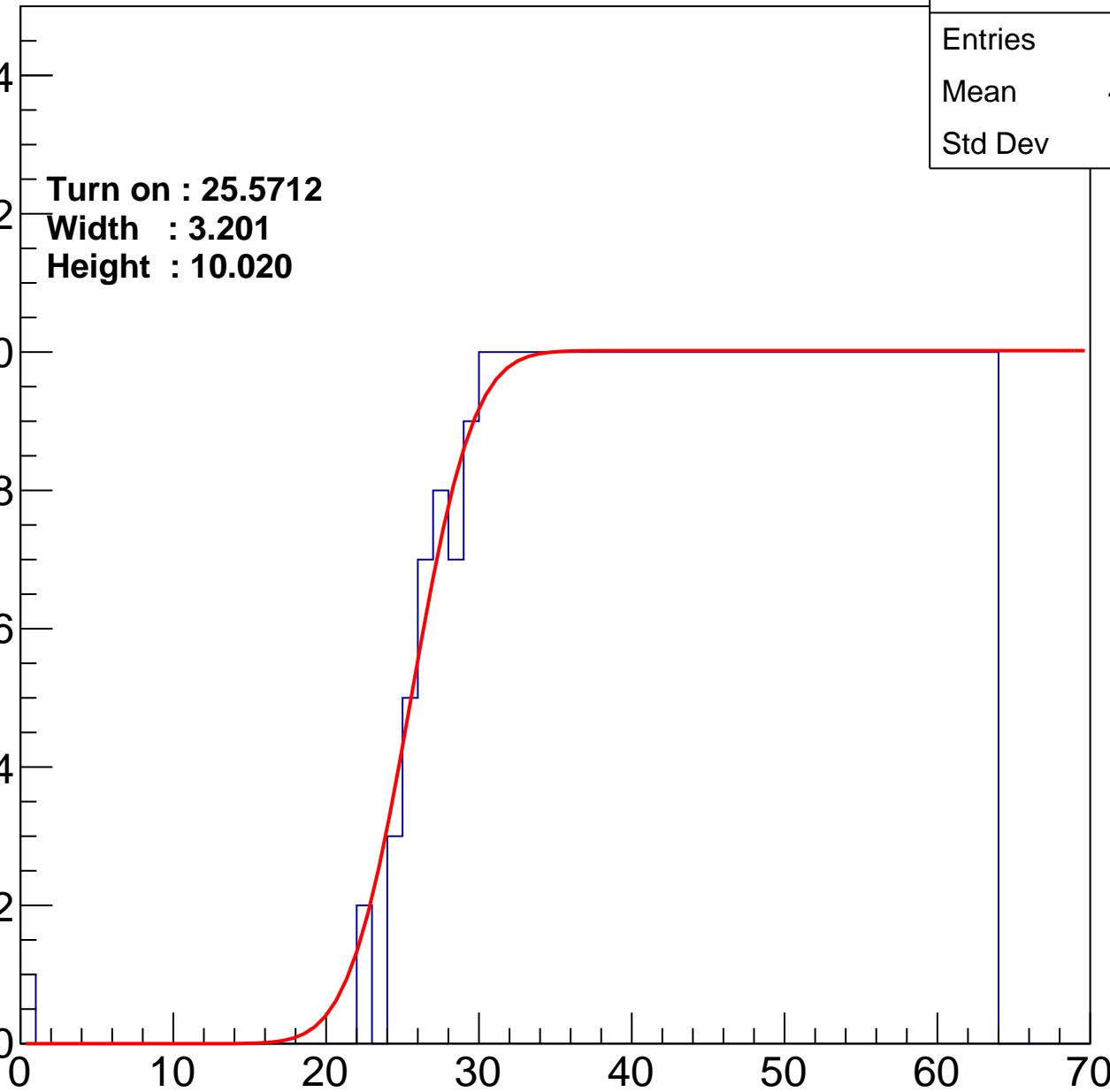
Width : 3.201

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.98
Std Dev	11.71

Turn on : 26.1334

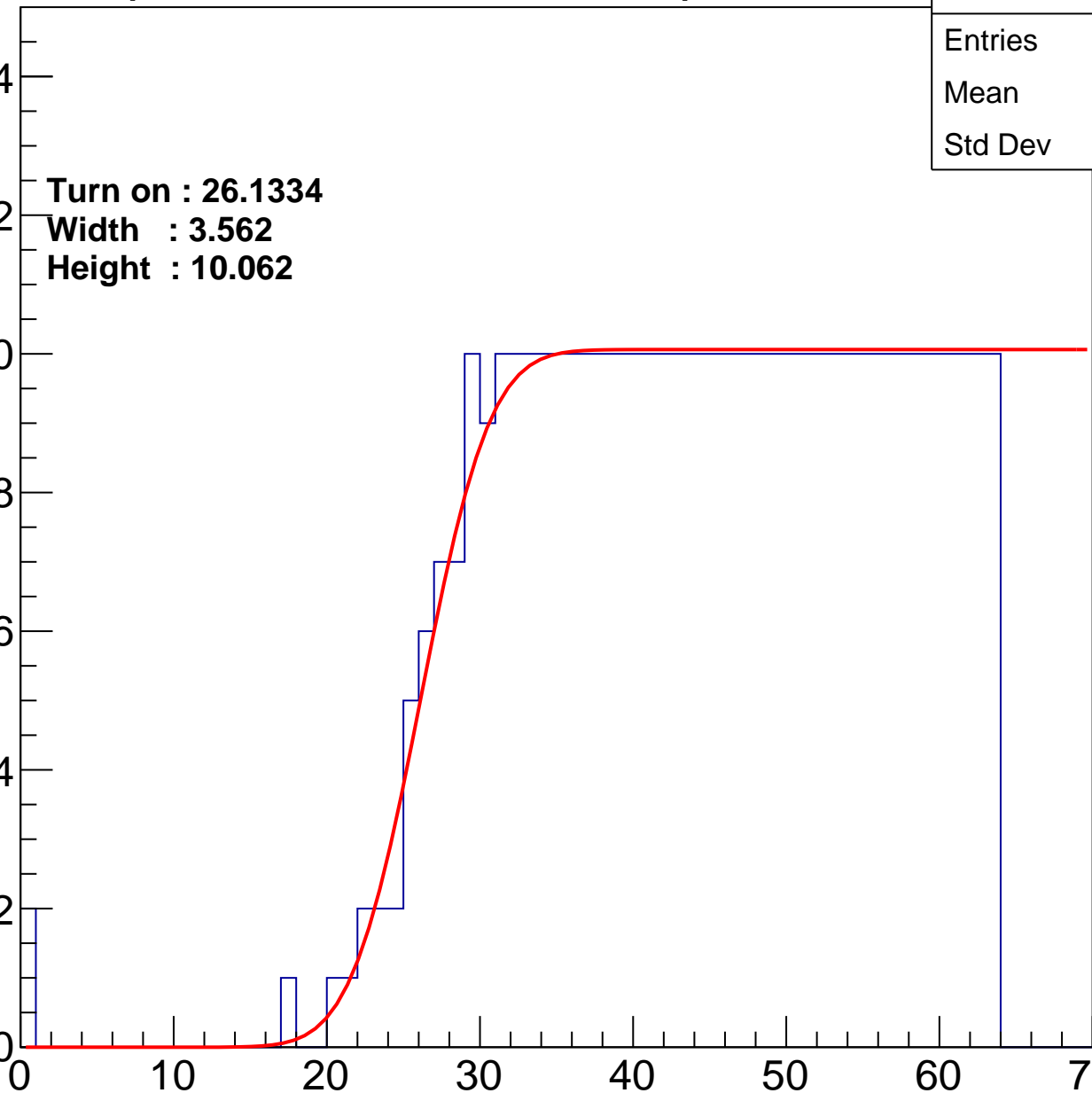
Width : 3.562

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch23

calib_packv5_042523_0143.root, FC#10, port B3

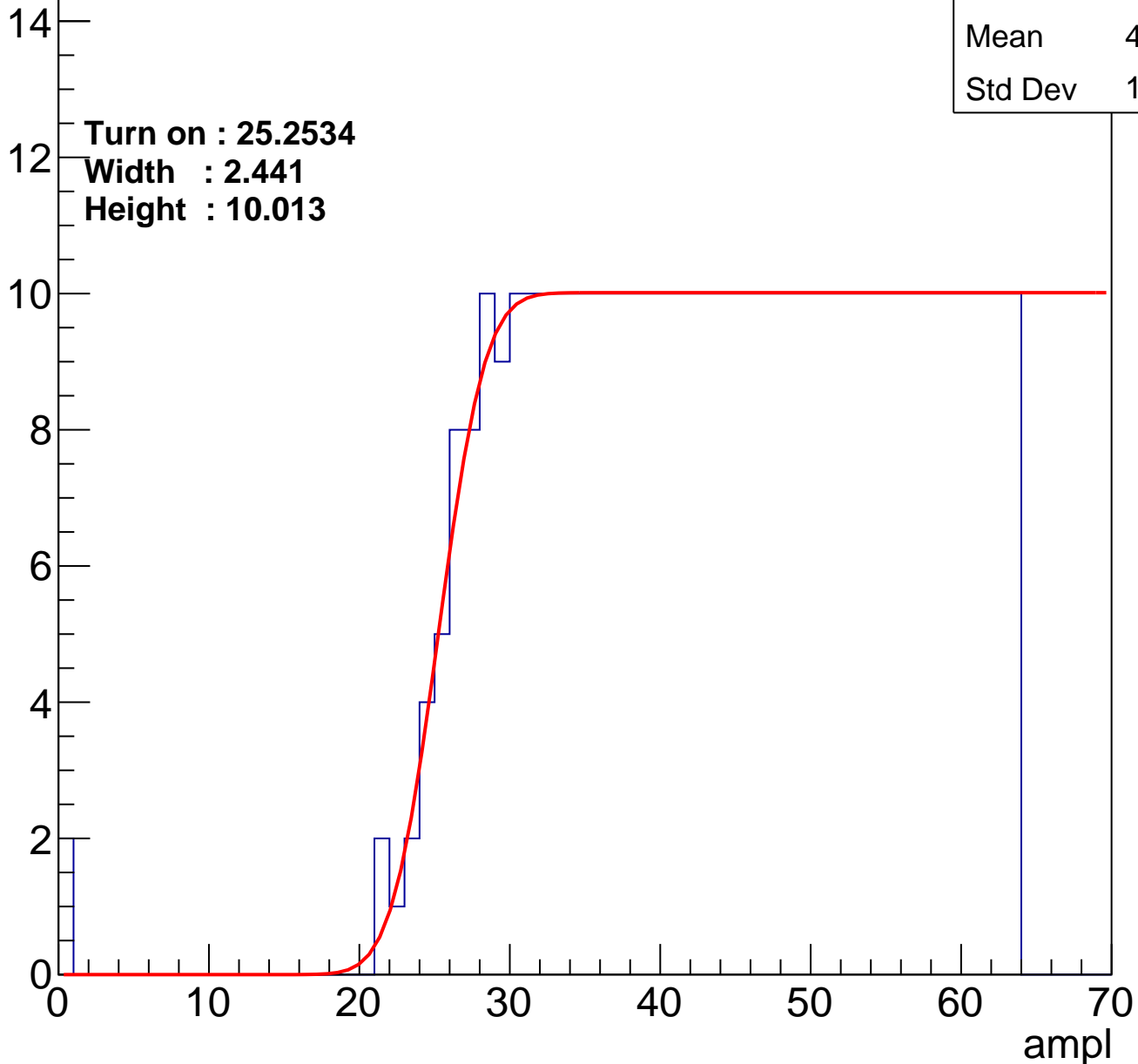
Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.2534

Width : 2.441

Height : 10.013

Entry



B1L002S, U16-ch24

calib_packv5_042523_0143.root, FC#10, port B3

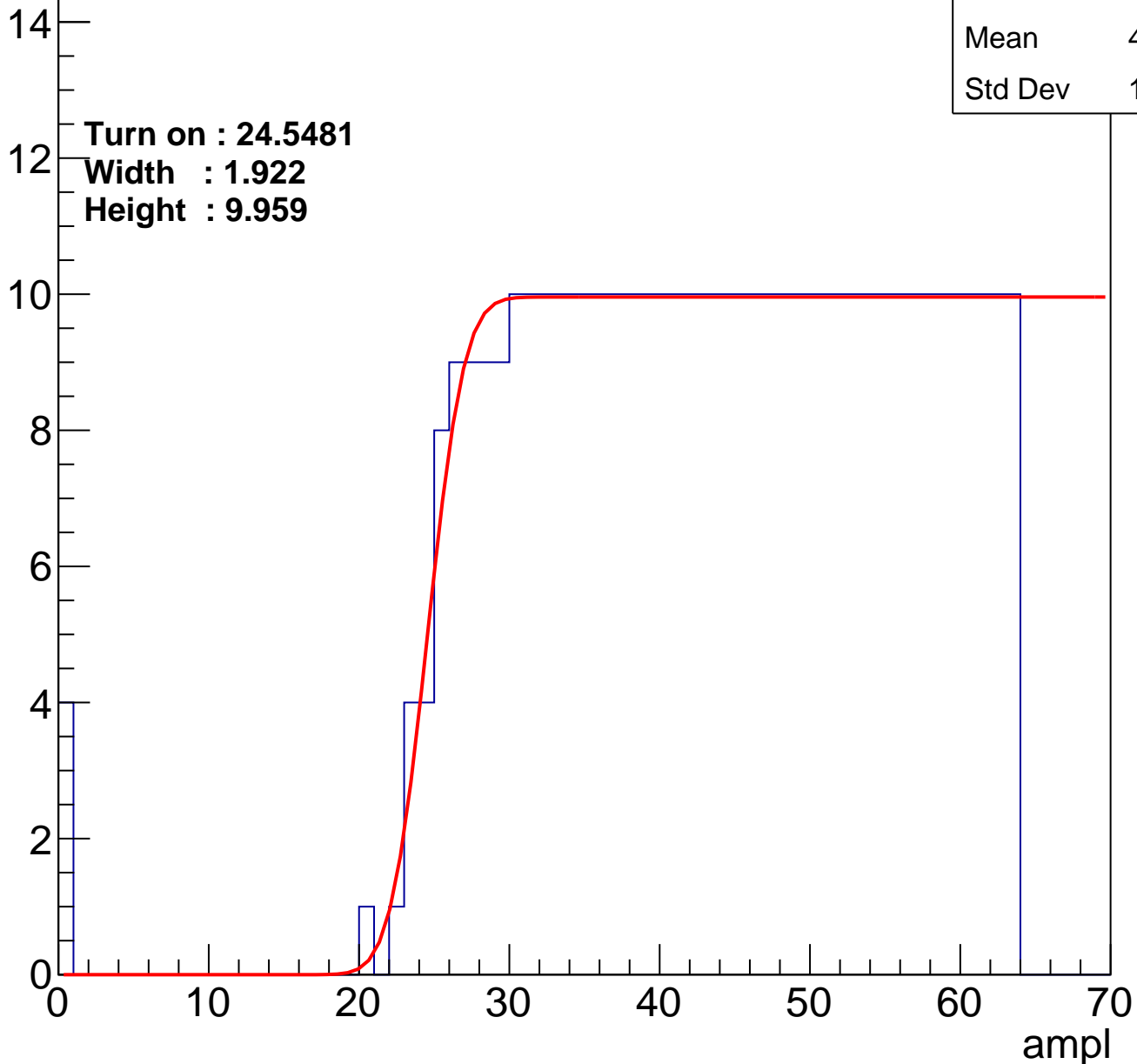
Entries	398
Mean	43.29
Std Dev	12.23

Turn on : 24.5481

Width : 1.922

Height : 9.959

Entry



B1L002S, U16-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.58
Std Dev	11.99

Turn on : 25.2357

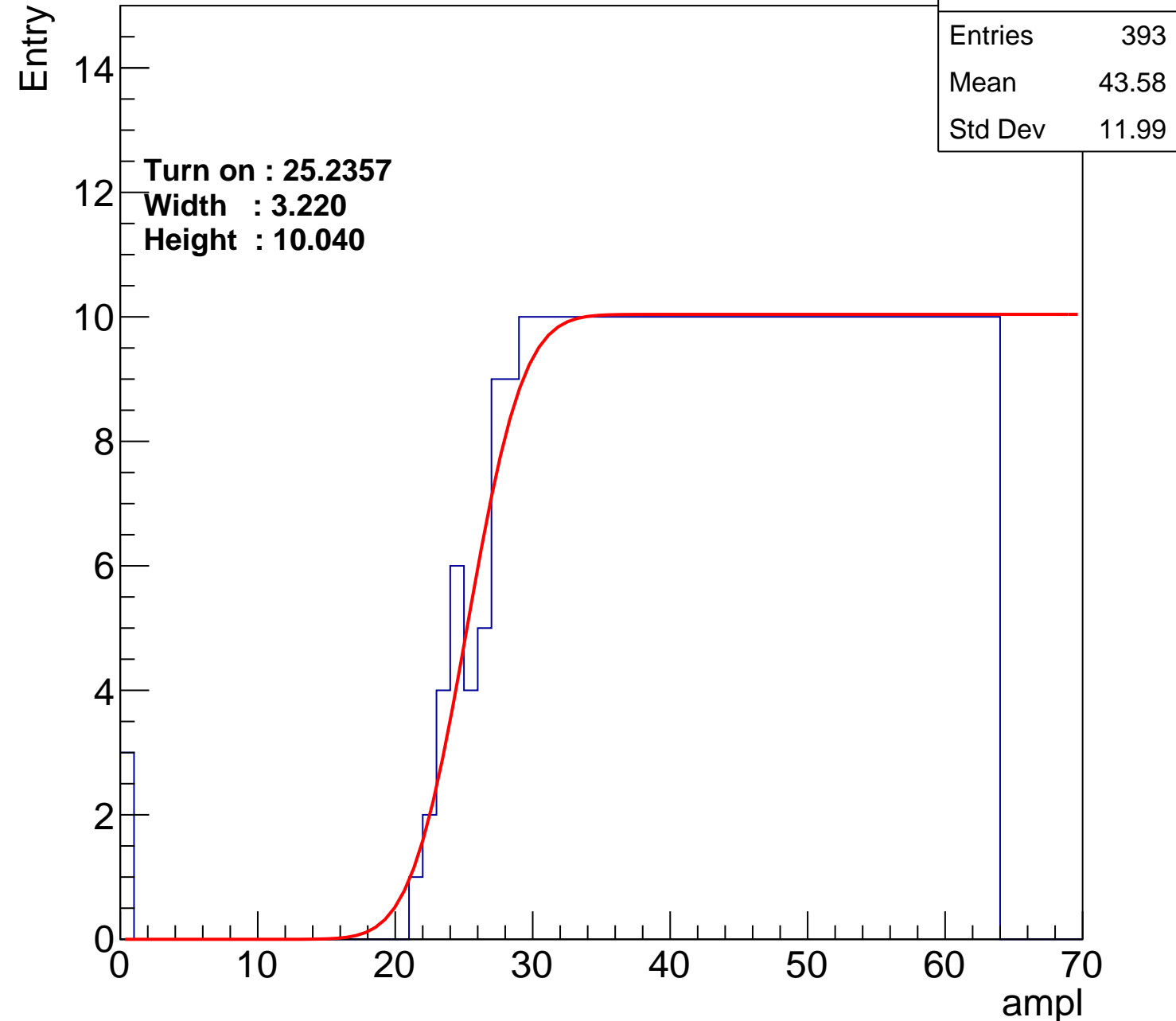
Width : 3.220

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.2078

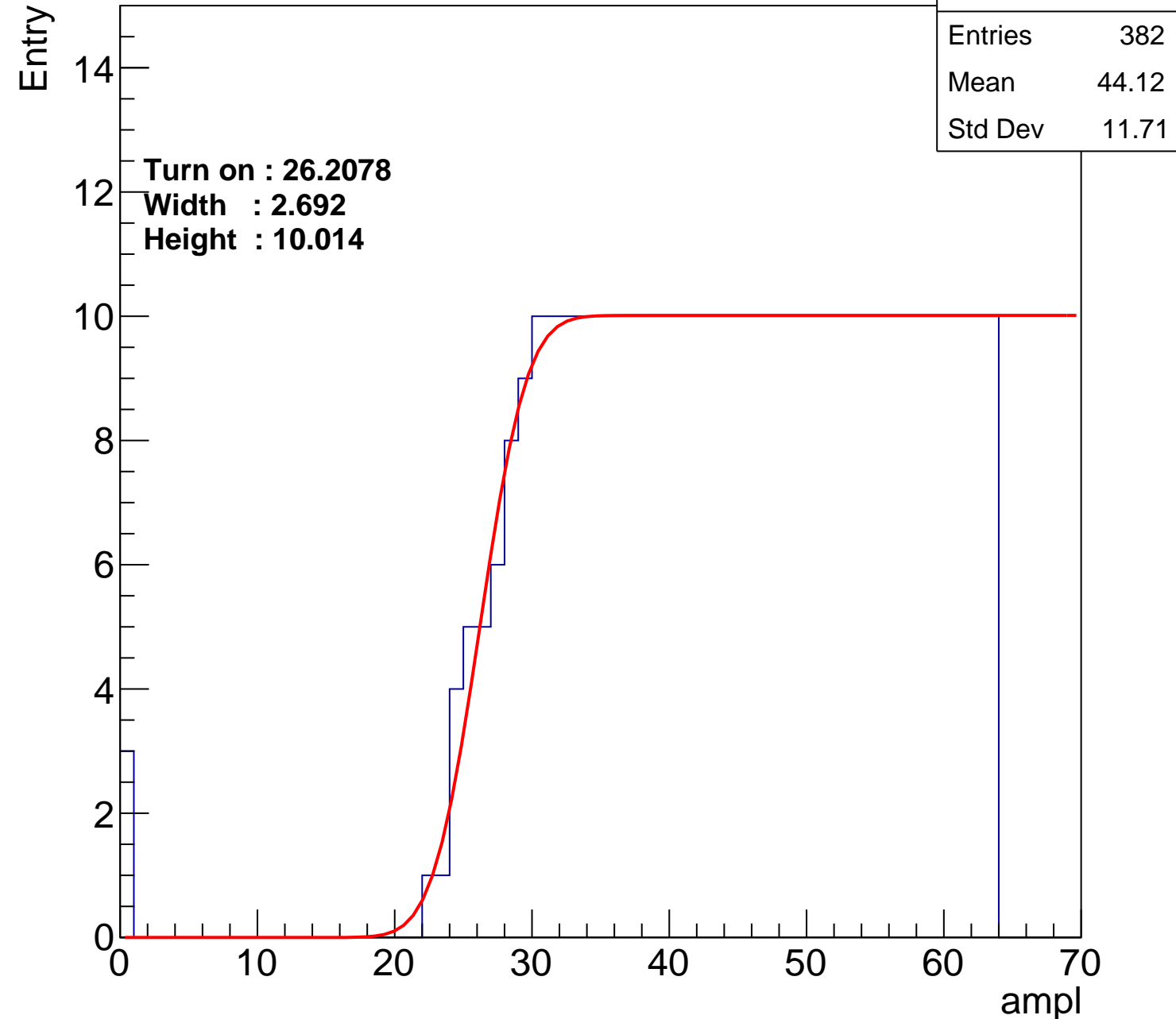
Width : 2.692

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch27

calib_packv5_042523_0143.root, FC#10, port B3

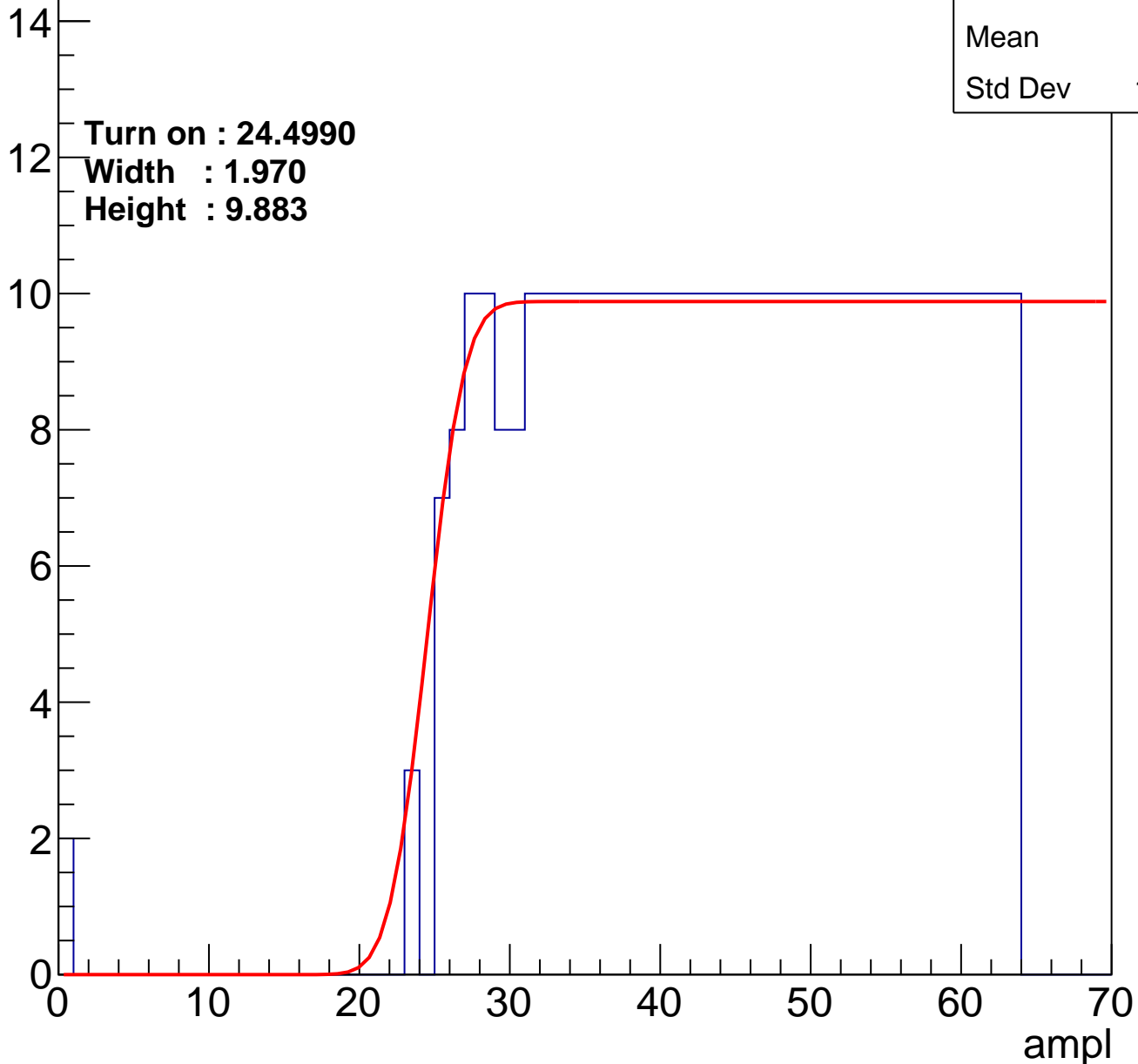
Entries	386
Mean	44
Std Dev	11.61

Turn on : 24.4990

Width : 1.970

Height : 9.883

Entry



B1L002S, U16-ch28

calib_packv5_042523_0143.root, FC#10, port B3

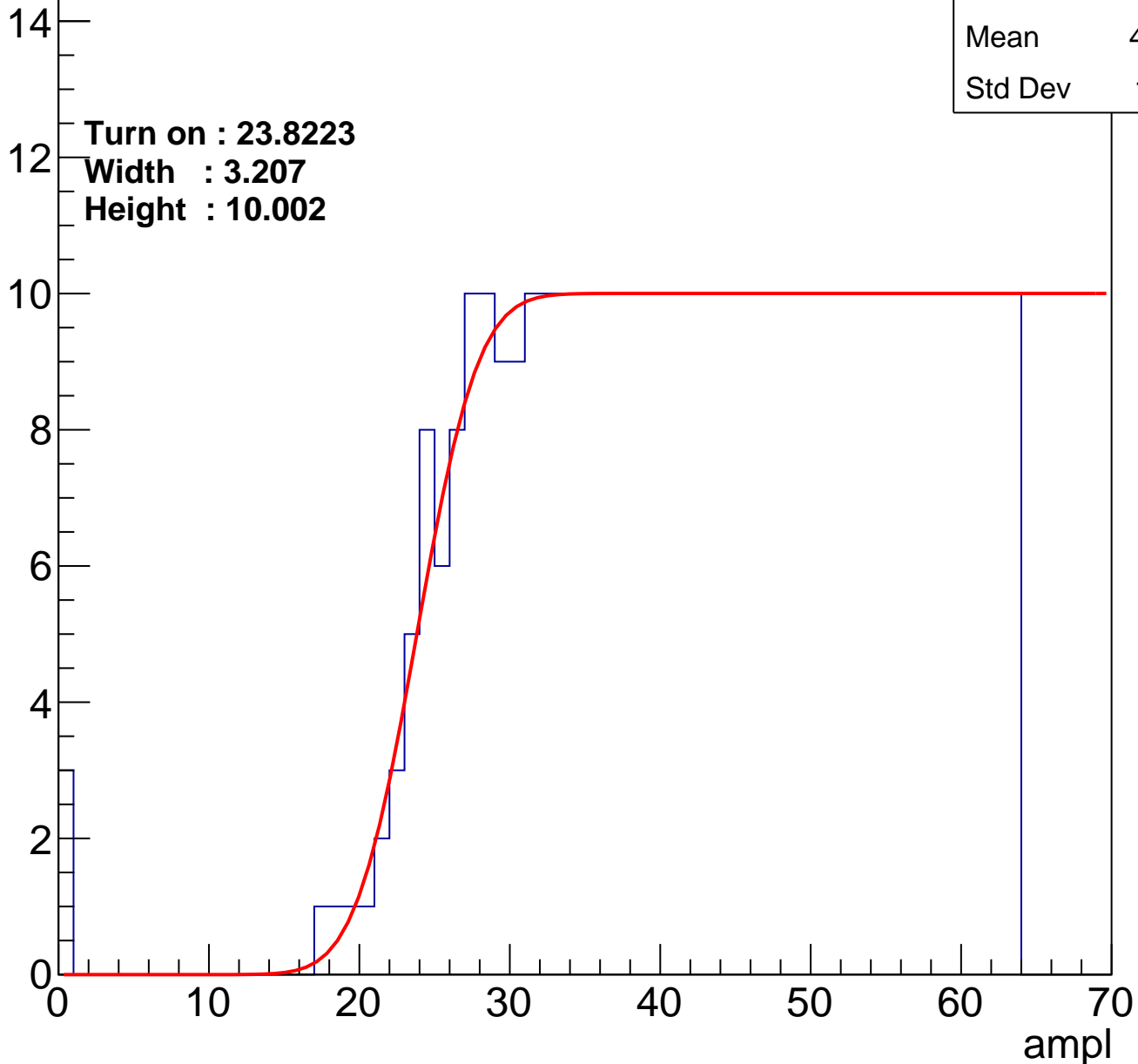
Entries	407
Mean	42.85
Std Dev	12.41

Turn on : 23.8223

Width : 3.207

Height : 10.002

Entry



B1L002S, U16-ch29

calib_packv5_042523_0143.root, FC#10, port B3

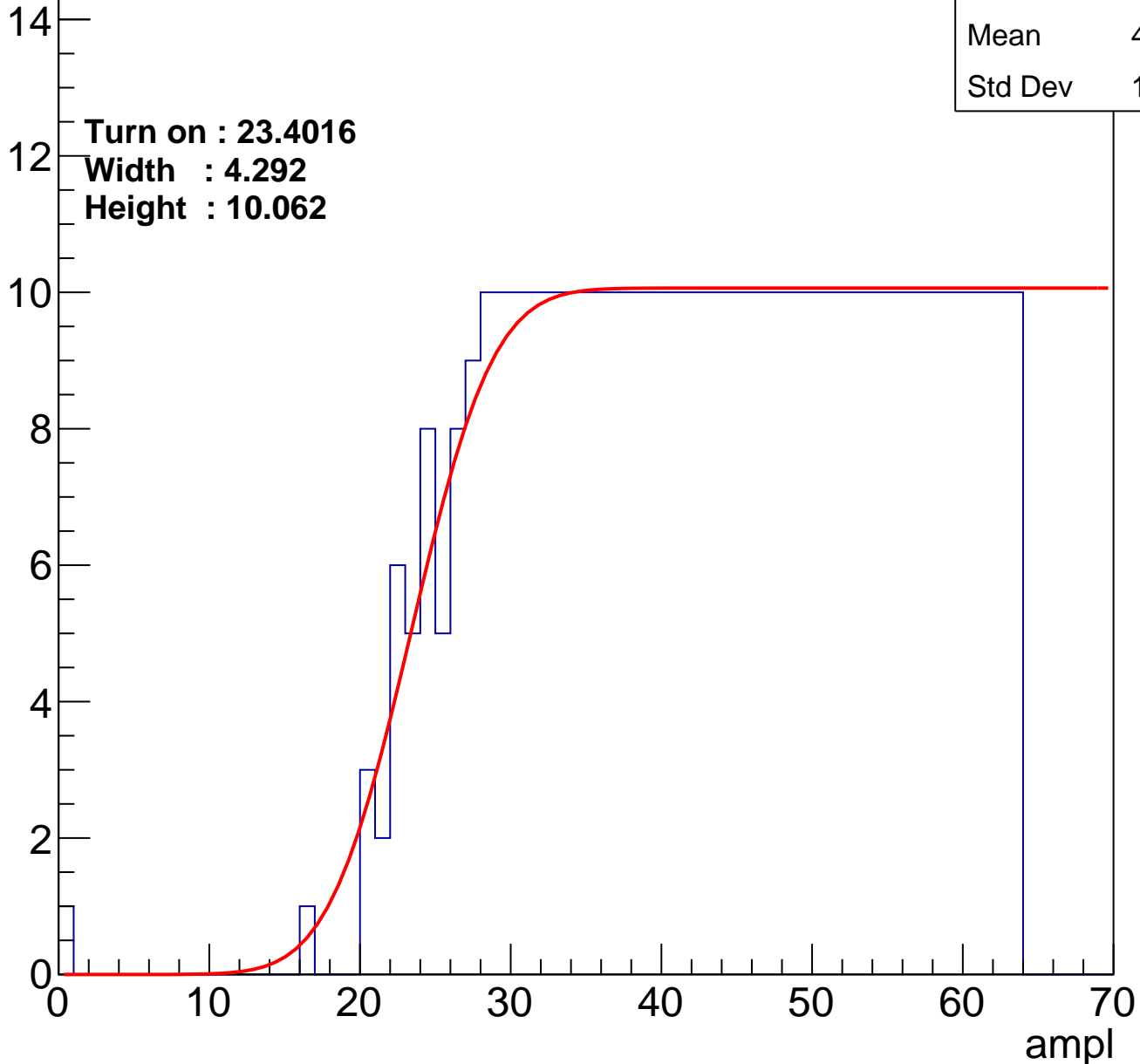
Entries	408
Mean	42.92
Std Dev	12.13

Turn on : 23.4016

Width : 4.292

Height : 10.062

Entry



B1L002S, U16-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	42.94
Std Dev	12.53

Turn on : 24.8516

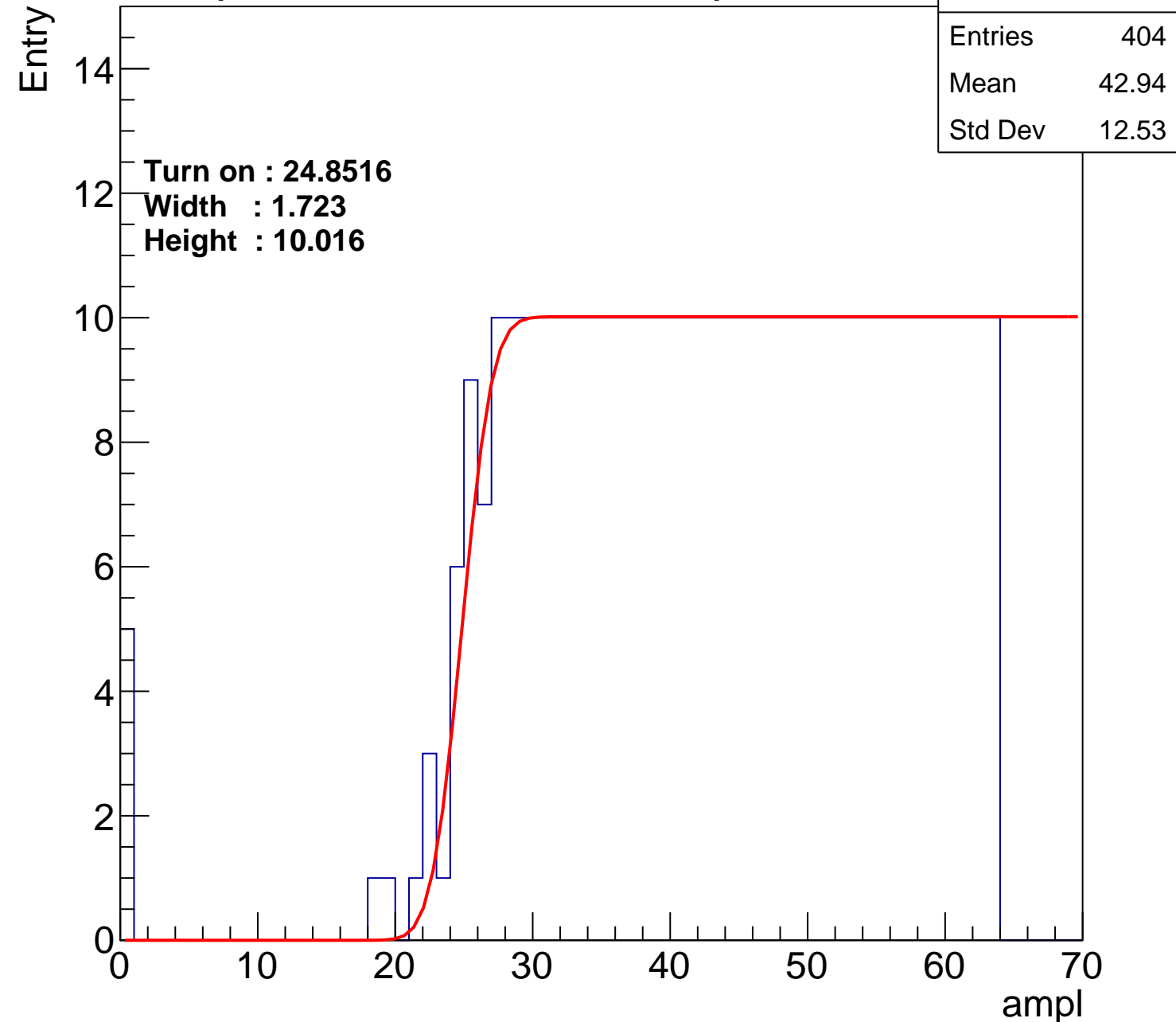
Width : 1.723

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.05
Std Dev	11.77

Turn on : 26.5646

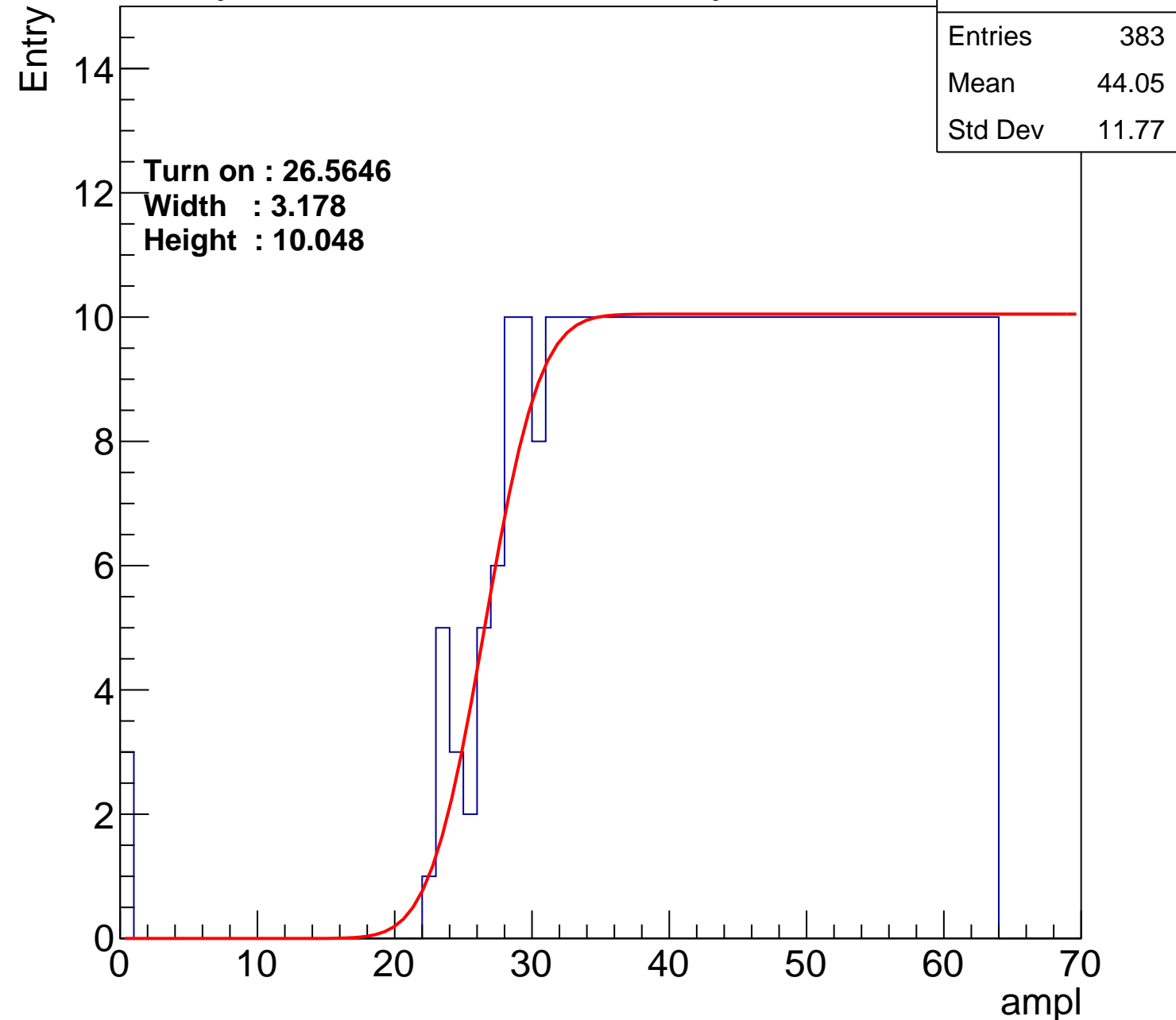
Width : 3.178

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.55
Std Dev	12.97

Turn on : 24.0659

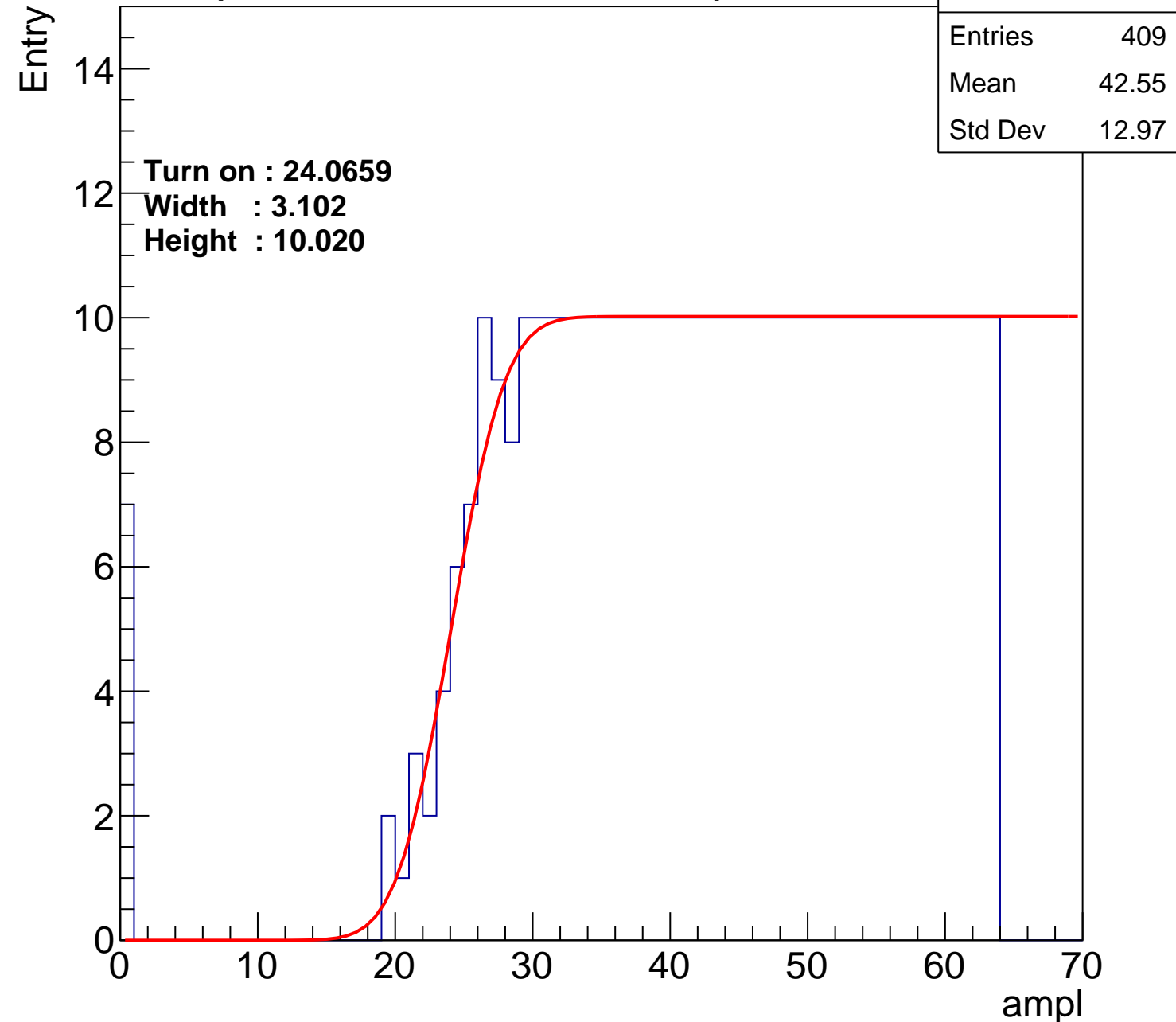
Width : 3.102

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.69
Std Dev	11.68

Turn on : 25.2212

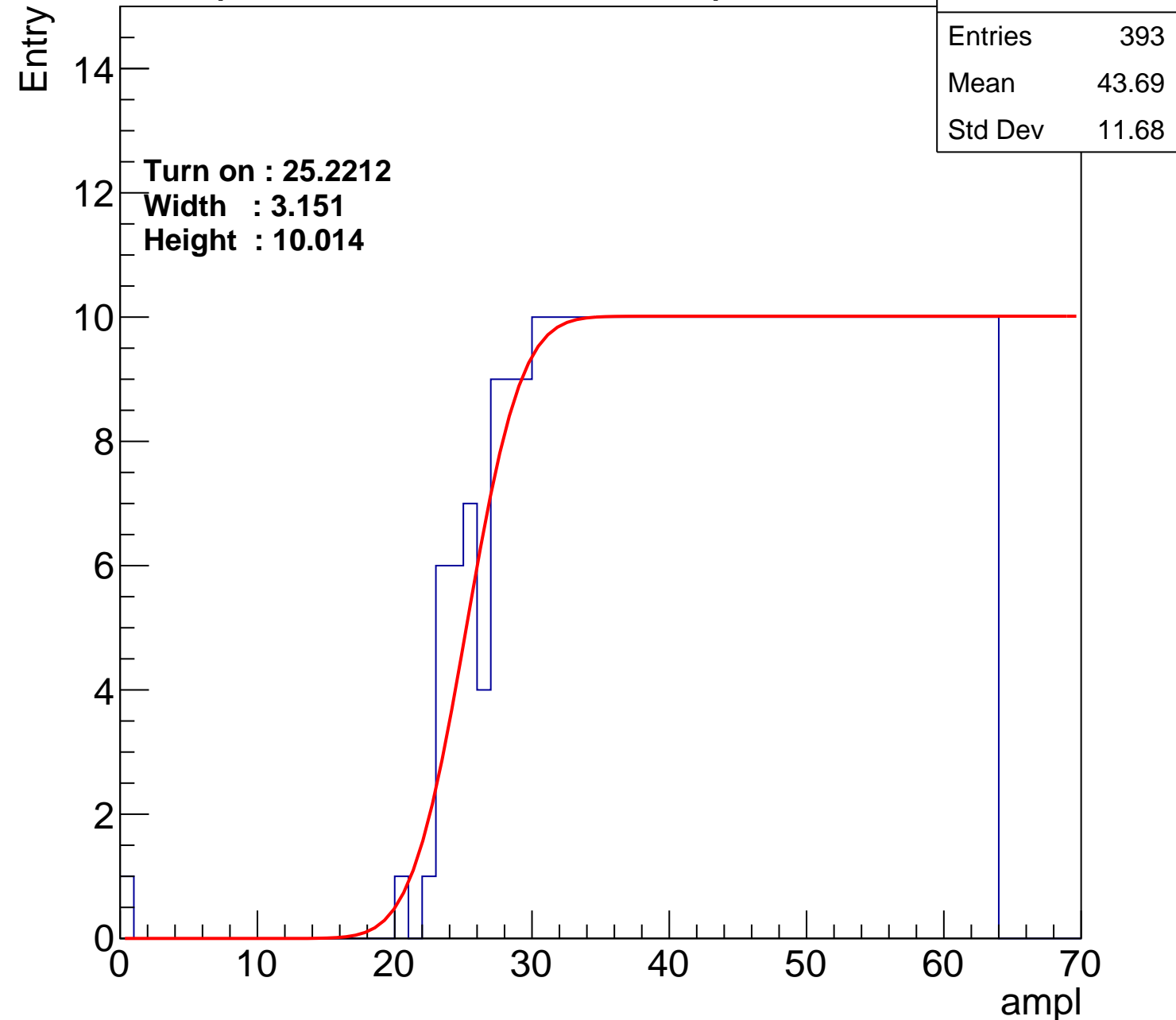
Width : 3.151

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.22
Std Dev	11.47

Turn on : 25.6794

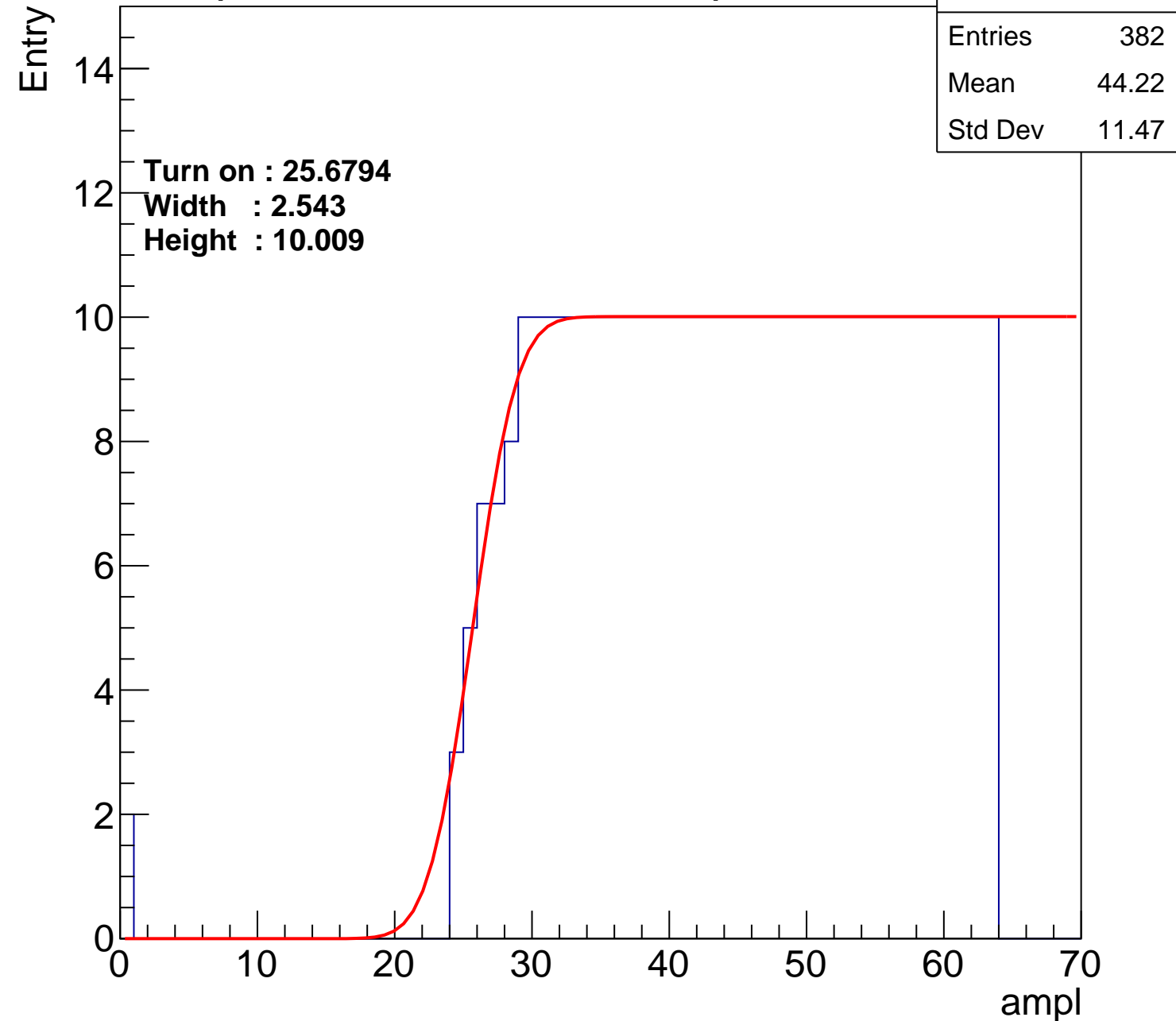
Width : 2.543

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch35

calib_packv5_042523_0143.root, FC#10, port B3

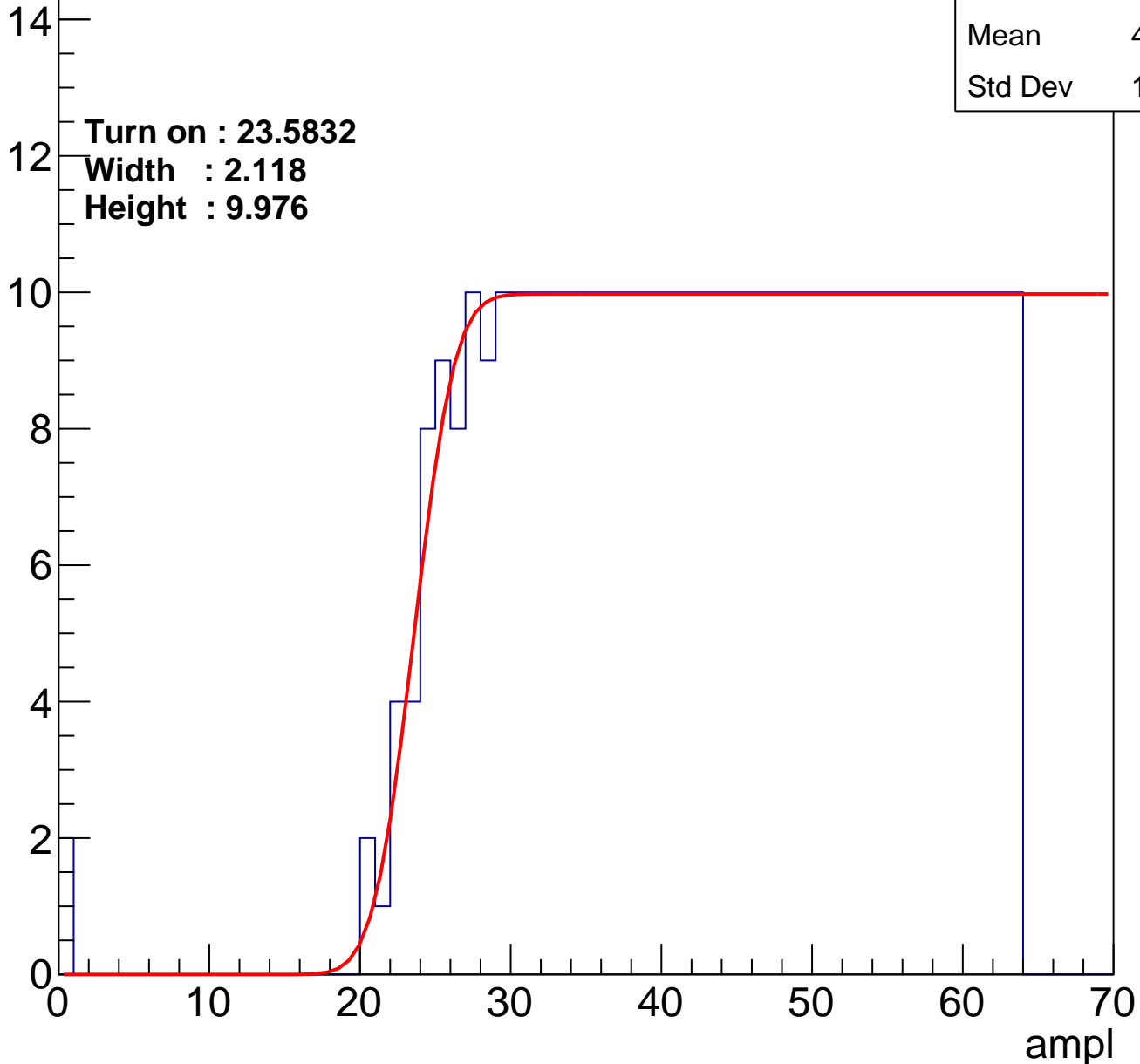
Entries	407
Mean	42.97
Std Dev	12.16

Turn on : 23.5832

Width : 2.118

Height : 9.976

Entry



B1L002S, U16-ch36

calib_packv5_042523_0143.root, FC#10, port B3

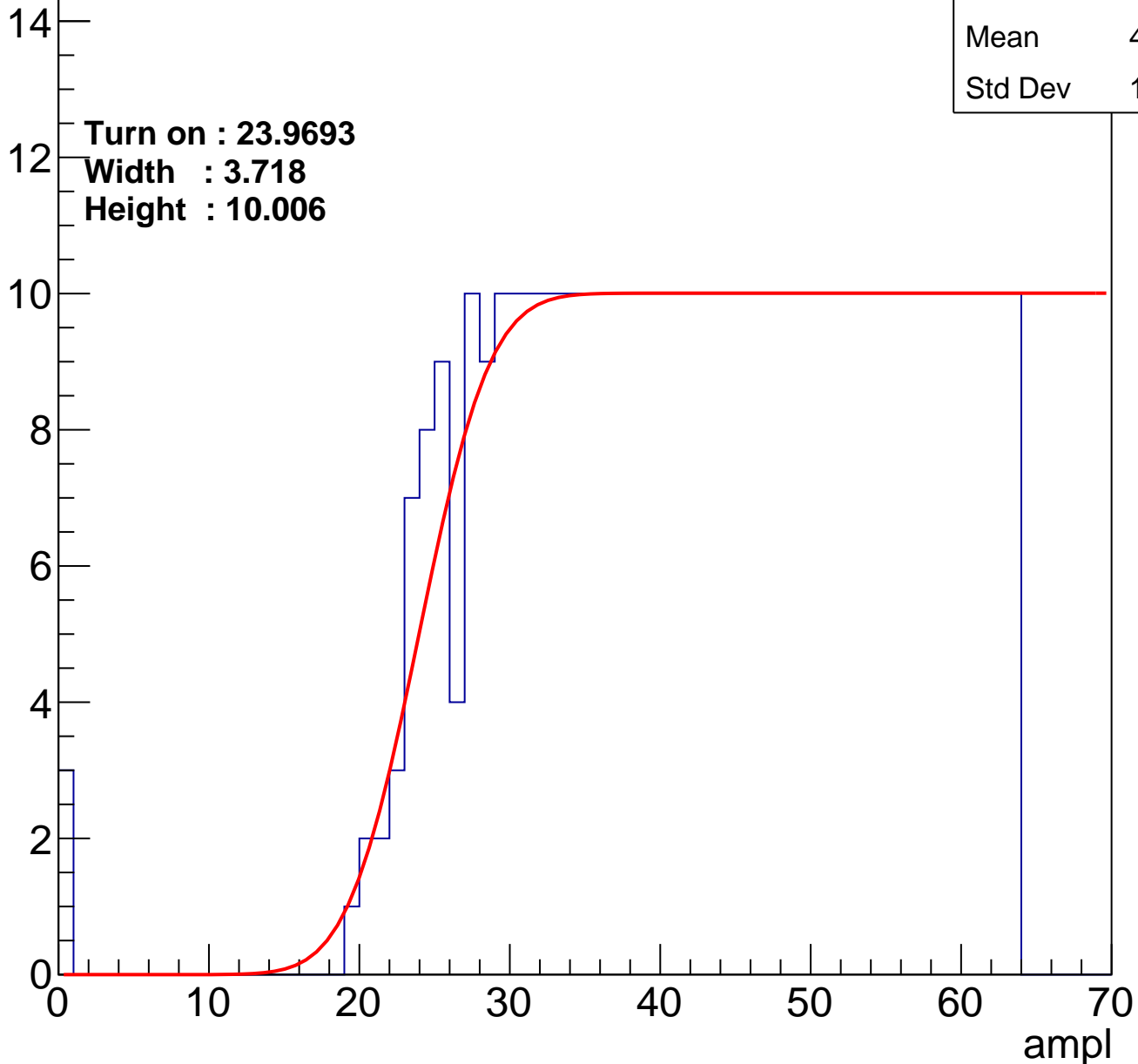
Entries	408
Mean	42.82
Std Dev	12.39

Turn on : 23.9693

Width : 3.718

Height : 10.006

Entry



B1L002S, U16-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	420
Mean	42.18
Std Dev	12.87

Turn on : 22.6821

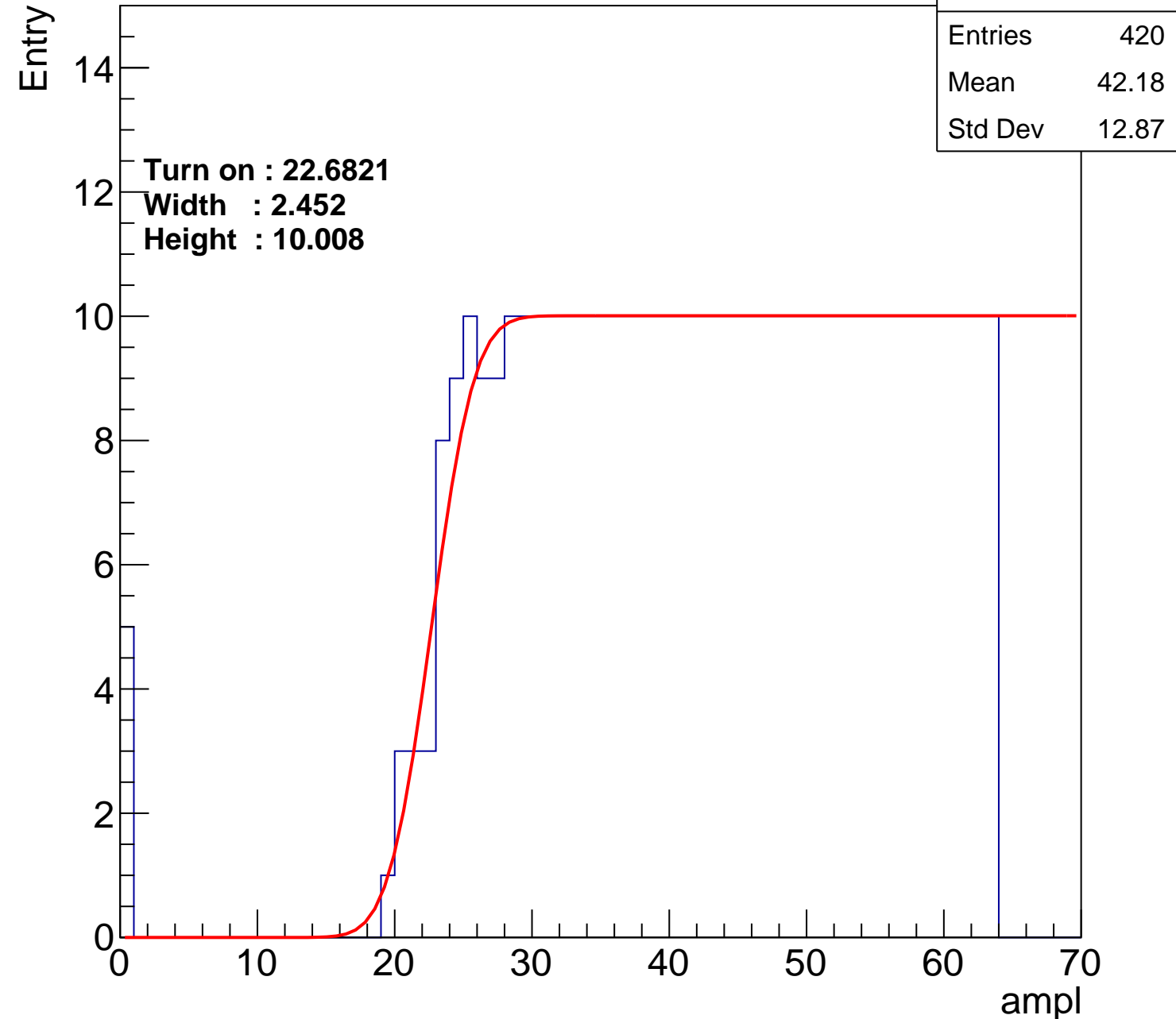
Width : 2.452

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch38

calib_packv5_042523_0143.root, FC#10, port B3

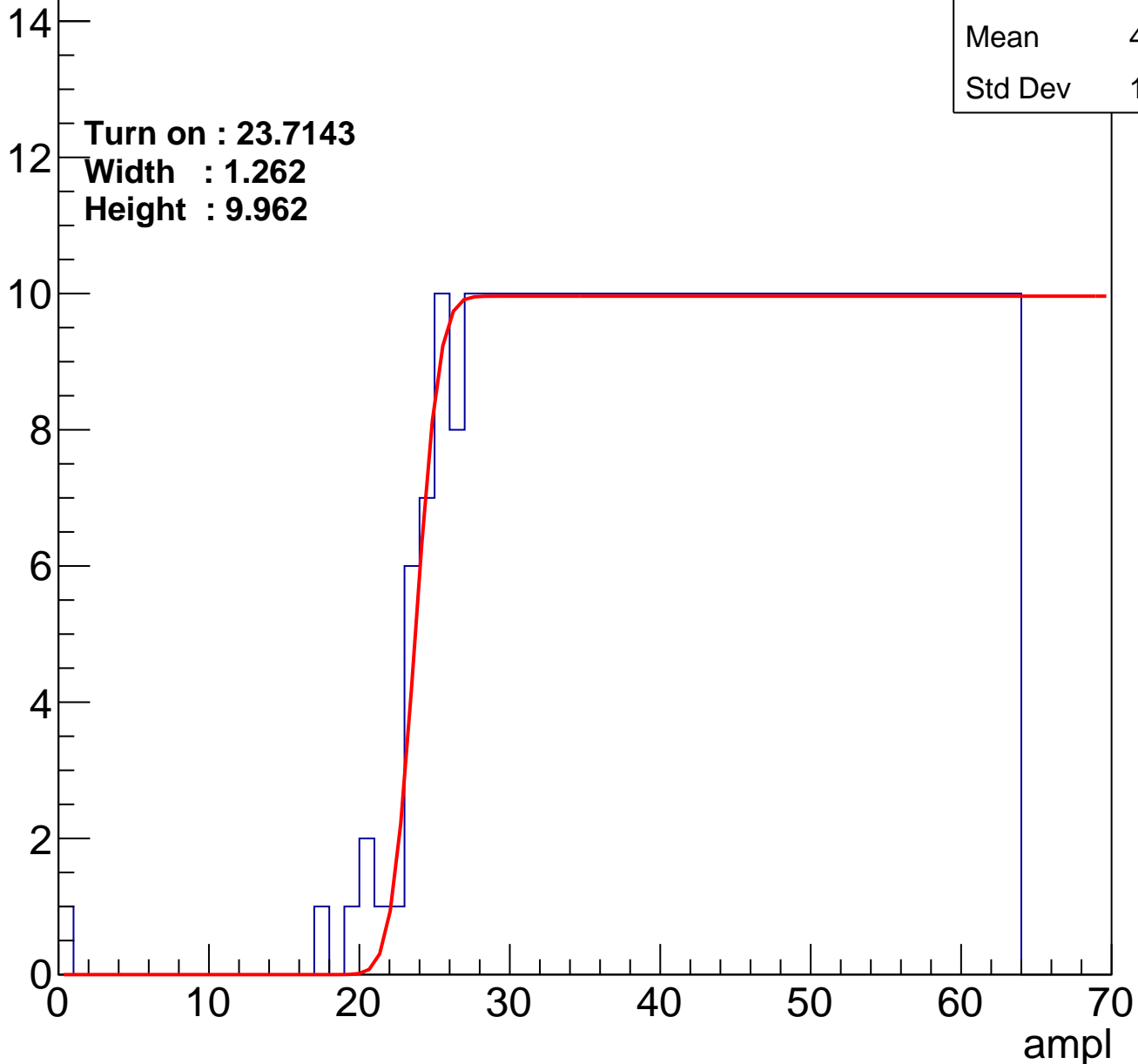
Entries	408
Mean	42.97
Std Dev	12.05

Turn on : 23.7143

Width : 1.262

Height : 9.962

Entry



B1L002S, U16-ch39

calib_packv5_042523_0143.root, FC#10, port B3

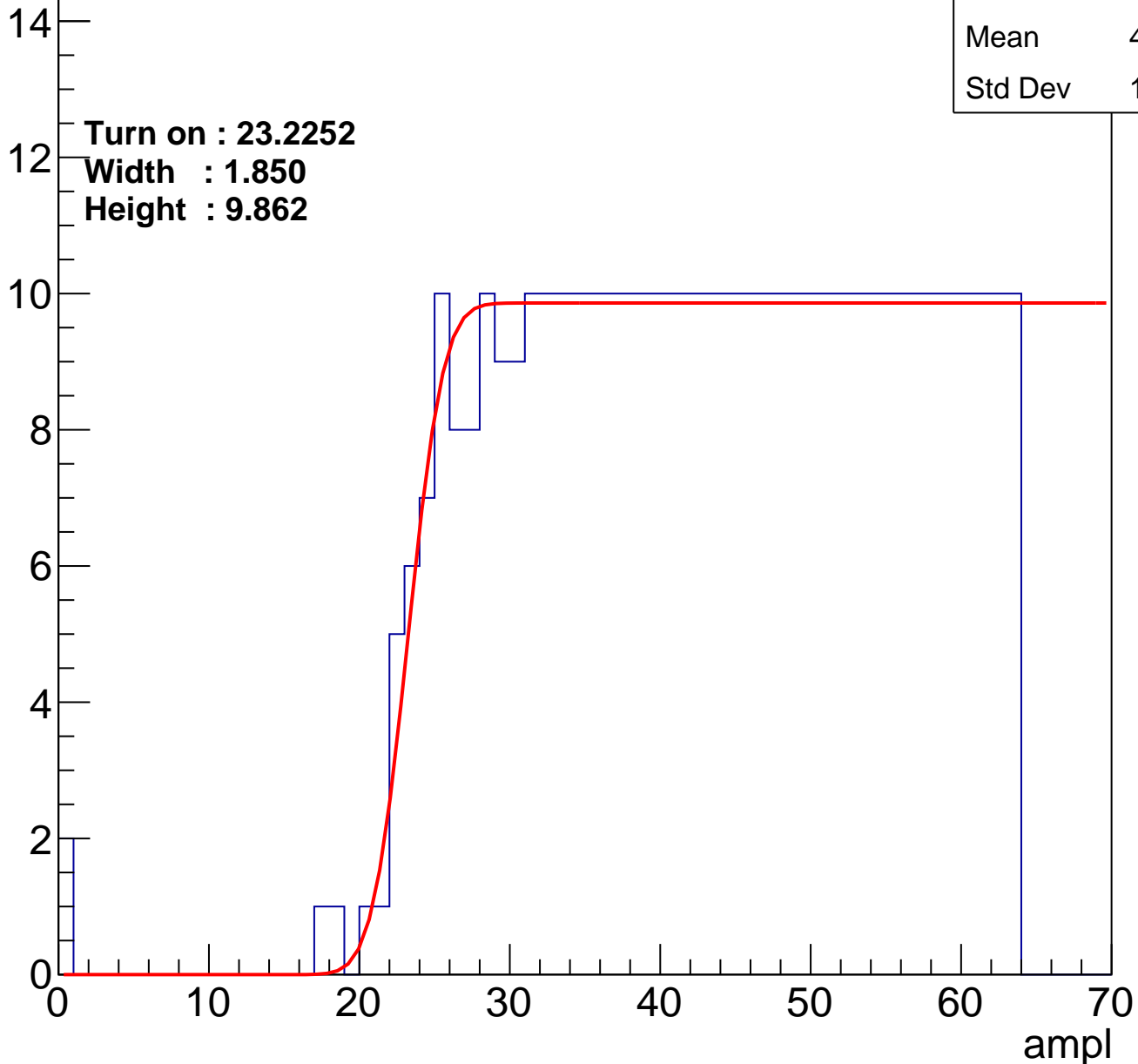
Entries	408
Mean	42.86
Std Dev	12.27

Turn on : 23.2252

Width : 1.850

Height : 9.862

Entry



B1L002S, U16-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.76
Std Dev	11.79

Turn on : 25.6210

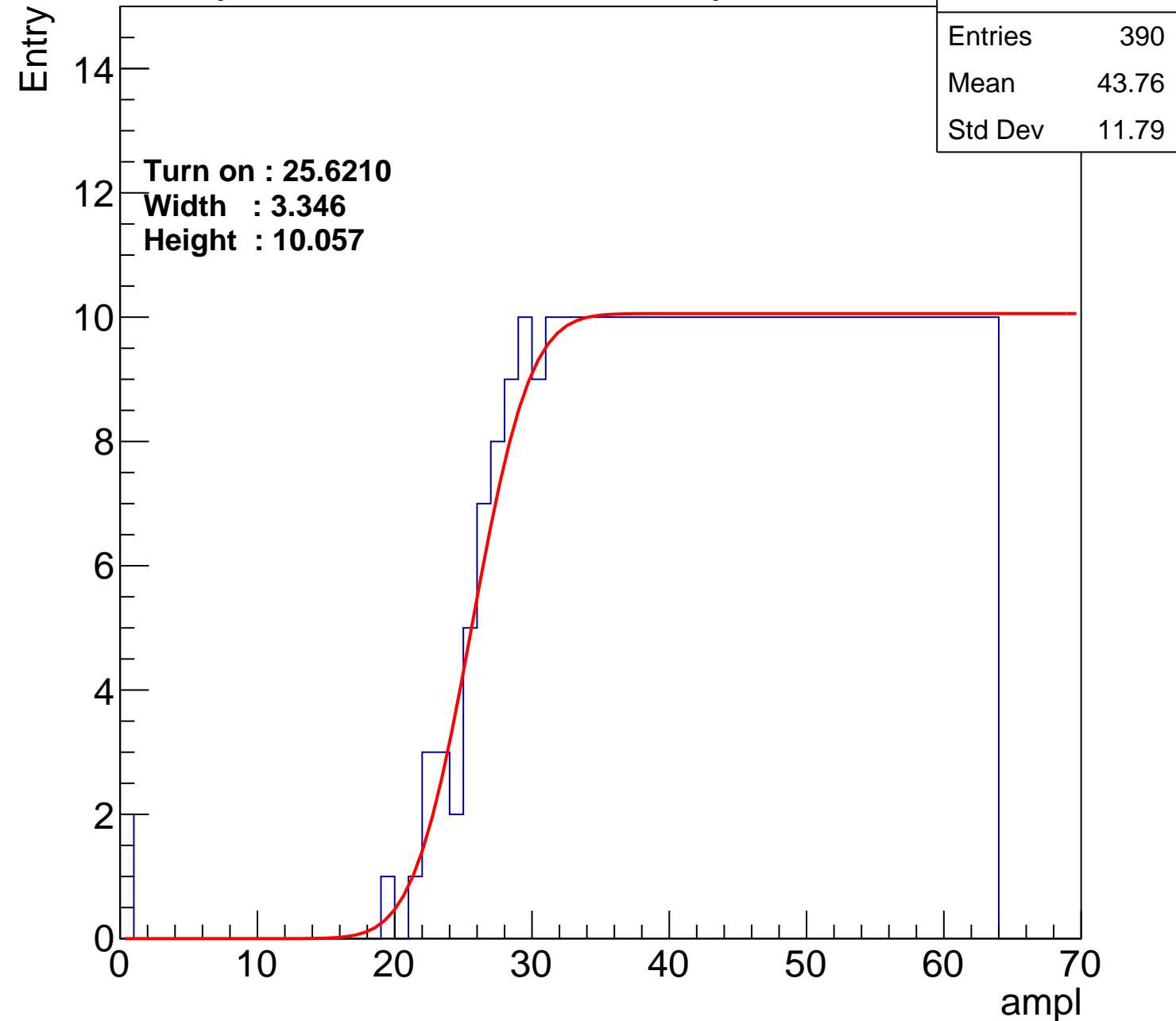
Width : 3.346

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.91
Std Dev	12.41

Turn on : 23.7399

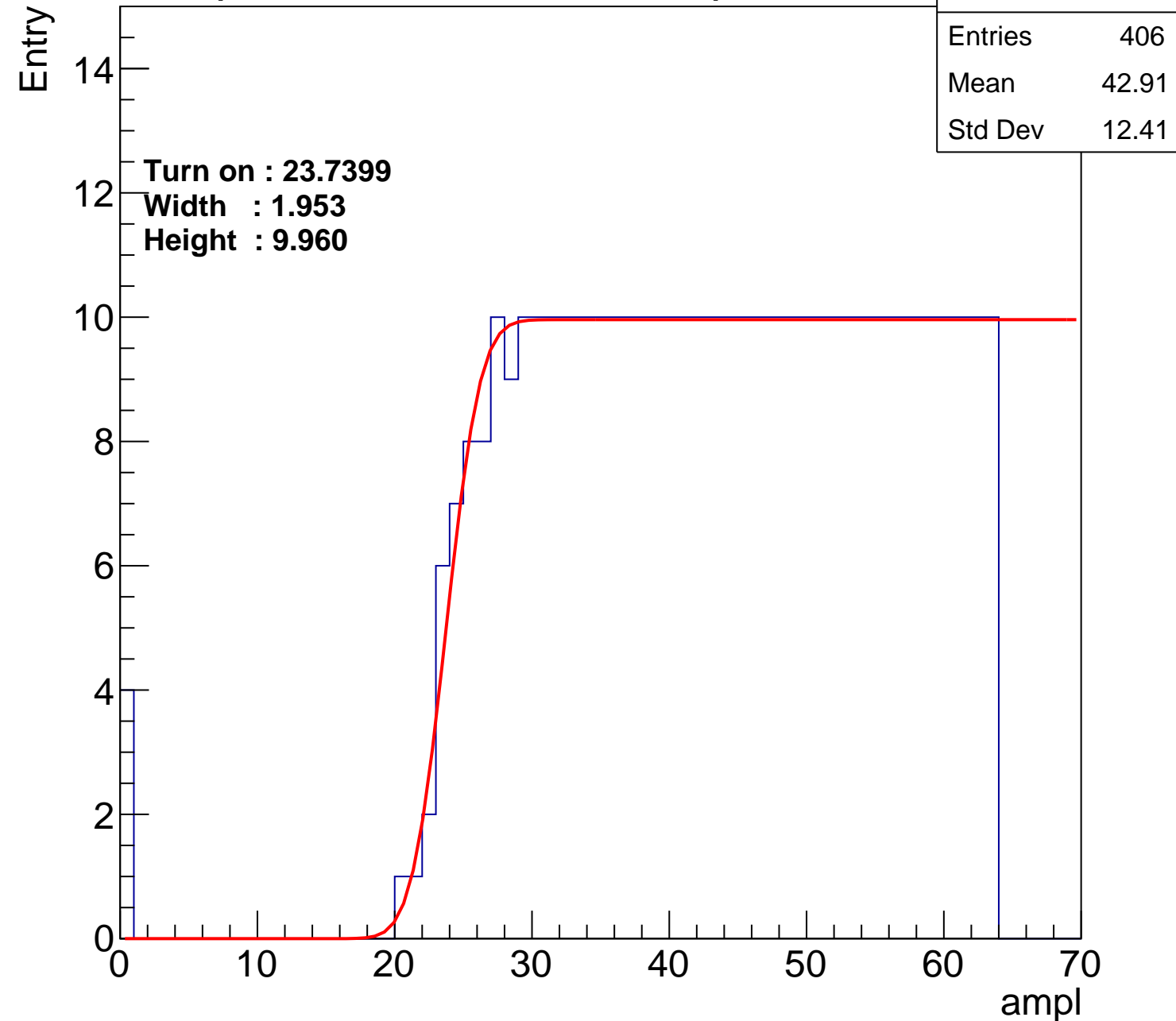
Width : 1.953

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch42

calib_packv5_042523_0143.root, FC#10, port B3

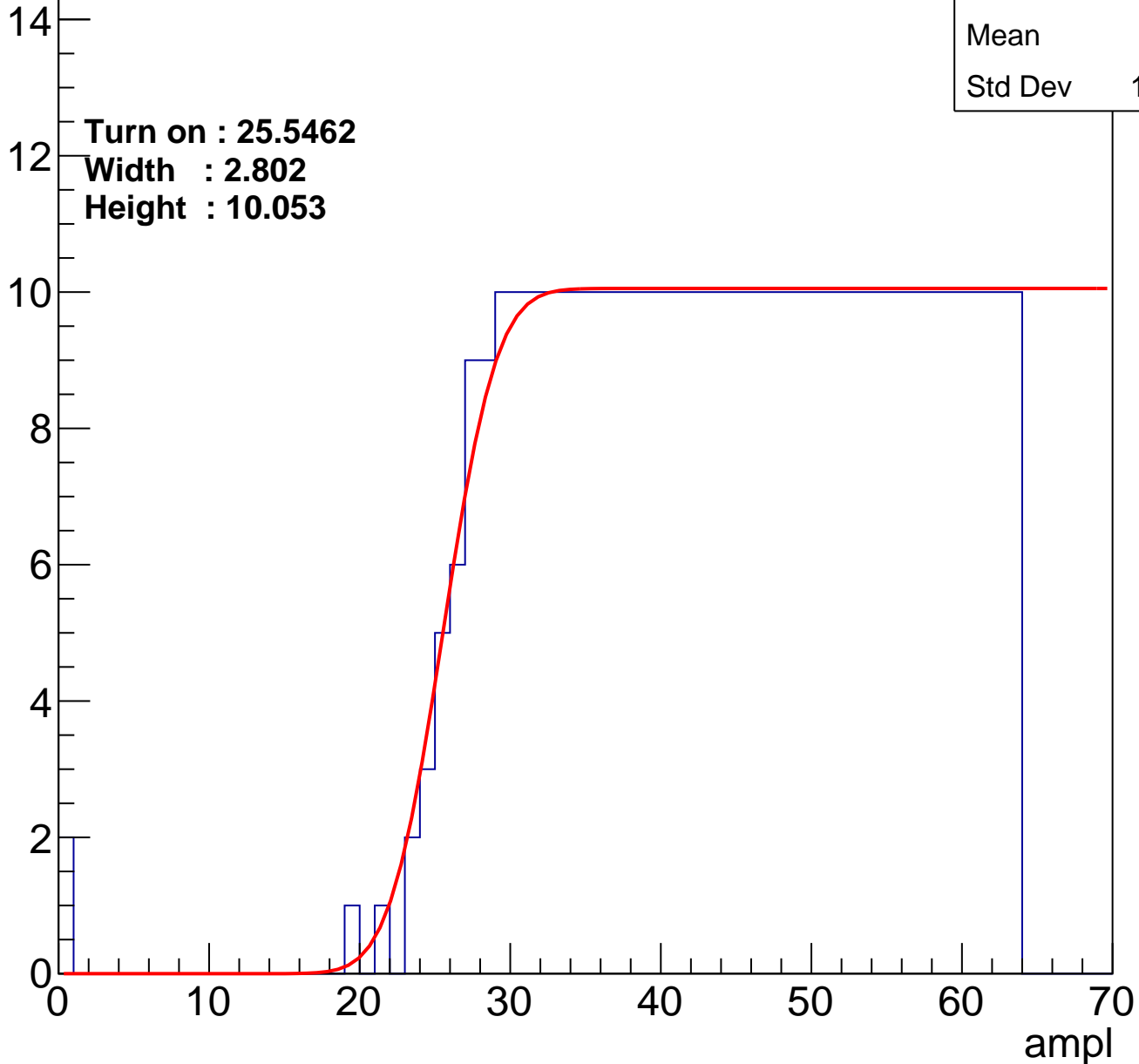
Entries	388
Mean	43.9
Std Dev	11.67

Turn on : 25.5462

Width : 2.802

Height : 10.053

Entry



B1L002S, U16-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.97
Std Dev	11.9

Turn on : 25.8305

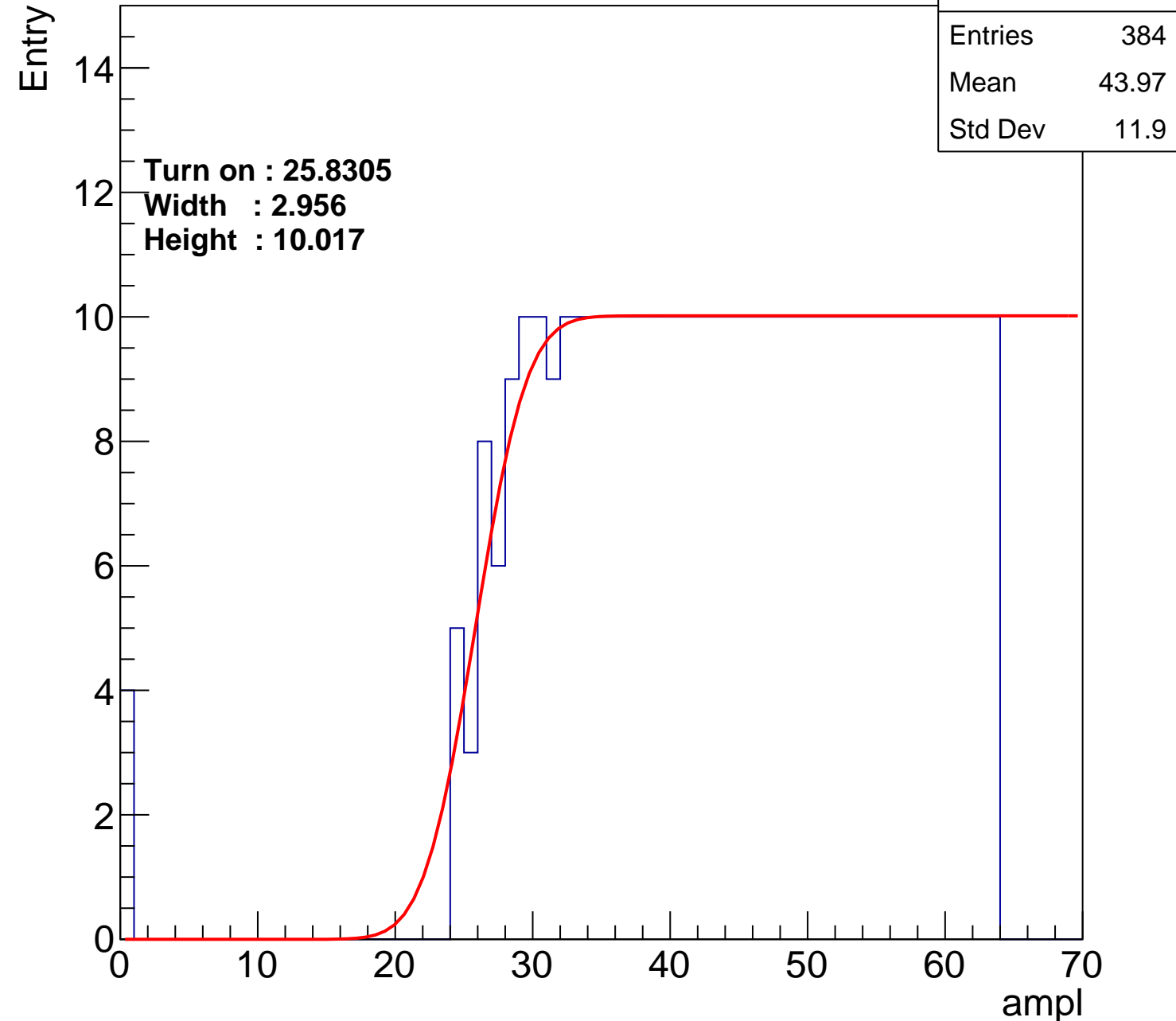
Width : 2.956

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.99
Std Dev	12.26

Turn on : 24.3924

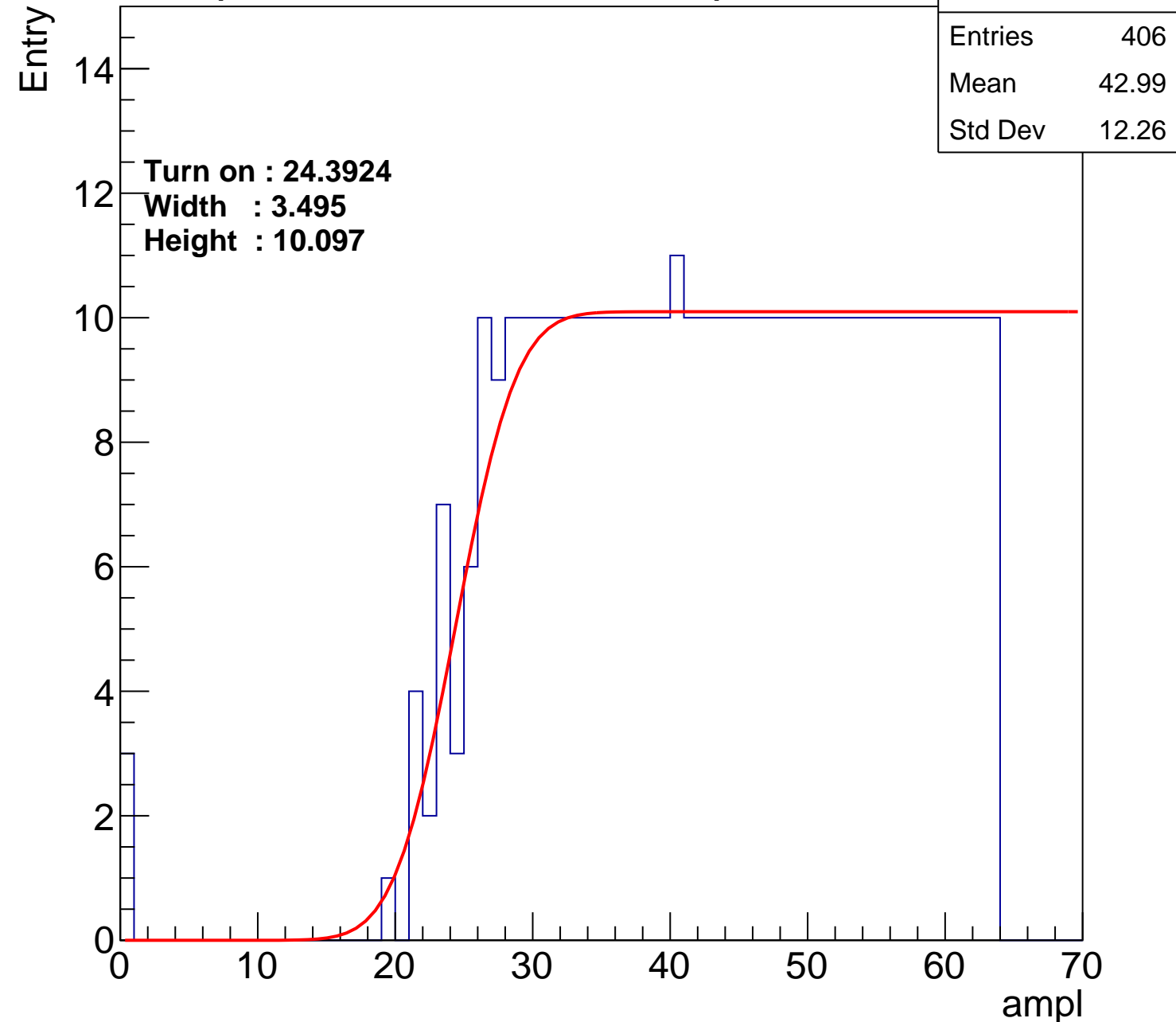
Width : 3.495

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch45

calib_packv5_042523_0143.root, FC#10, port B3

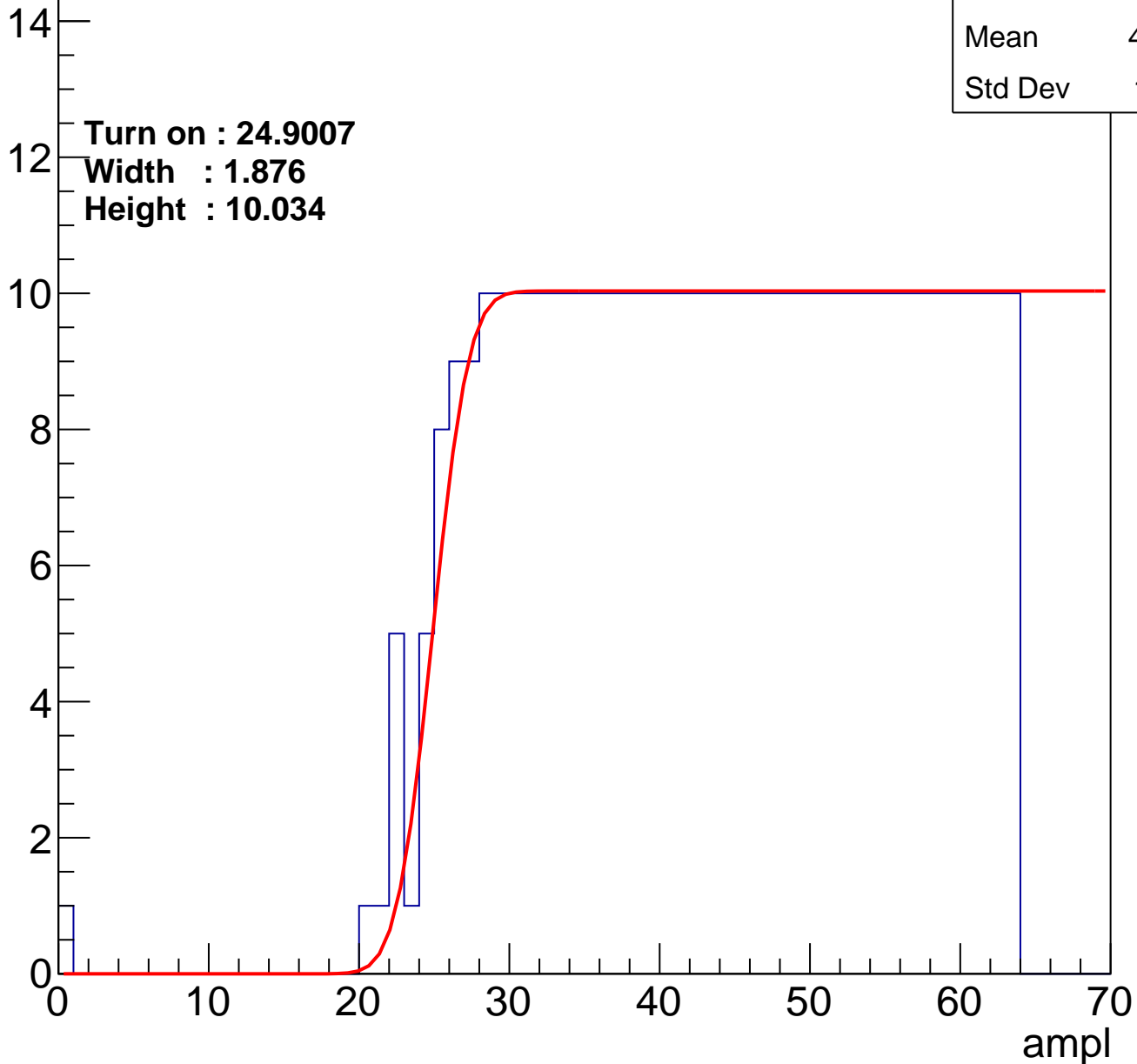
Entries	400
Mean	43.38
Std Dev	11.81

Turn on : 24.9007

Width : 1.876

Height : 10.034

Entry



B1L002S, U16-ch46

calib_packv5_042523_0143.root, FC#10, port B3

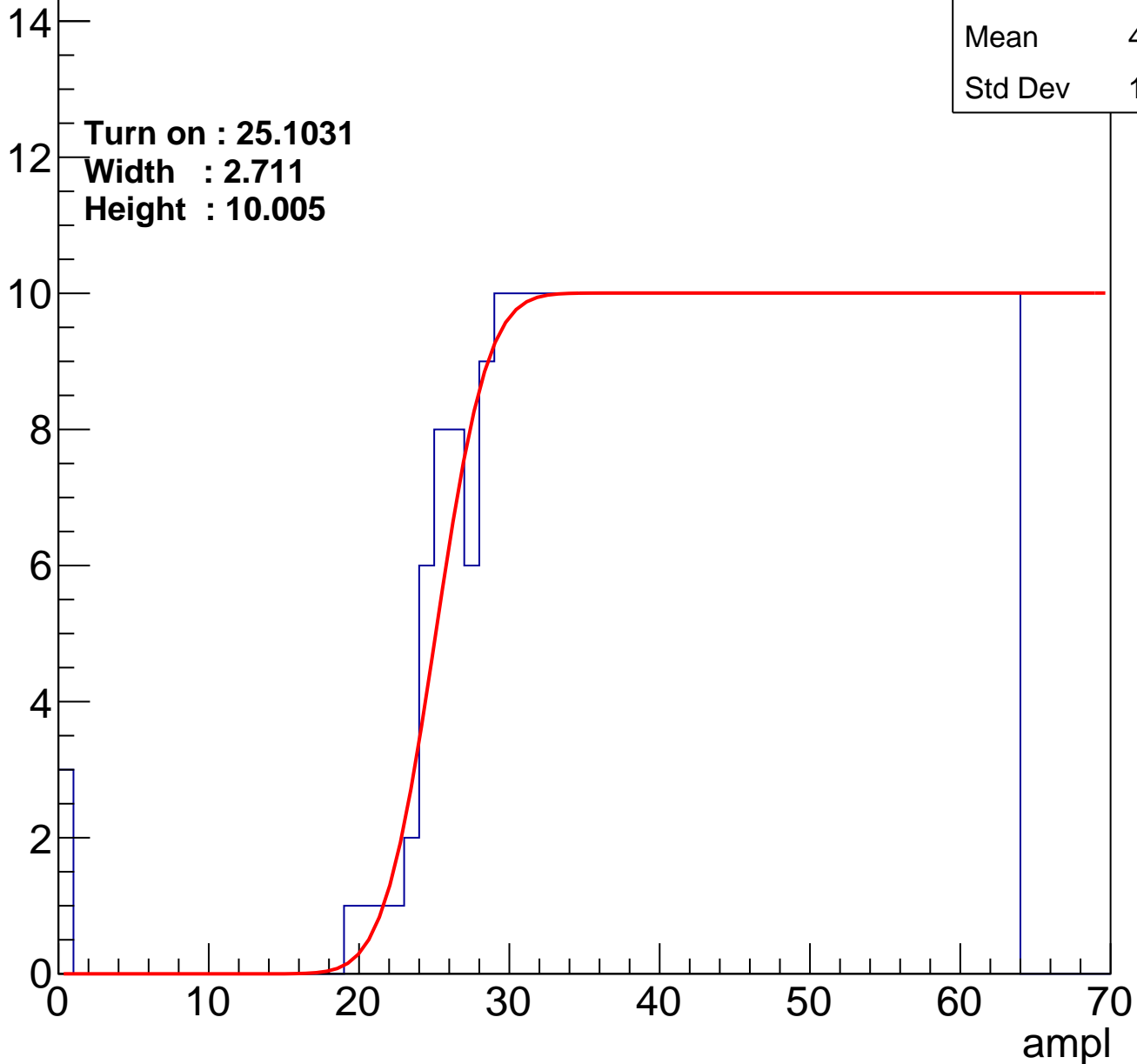
Entries	396
Mean	43.42
Std Dev	12.08

Turn on : 25.1031

Width : 2.711

Height : 10.005

Entry



B1L002S, U16-ch47

calib_packv5_042523_0143.root, FC#10, port B3

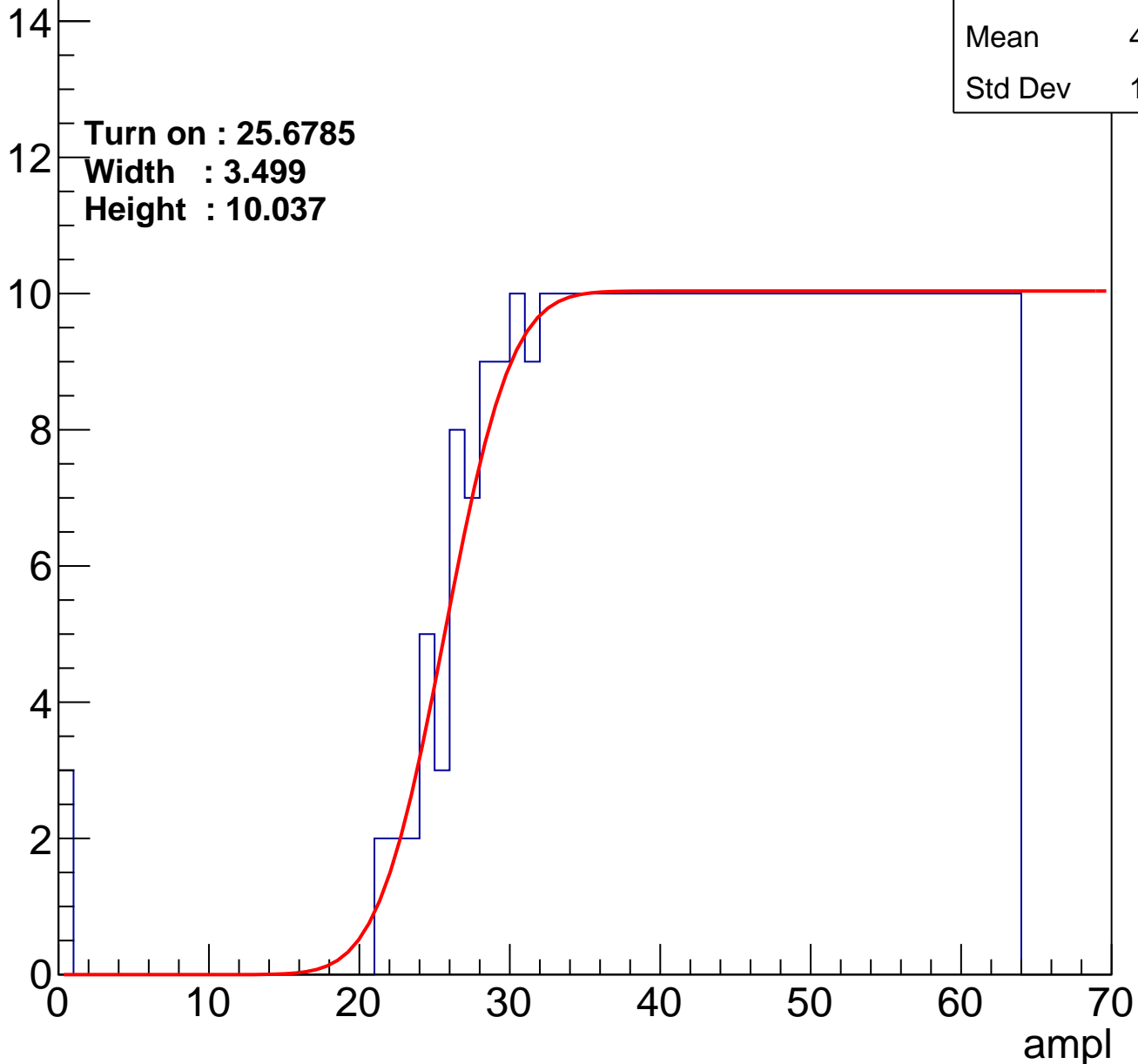
Entries	389
Mean	43.74
Std Dev	11.93

Turn on : 25.6785

Width : 3.499

Height : 10.037

Entry



B1L002S, U16-ch48

calib_packv5_042523_0143.root, FC#10, port B3

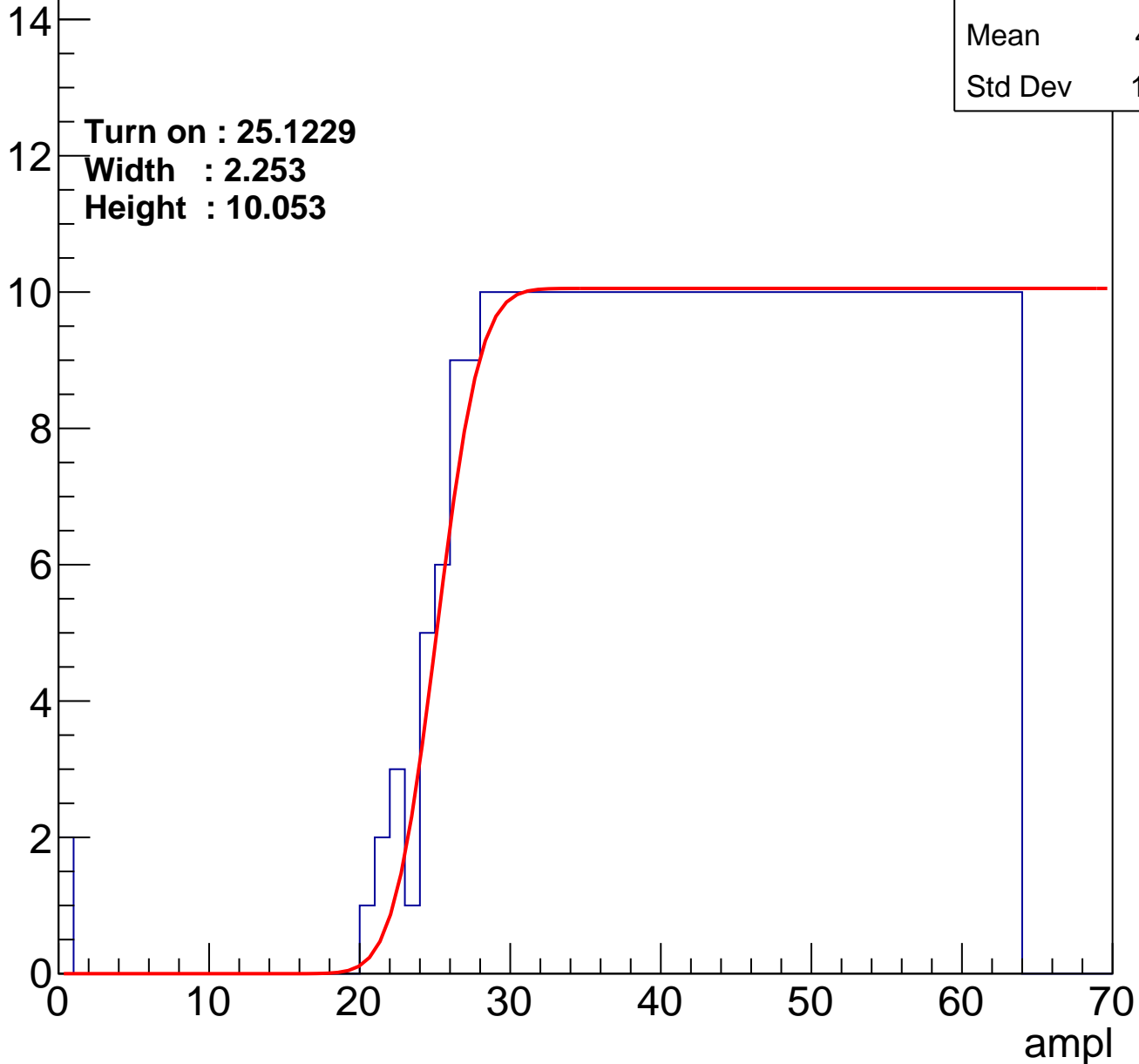
Entries	398
Mean	43.41
Std Dev	11.93

Turn on : 25.1229

Width : 2.253

Height : 10.053

Entry



B1L002S, U16-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.56
Std Dev	12.01

Turn on : 25.2536

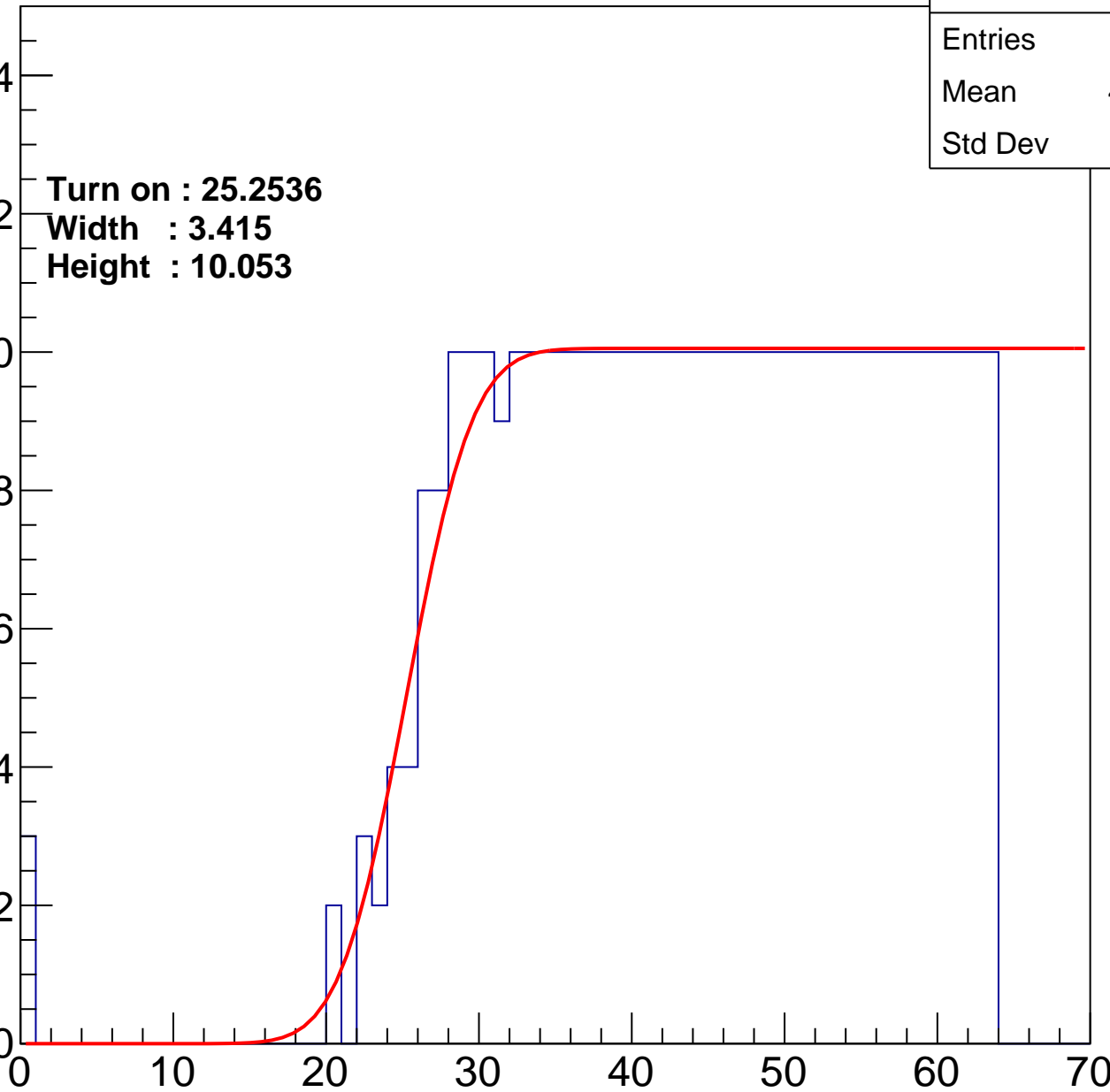
Width : 3.415

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch50

calib_packv5_042523_0143.root, FC#10, port B3

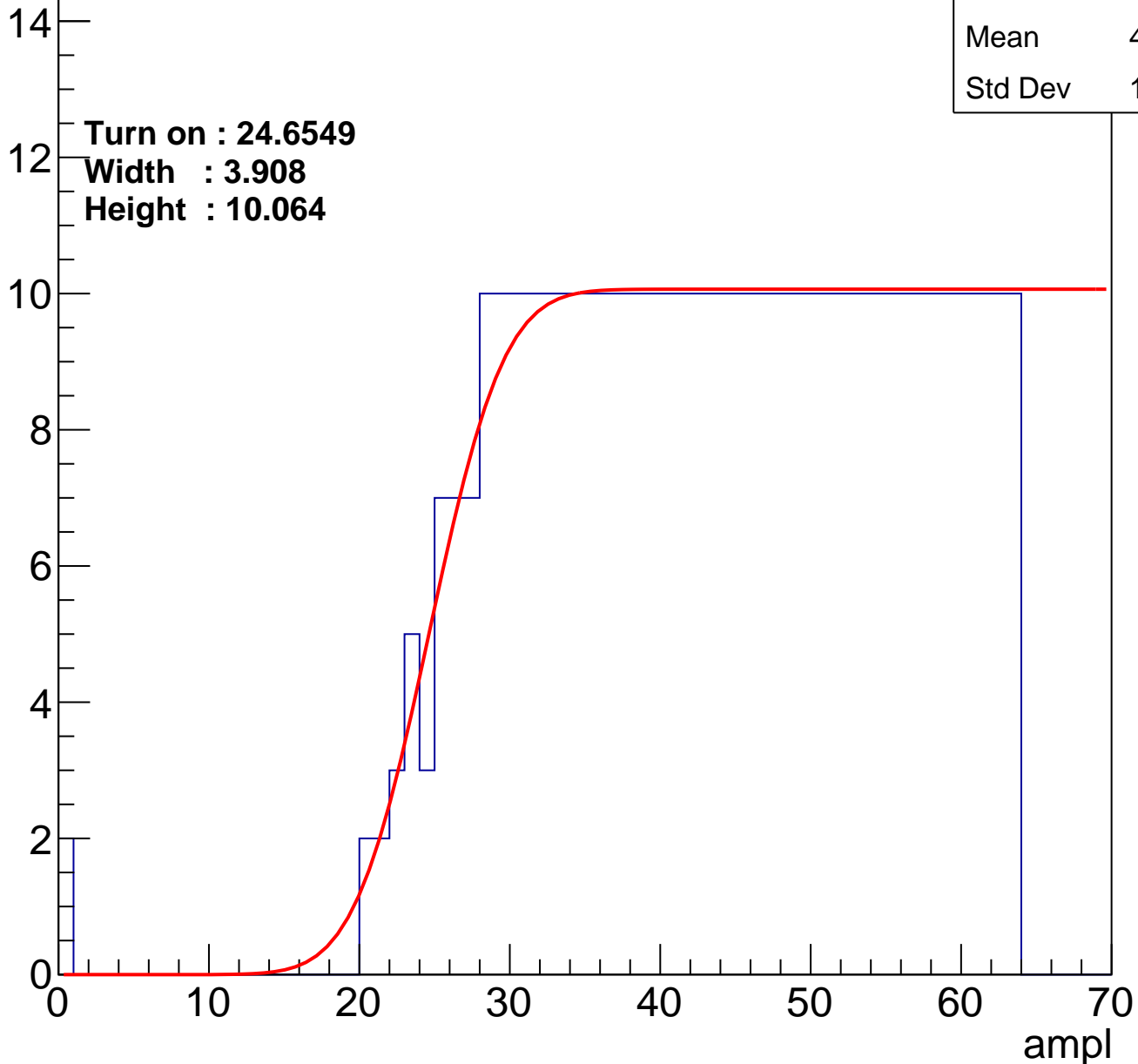
Entries	398
Mean	43.37
Std Dev	11.99

Turn on : 24.6549

Width : 3.908

Height : 10.064

Entry



B1L002S, U16-ch51

calib_packv5_042523_0143.root, FC#10, port B3

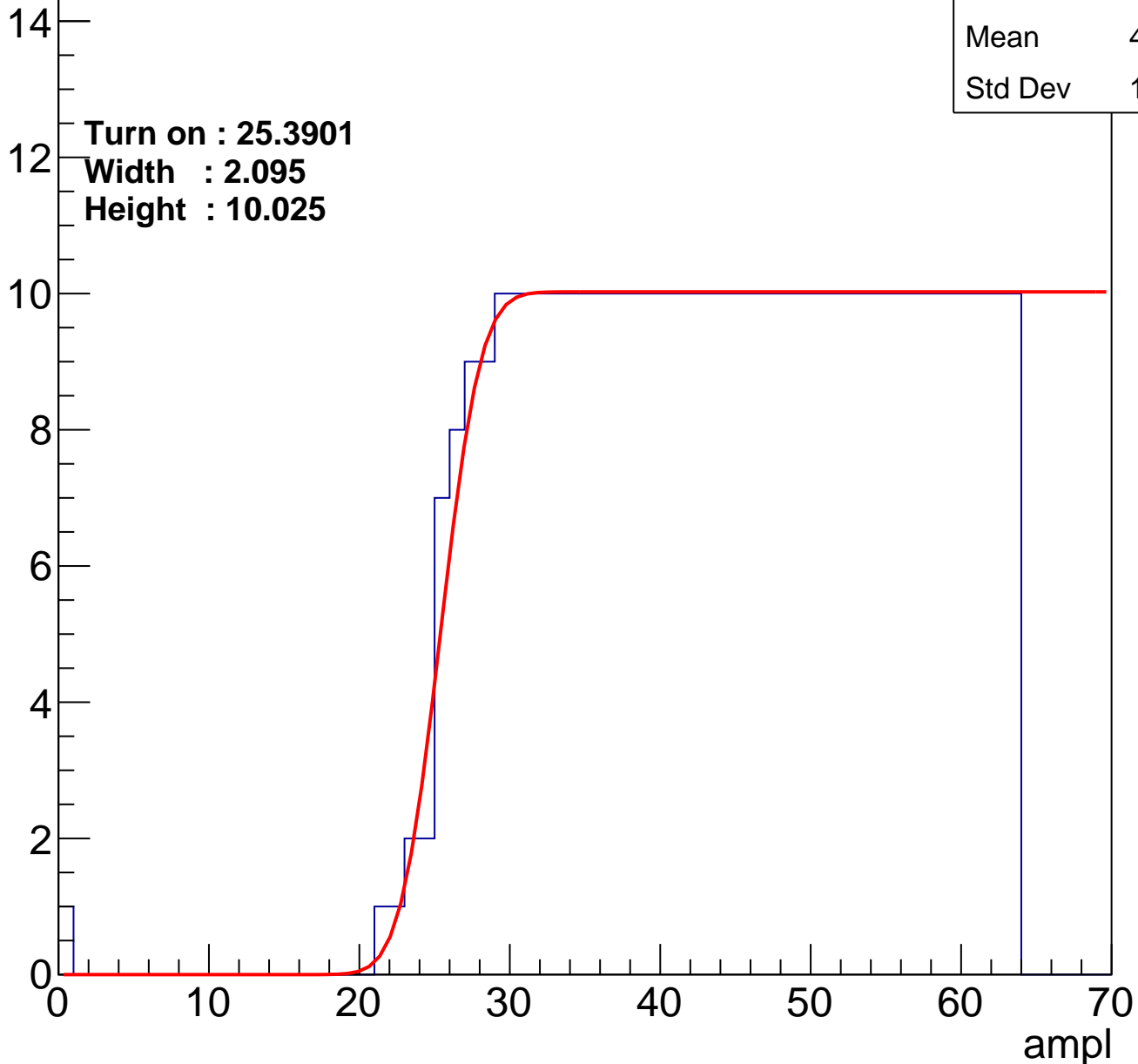
Entries	390
Mean	43.88
Std Dev	11.52

Turn on : 25.3901

Width : 2.095

Height : 10.025

Entry



B1L002S, U16-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.2651

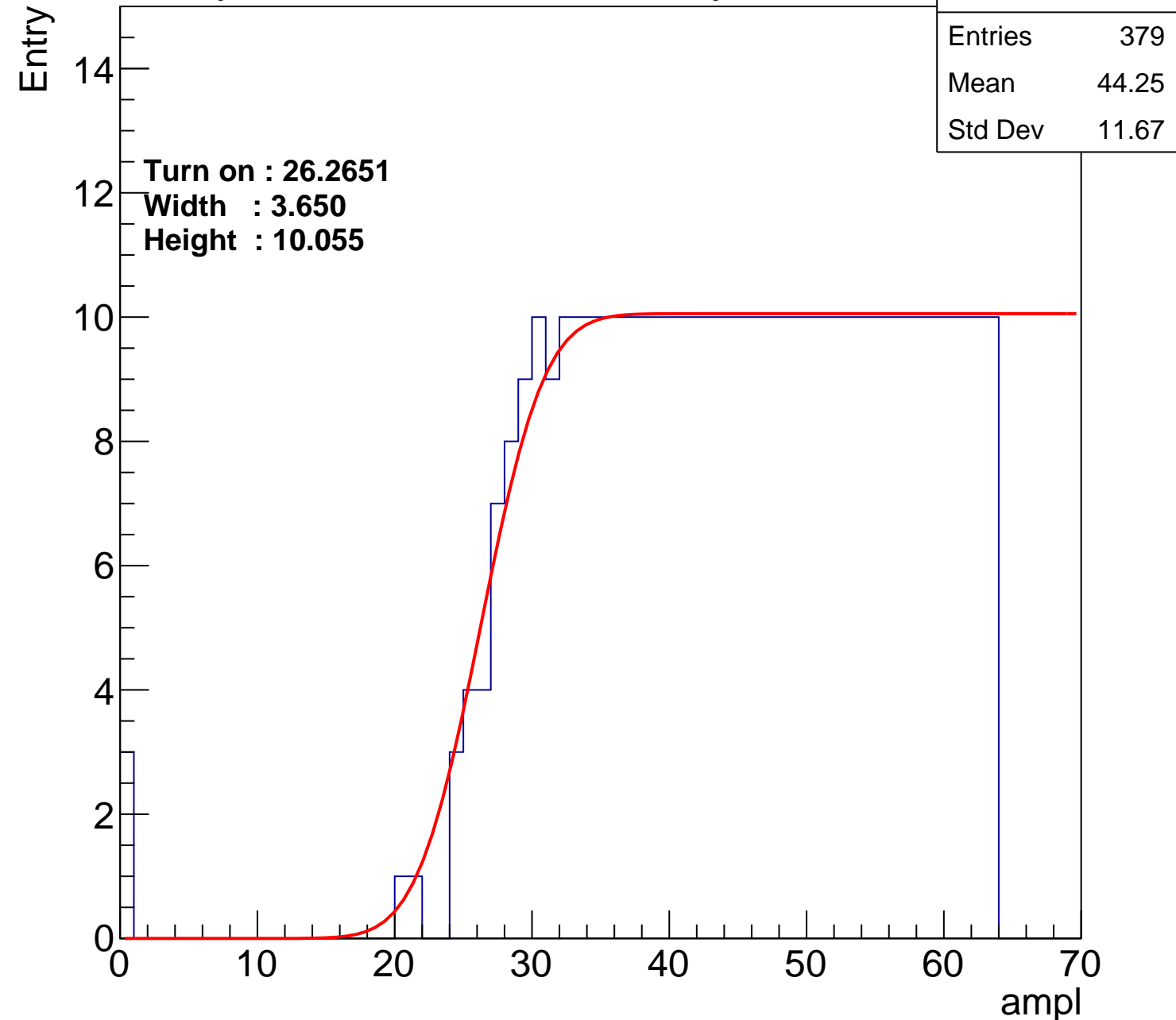
Width : 3.650

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.74
Std Dev	11.65

Turn on : 25.4286

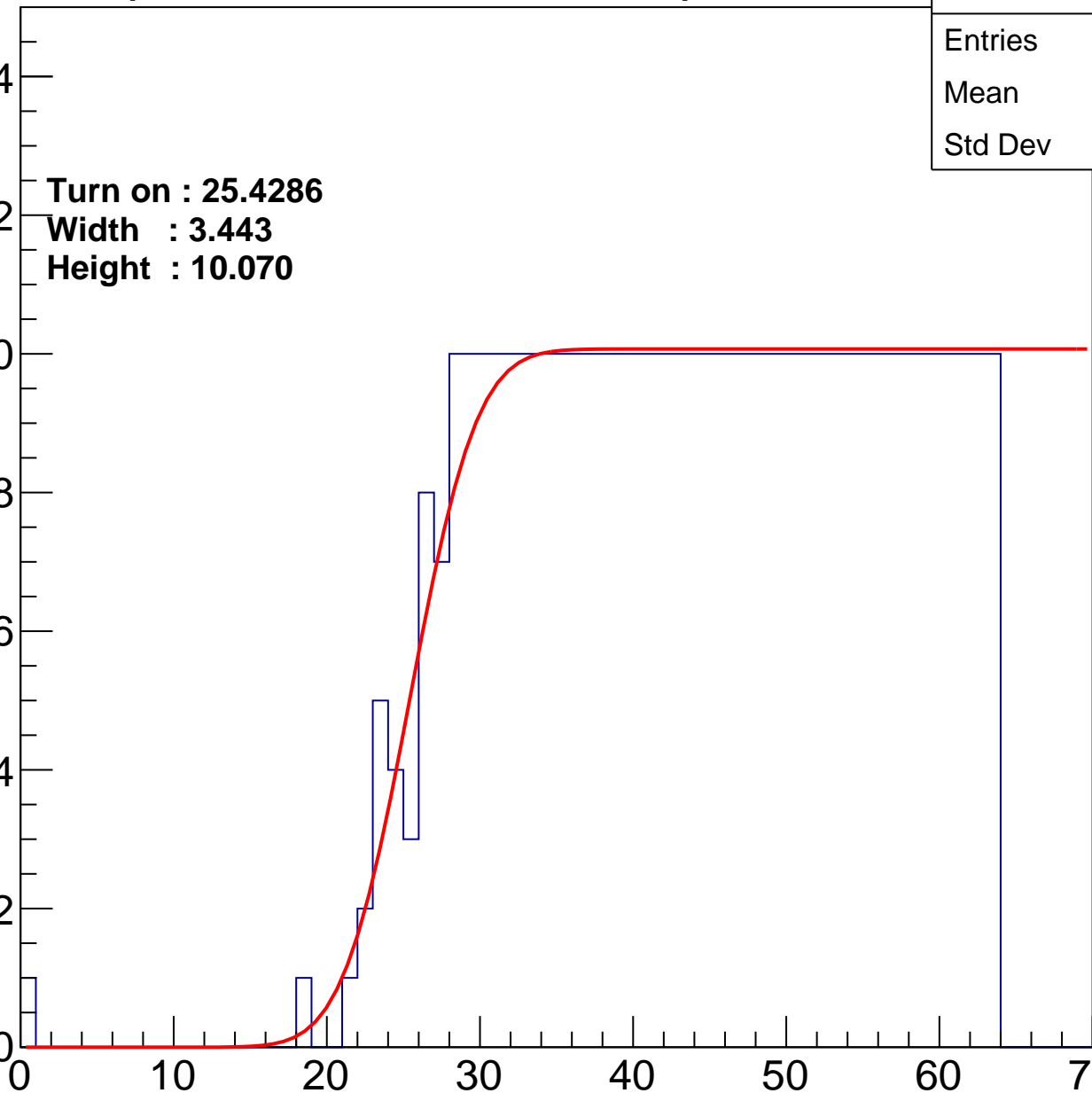
Width : 3.443

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch54

calib_packv5_042523_0143.root, FC#10, port B3

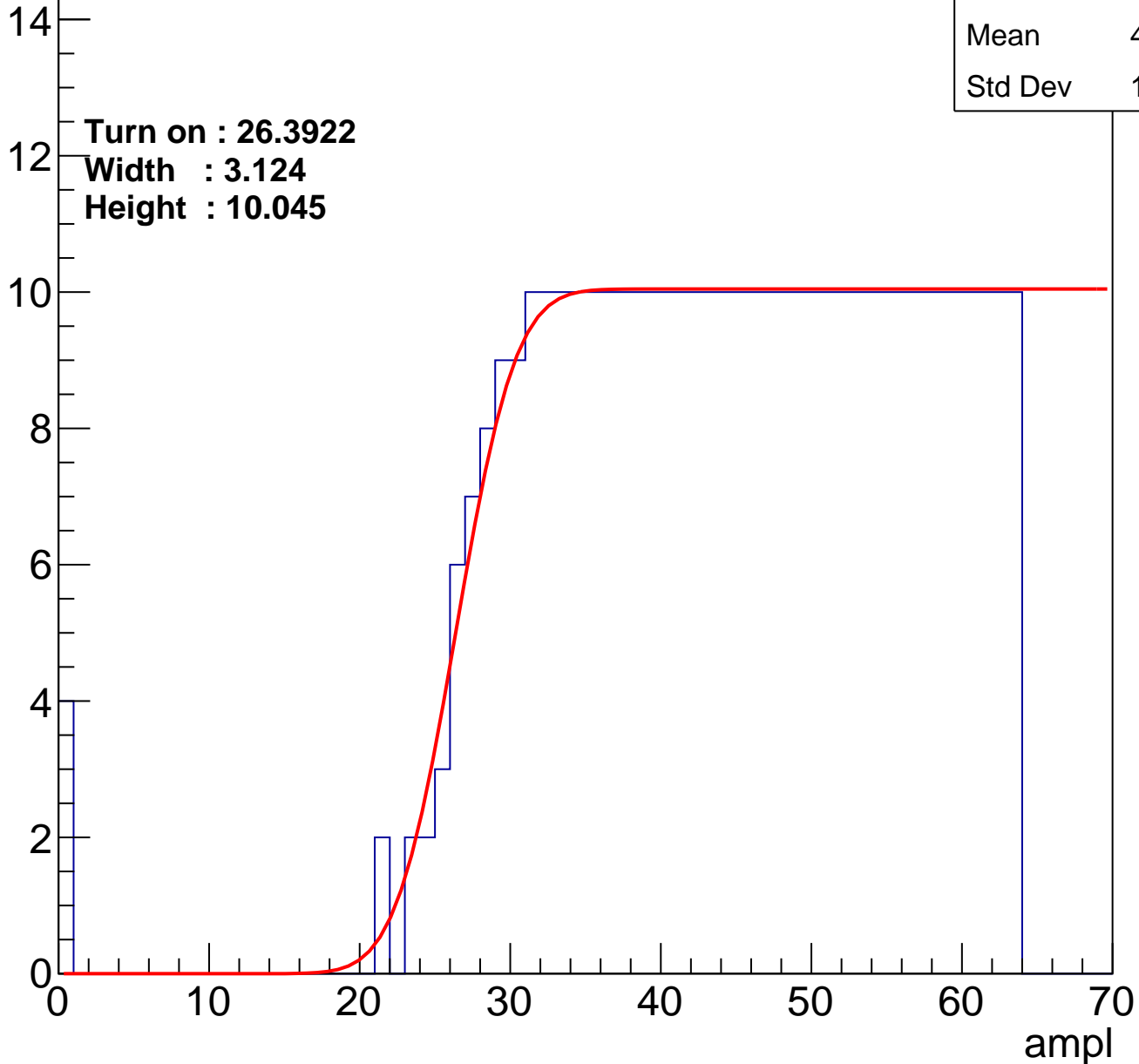
Entries	382
Mean	44.03
Std Dev	11.92

Turn on : 26.3922

Width : 3.124

Height : 10.045

Entry

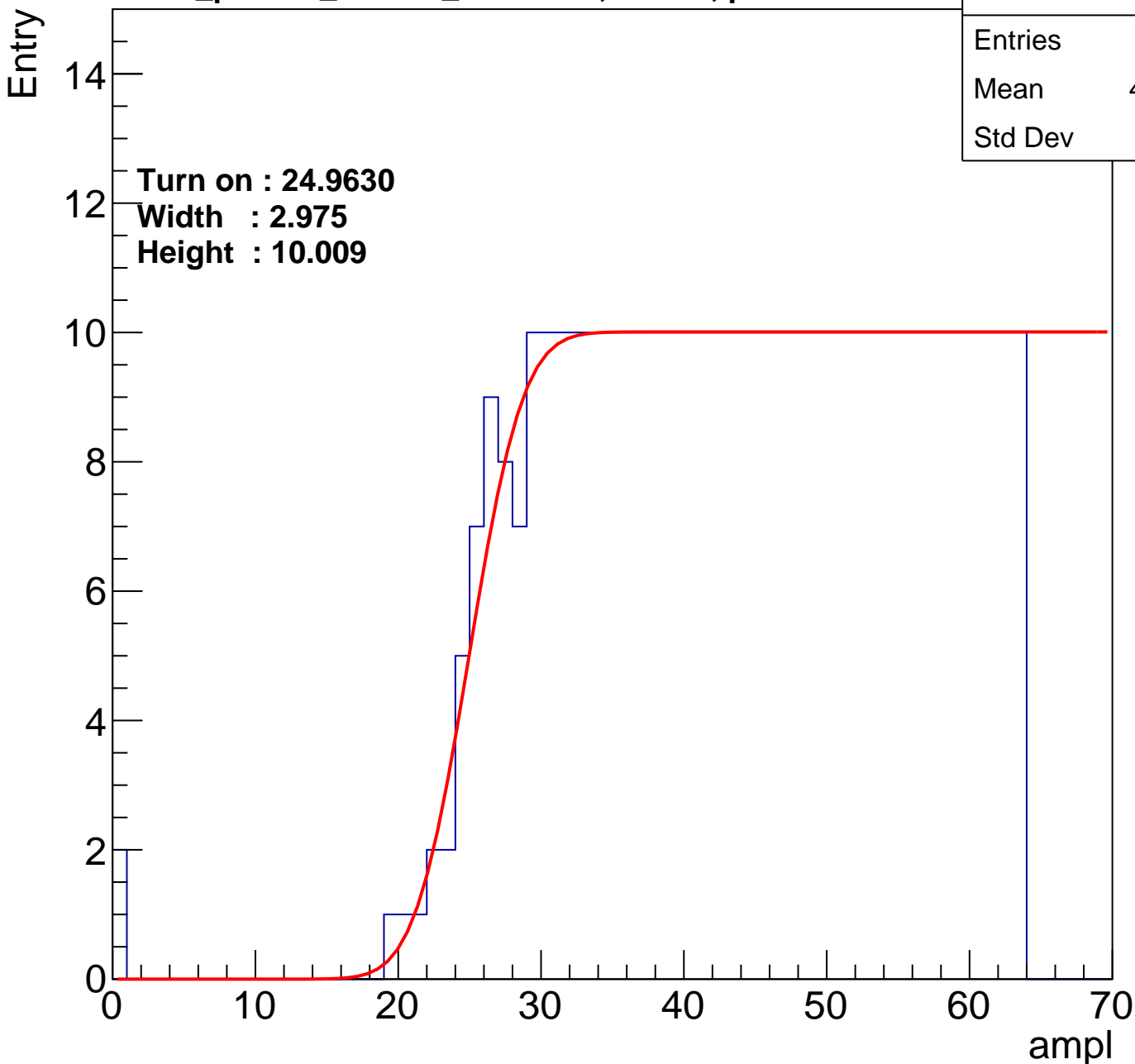


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.52
Std Dev	11.91

Height : 10.009



B1L002S, U16-ch56

calib_packv5_042523_0143.root, FC#10, port B3

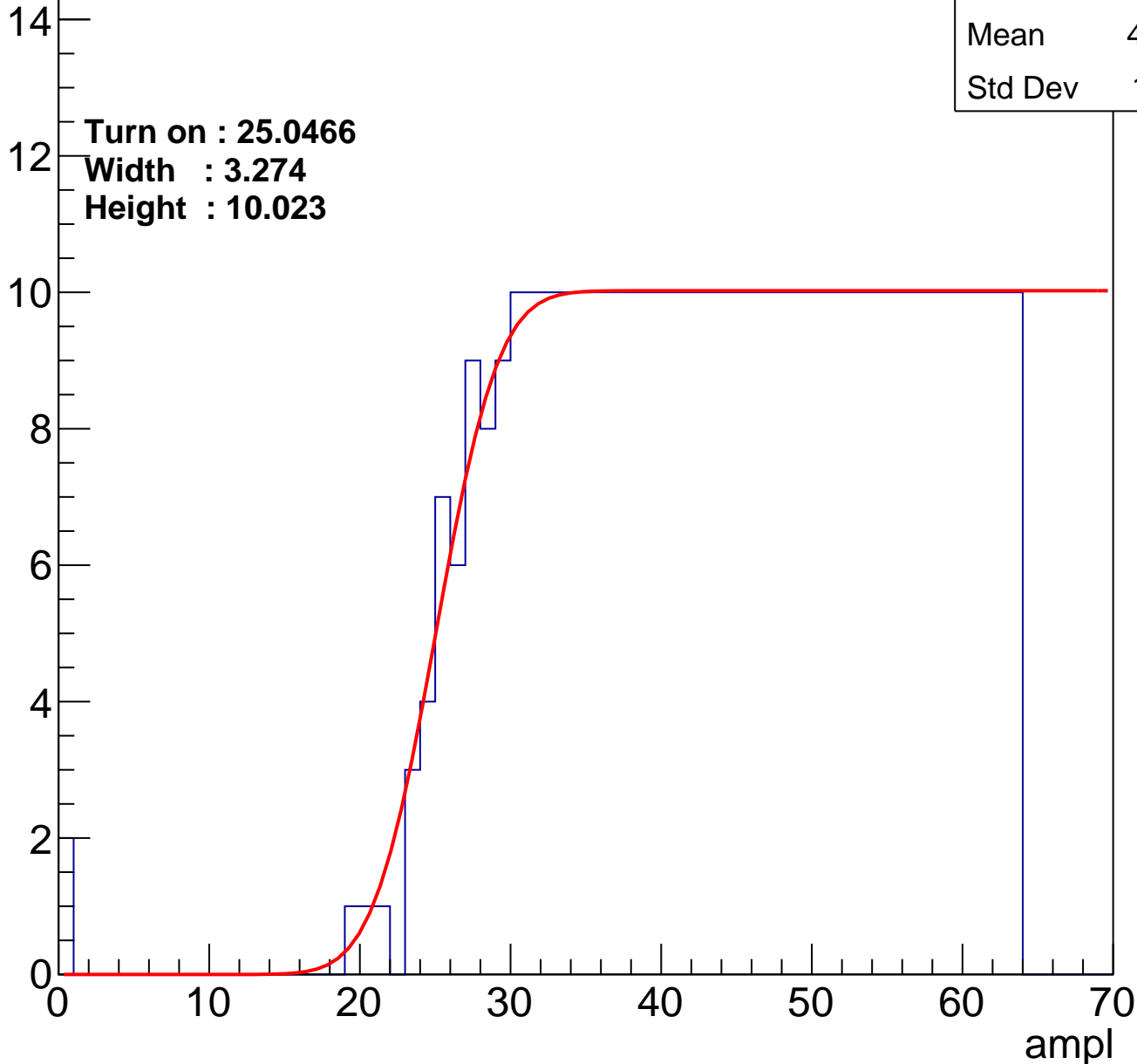
Entries	391
Mean	43.72
Std Dev	11.81

Turn on : 25.0466

Width : 3.274

Height : 10.023

Entry



B1L002S, U16-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.68
Std Dev	11.63

Turn on : 24.6614

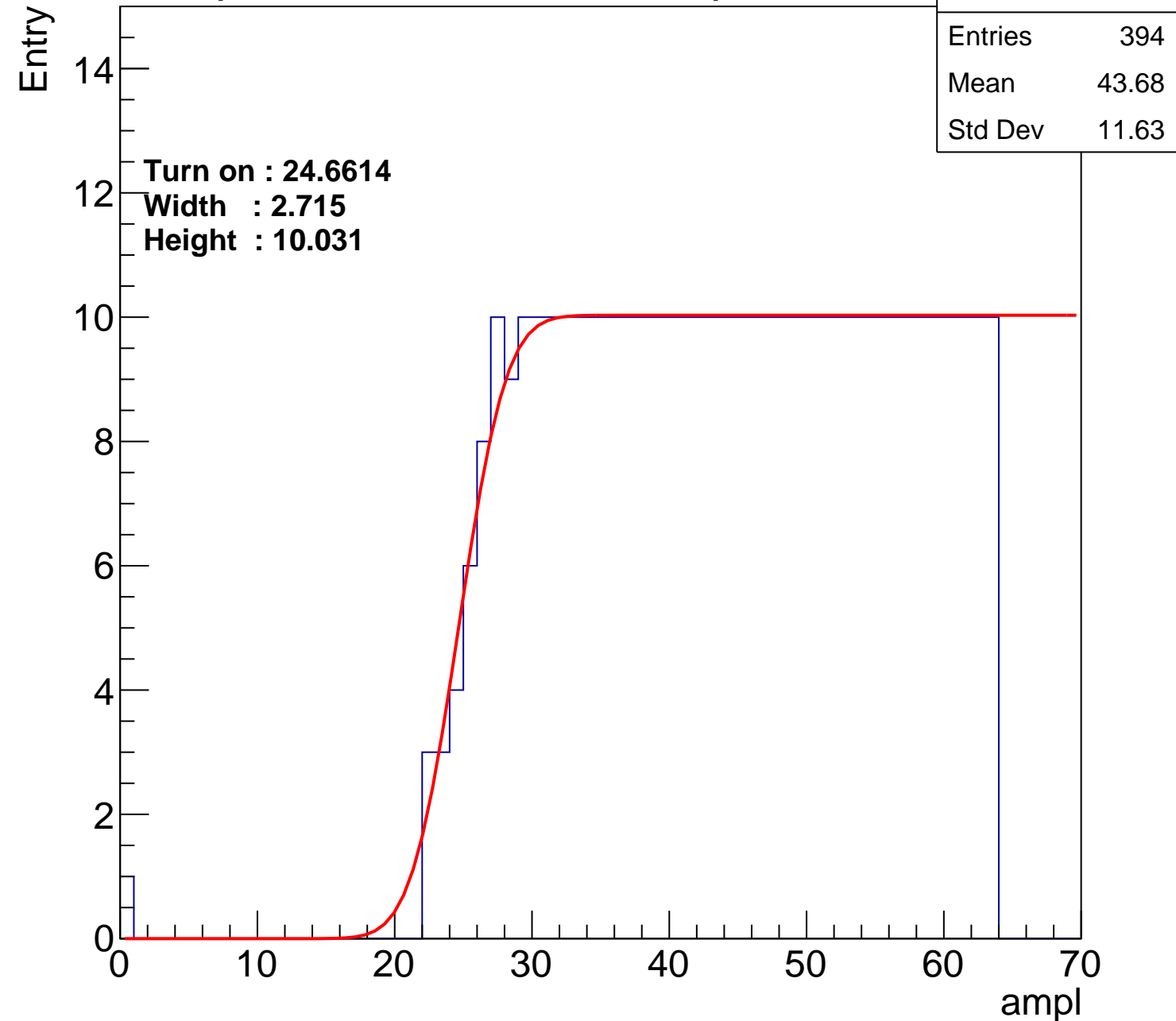
Width : 2.715

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.88
Std Dev	12.14

Turn on : 23.1837

Width : 3.703

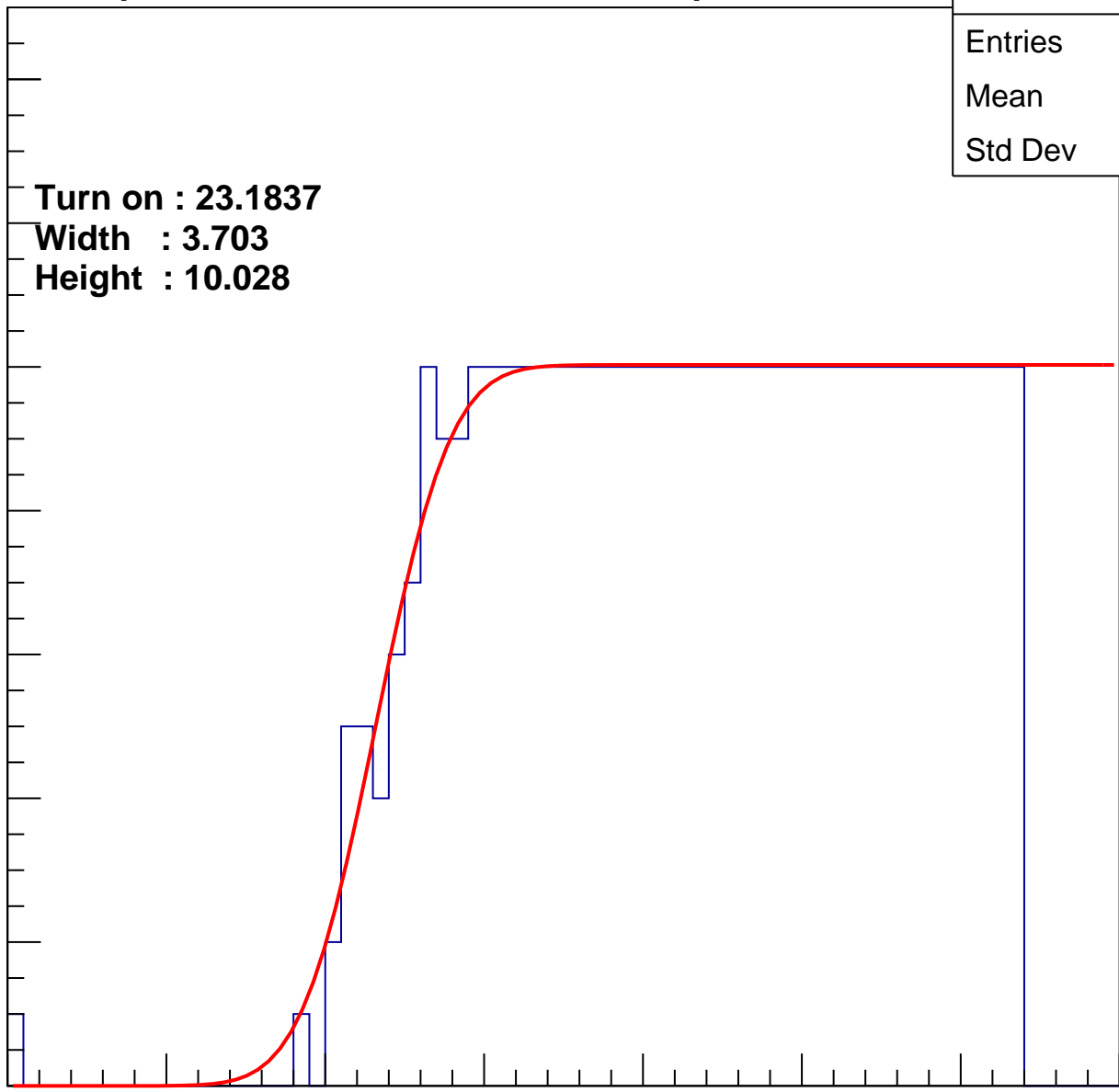
Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U16-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.07
Std Dev	11.48

Turn on : 24.7937

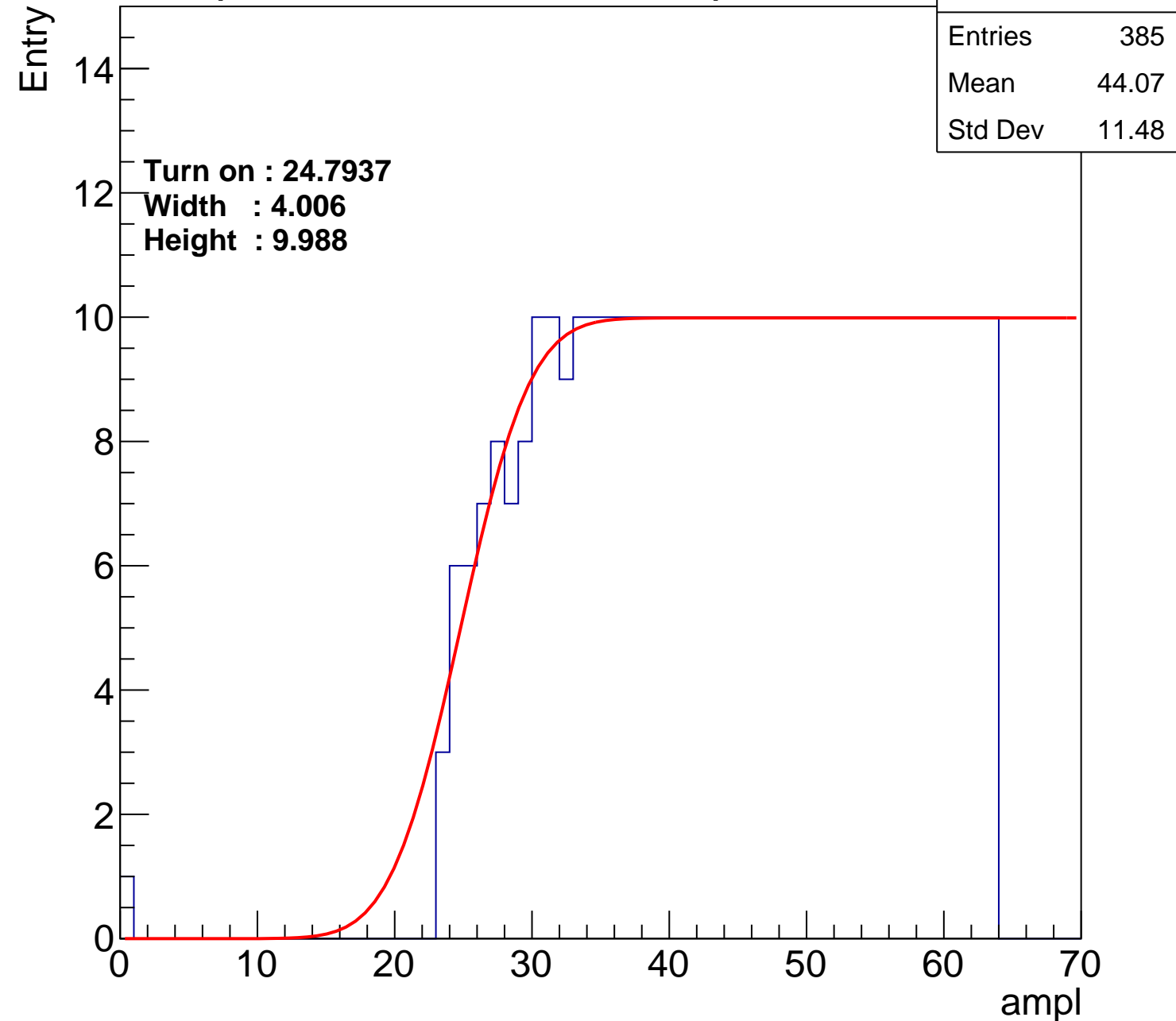
Width : 4.006

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.45
Std Dev	12.03

Turn on : 25.2282

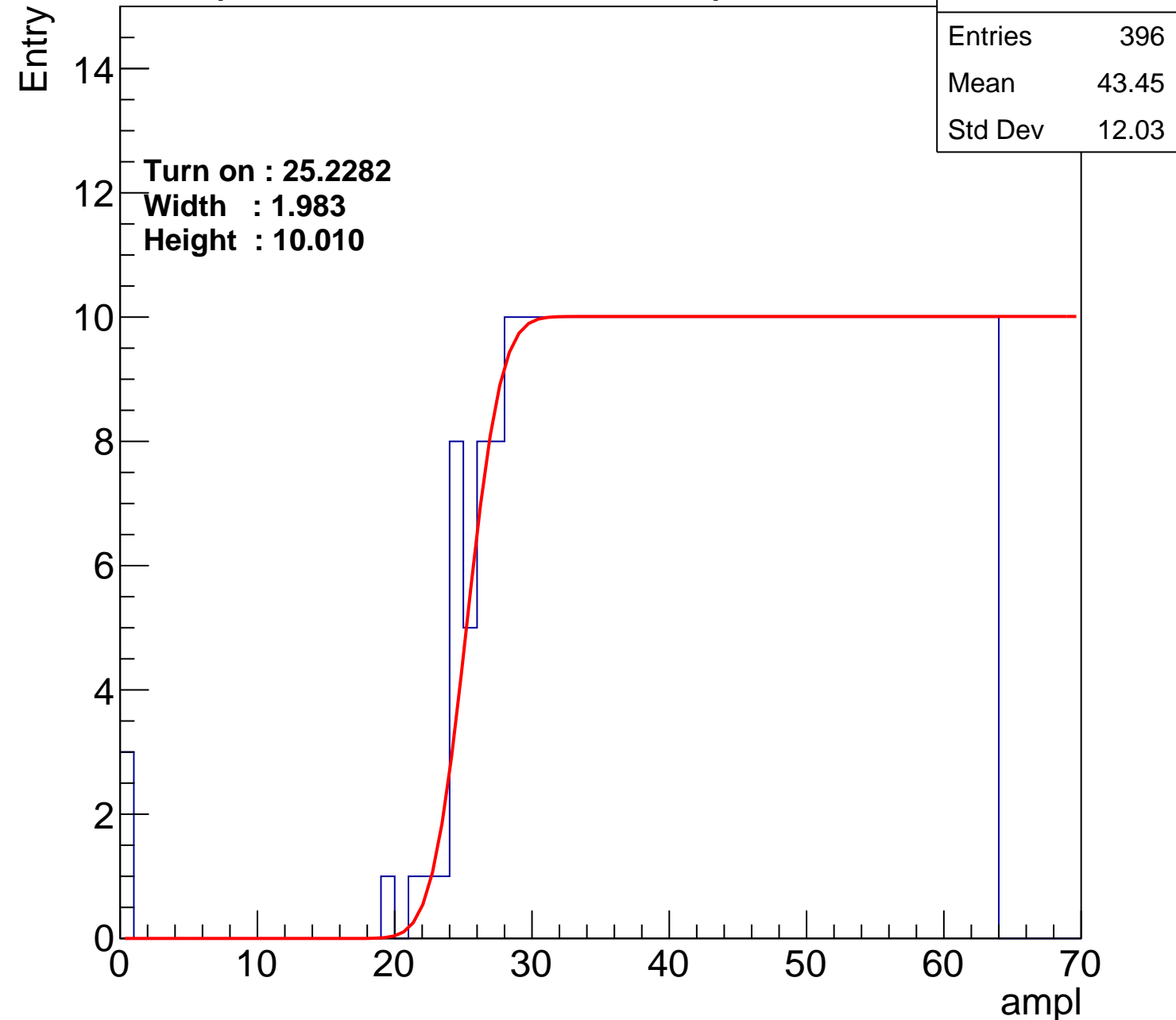
Width : 1.983

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 25.3256

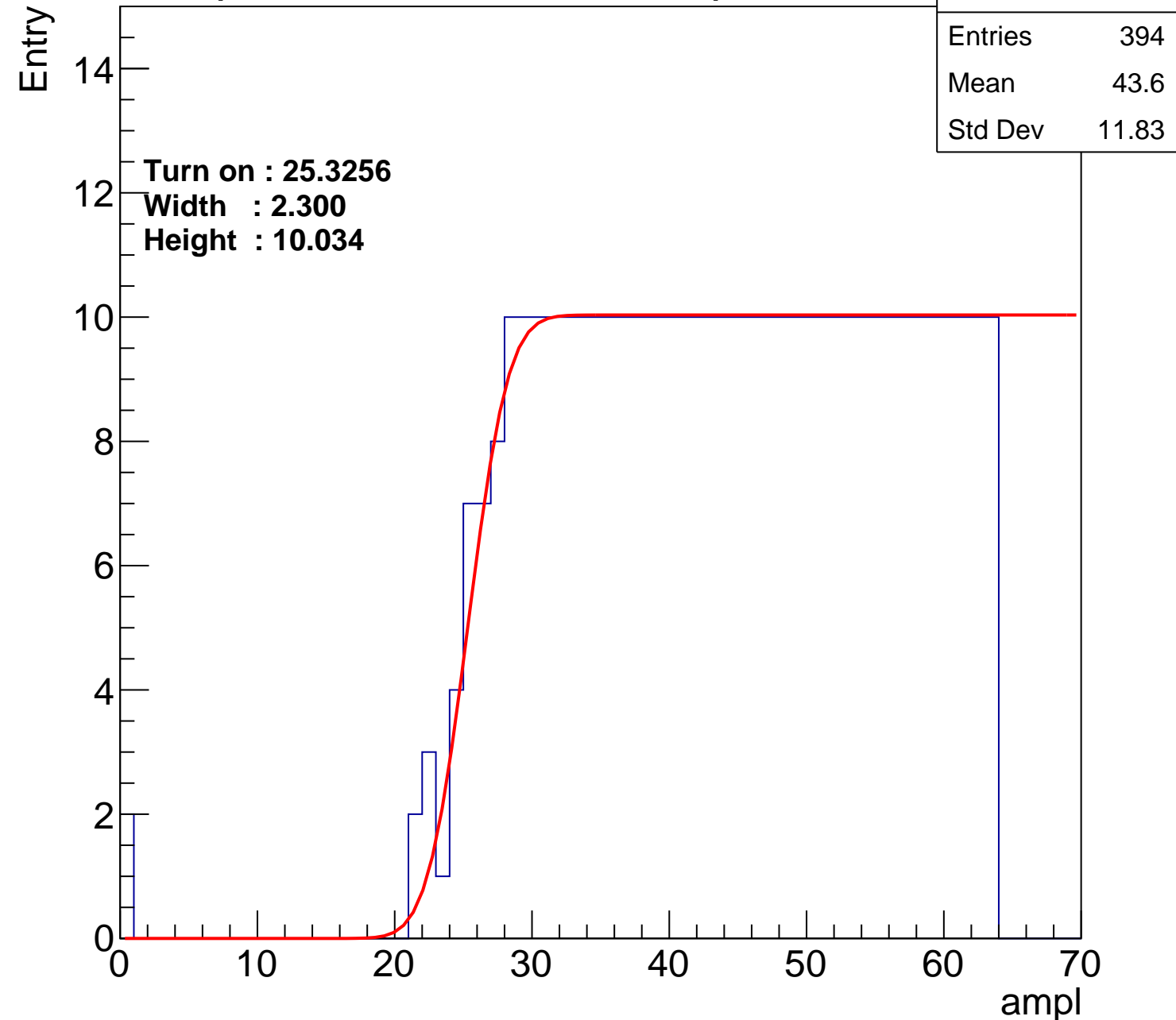
Width : 2.300

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.43
Std Dev	12.45

Turn on : 25.5108

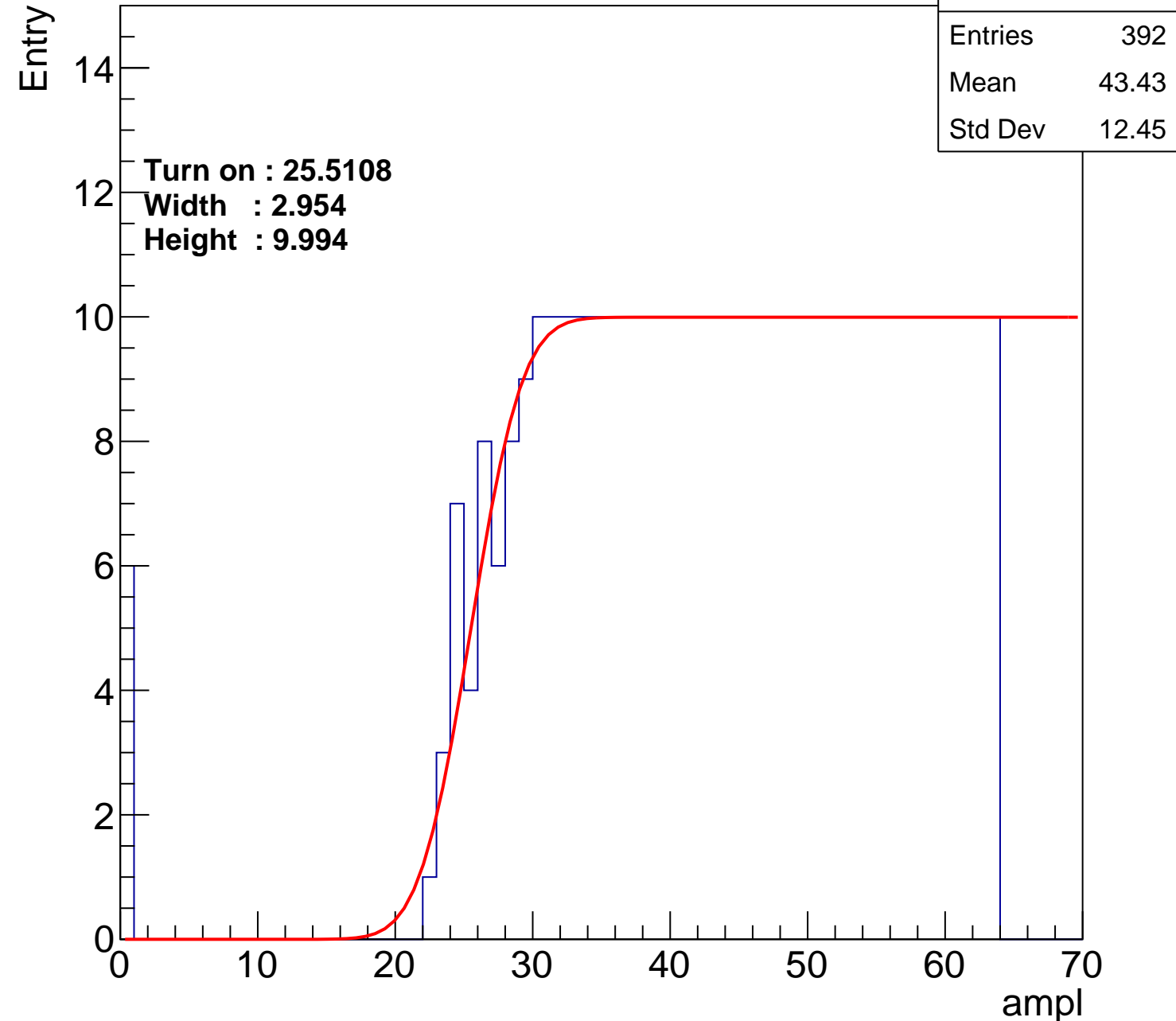
Width : 2.954

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.58
Std Dev	11.88

Turn on : 25.1181

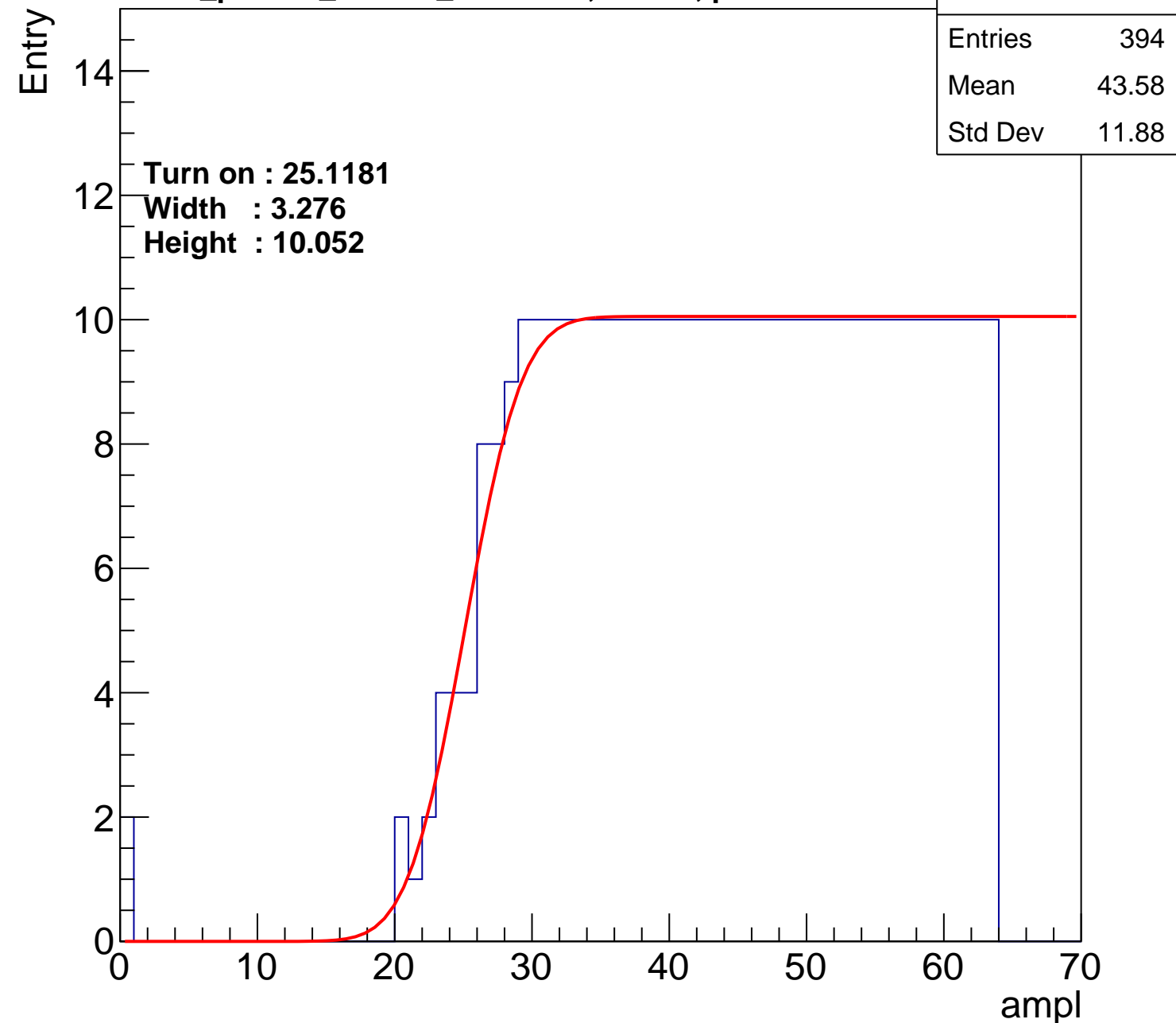
Width : 3.276

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.82
Std Dev	12.35

Turn on : 24.1571

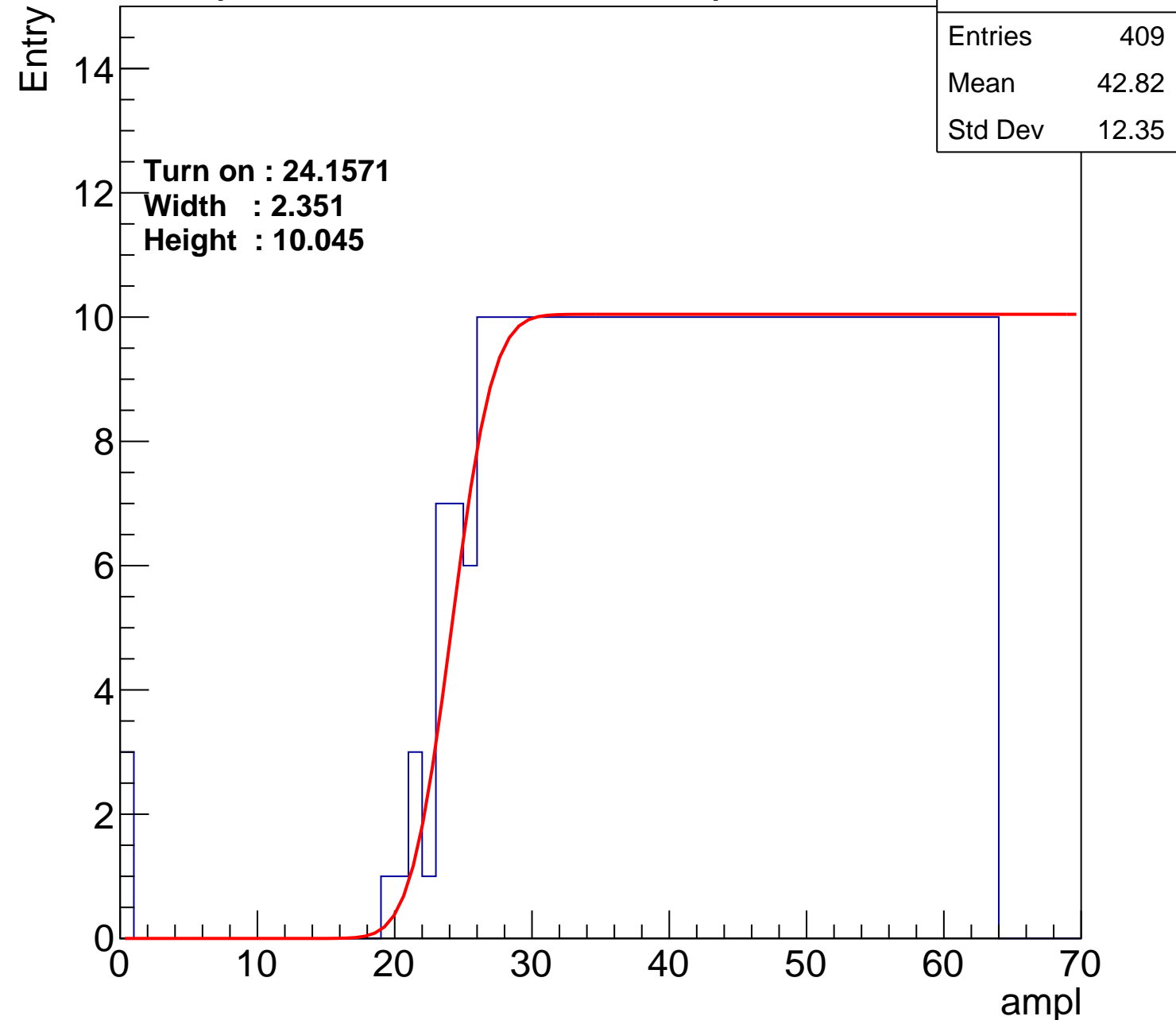
Width : 2.351

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch65

calib_packv5_042523_0143.root, FC#10, port B3

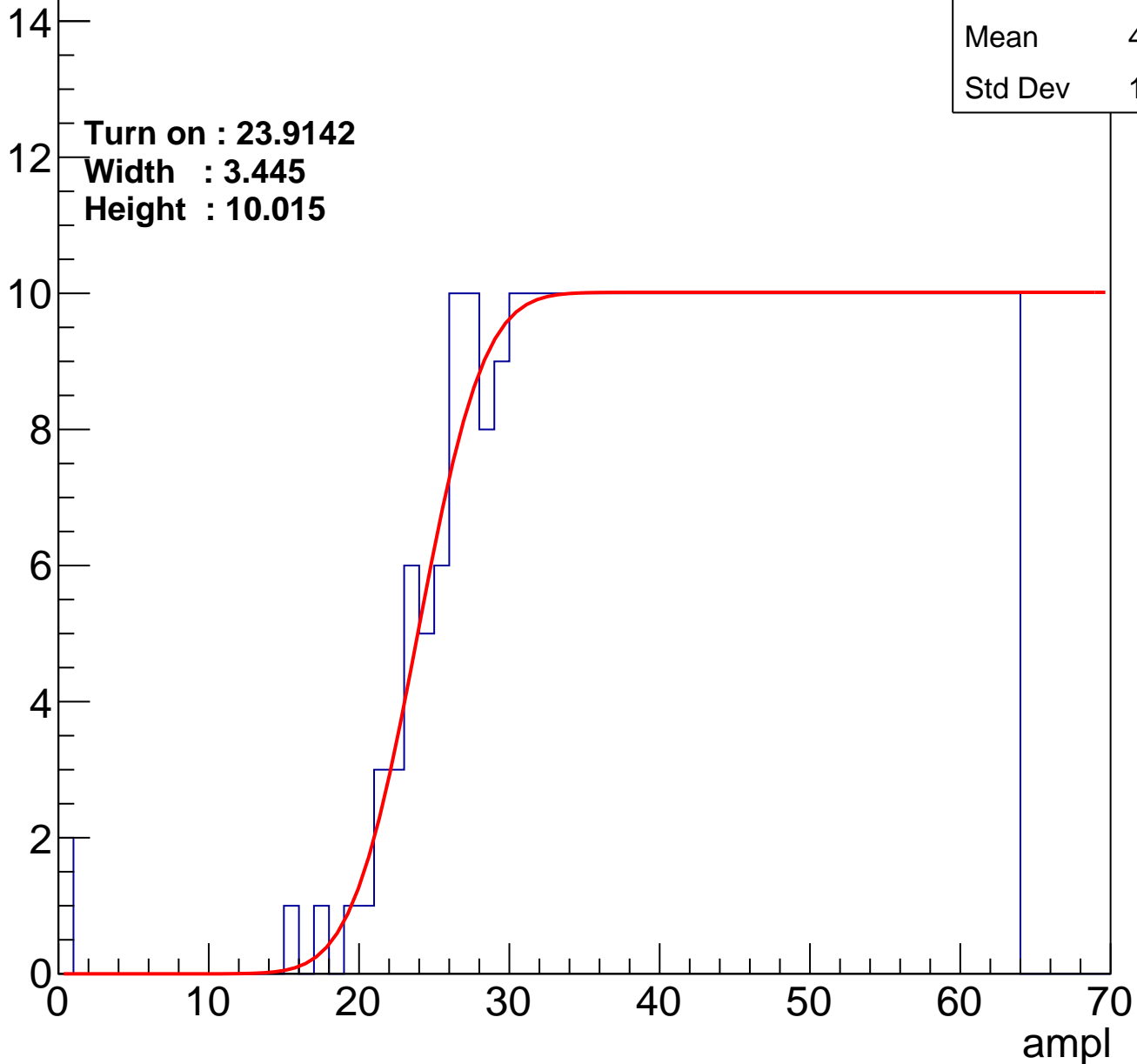
Entries	406
Mean	42.94
Std Dev	12.27

Turn on : 23.9142

Width : 3.445

Height : 10.015

Entry



B1L002S, U16-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.82
Std Dev	11.69

Turn on : 24.8981

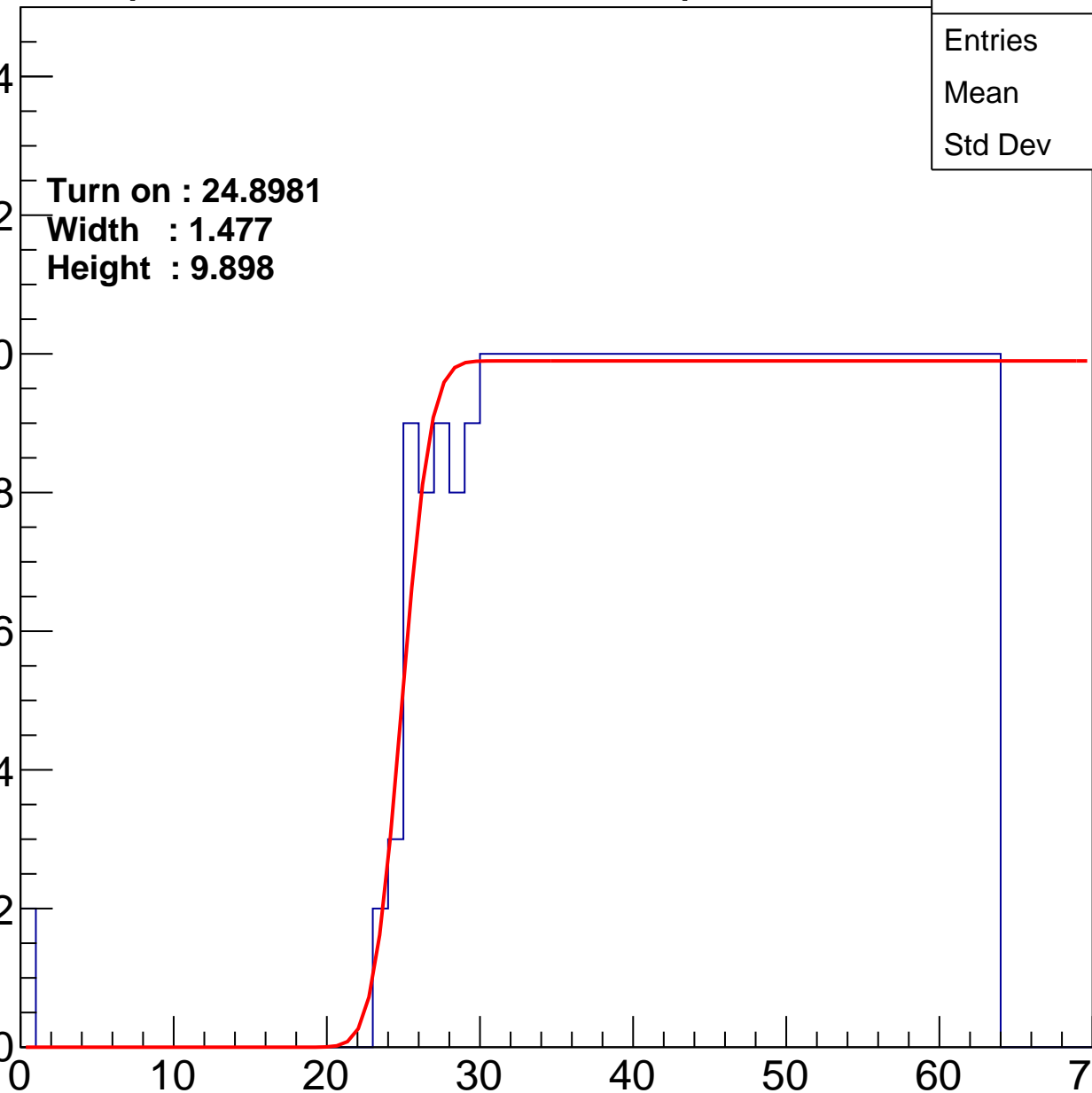
Width : 1.477

Height : 9.898

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch67

calib_packv5_042523_0143.root, FC#10, port B3

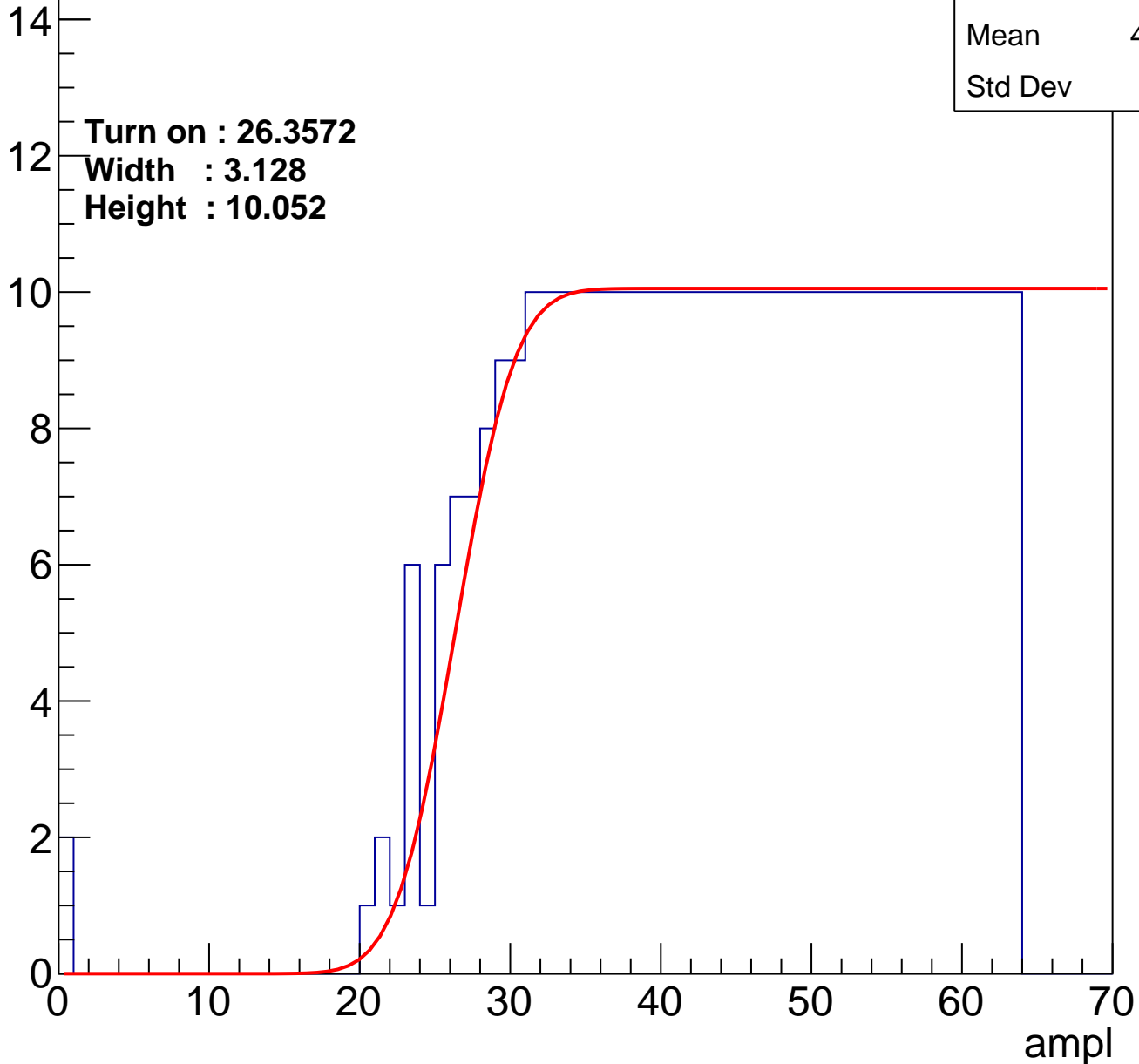
Entries	389
Mean	43.78
Std Dev	11.8

Turn on : 26.3572

Width : 3.128

Height : 10.052

Entry



B1L002S, U16-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.39
Std Dev	11.97

Turn on : 25.8152

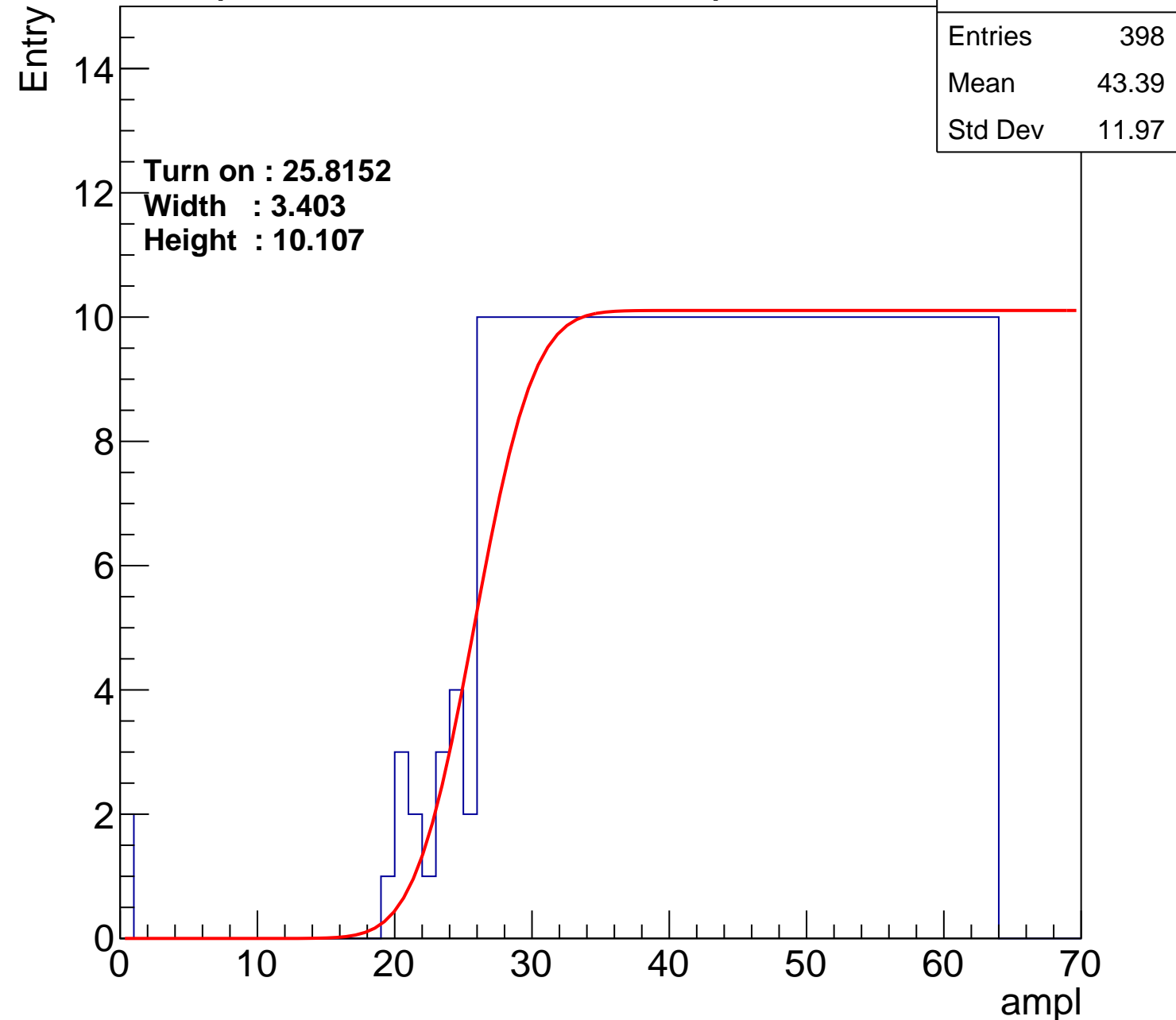
Width : 3.403

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch69

calib_packv5_042523_0143.root, FC#10, port B3

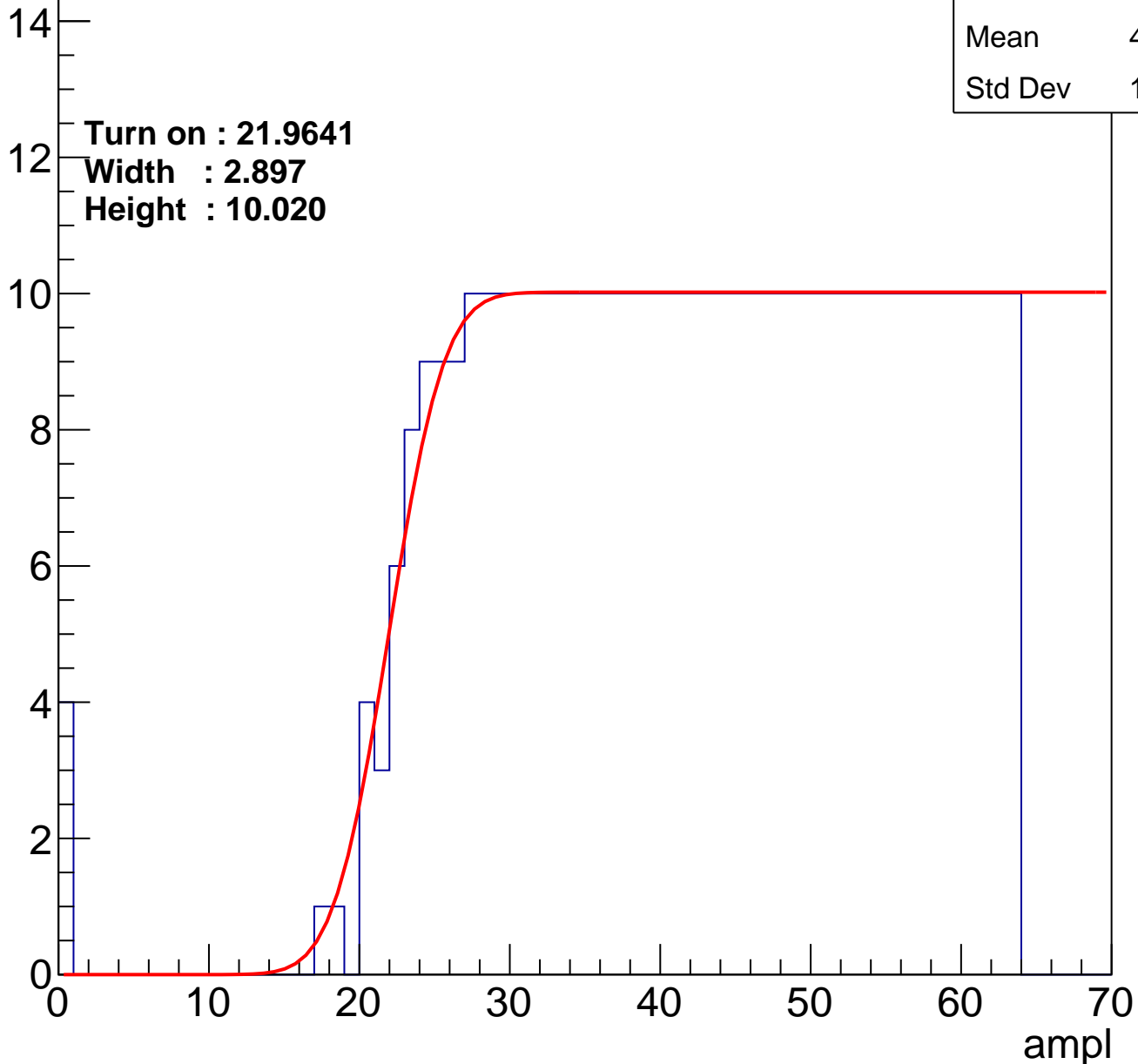
Entries	424
Mean	42.03
Std Dev	12.86

Turn on : 21.9641

Width : 2.897

Height : 10.020

Entry



B1L002S, U16-ch70

calib_packv5_042523_0143.root, FC#10, port B3

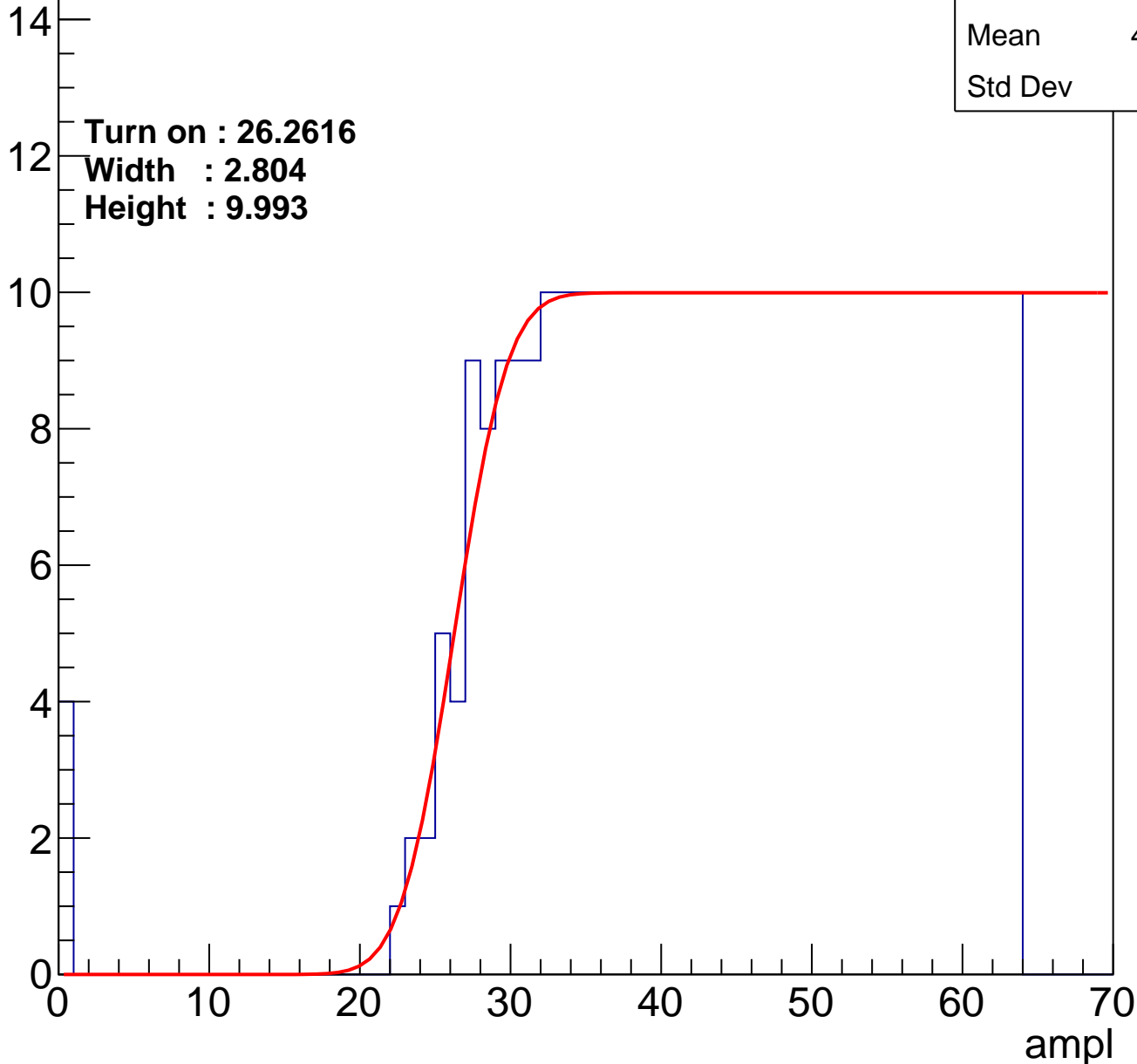
Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.2616

Width : 2.804

Height : 9.993

Entry



B1L002S, U16-ch71

calib_packv5_042523_0143.root, FC#10, port B3

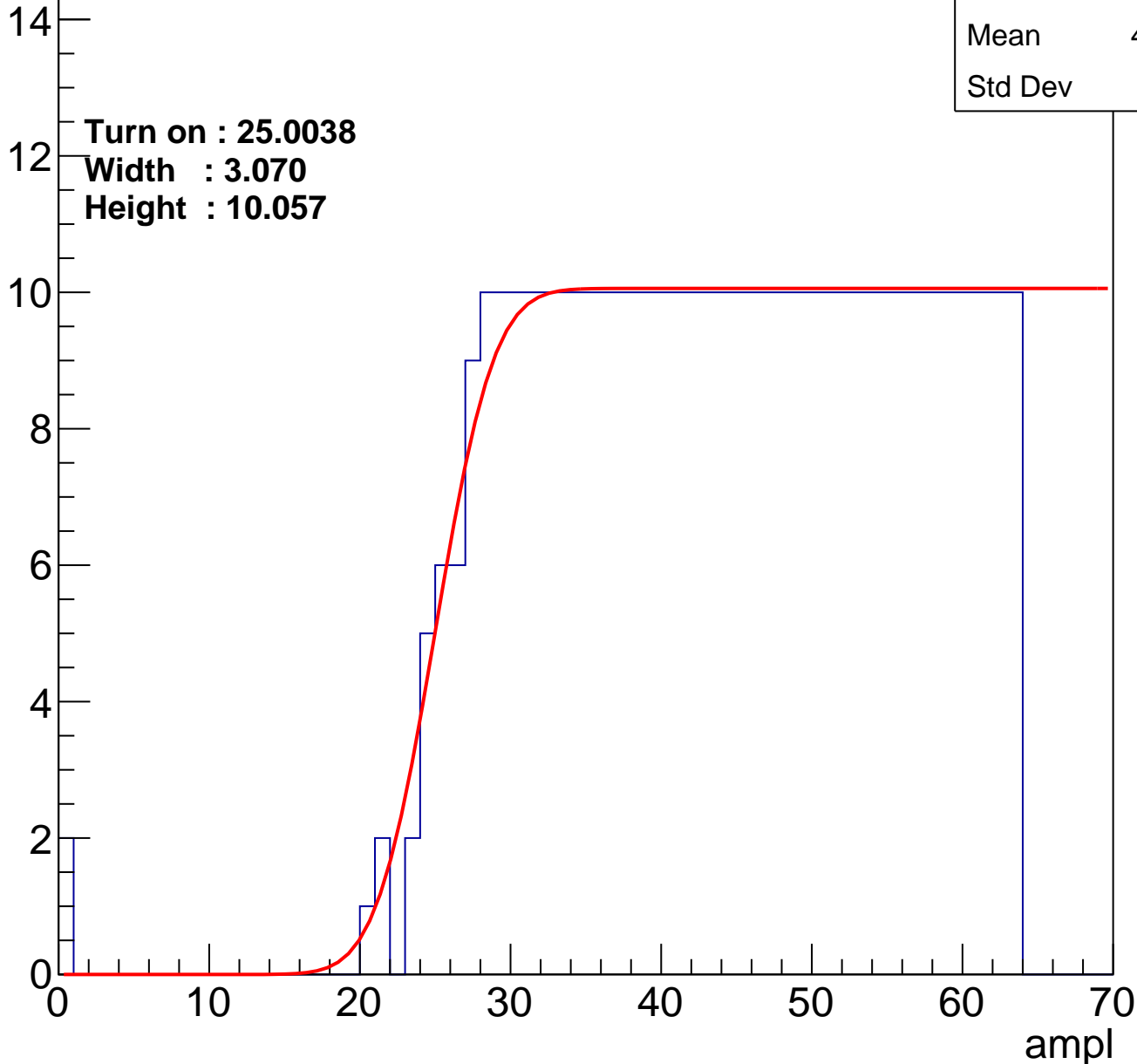
Entries	393
Mean	43.66
Std Dev	11.8

Turn on : 25.0038

Width : 3.070

Height : 10.057

Entry



B1L002S, U16-ch72

calib_packv5_042523_0143.root, FC#10, port B3

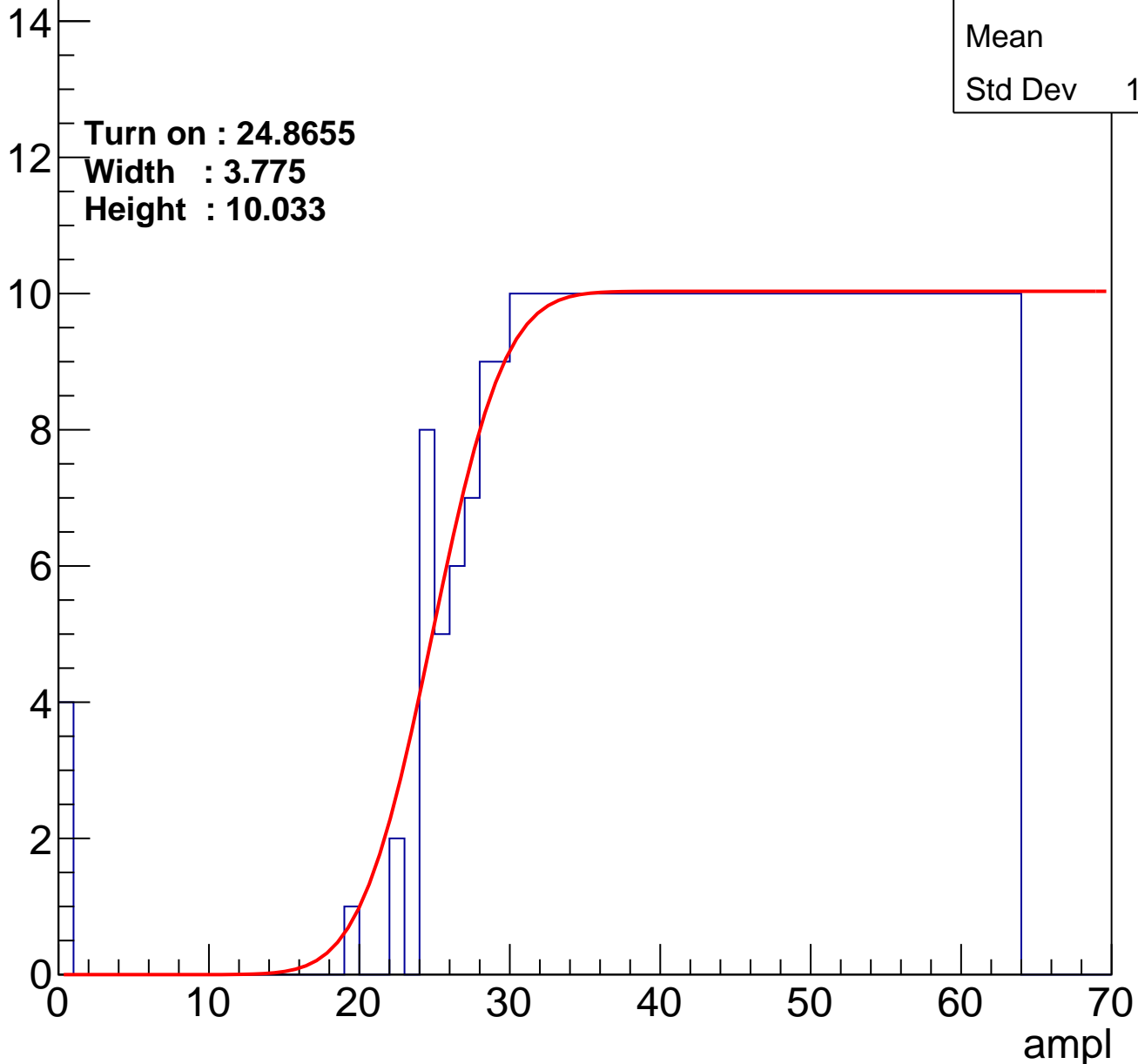
Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 24.8655

Width : 3.775

Height : 10.033

Entry



B1L002S, U16-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 25.5426

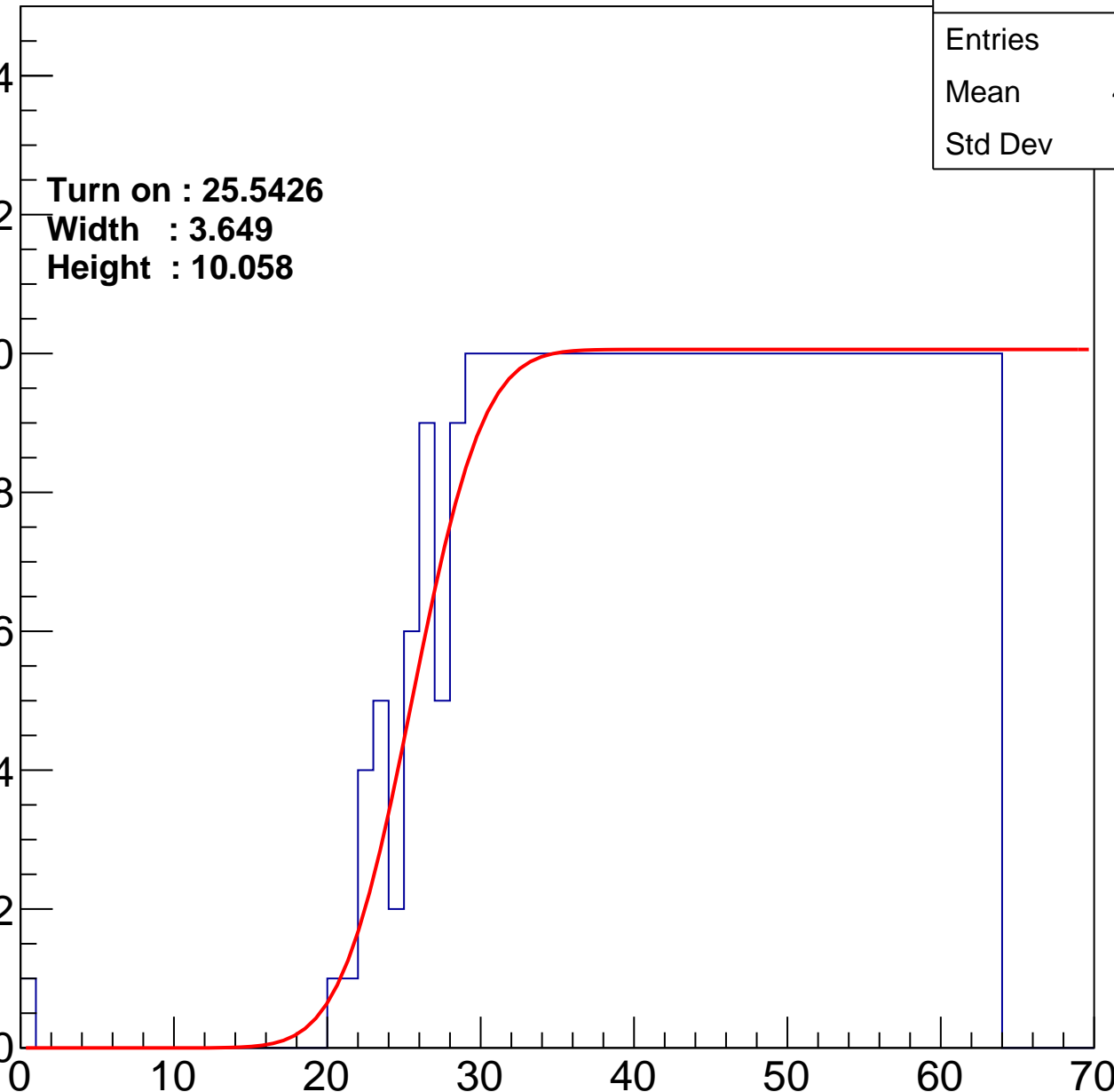
Width : 3.649

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch74

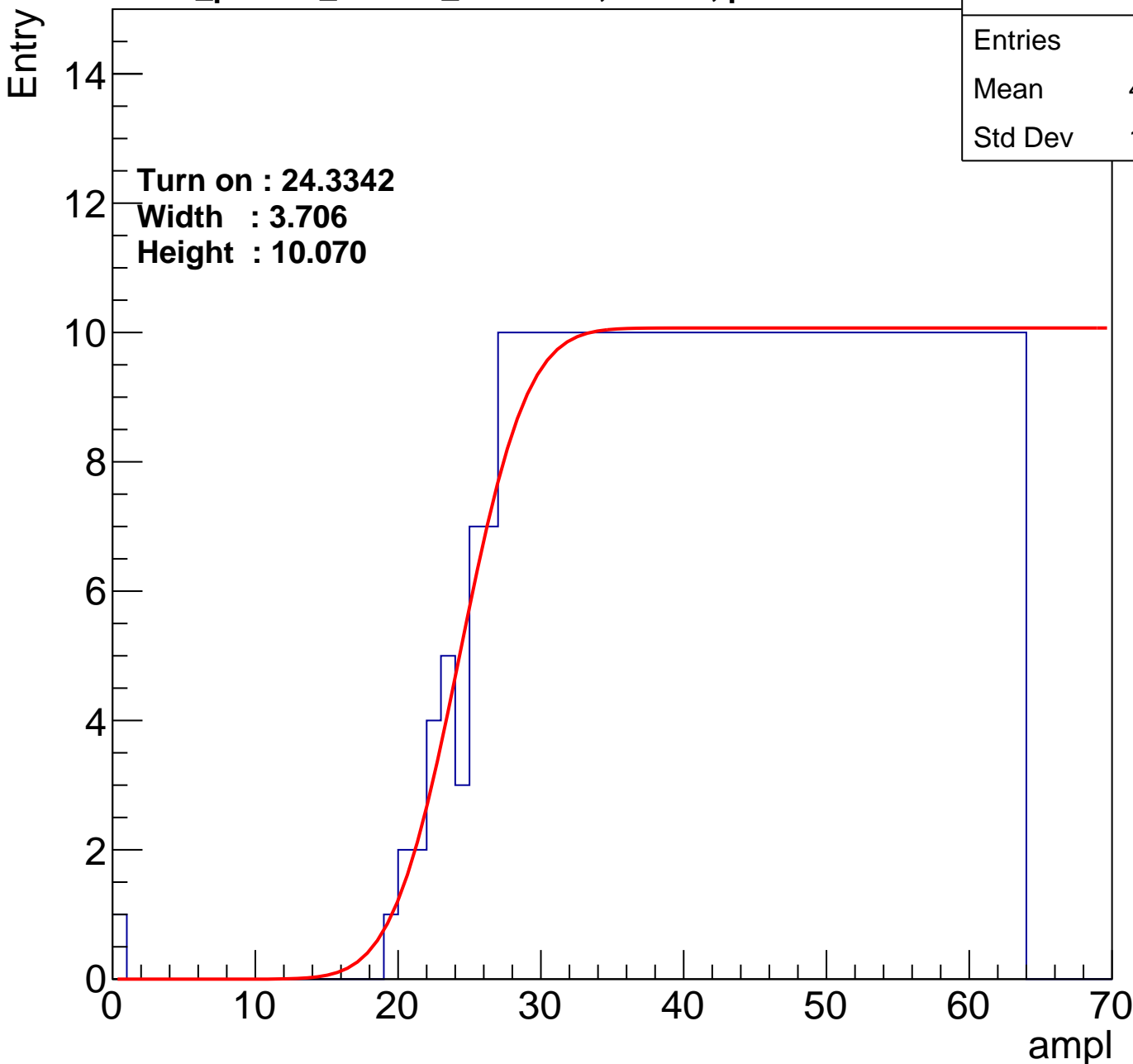
calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.24
Std Dev	11.93

Turn on : 24.3342

Width : 3.706

Height : 10.070



B1L002S, U16-ch75

calib_packv5_042523_0143.root, FC#10, port B3

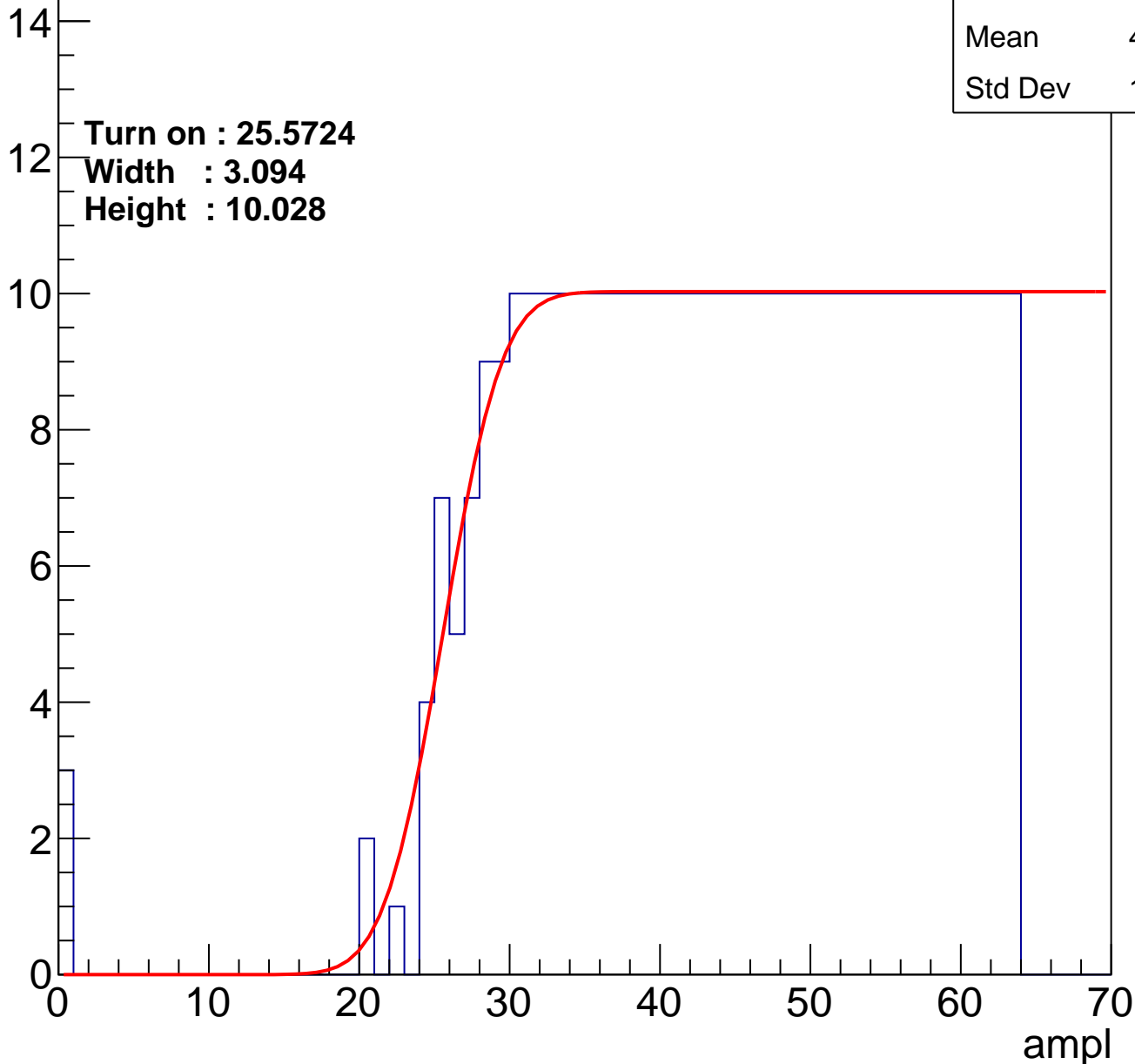
Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 25.5724

Width : 3.094

Height : 10.028

Entry



B1L002S, U16-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.95
Std Dev	11.95

Turn on : 26.2551

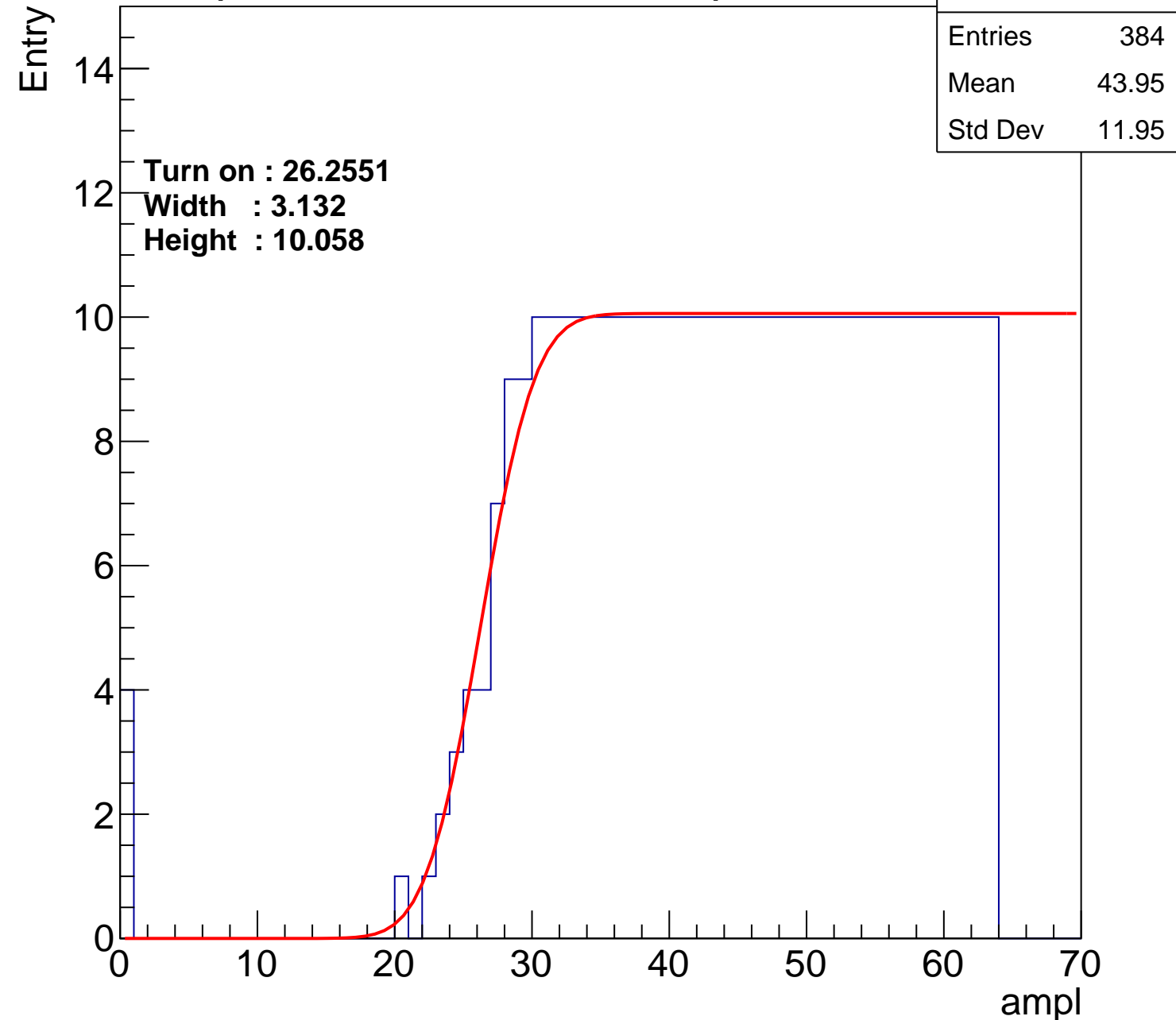
Width : 3.132

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.57

Turn on : 25.6762

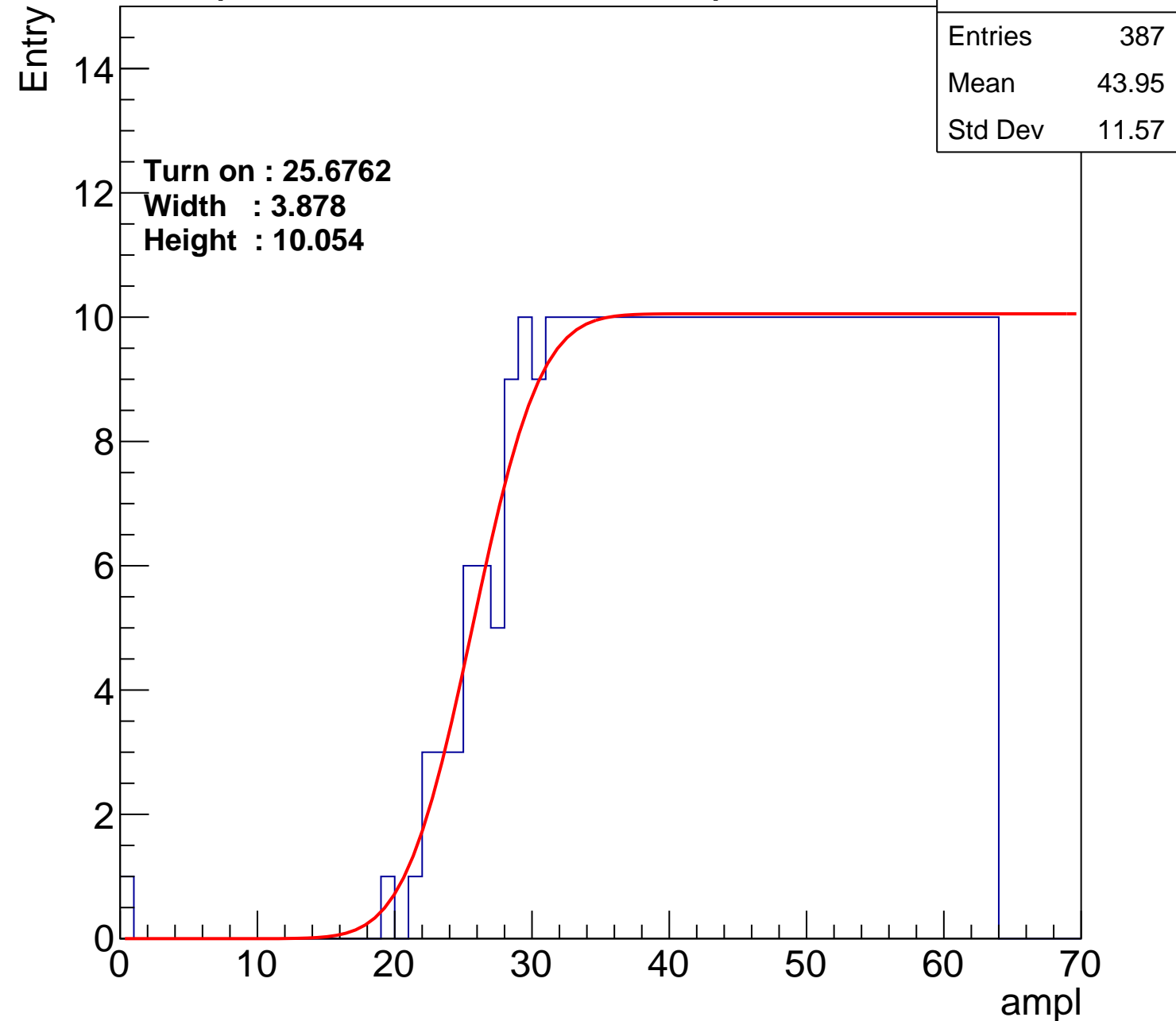
Width : 3.878

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.93
Std Dev	12.43

Turn on : 23.7410

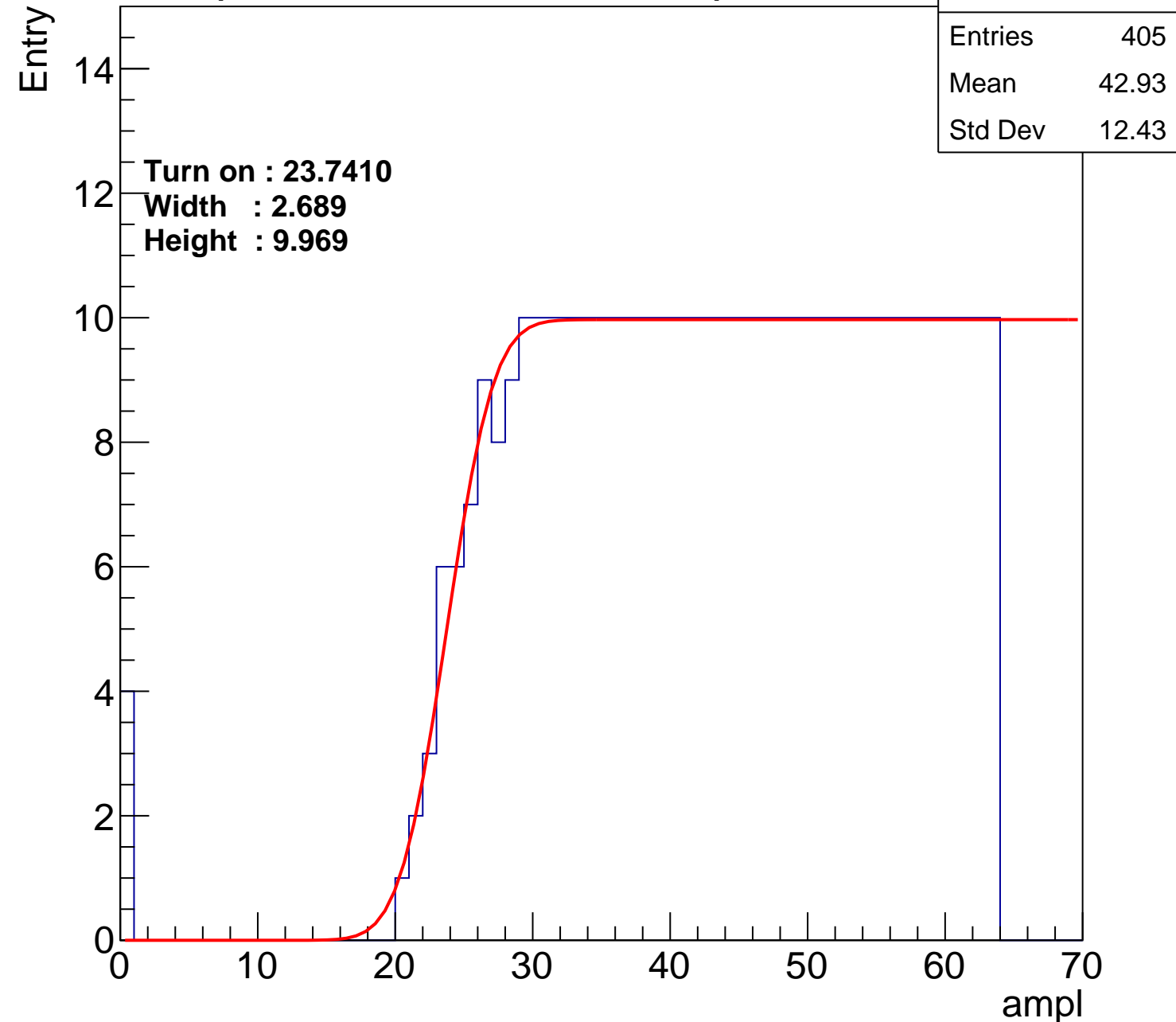
Width : 2.689

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch79

calib_packv5_042523_0143.root, FC#10, port B3

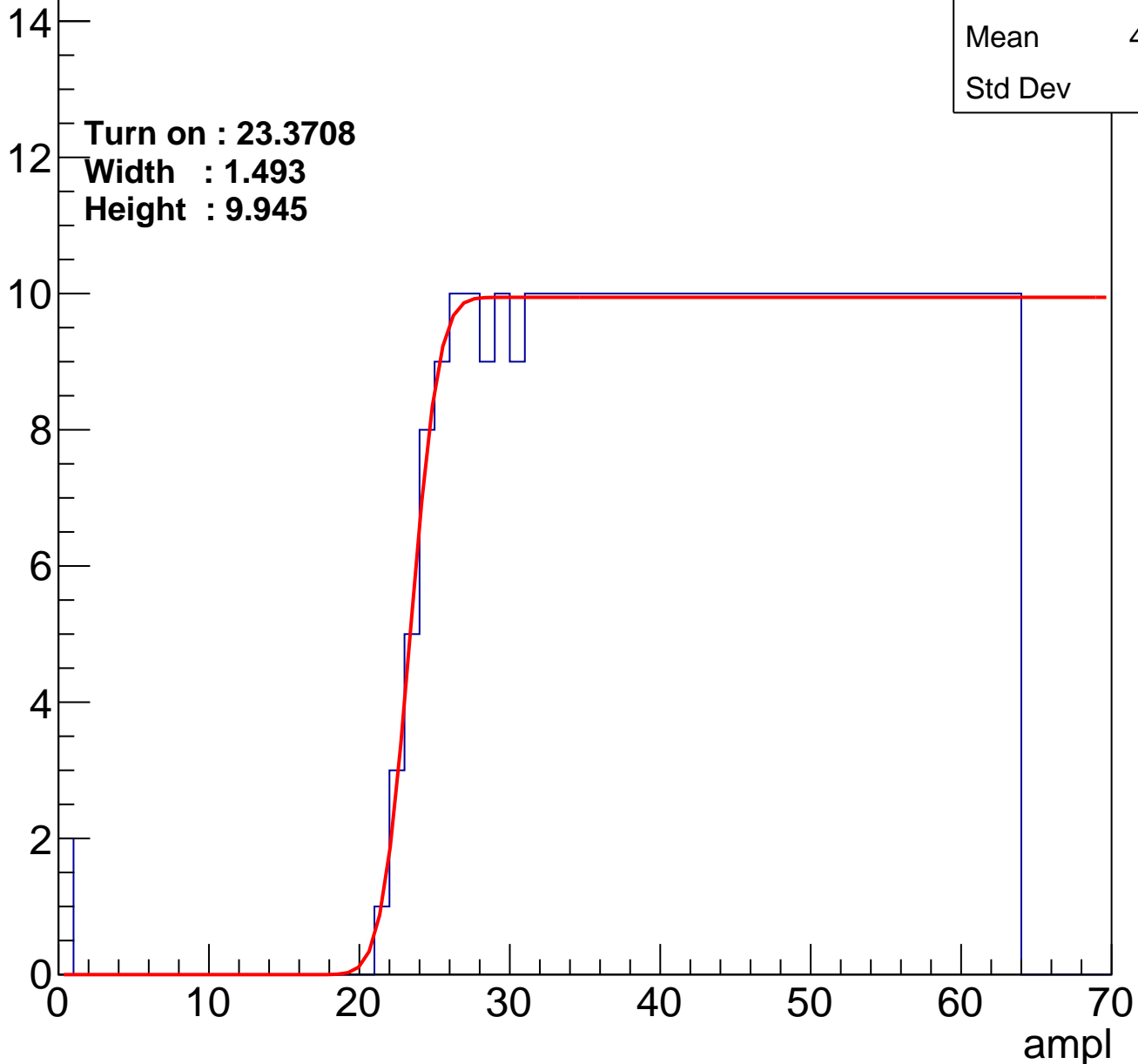
Entries	406
Mean	43.03
Std Dev	12.1

Turn on : 23.3708

Width : 1.493

Height : 9.945

Entry



B1L002S, U16-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.53
Std Dev	11.89

Turn on : 24.8706

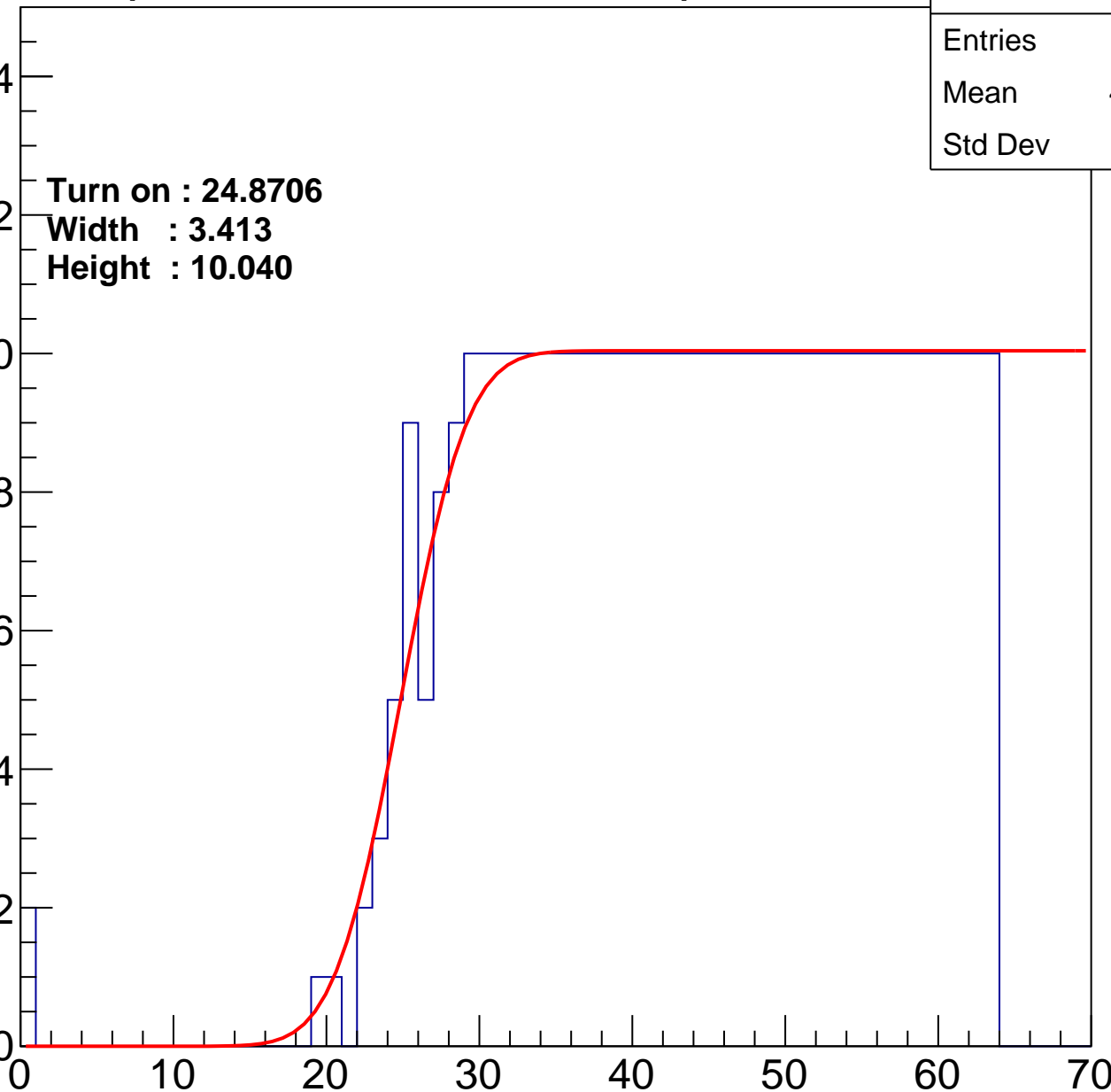
Width : 3.413

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.38
Std Dev	12.09

Turn on : 24.8758

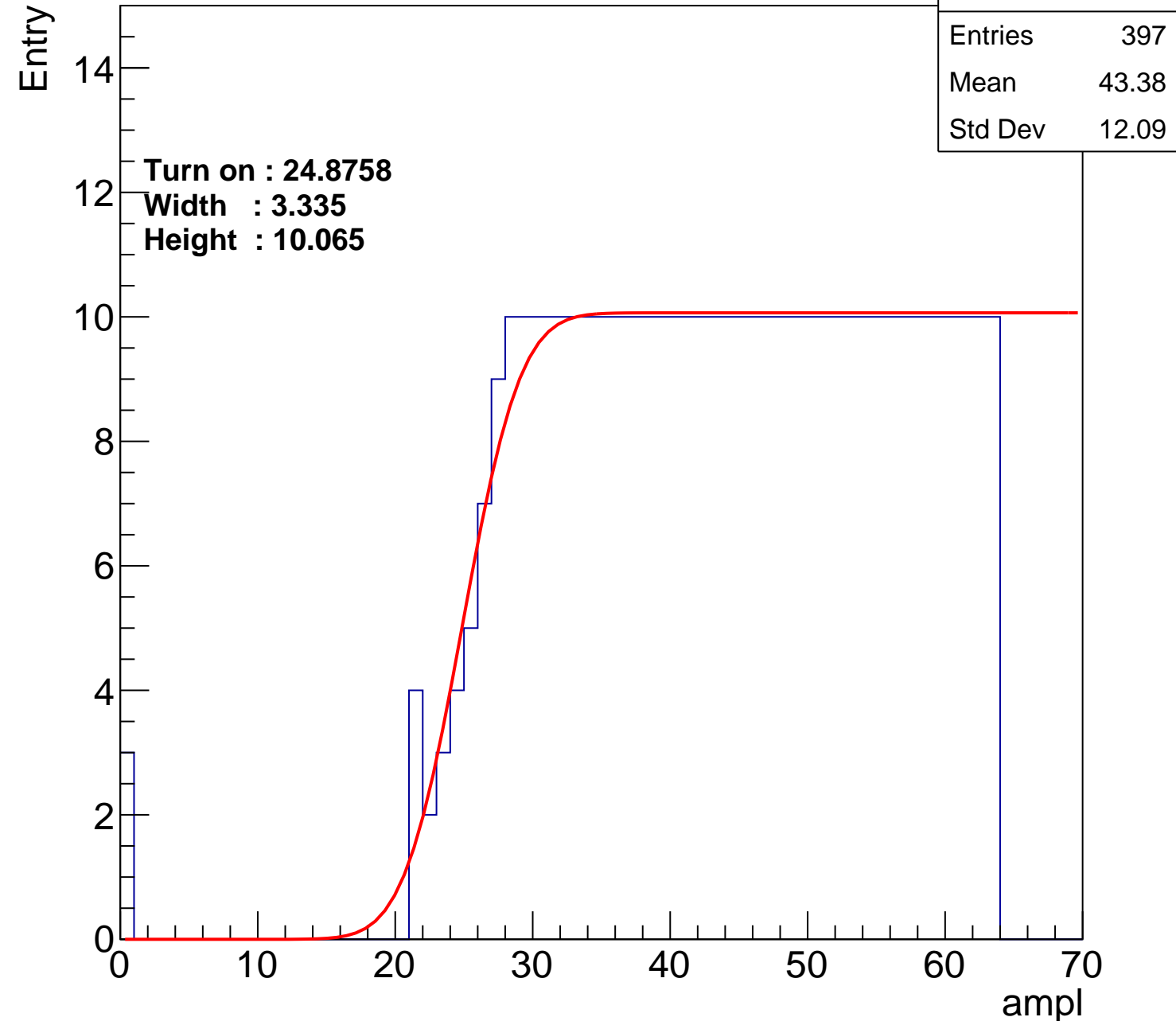
Width : 3.335

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.17
Std Dev	12.04

Turn on : 23.9938

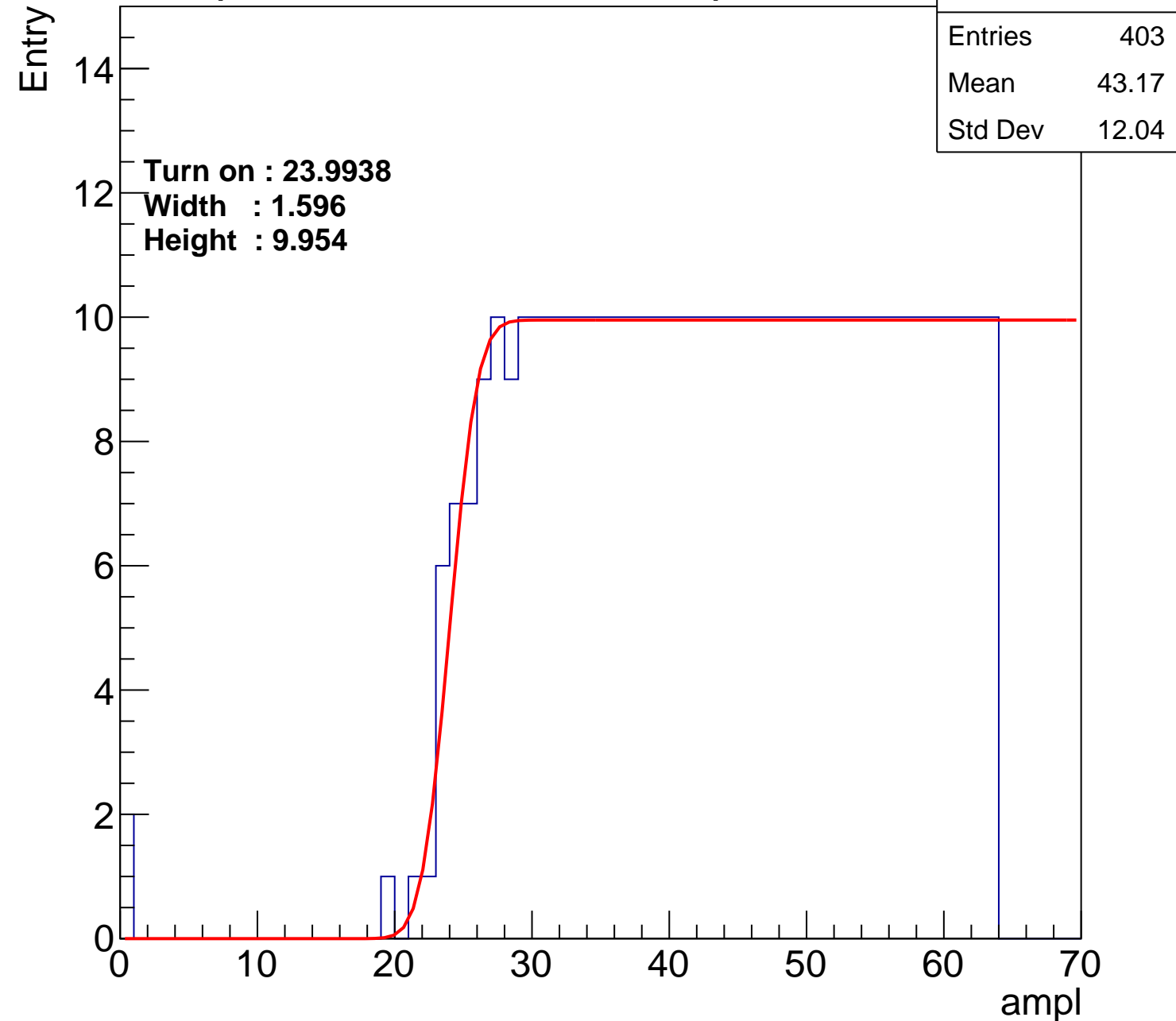
Width : 1.596

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.08
Std Dev	11.42

Turn on : 25.4697

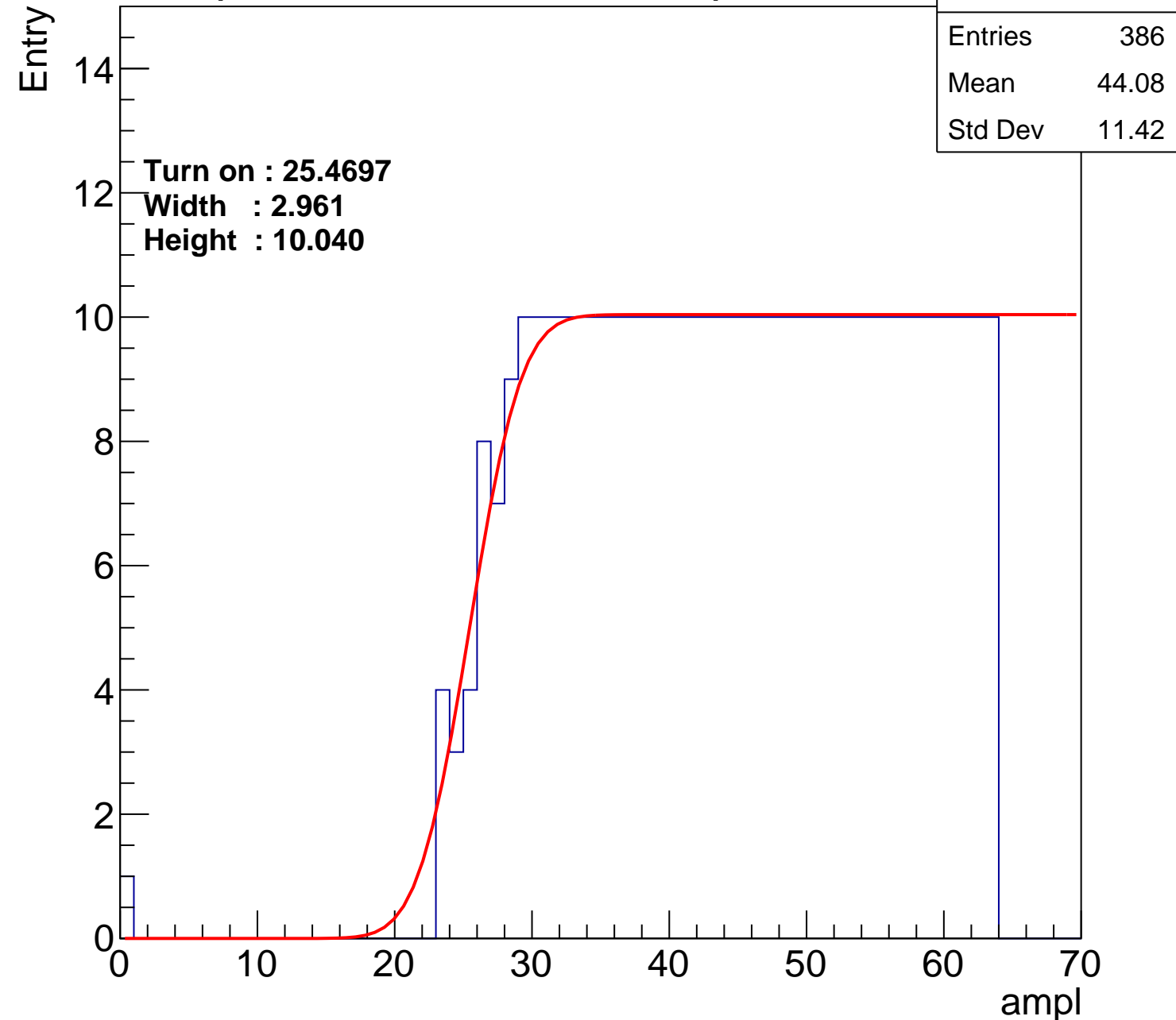
Width : 2.961

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch84

calib_packv5_042523_0143.root, FC#10, port B3

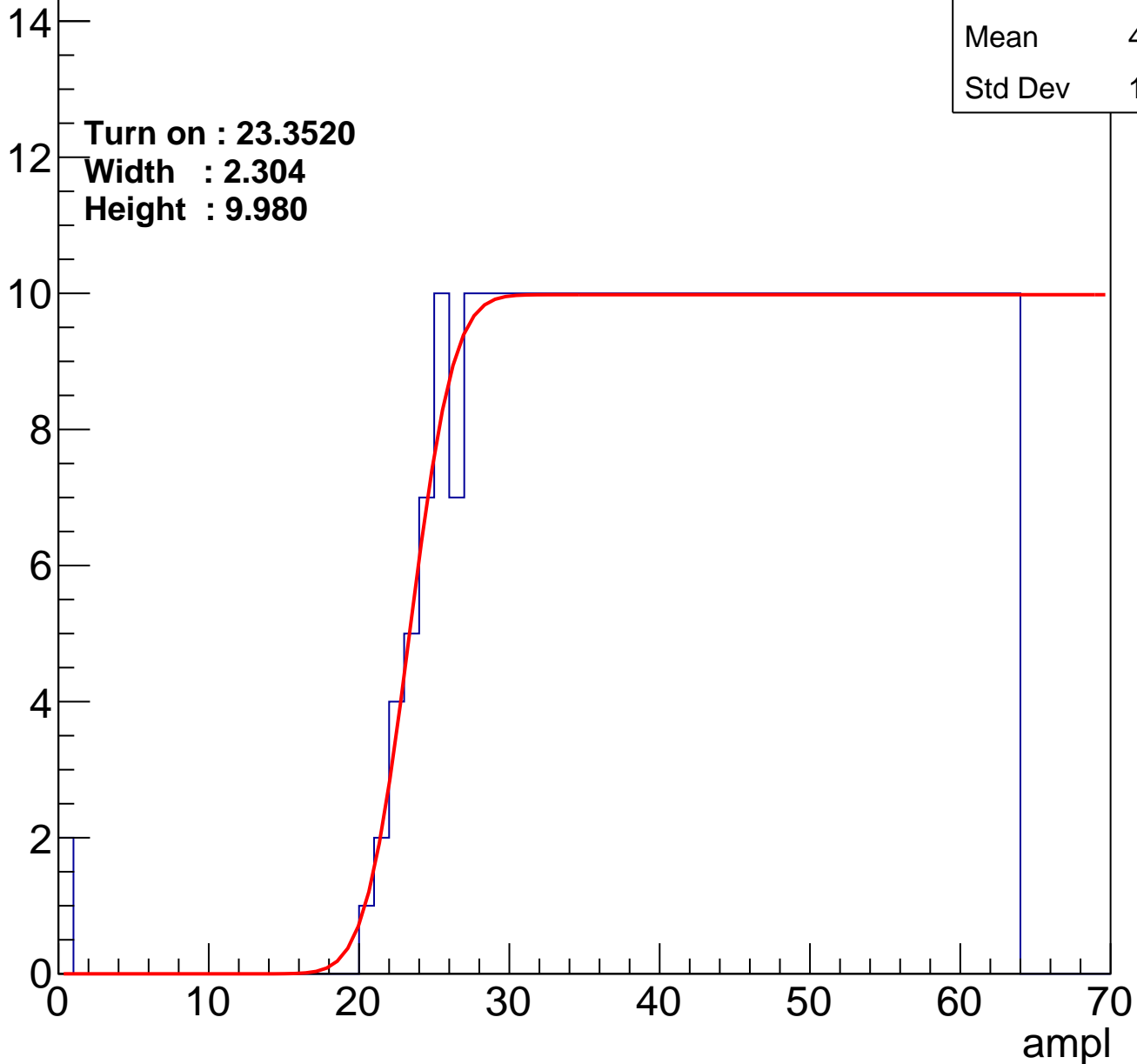
Entries	408
Mean	42.93
Std Dev	12.17

Turn on : 23.3520

Width : 2.304

Height : 9.980

Entry



B1L002S, U16-ch85

calib_packv5_042523_0143.root, FC#10, port B3

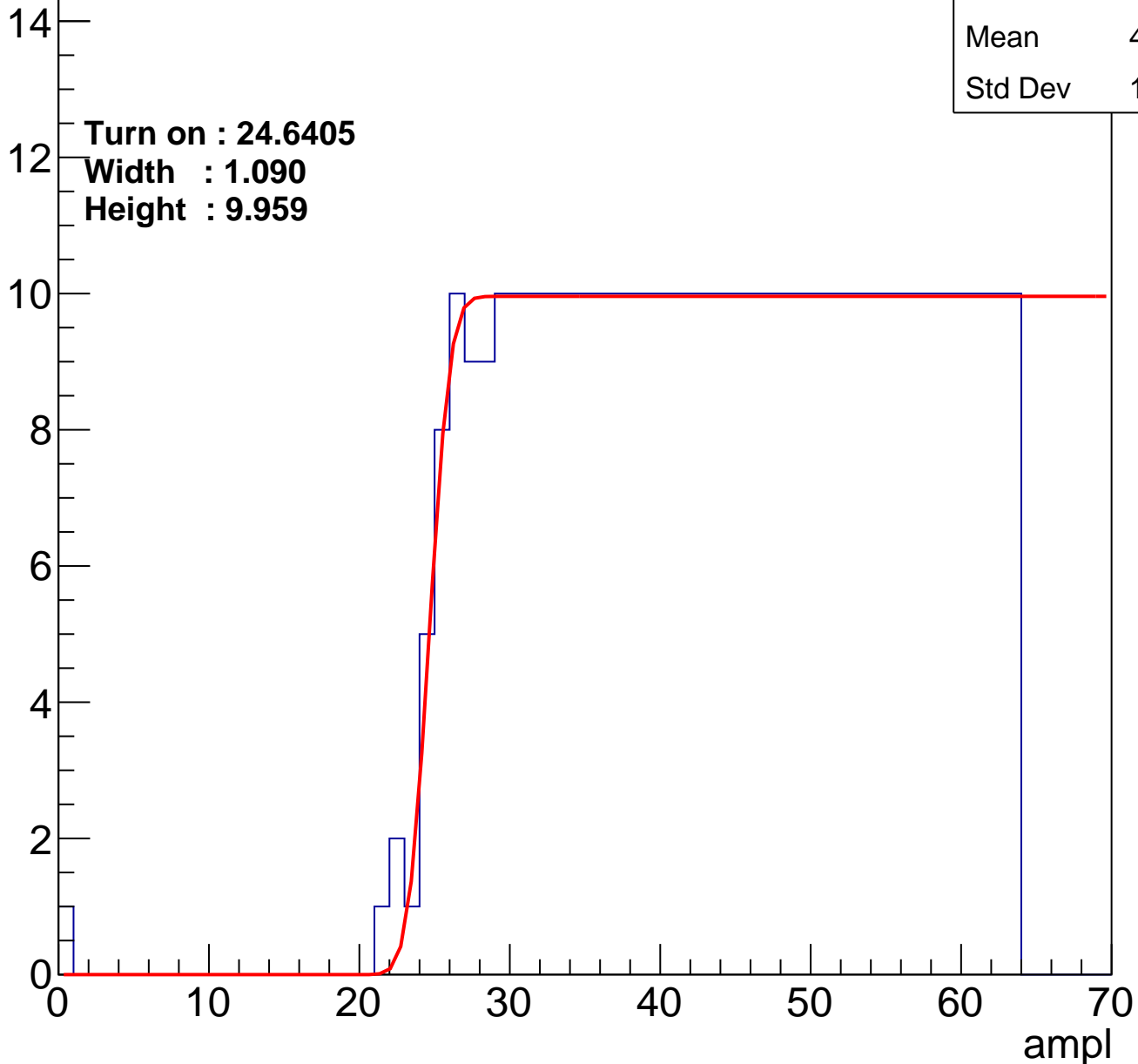
Entries	396
Mean	43.59
Std Dev	11.67

Turn on : 24.6405

Width : 1.090

Height : 9.959

Entry



B1L002S, U16-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.6642

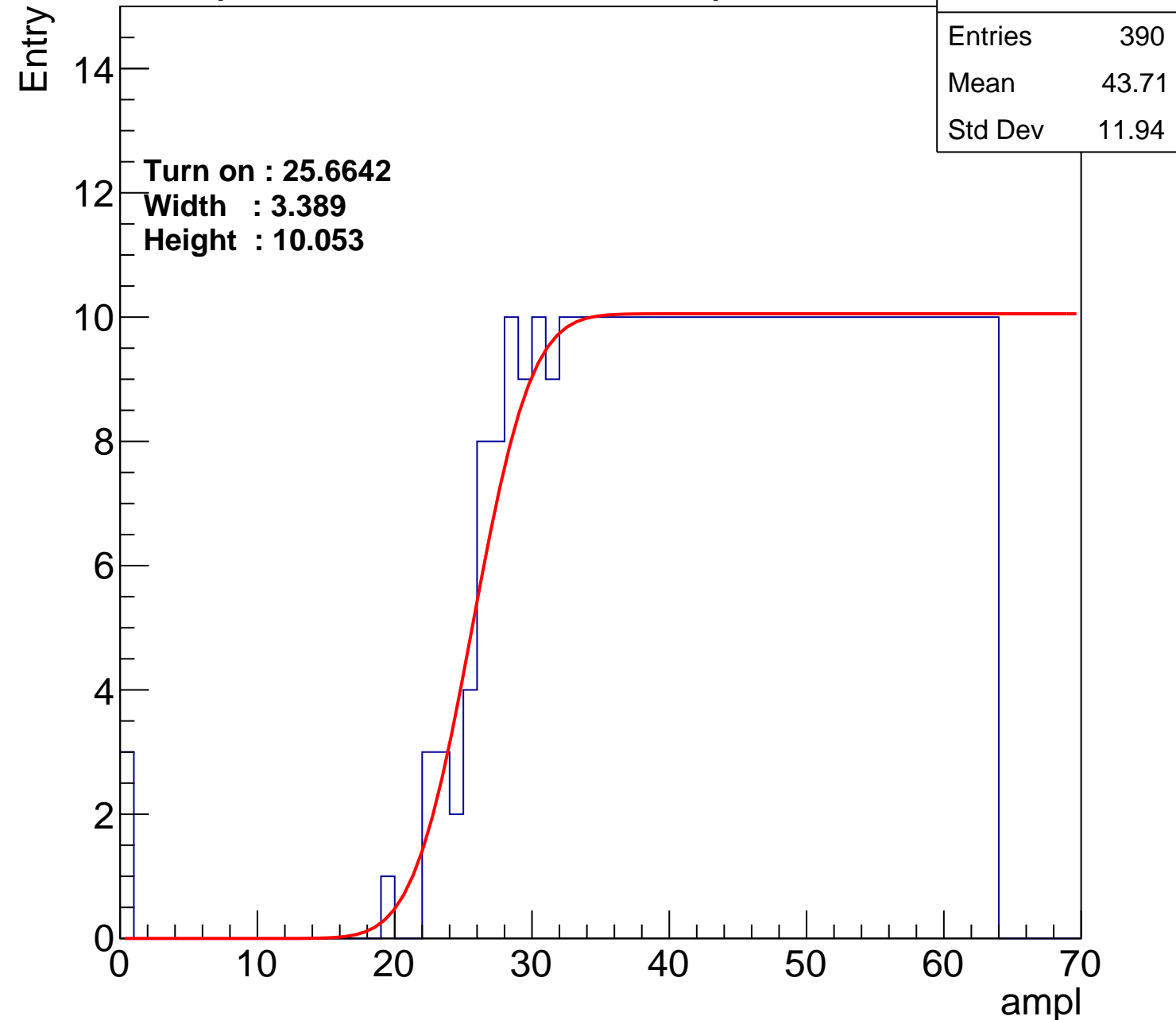
Width : 3.389

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 24.1964

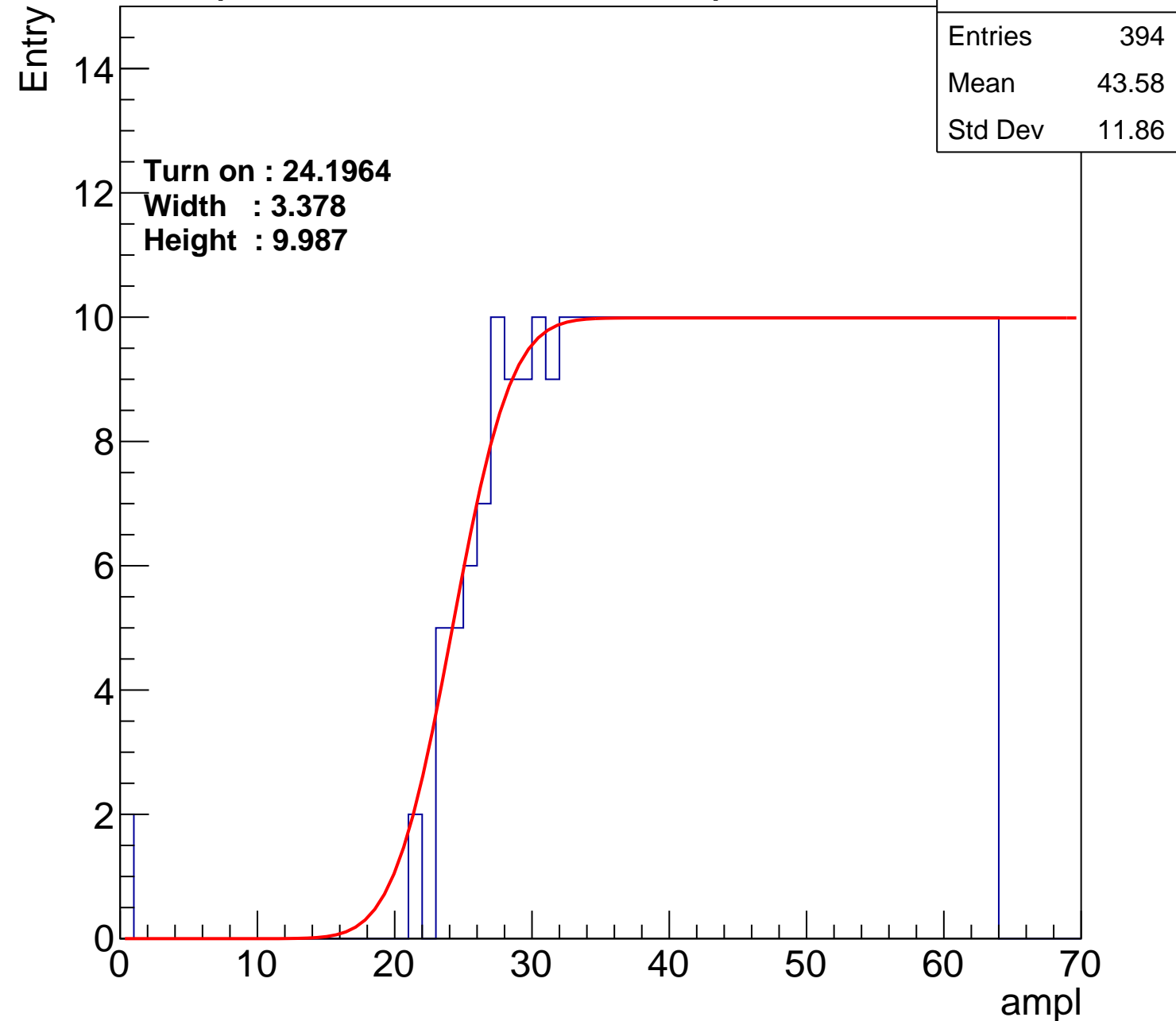
Width : 3.378

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.48

Turn on : 26.0086

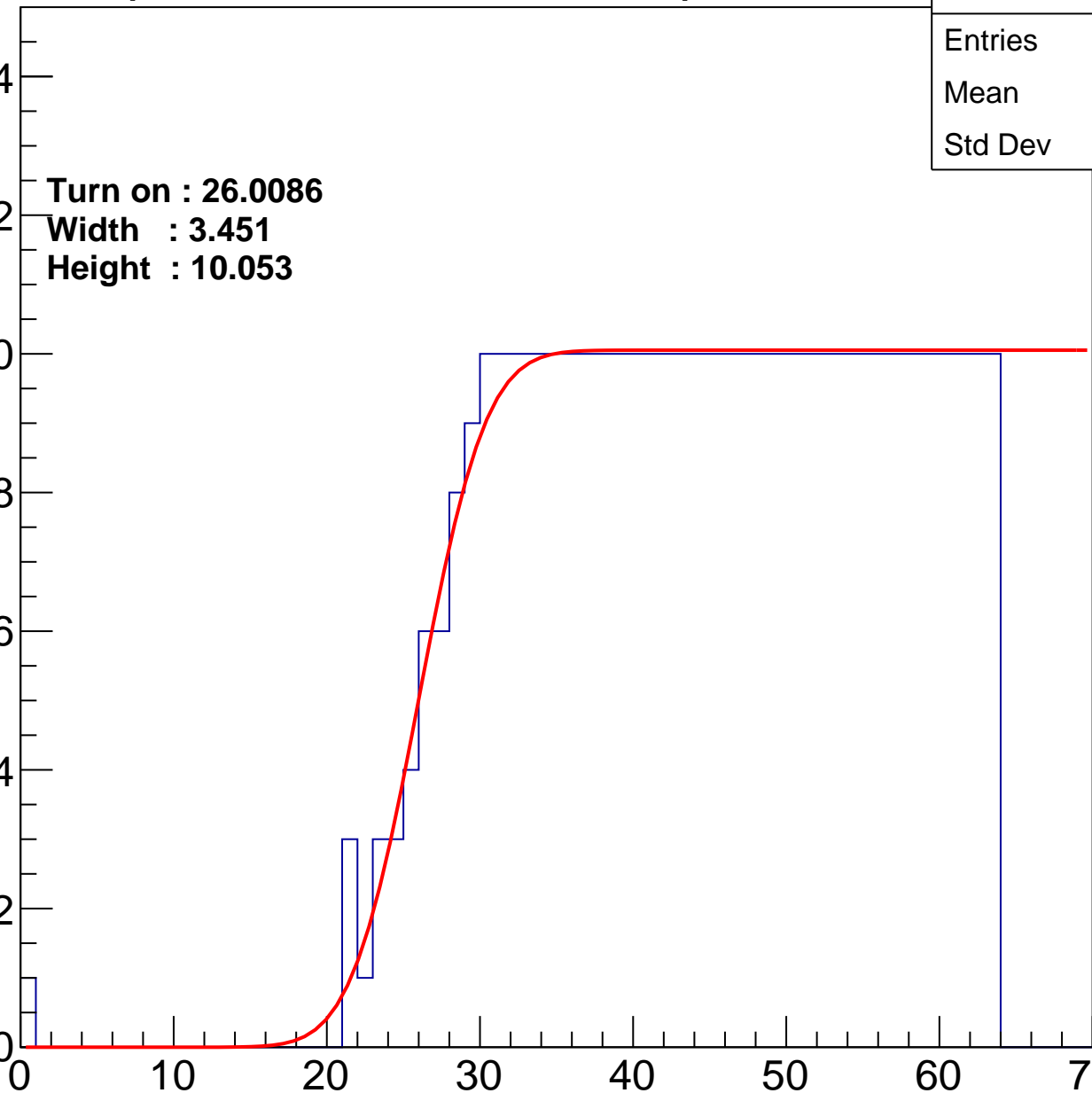
Width : 3.451

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch89

calib_packv5_042523_0143.root, FC#10, port B3

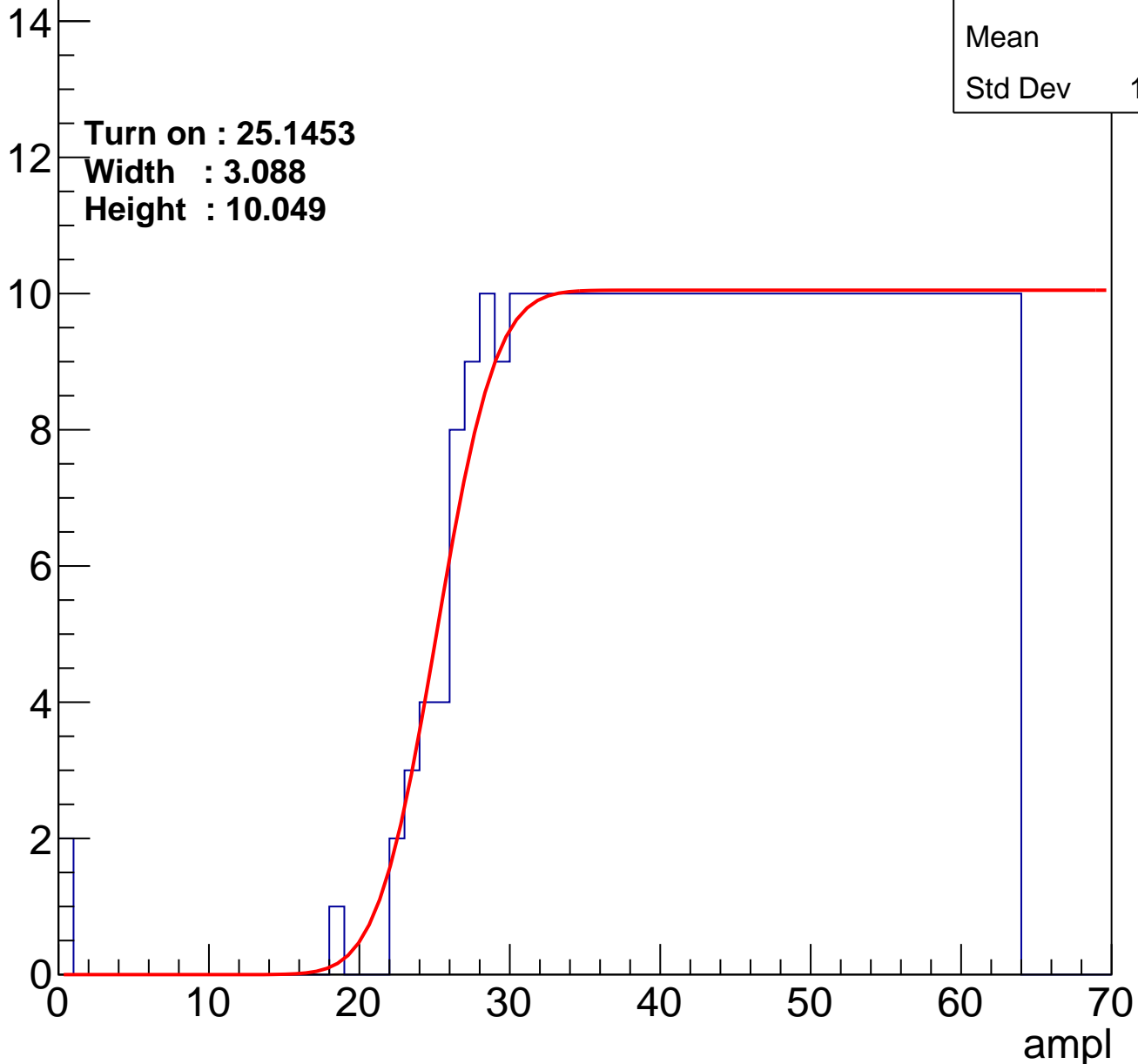
Entries	392
Mean	43.7
Std Dev	11.79

Turn on : 25.1453

Width : 3.088

Height : 10.049

Entry



B1L002S, U16-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 26.2011

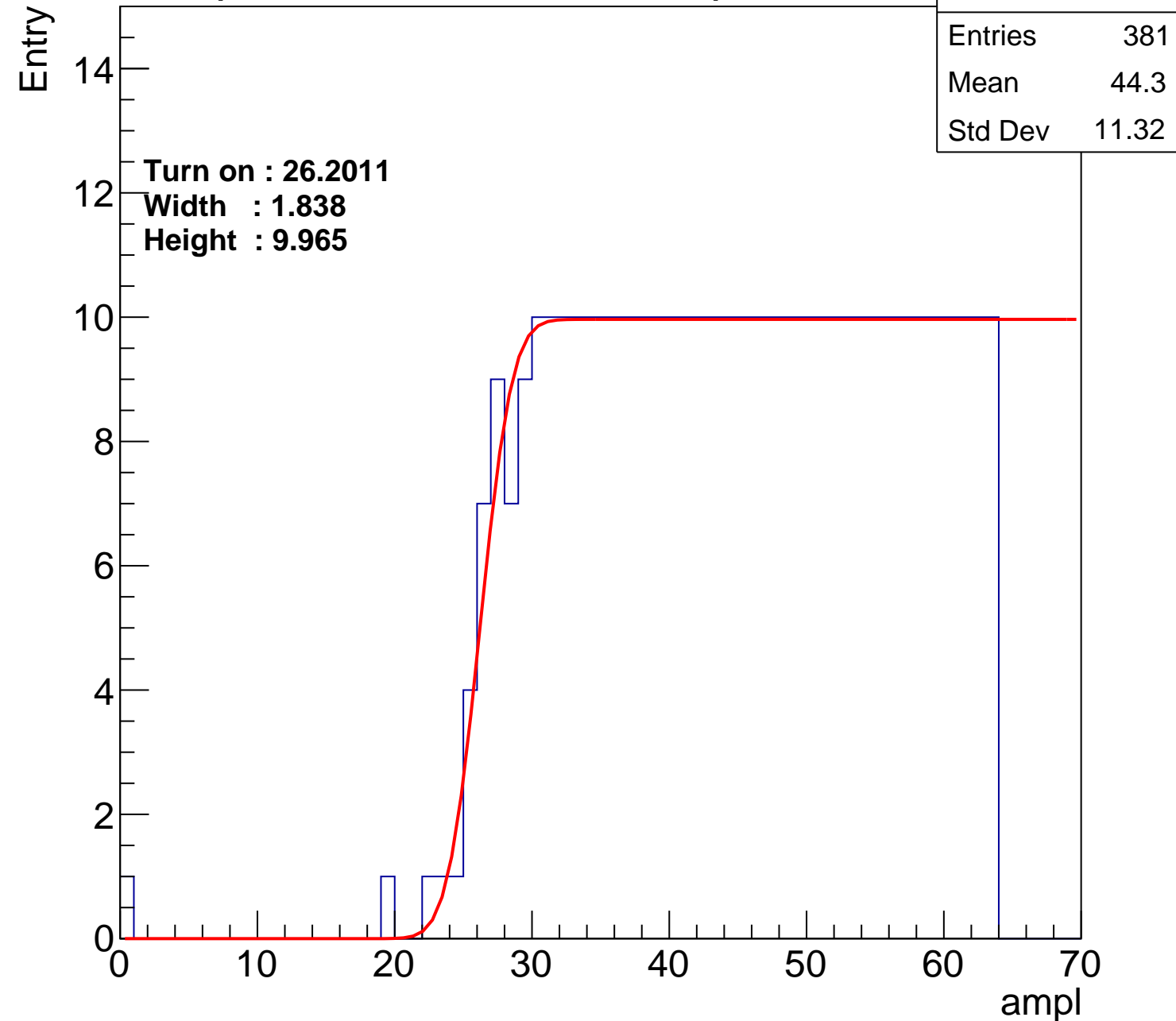
Width : 1.838

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch91

calib_packv5_042523_0143.root, FC#10, port B3

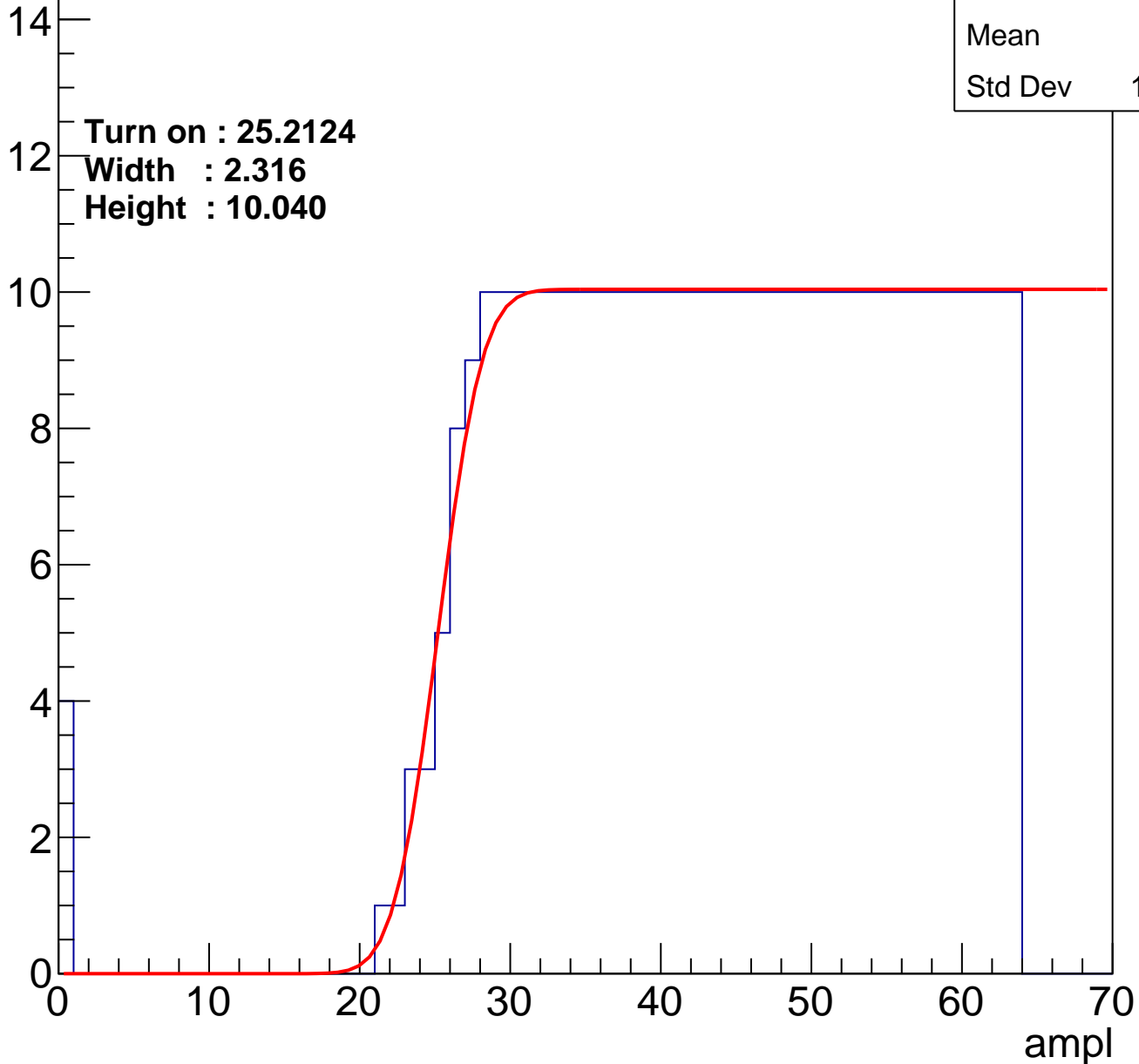
Entries	394
Mean	43.5
Std Dev	12.12

Turn on : 25.2124

Width : 2.316

Height : 10.040

Entry



B1L002S, U16-ch92

calib_packv5_042523_0143.root, FC#10, port B3

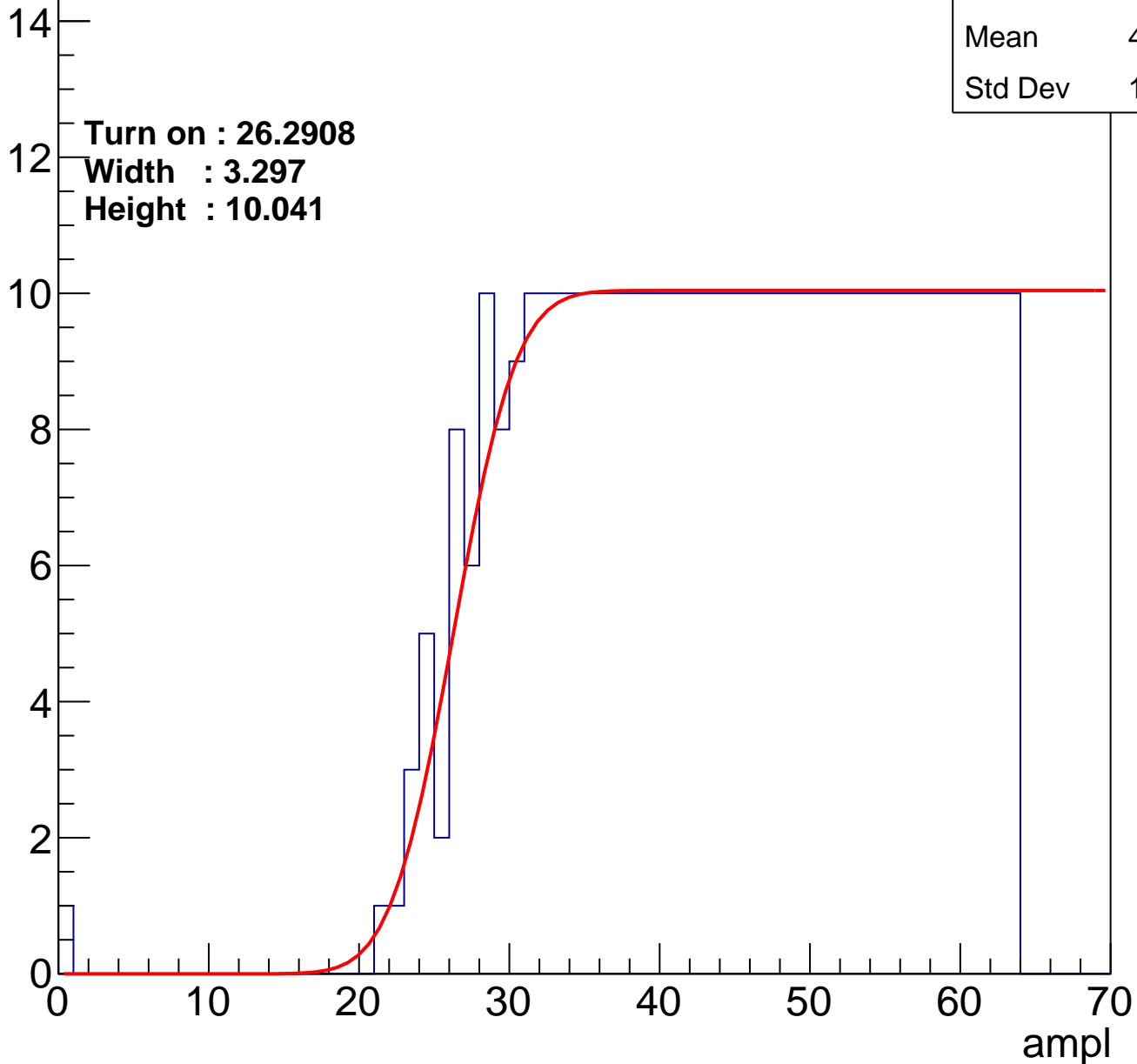
Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 26.2908

Width : 3.297

Height : 10.041

Entry



B1L002S, U16-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 25.9492

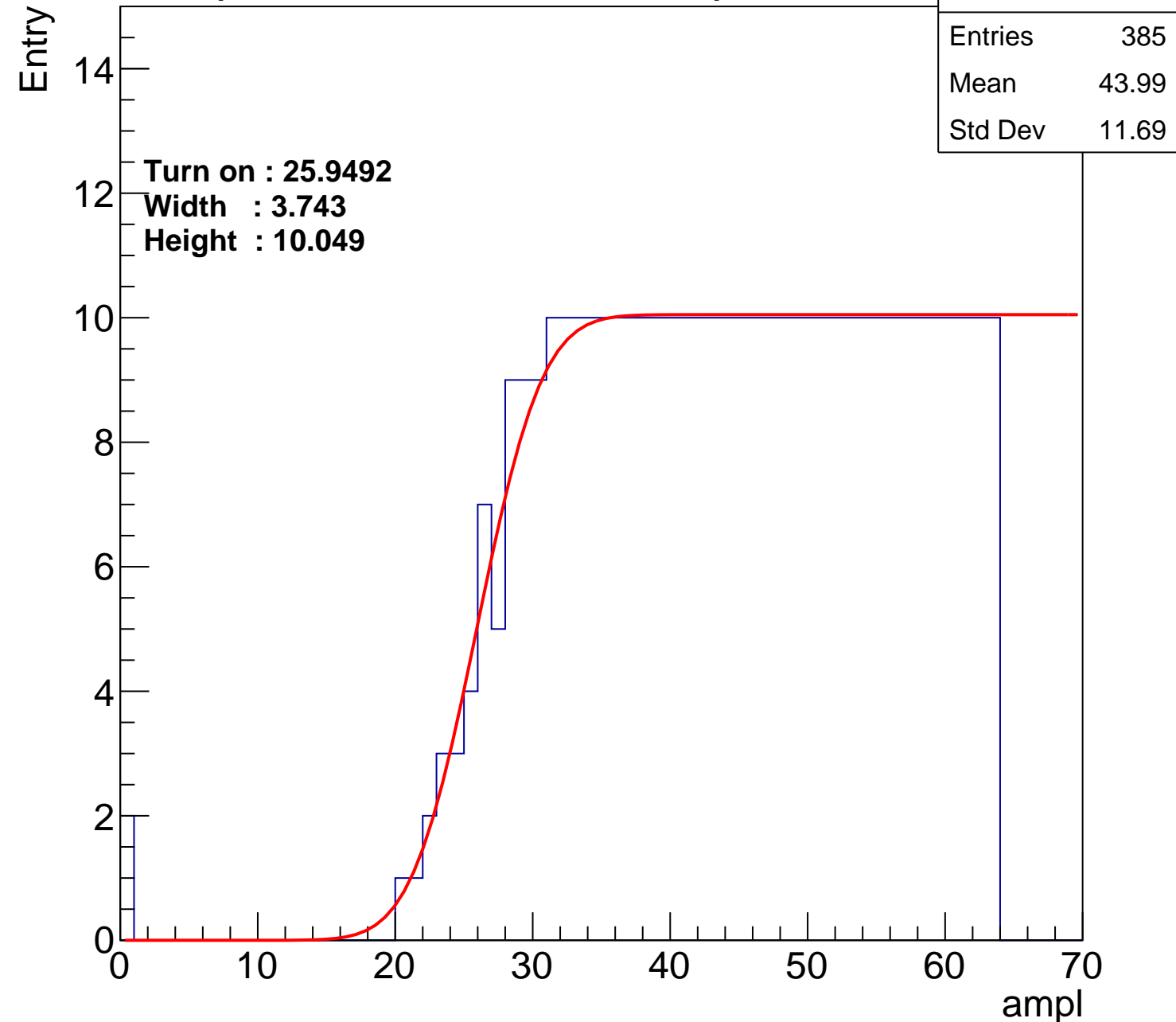
Width : 3.743

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.89
Std Dev	12.13

Turn on : 23.3246

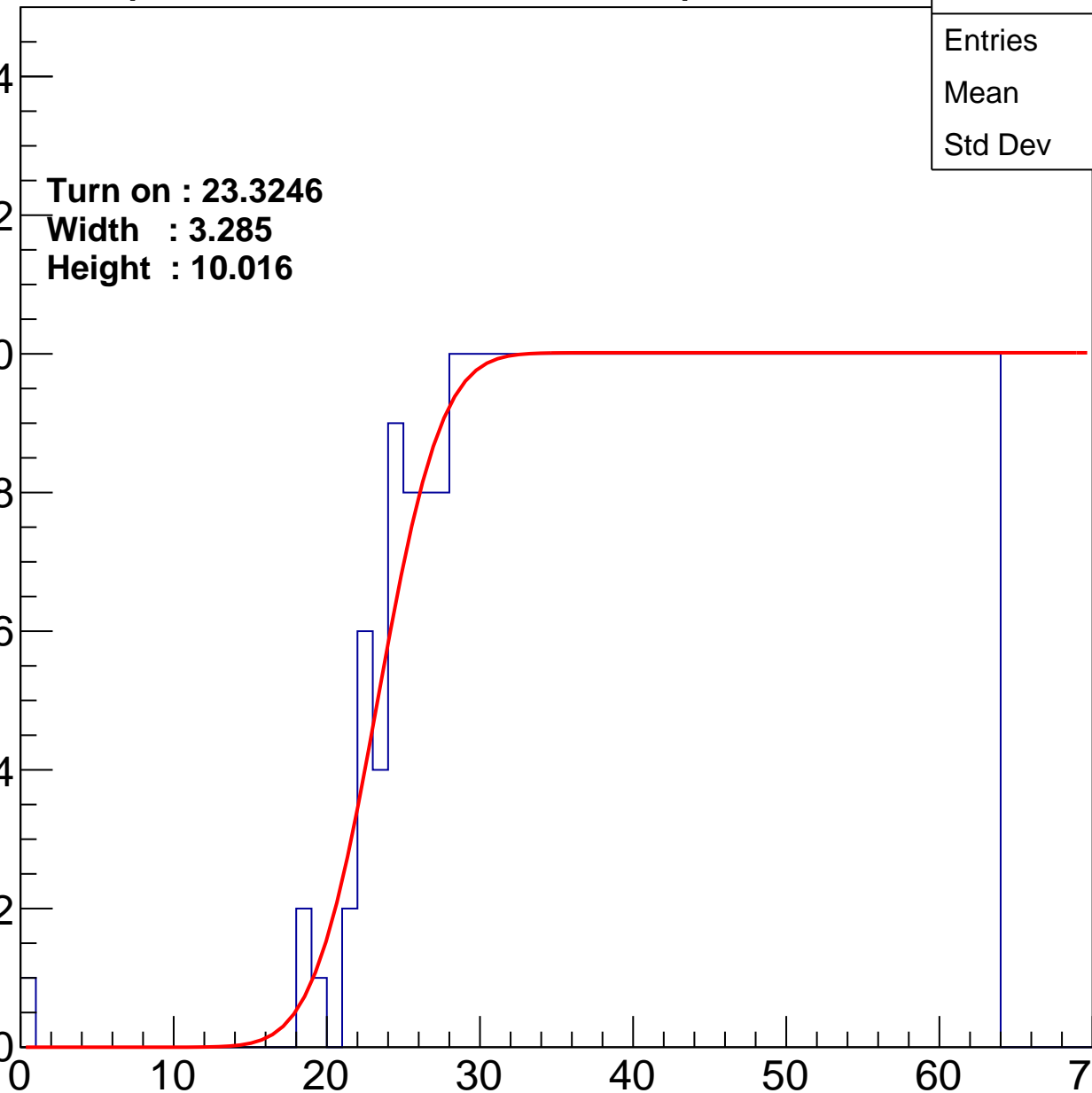
Width : 3.285

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch95

calib_packv5_042523_0143.root, FC#10, port B3

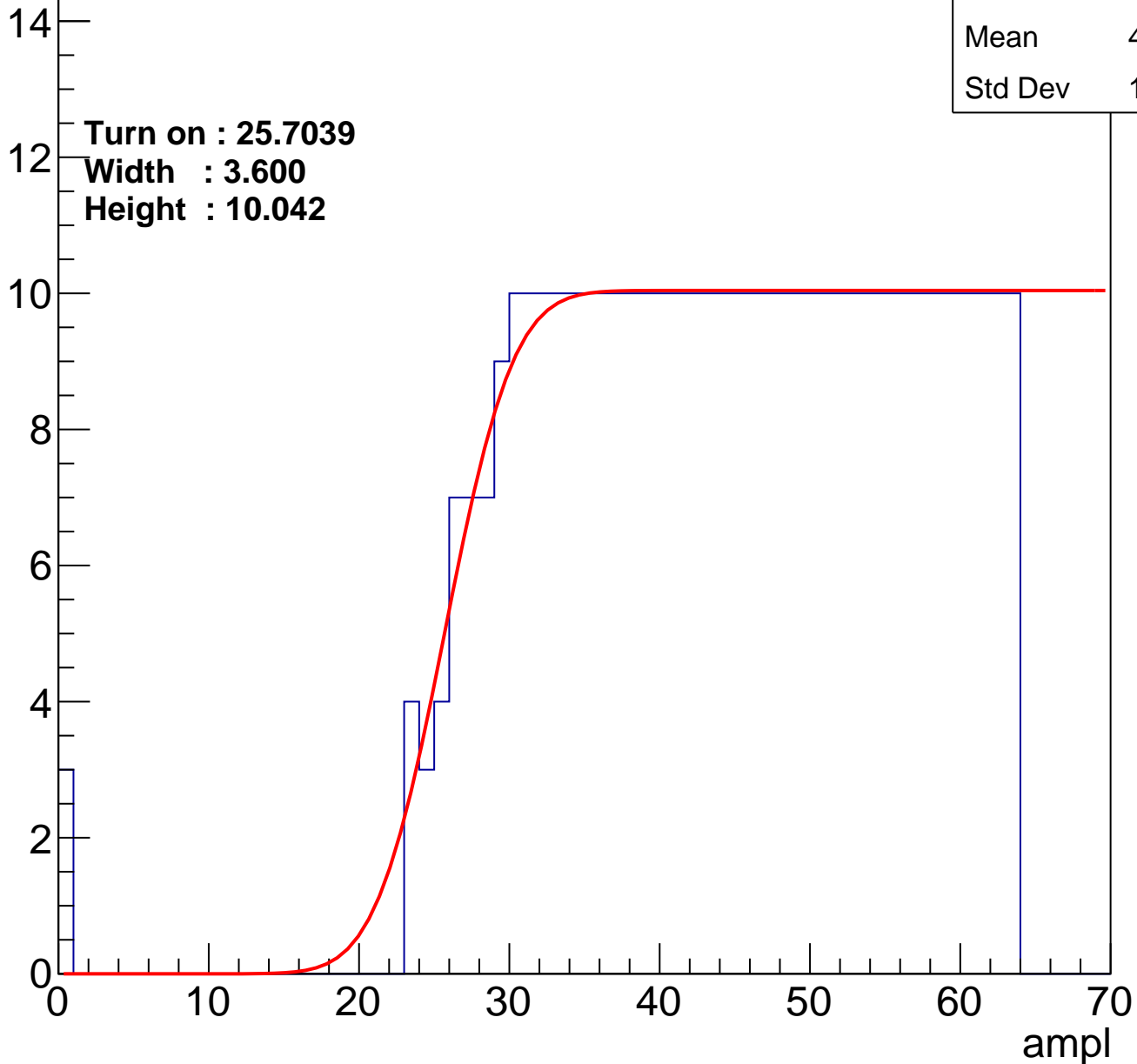
Entries	384
Mean	44.02
Std Dev	11.77

Turn on : 25.7039

Width : 3.600

Height : 10.042

Entry



B1L002S, U16-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.93
Std Dev	11.56

Turn on : 25.3483

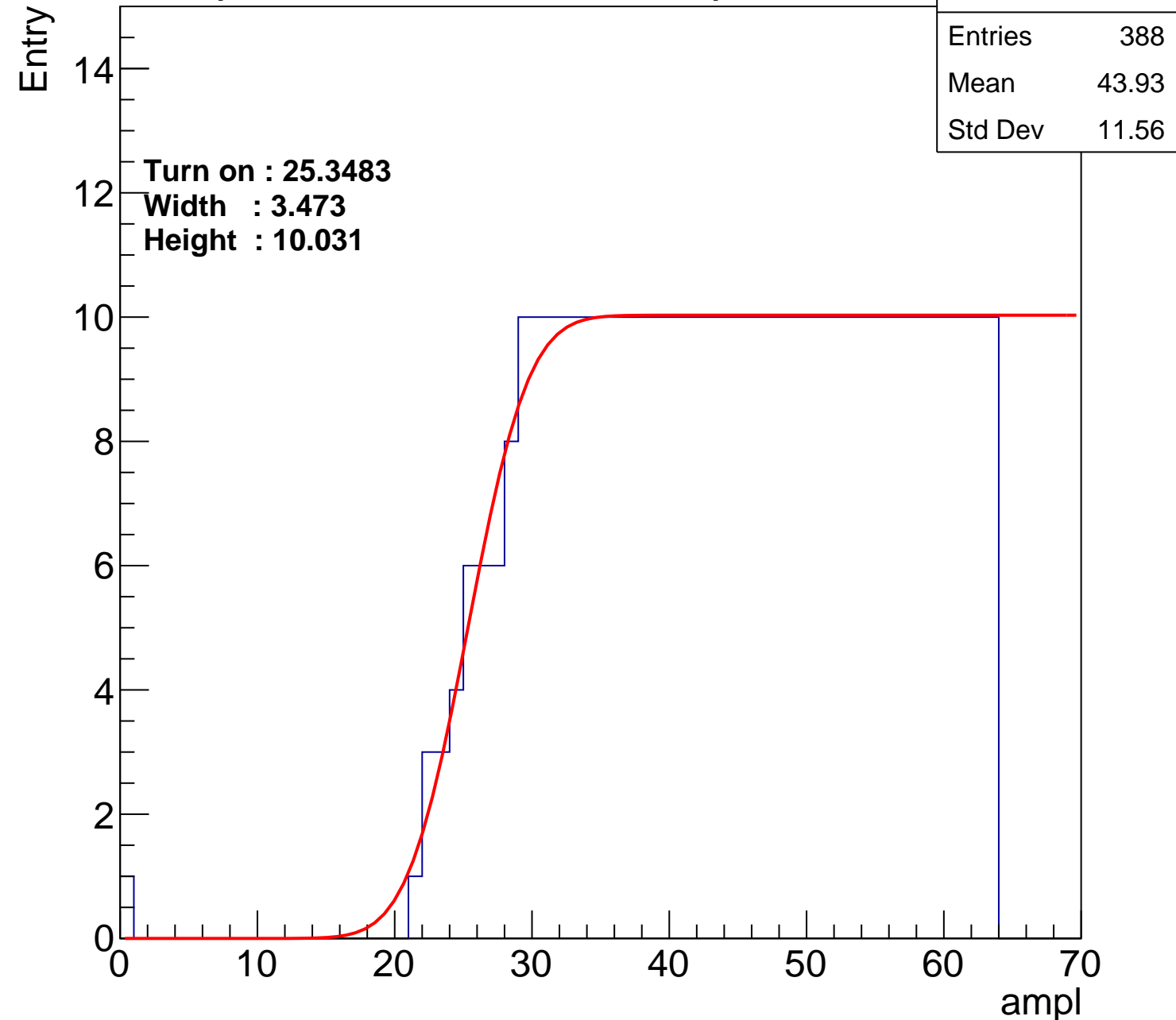
Width : 3.473

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.83
Std Dev	12.28

Turn on : 23.4027

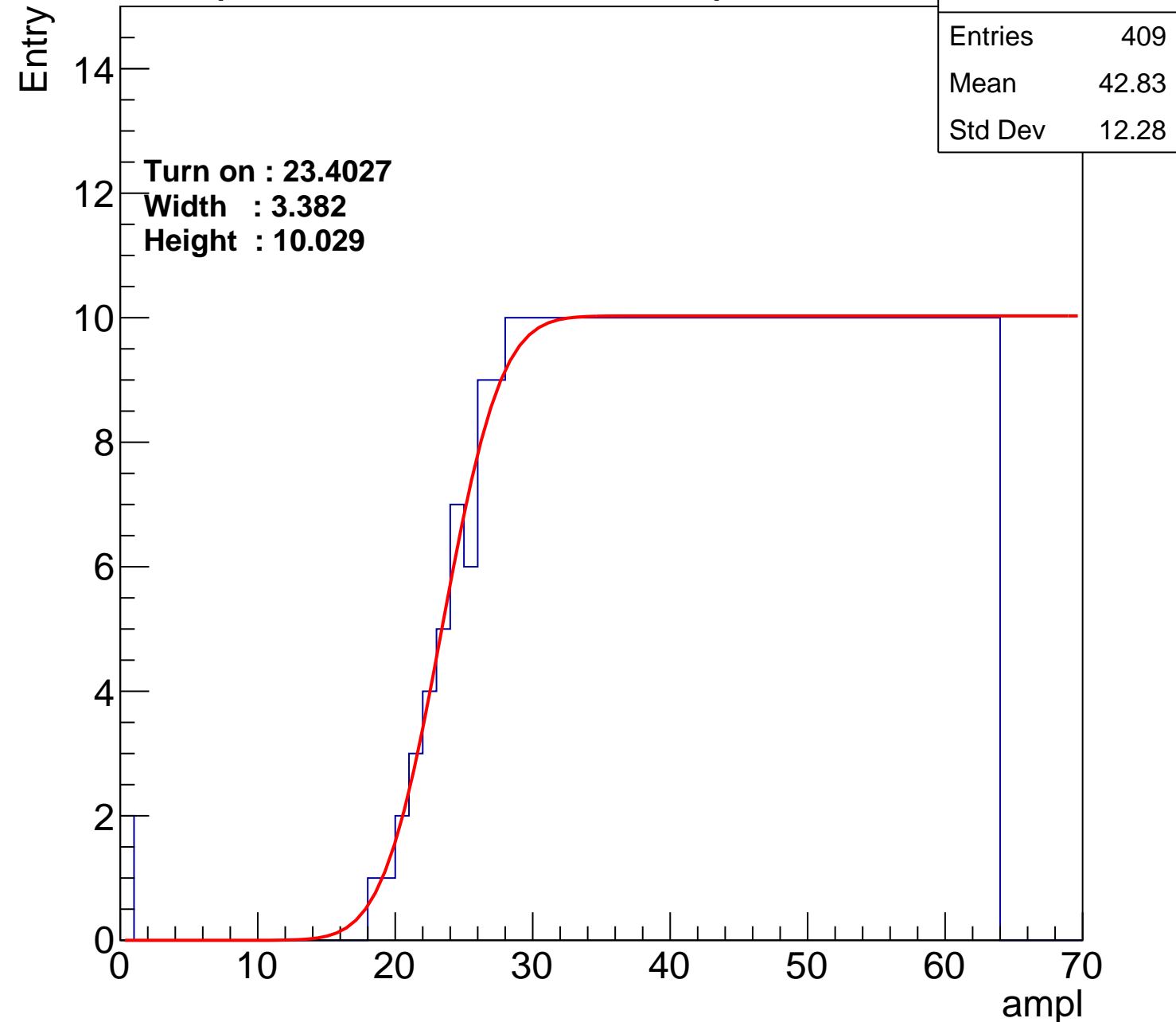
Width : 3.382

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.9
Std Dev	12.21

Turn on : 23.9641

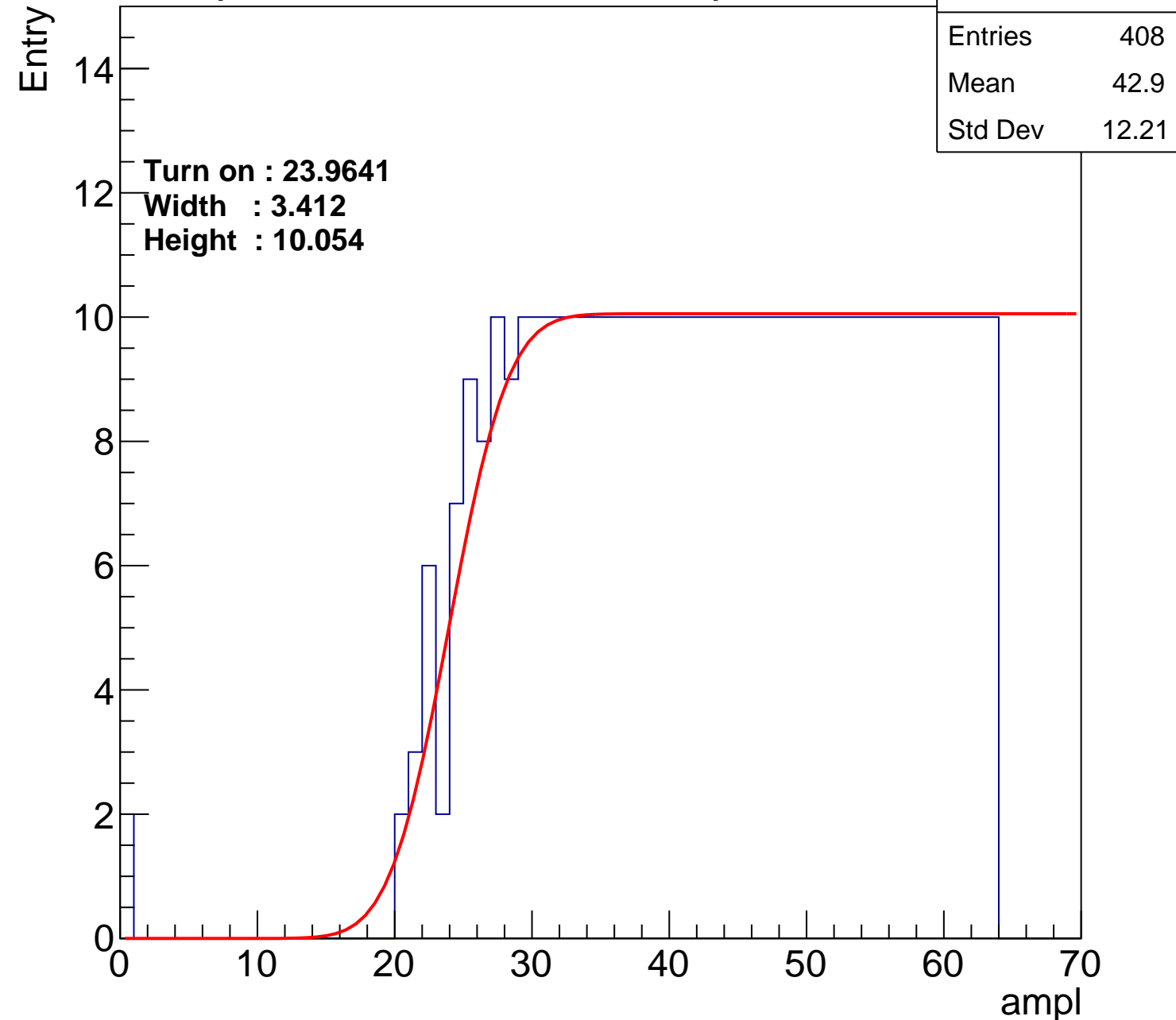
Width : 3.412

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch99

calib_packv5_042523_0143.root, FC#10, port B3

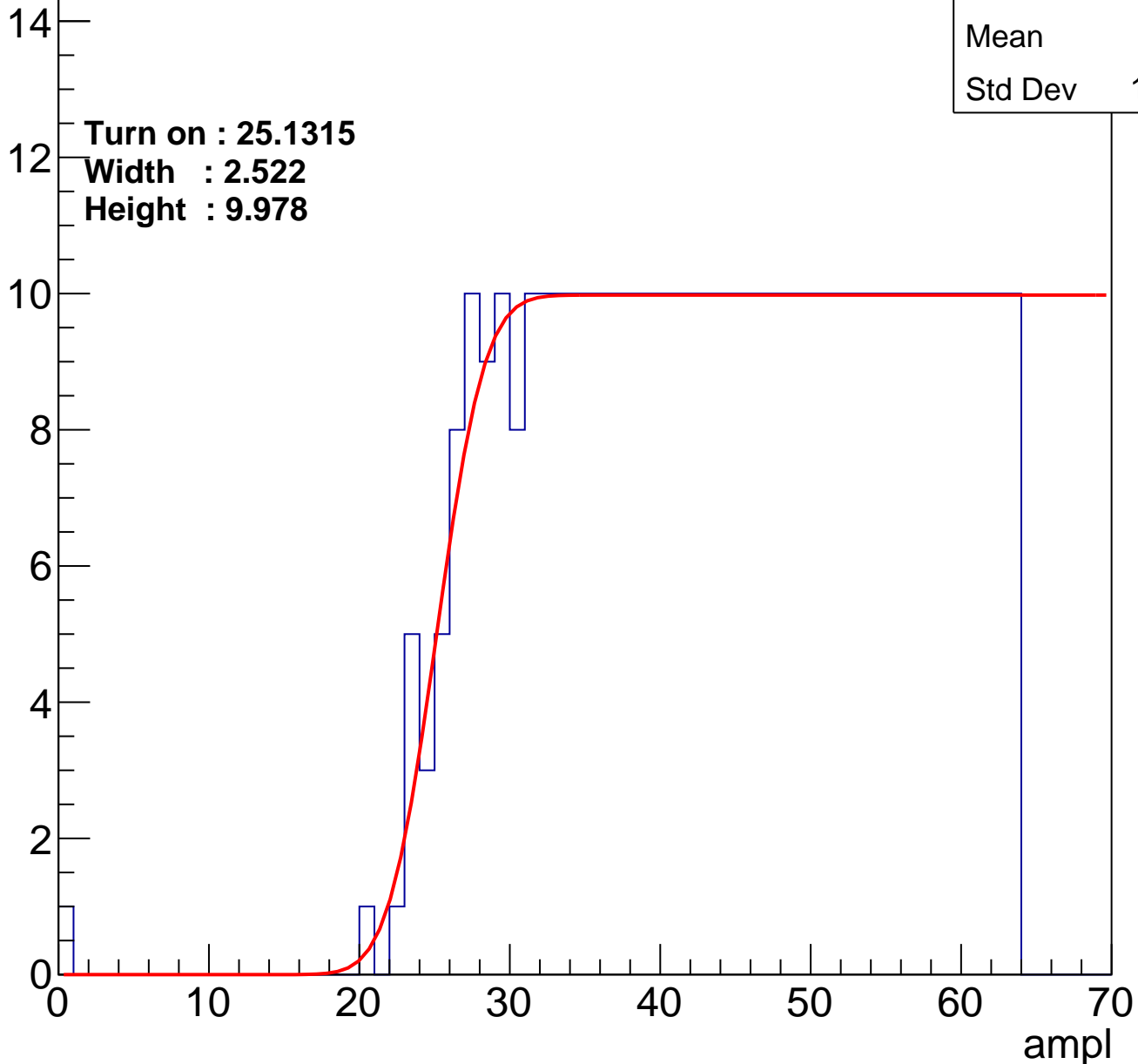
Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 25.1315

Width : 2.522

Height : 9.978

Entry



B1L002S, U16-ch100

calib_packv5_042523_0143.root, FC#10, port B3

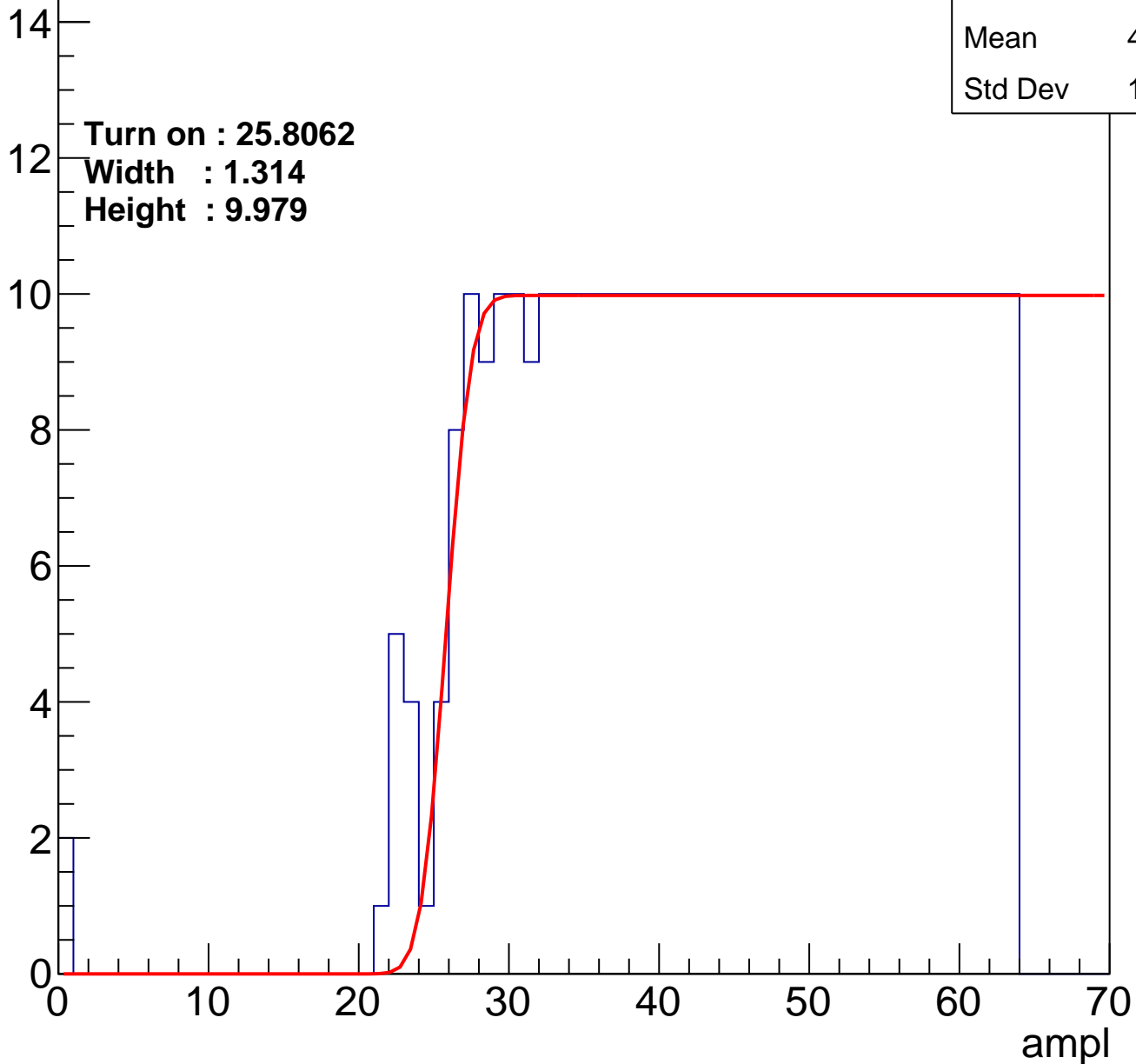
Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.8062

Width : 1.314

Height : 9.979

Entry



B1L002S, U16-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.08
Std Dev	11.9

Turn on : 26.5969

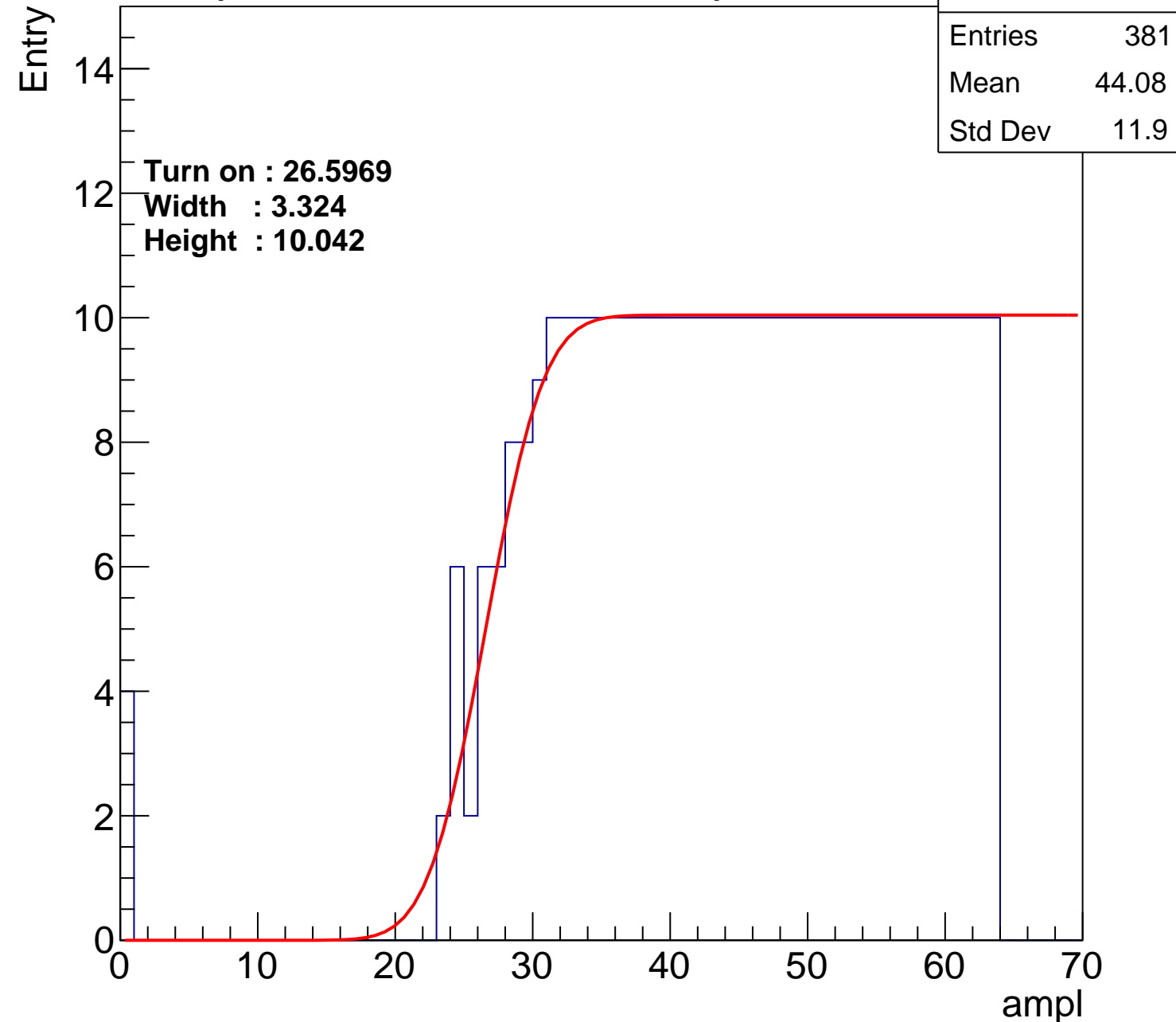
Width : 3.324

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch102

calib_packv5_042523_0143.root, FC#10, port B3

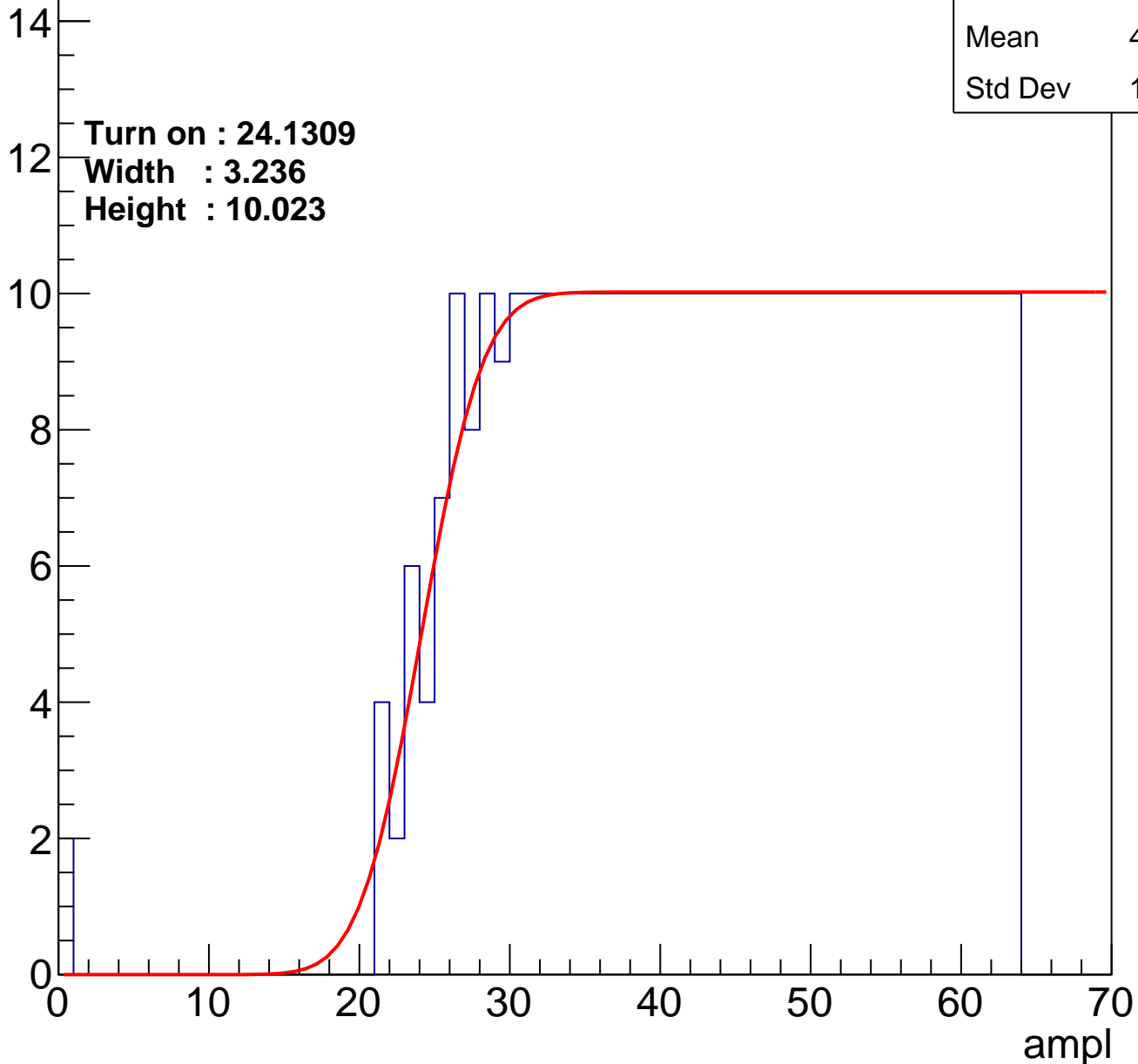
Entries	402
Mean	43.19
Std Dev	12.06

Turn on : 24.1309

Width : 3.236

Height : 10.023

Entry



B1L002S, U16-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.88
Std Dev	12.36

Turn on : 24.1621

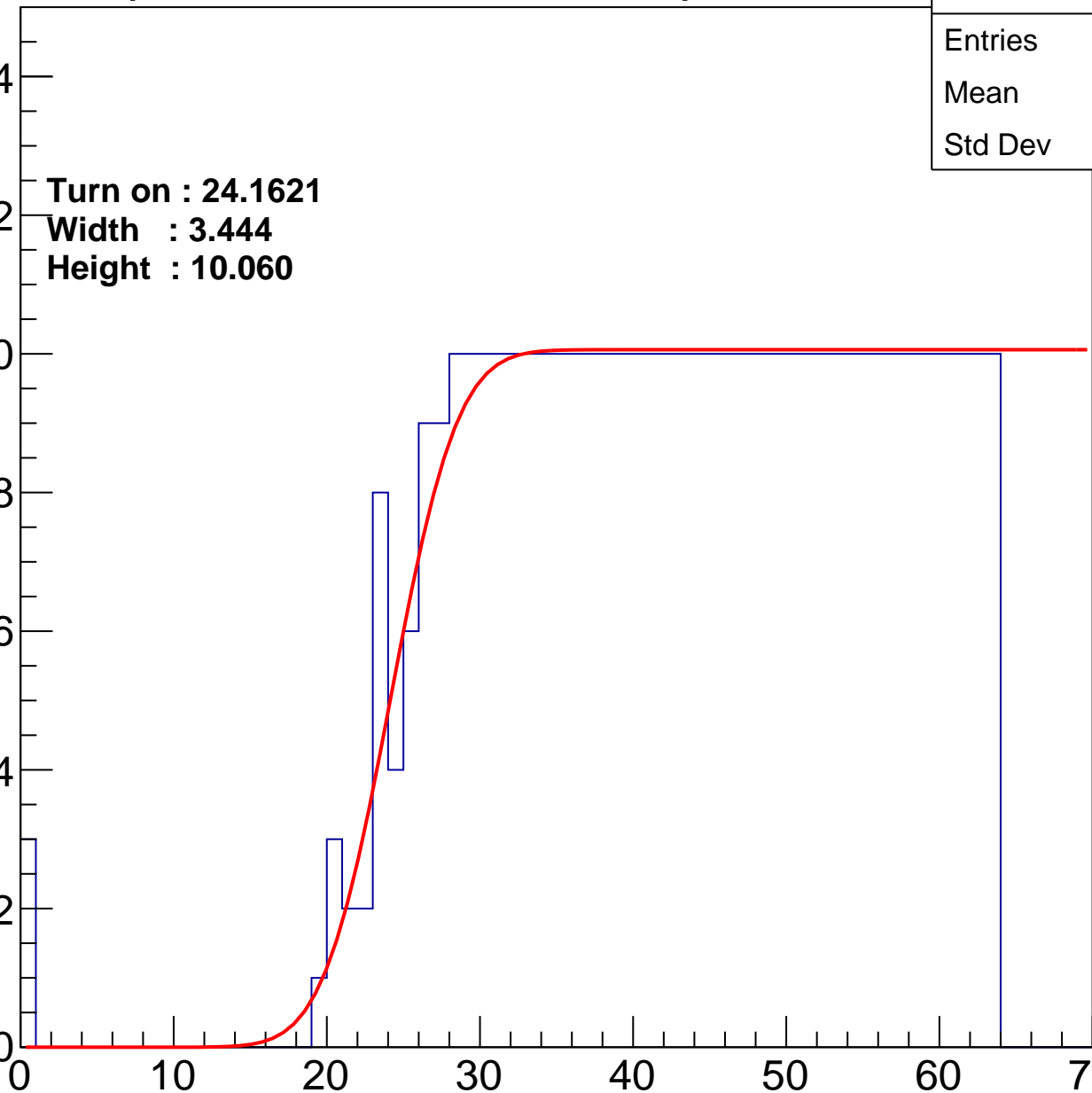
Width : 3.444

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch104

calib_packv5_042523_0143.root, FC#10, port B3

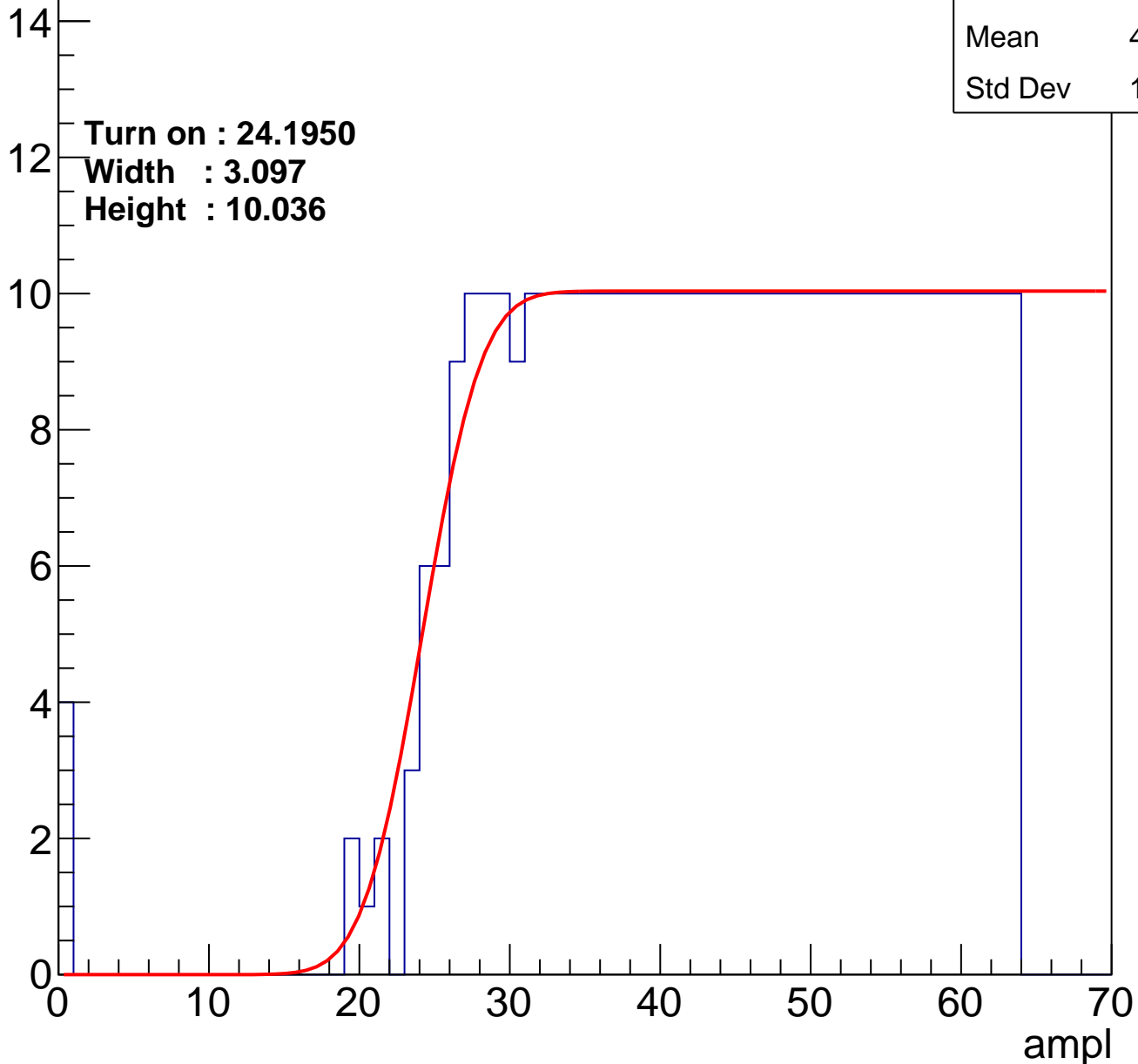
Entries	402
Mean	43.08
Std Dev	12.36

Turn on : 24.1950

Width : 3.097

Height : 10.036

Entry



B1L002S, U16-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.5
Std Dev	12

Turn on : 24.9025

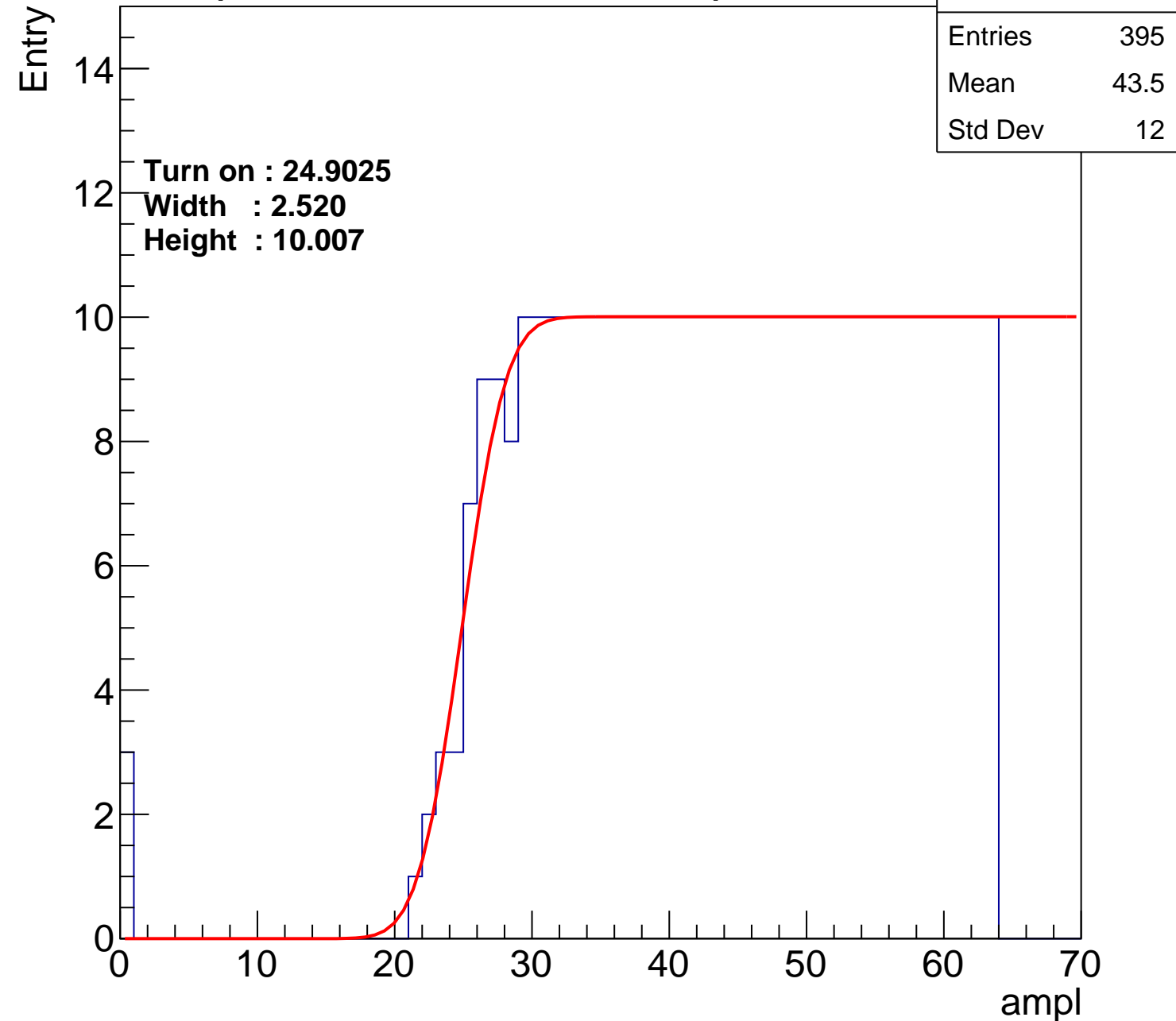
Width : 2.520

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.55
Std Dev	11.87

Turn on : 25.0421

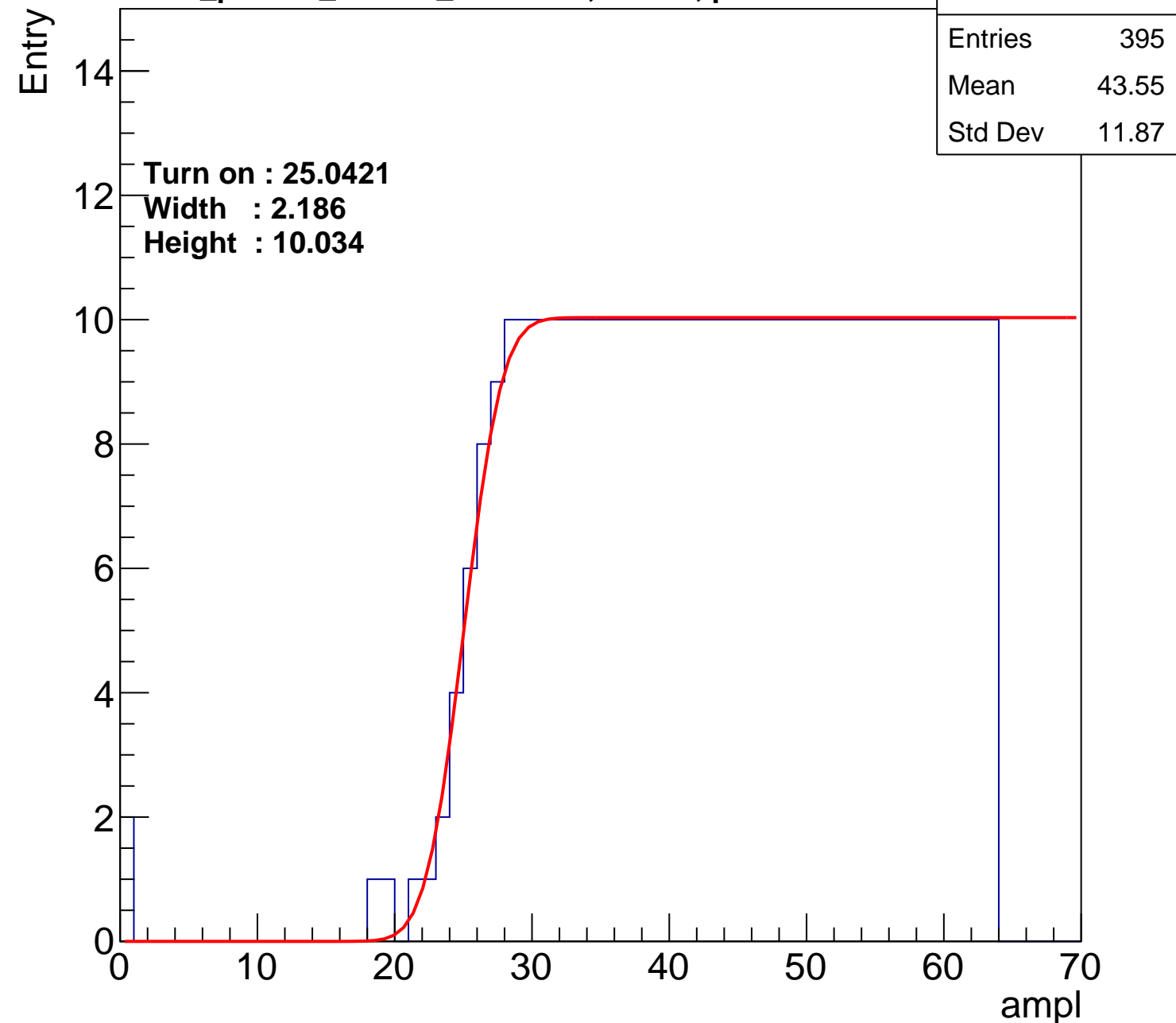
Width : 2.186

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.61
Std Dev	12.22

Turn on : 25.4450

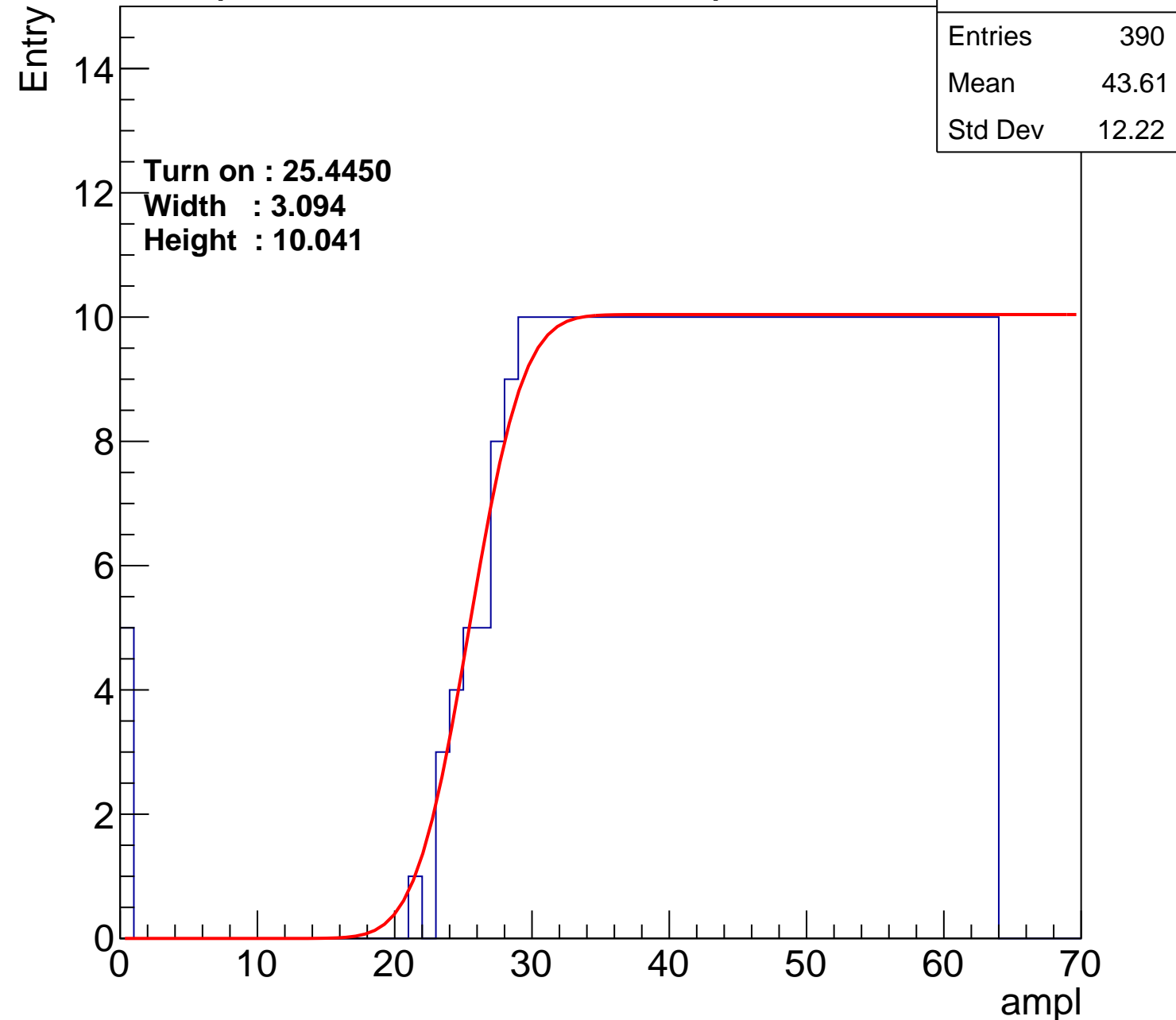
Width : 3.094

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.66
Std Dev	12.65

Turn on : 23.7677

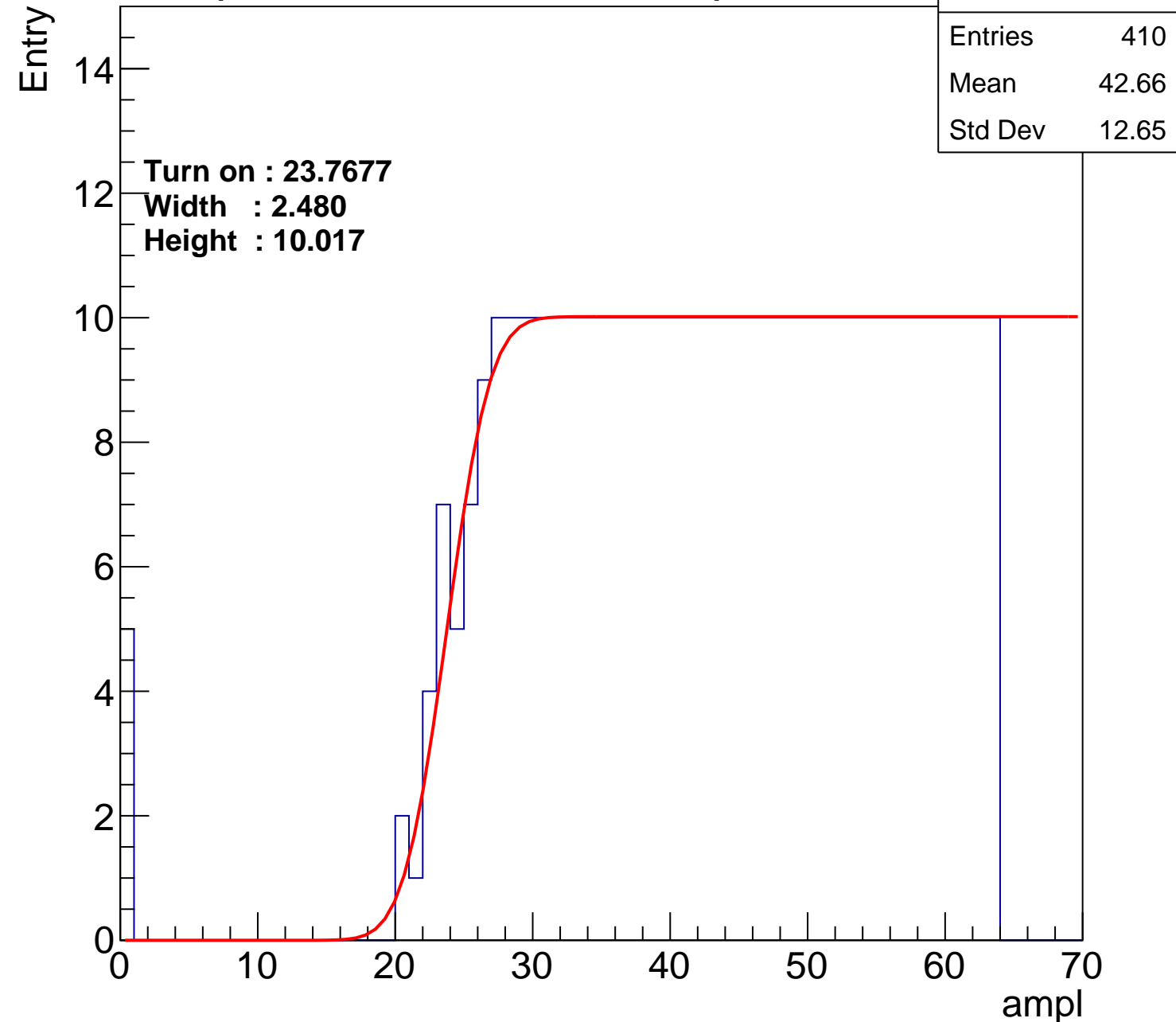
Width : 2.480

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch109

calib_packv5_042523_0143.root, FC#10, port B3

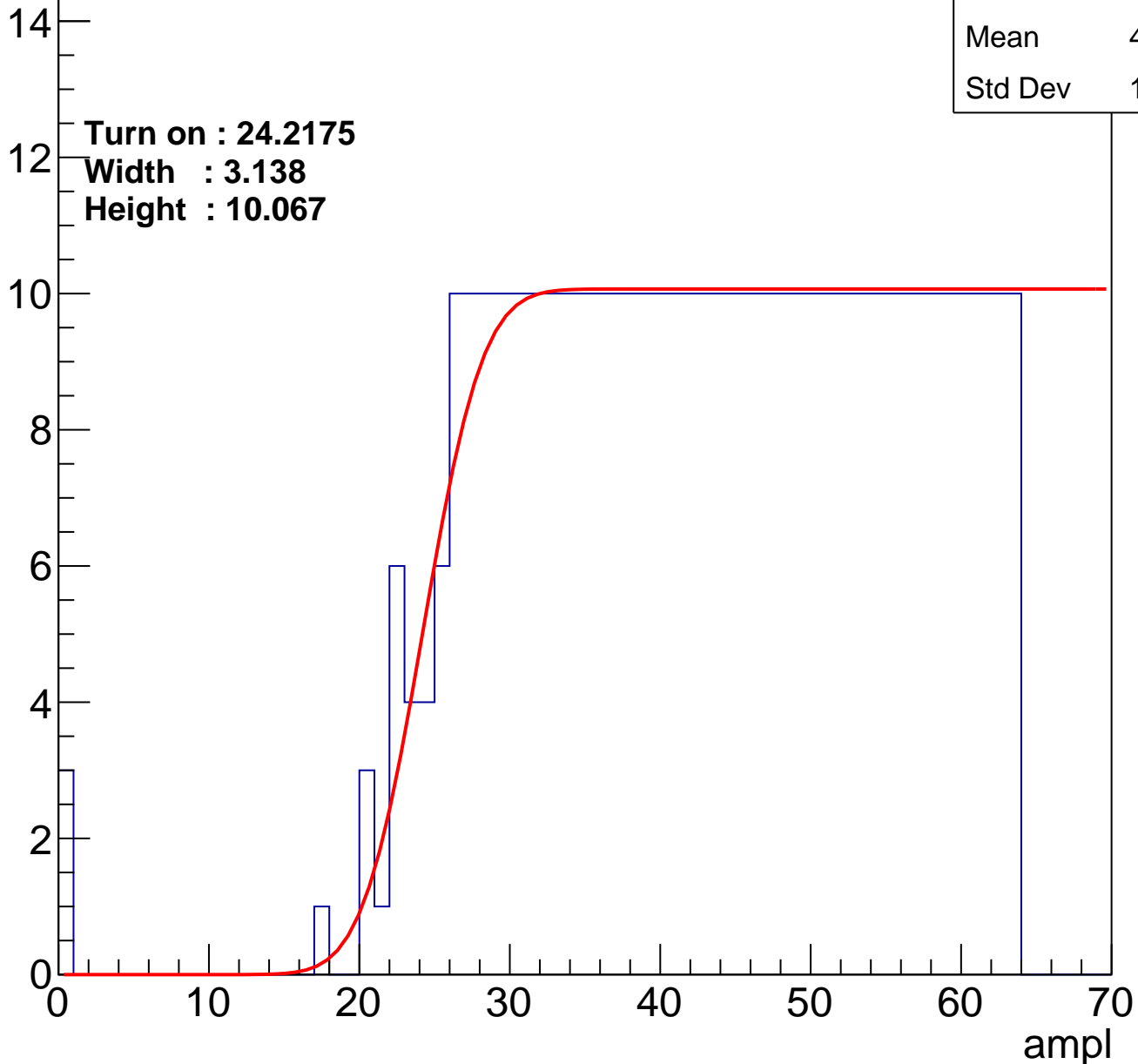
Entries	408
Mean	42.84
Std Dev	12.37

Turn on : 24.2175

Width : 3.138

Height : 10.067

Entry



B1L002S, U16-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.56
Std Dev	11.89

Turn on : 24.5163

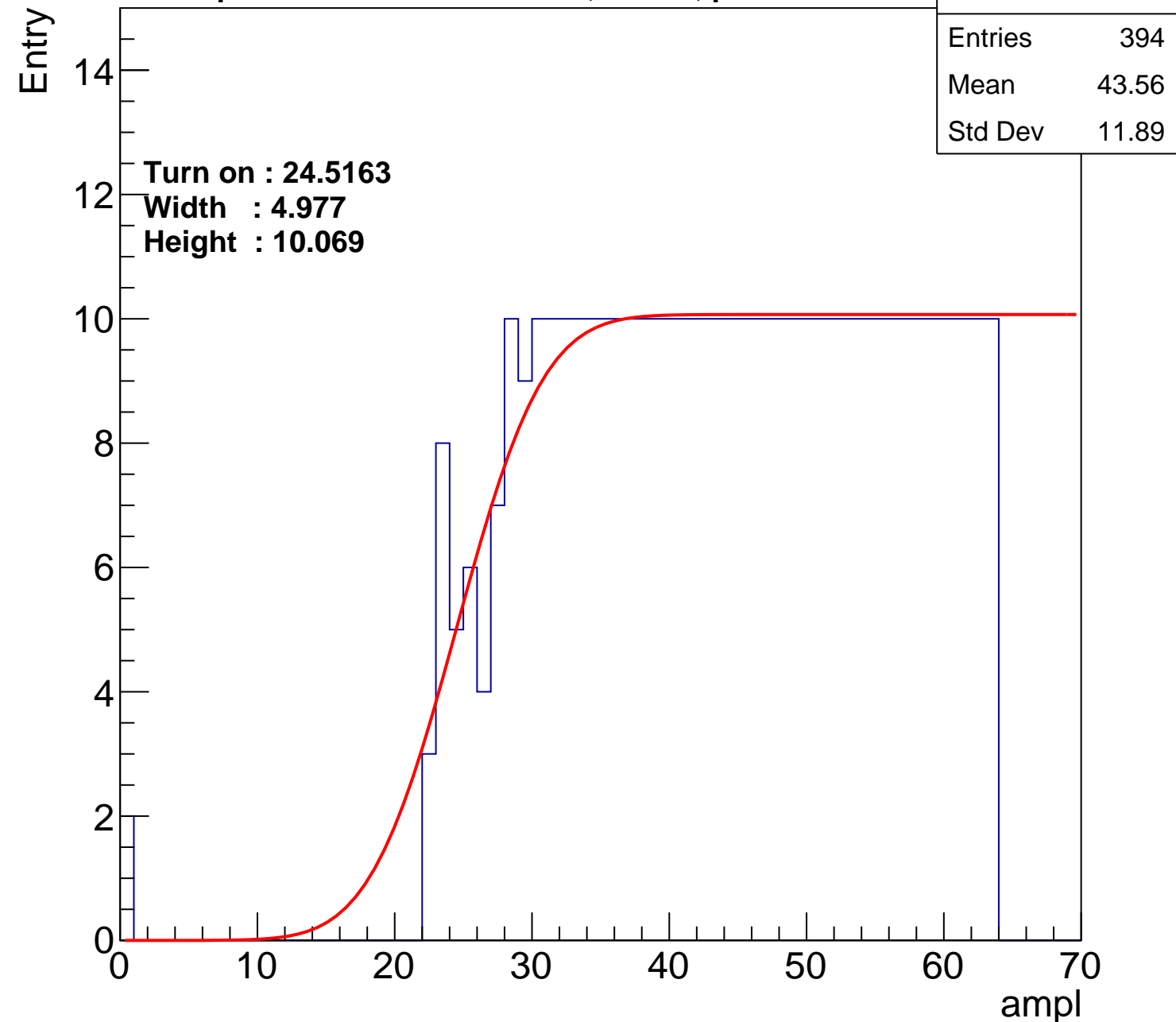
Width : 4.977

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.5749

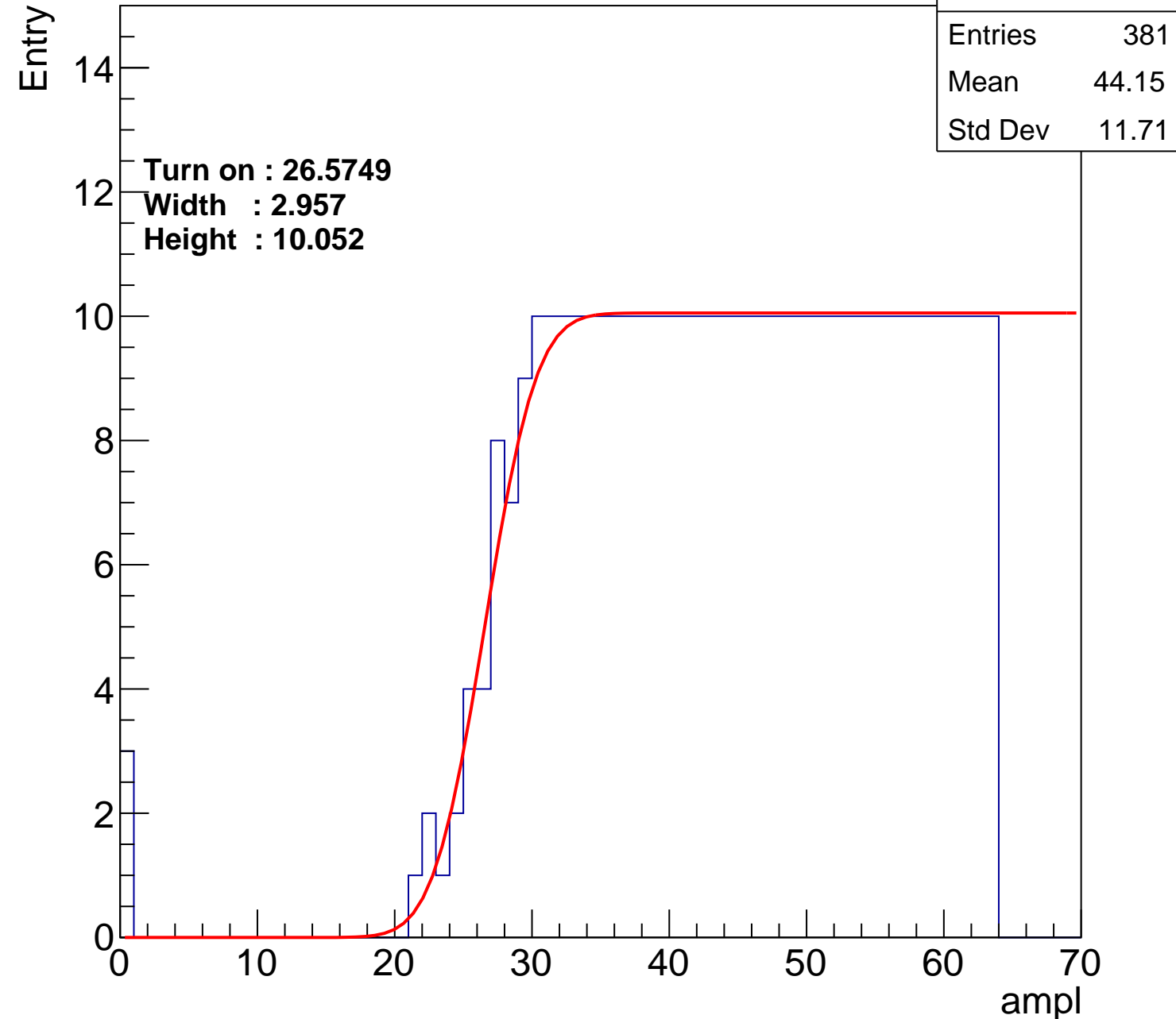
Width : 2.957

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.13
Std Dev	12.11

Turn on : 24.1023

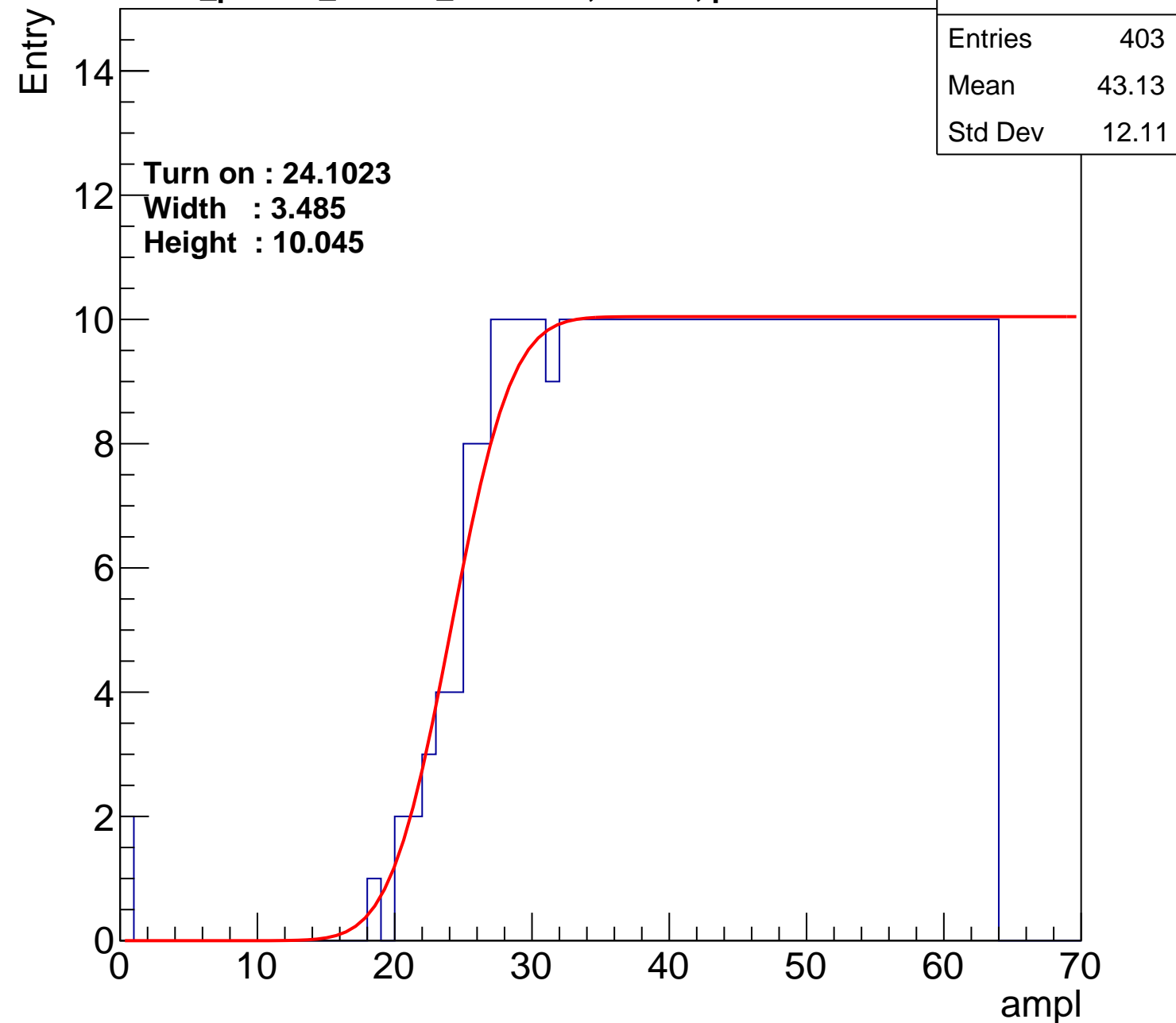
Width : 3.485

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.6
Std Dev	11.96

Turn on : 25.1722

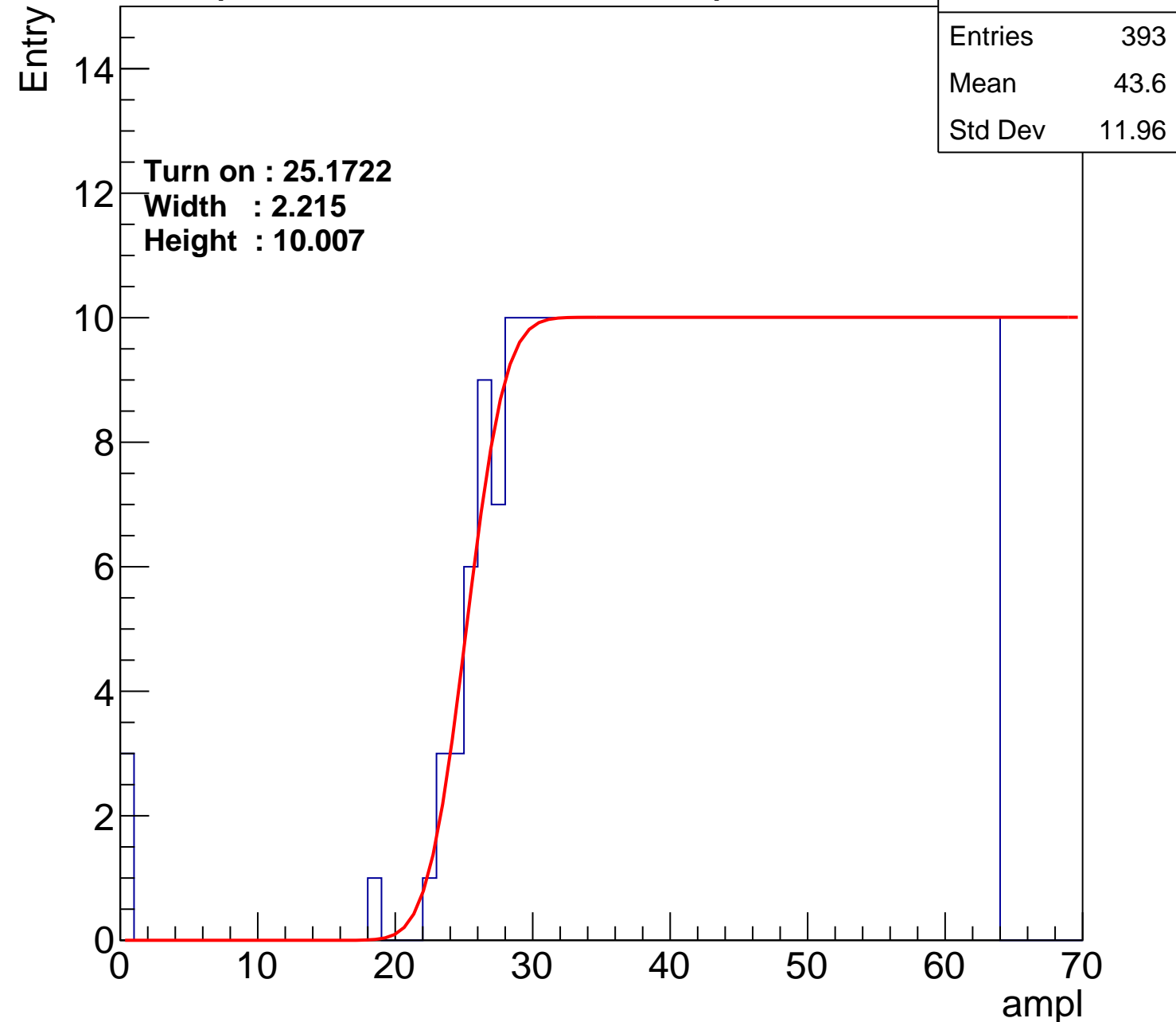
Width : 2.215

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.08
Std Dev	12.24

Turn on : 24.3342

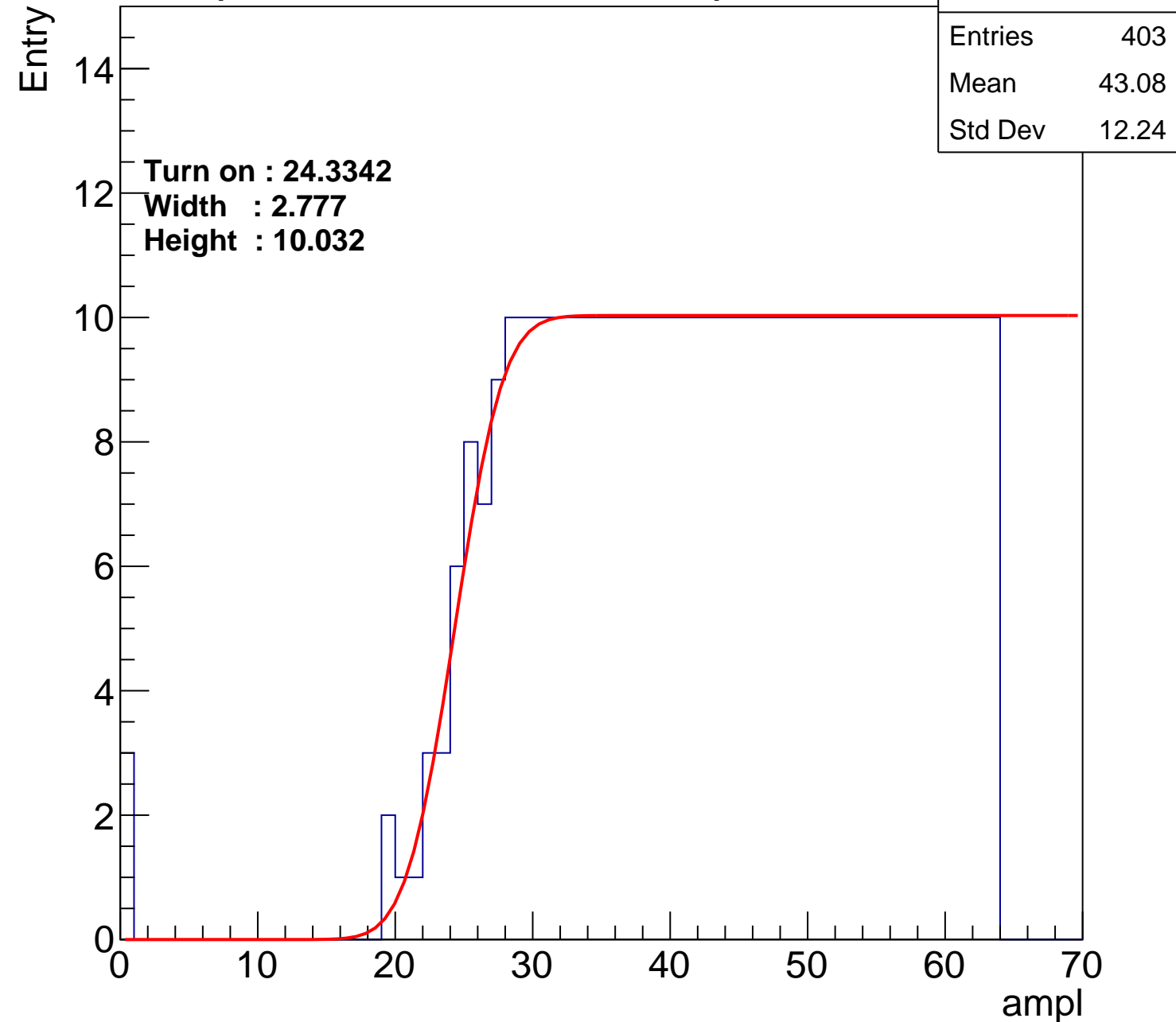
Width : 2.777

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.66

Turn on : 24.8915

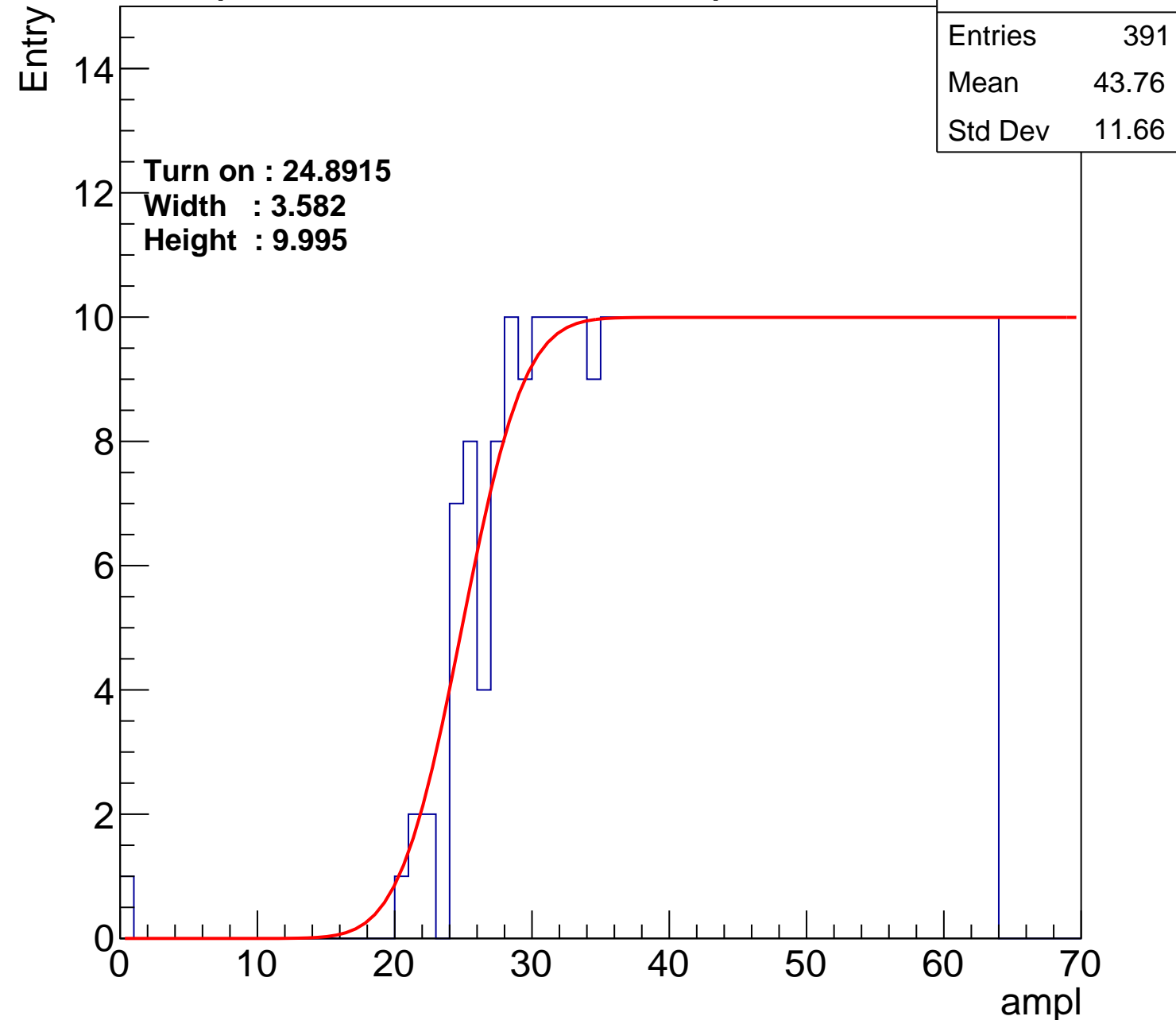
Width : 3.582

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.58
Std Dev	11.87

Turn on : 24.9148

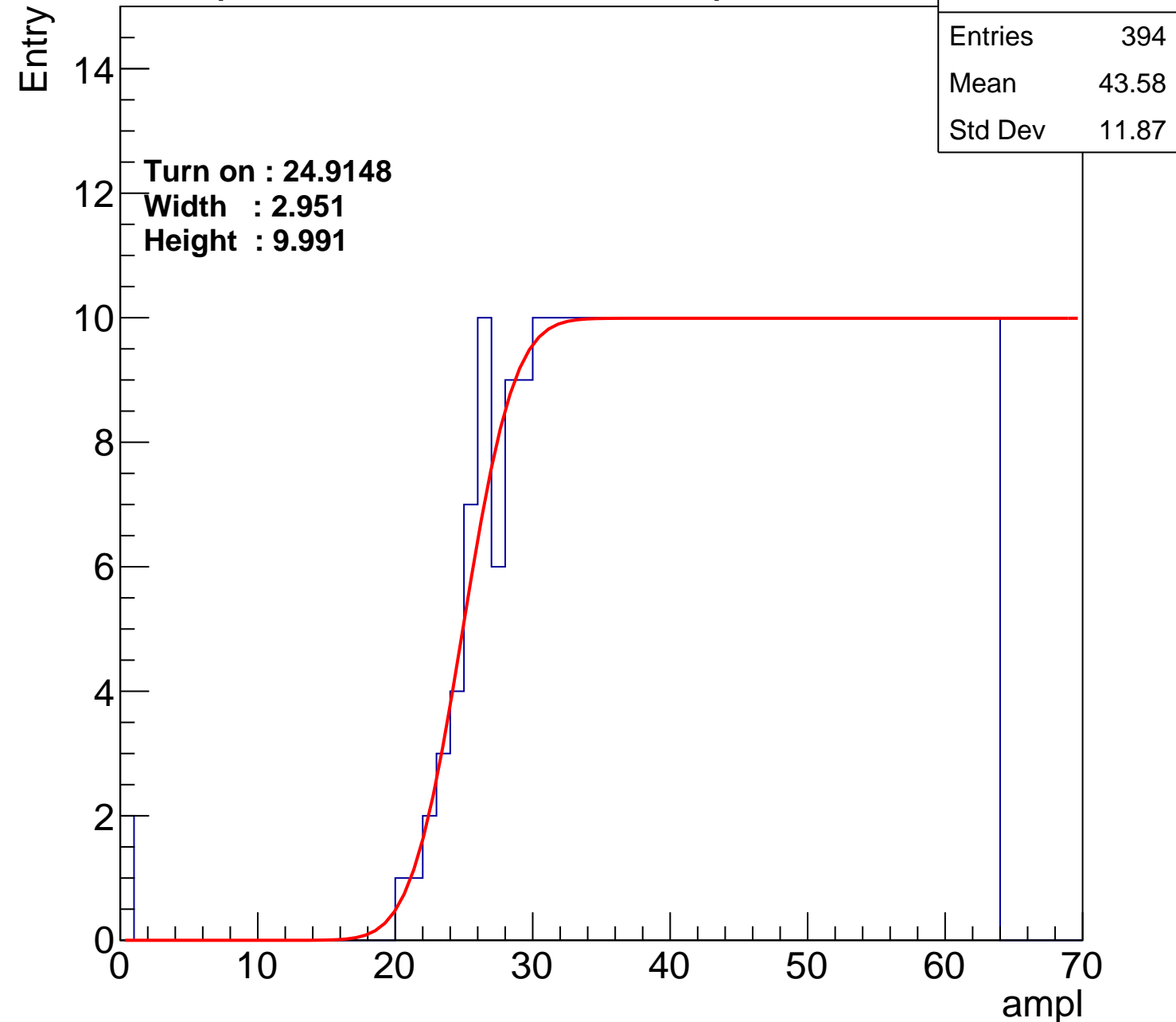
Width : 2.951

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 27.0574

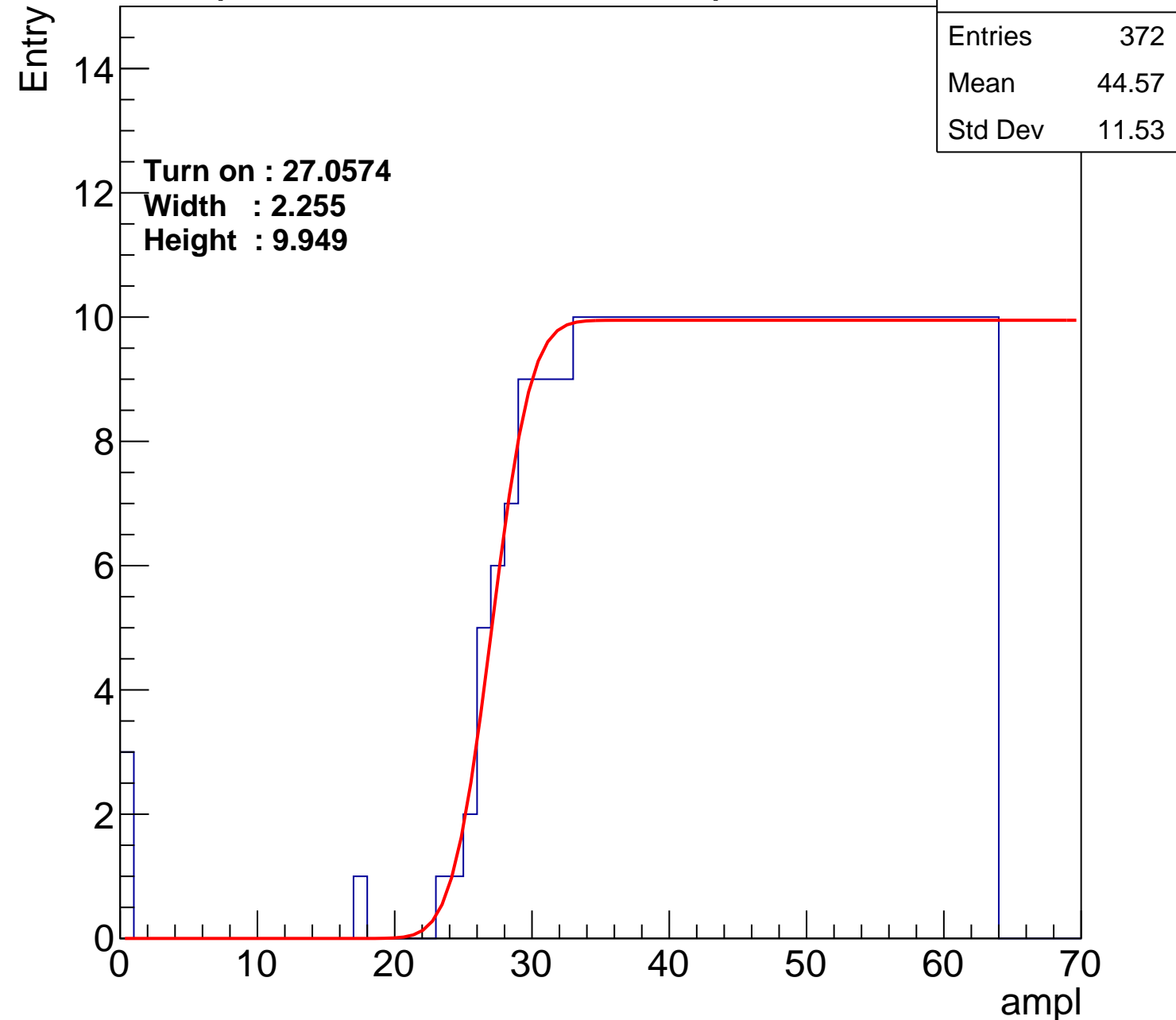
Width : 2.255

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch118

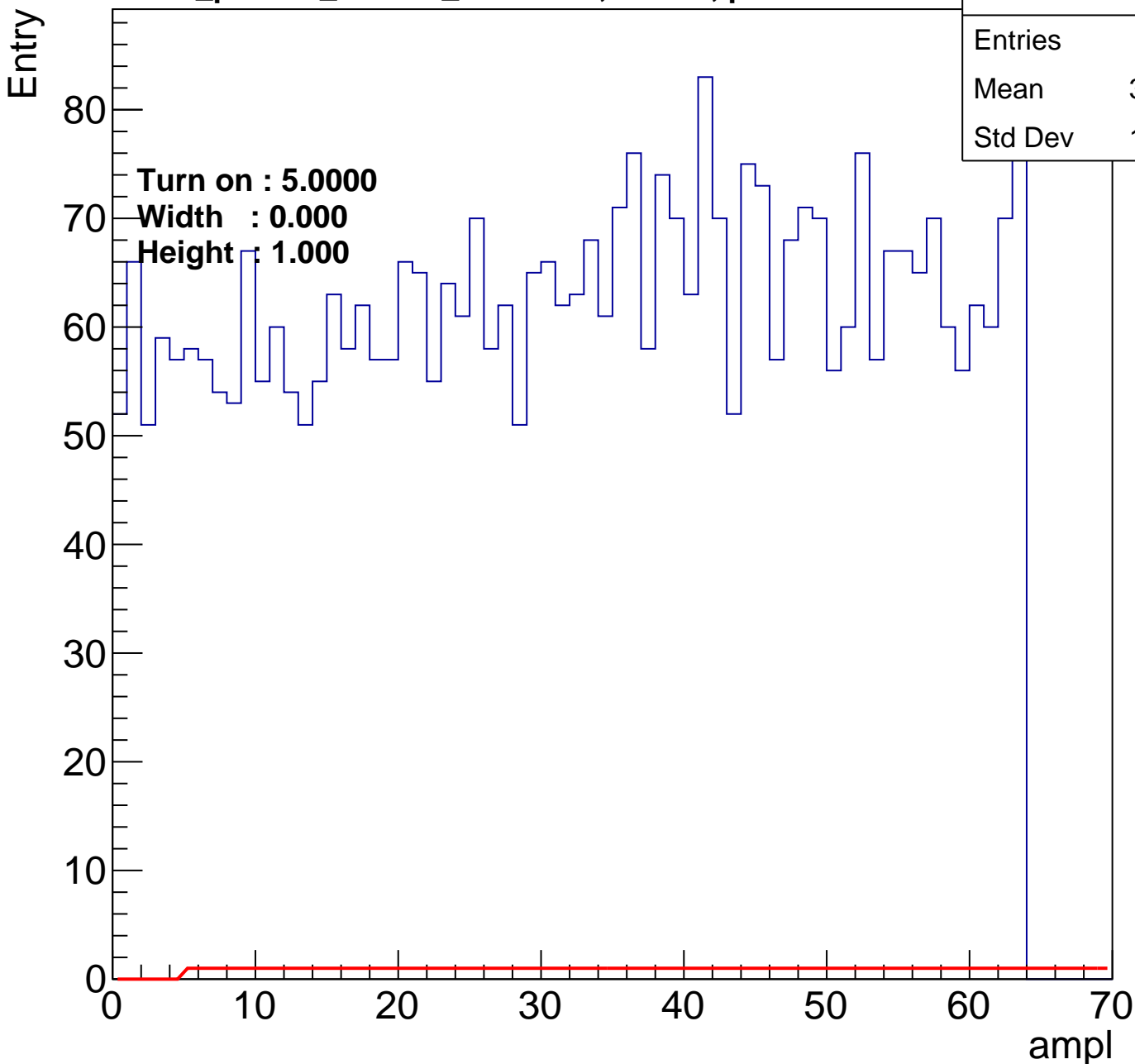
calib_packv5_042523_0143.root, FC#10, port B3

Entries	4025
Mean	32.58
Std Dev	18.29

Turn on : 5.0000

Width : 0.000

Height : 1.000



B1L002S, U16-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.41
Std Dev	12.21

Turn on : 25.1600

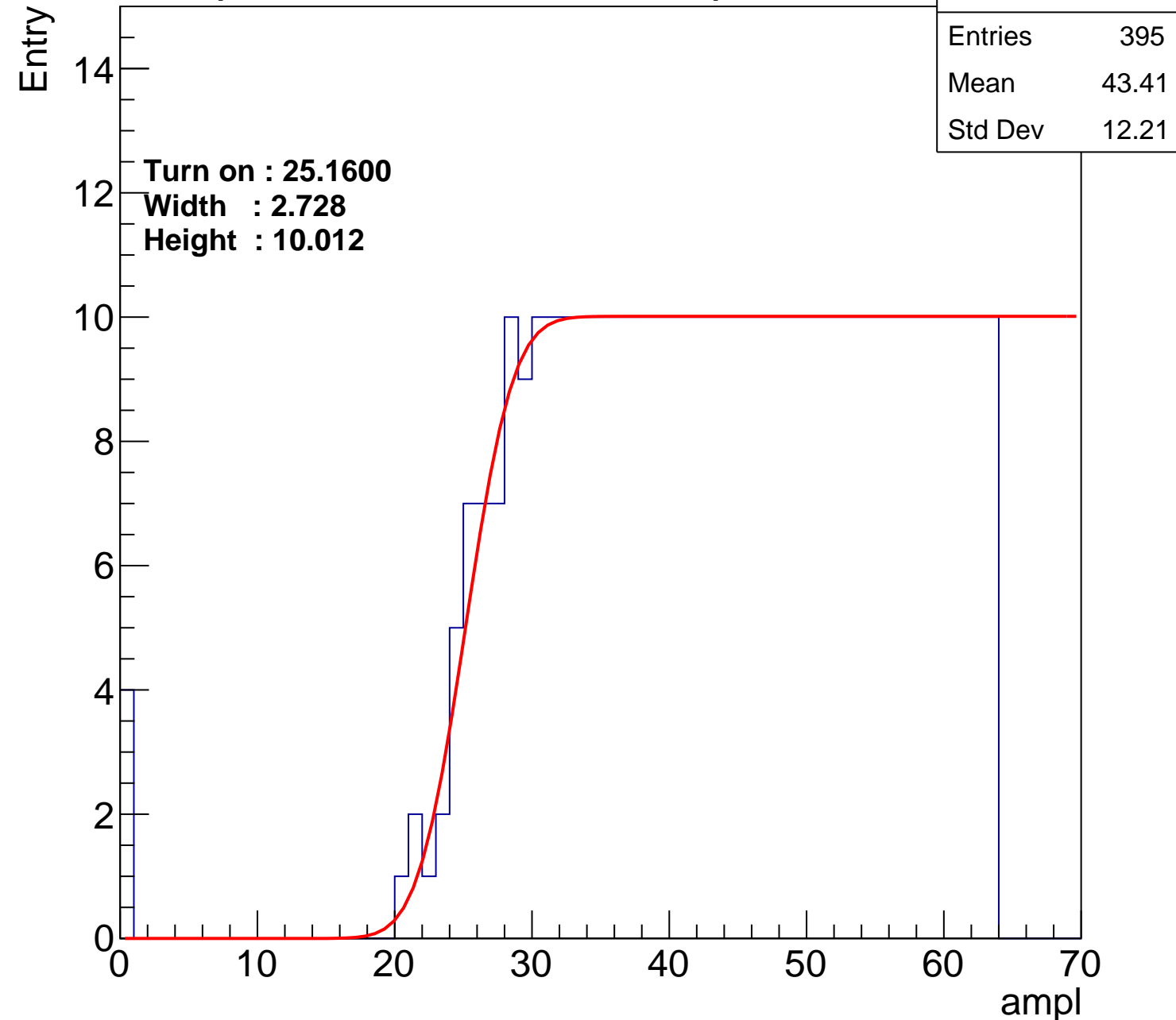
Width : 2.728

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch120

calib_packv5_042523_0143.root, FC#10, port B3

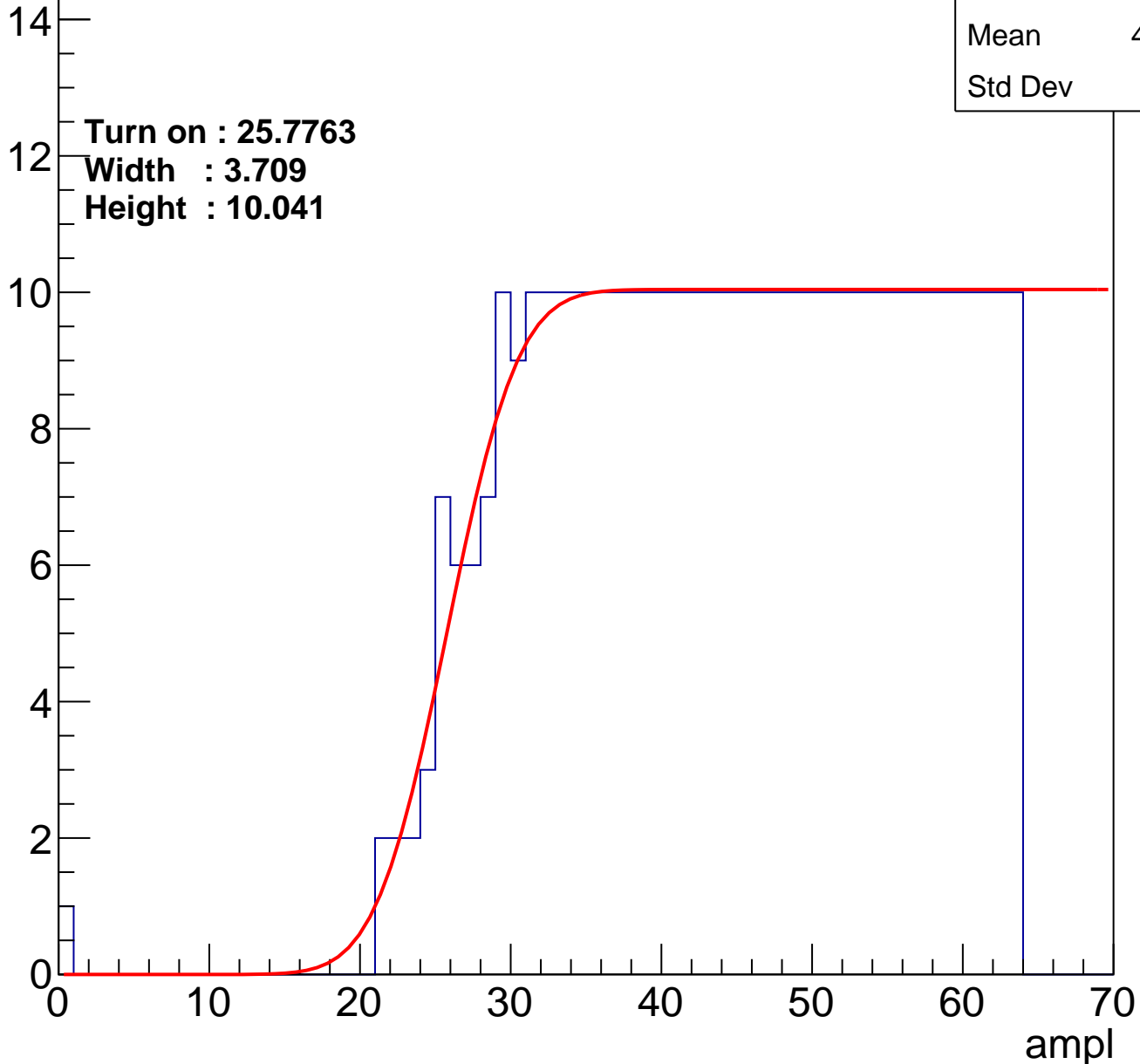
Entries	385
Mean	44.06
Std Dev	11.5

Turn on : 25.7763

Width : 3.709

Height : 10.041

Entry



B1L002S, U16-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.57
Std Dev	12.16

Turn on : 25.3392

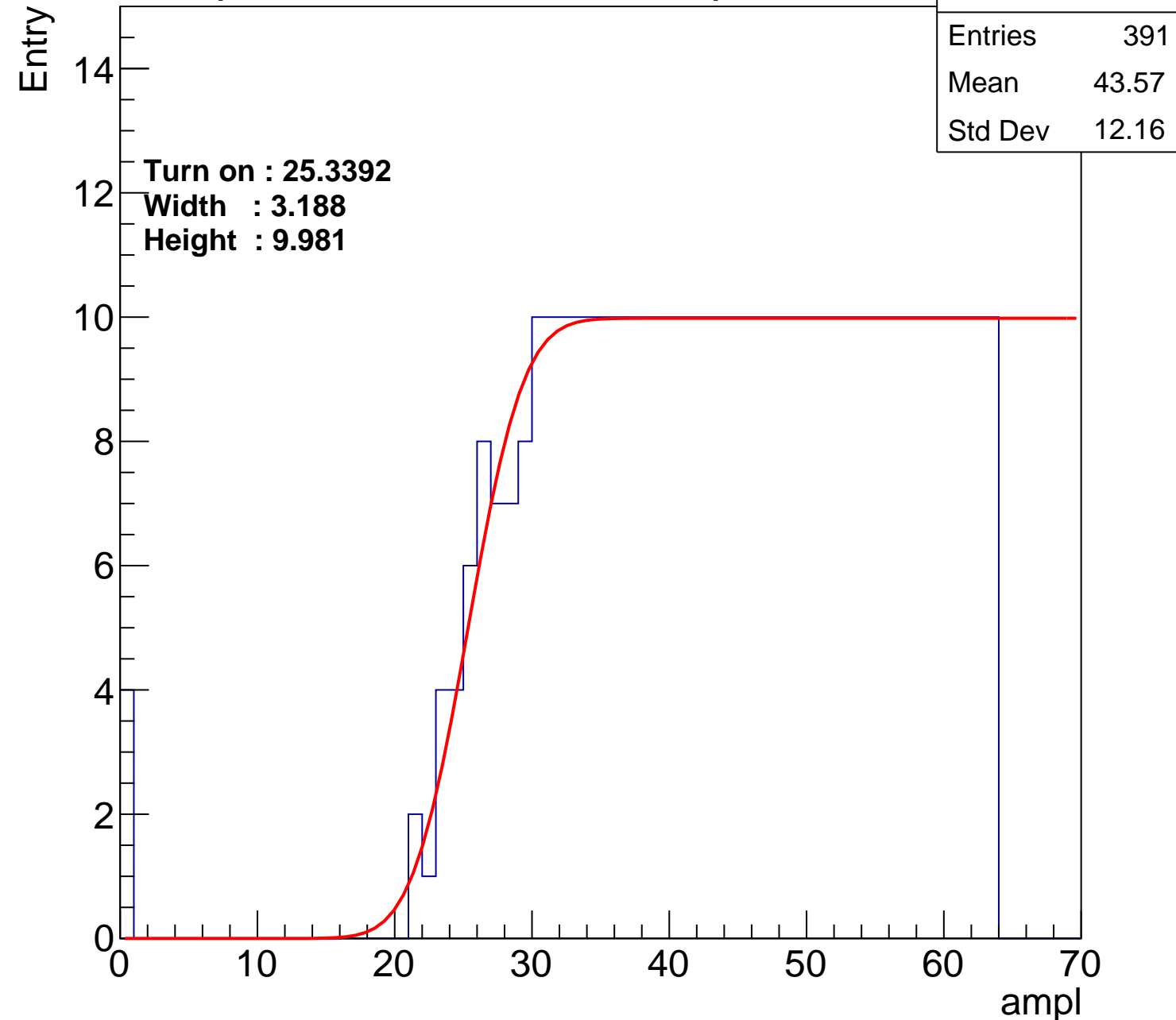
Width : 3.188

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	417
Mean	42.31
Std Dev	12.83

Turn on : 22.5323

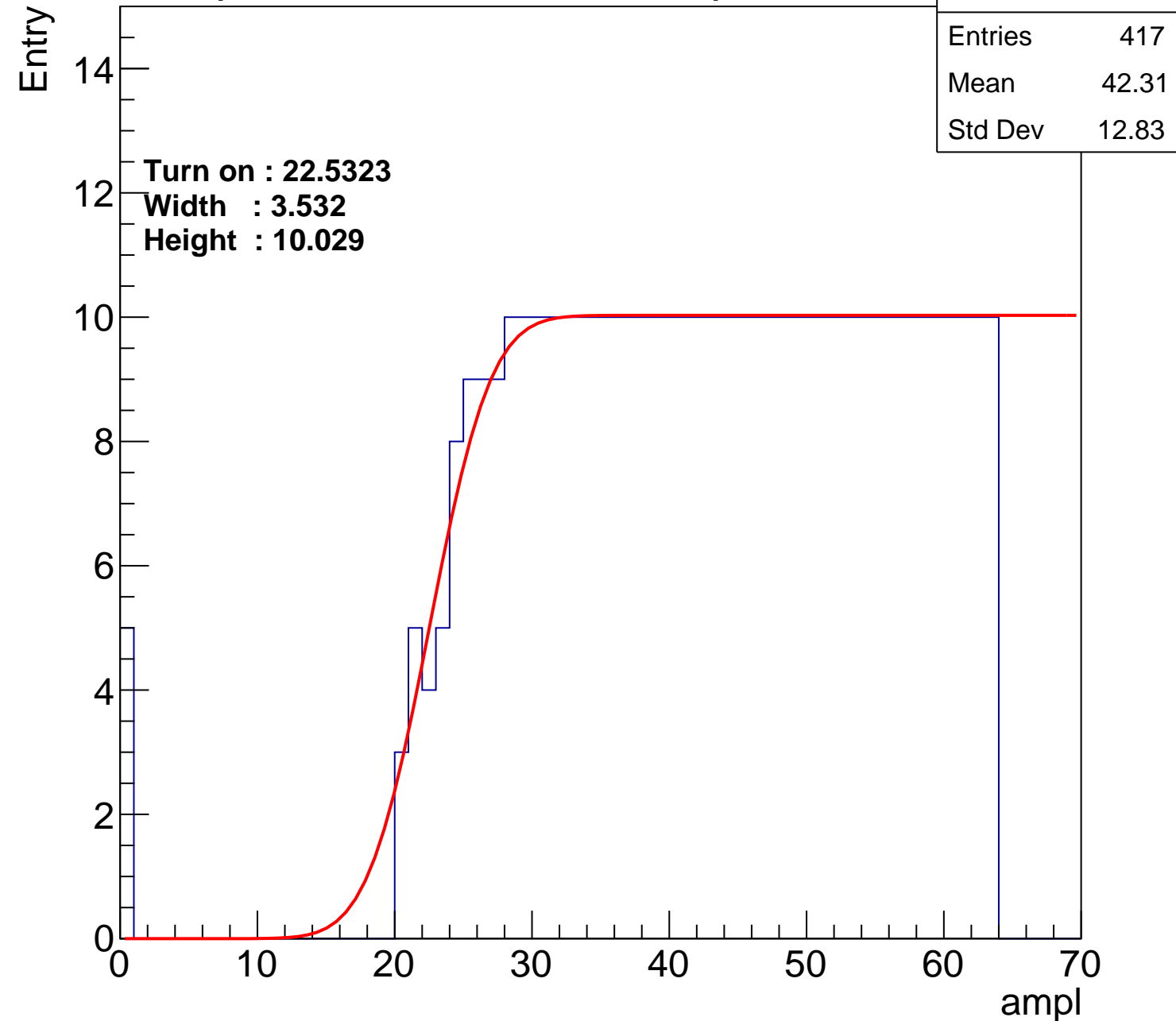
Width : 3.532

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.3759

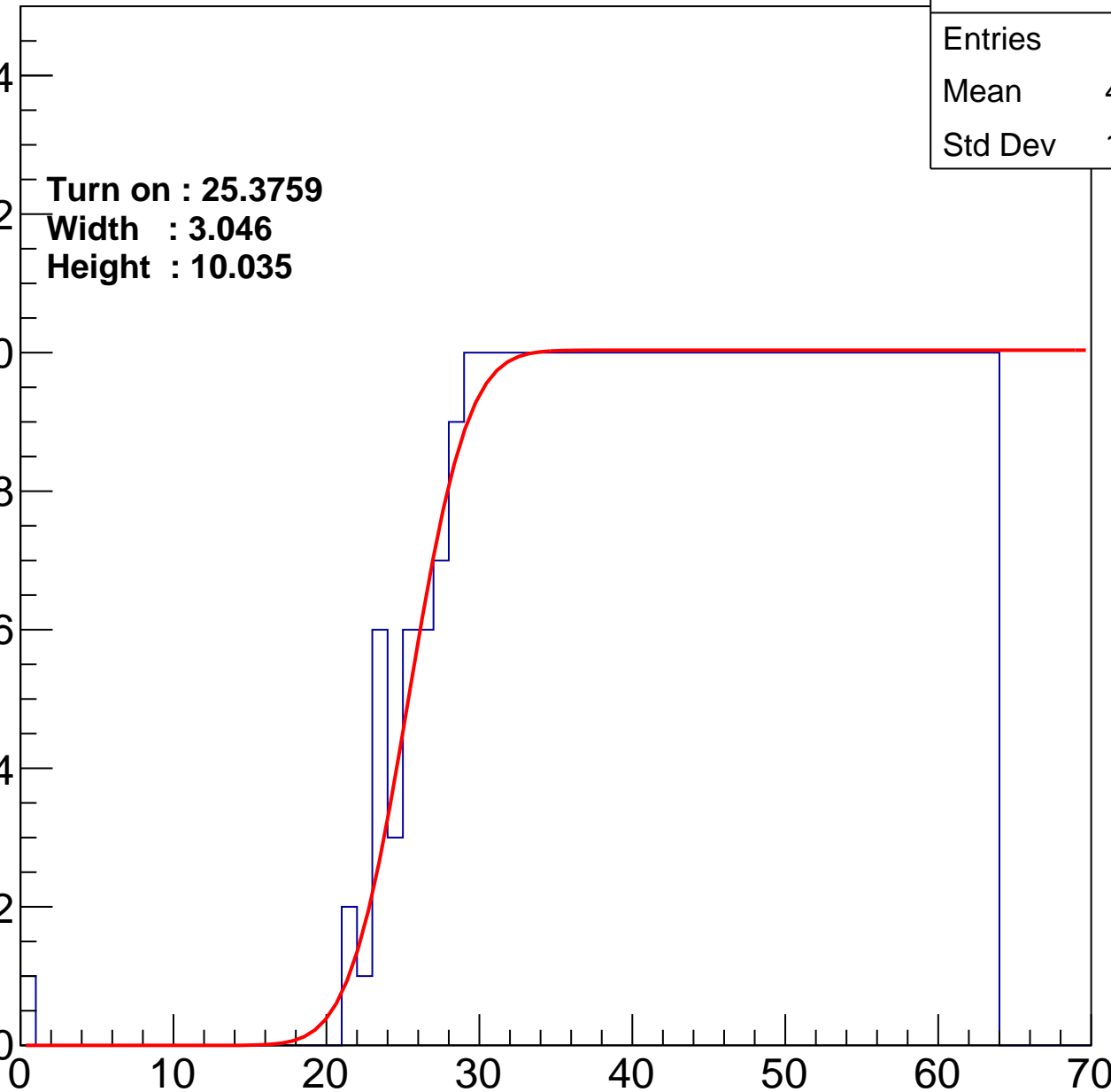
Width : 3.046

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.12
Std Dev	11.97

Turn on : 23.9564

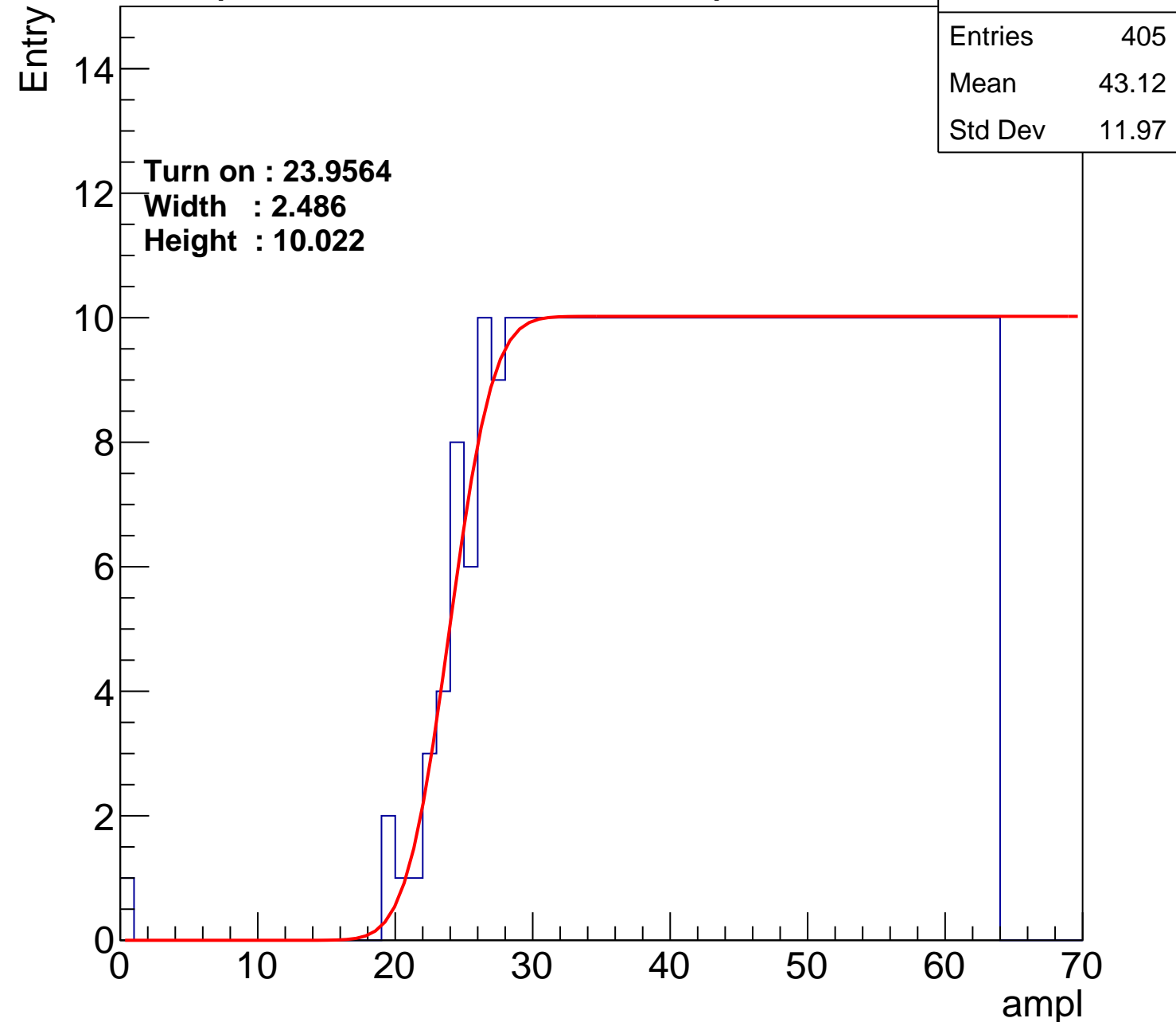
Width : 2.486

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.92
Std Dev	11.69

Turn on : 25.3066

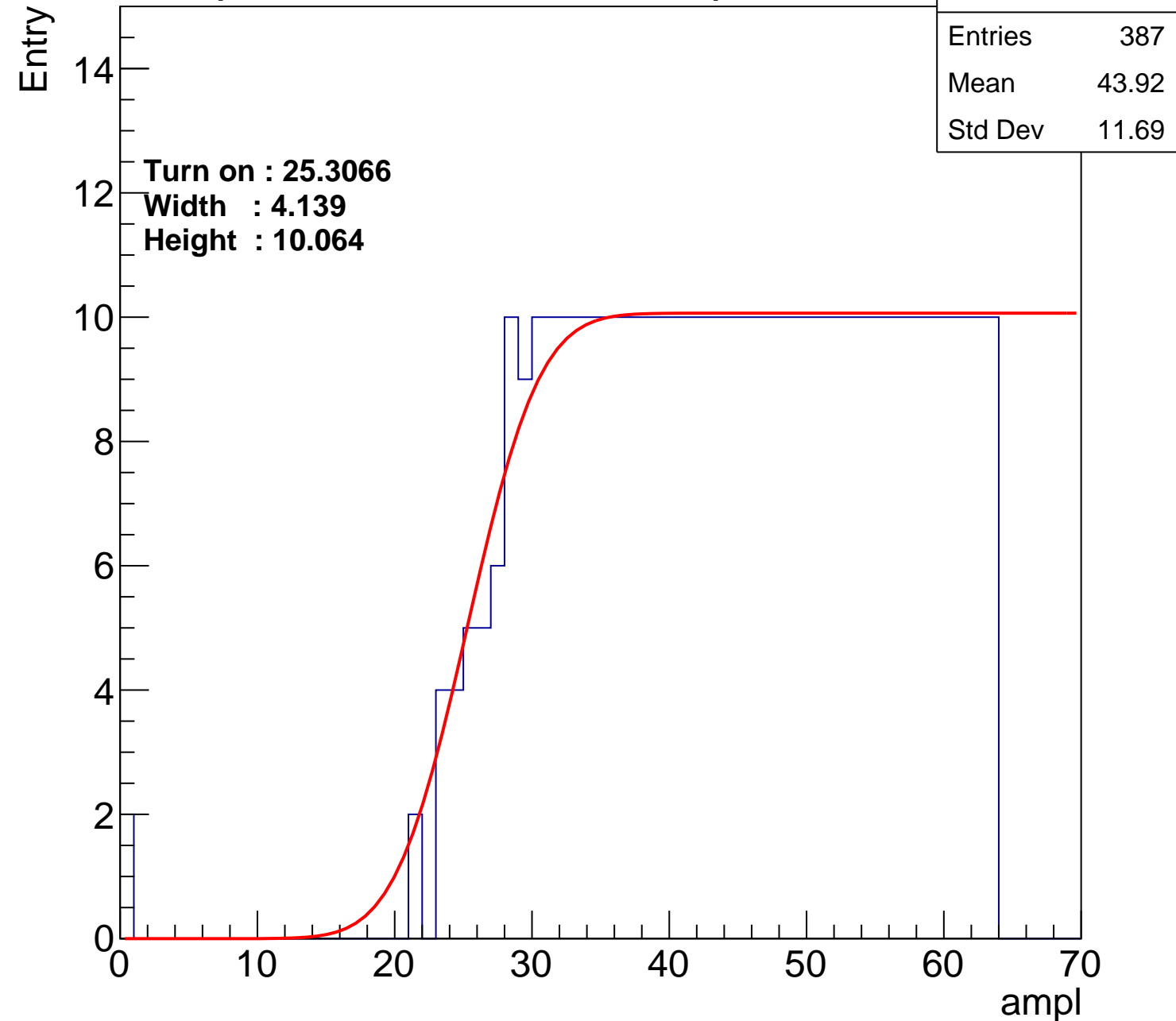
Width : 4.139

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.69
Std Dev	11.79

Turn on : 24.8071

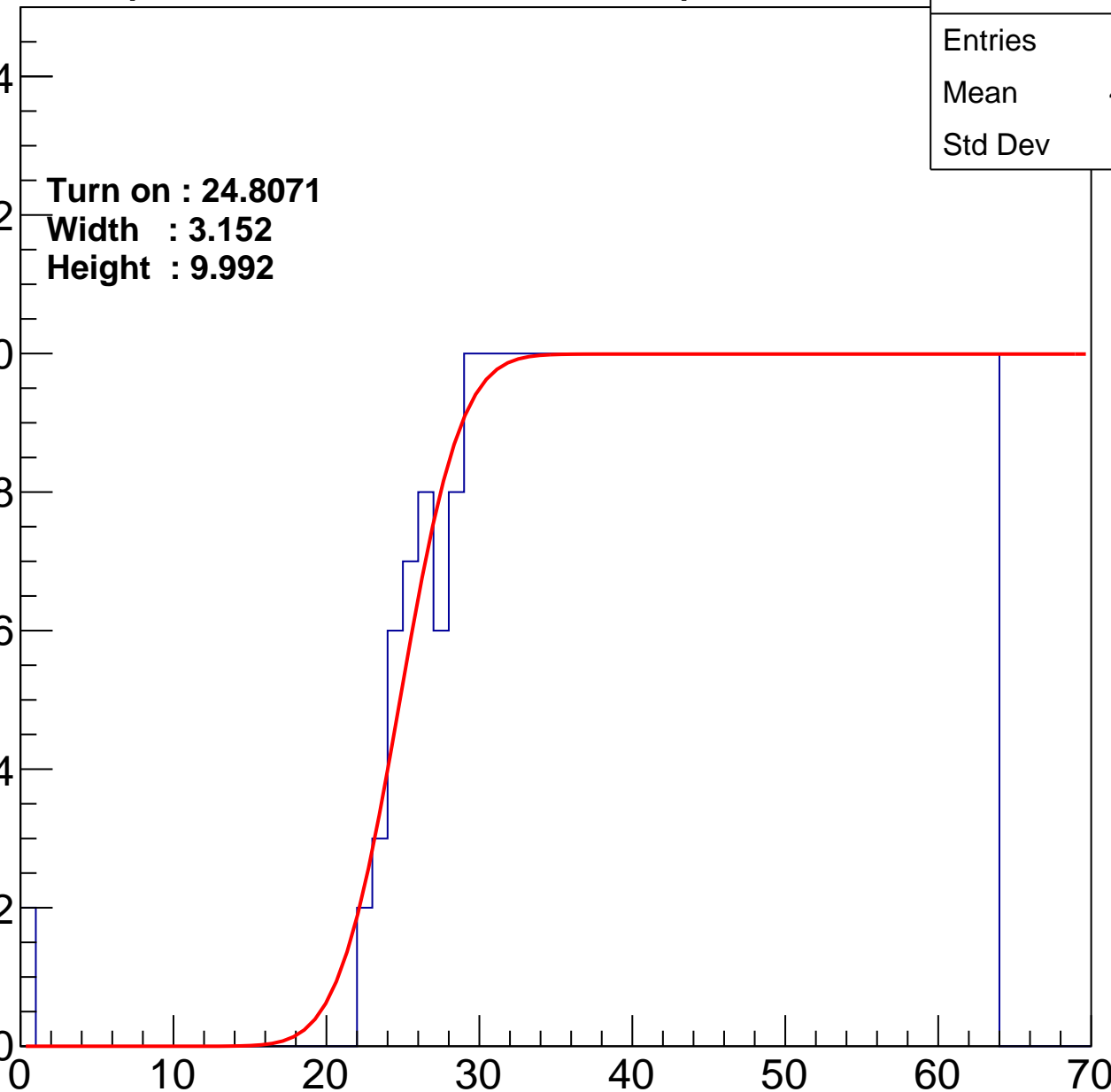
Width : 3.152

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U16-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 25.9712

Width : 2.891

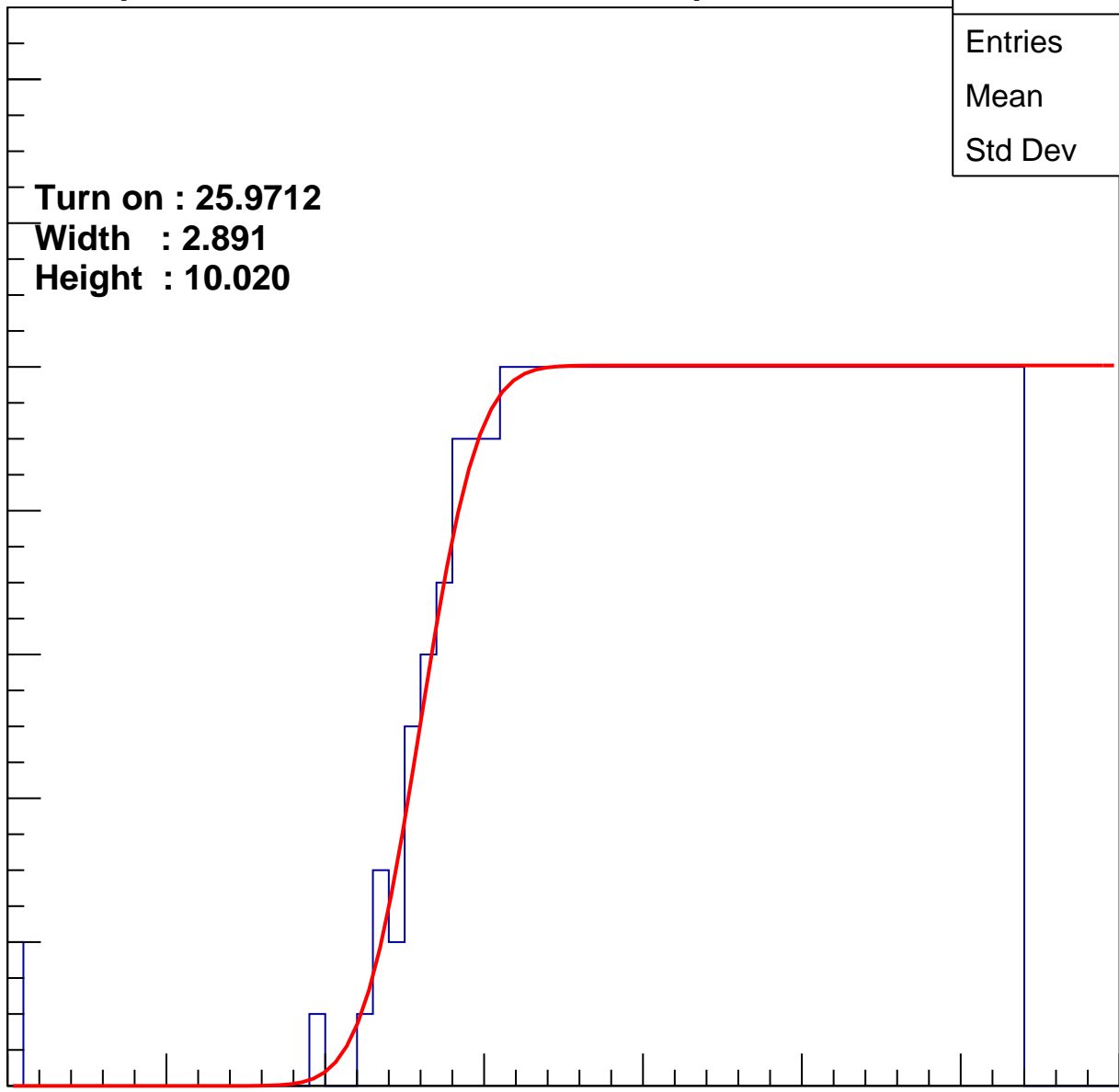
Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U16-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.07
Std Dev	11.62

Turn on : 25.9712

Width : 2.891

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl

