

B1L002S, U10-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.67

Turn on : 24.6917

Width : 2.319

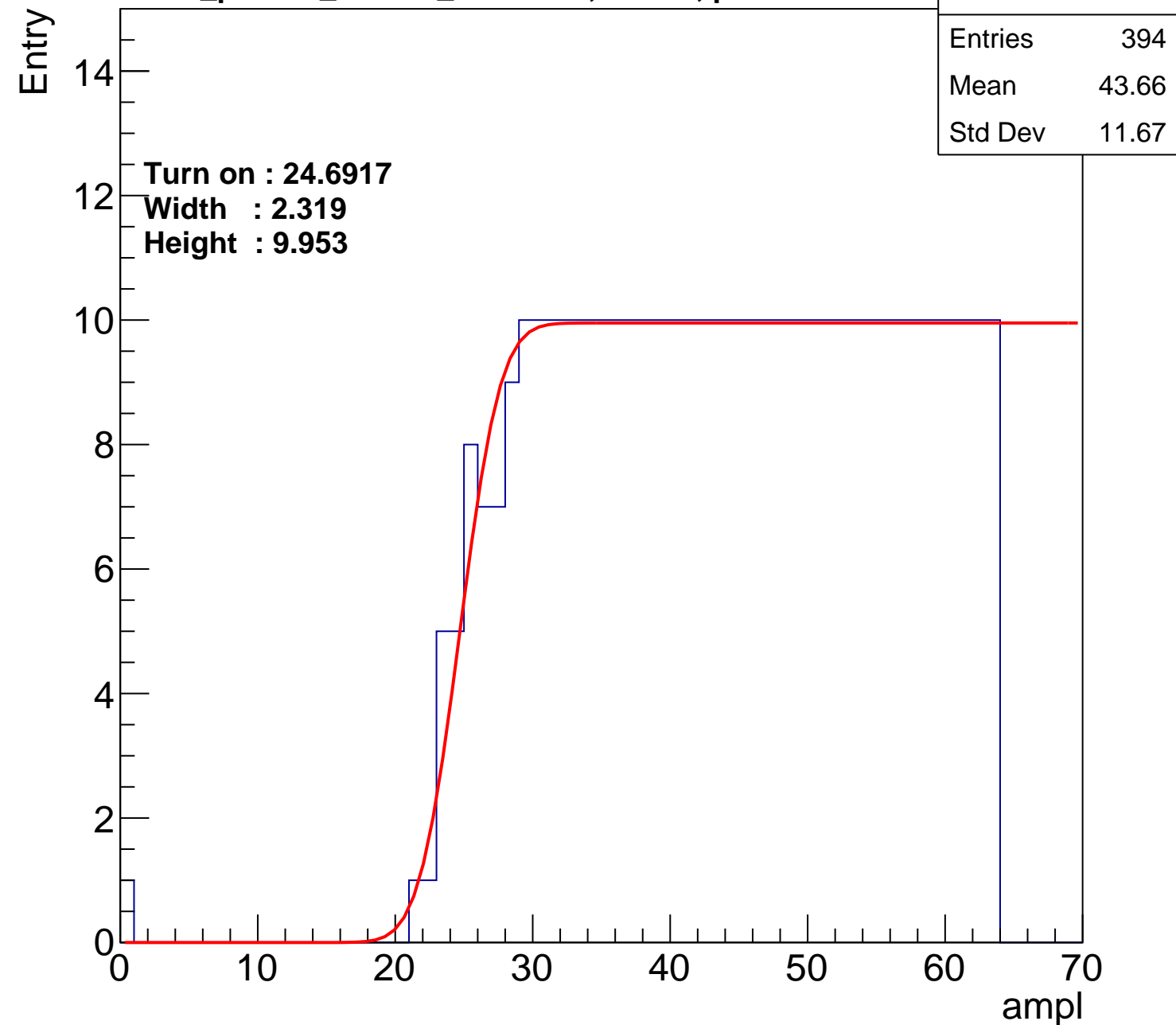
Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.42
Std Dev	11.91

Turn on : 24.4036

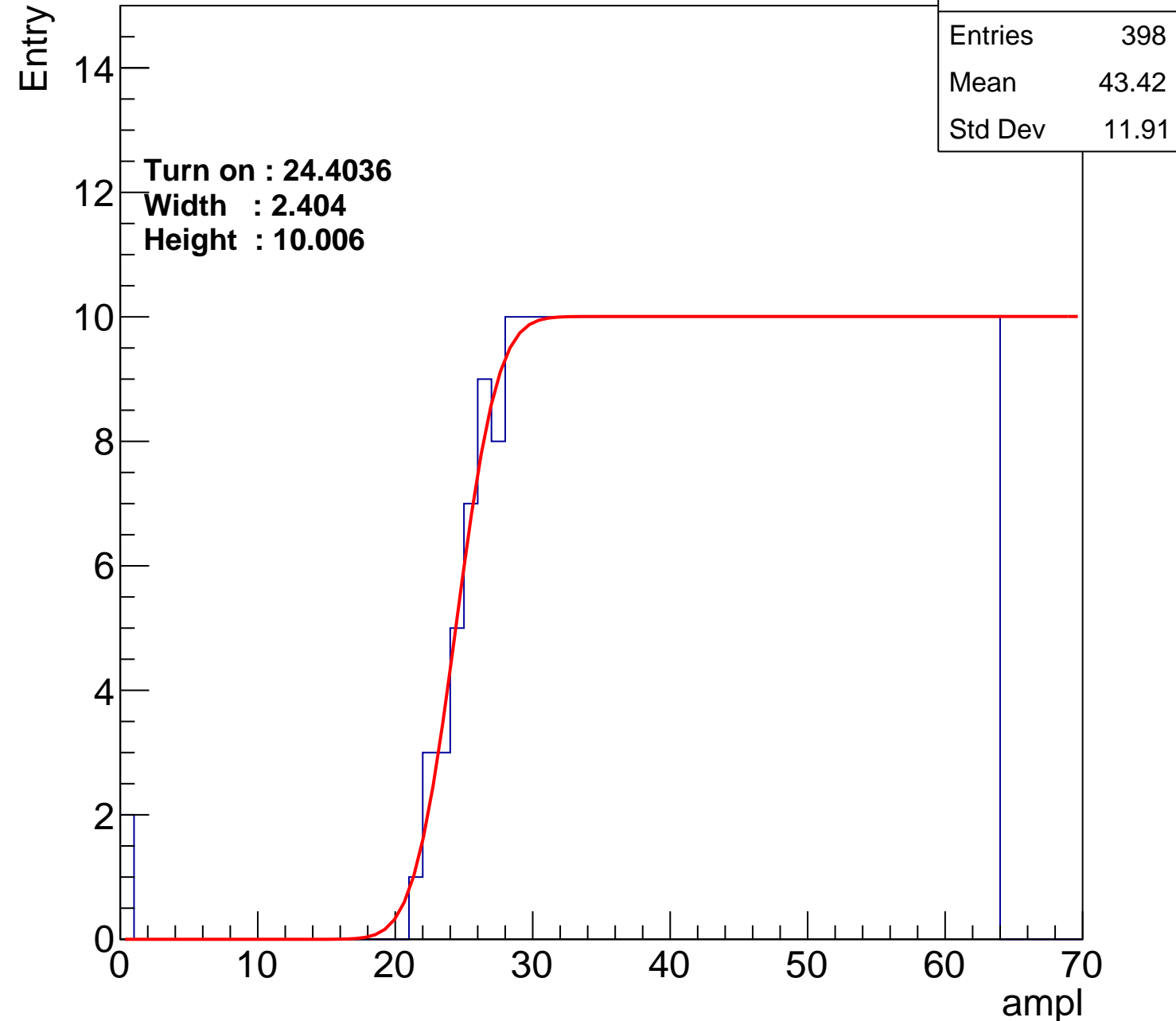
Width : 2.404

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.1
Std Dev	11.55

Turn on : 25.9144

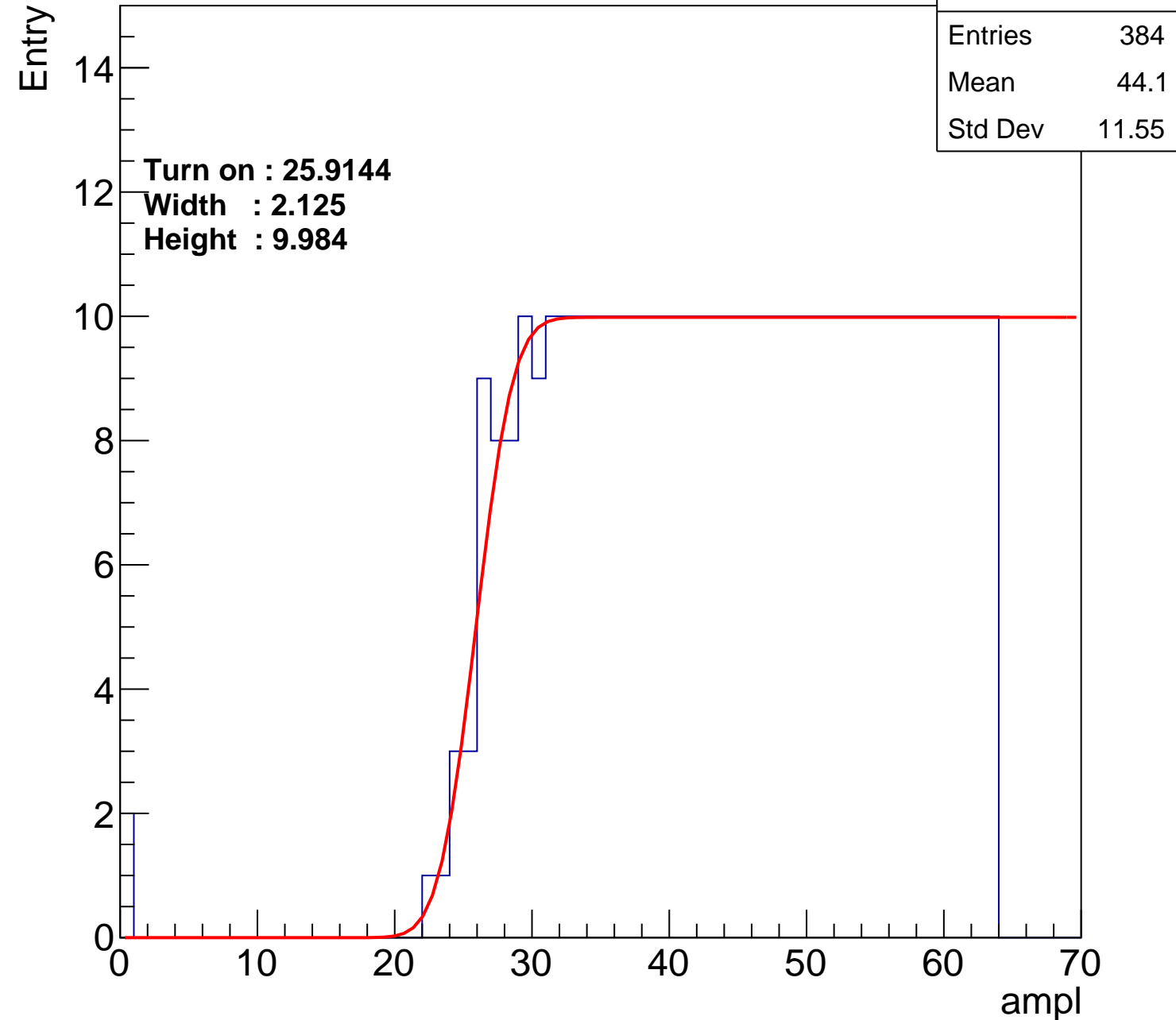
Width : 2.125

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.83
Std Dev	11

Turn on : 27.4616

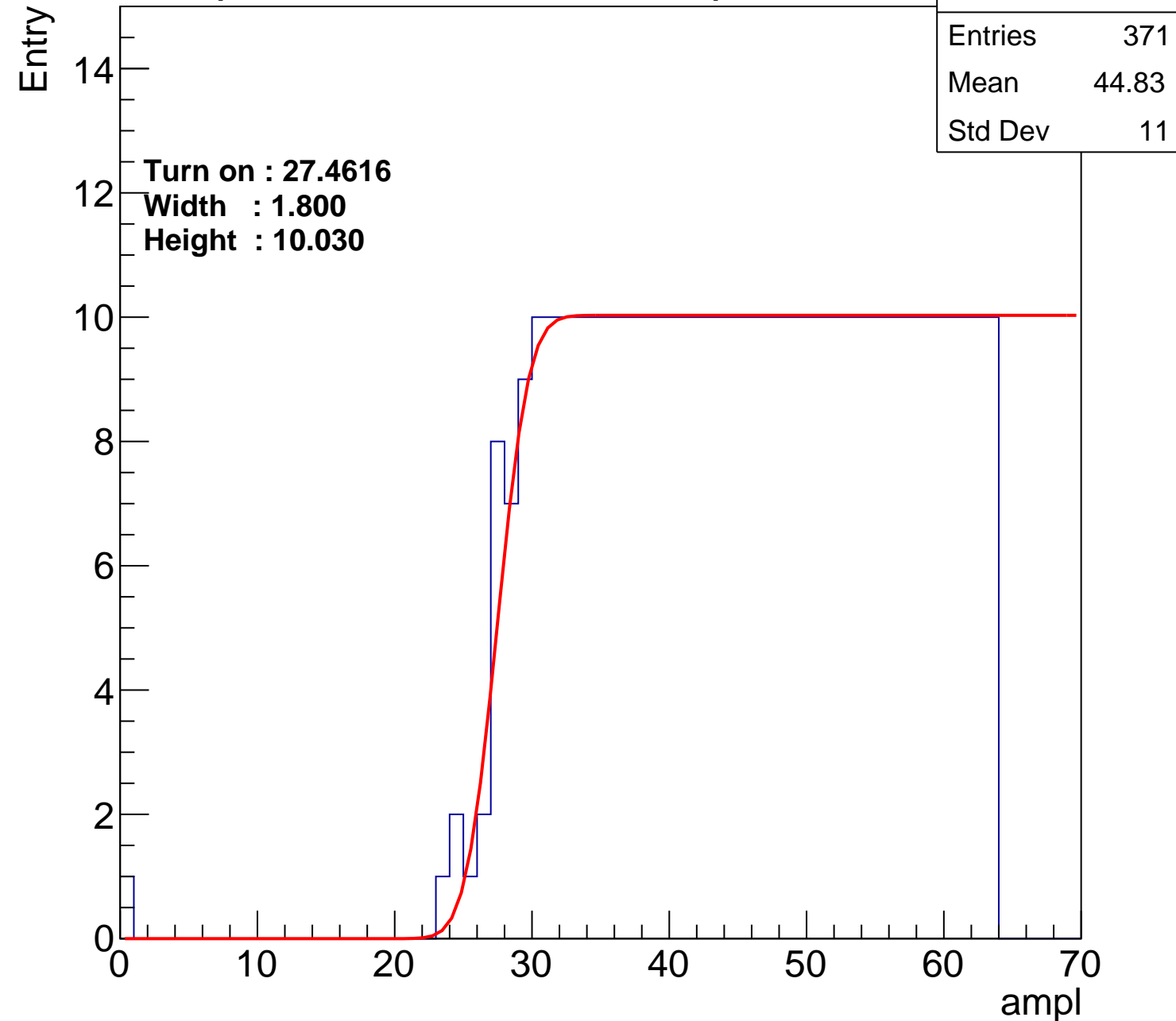
Width : 1.800

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 26.9448

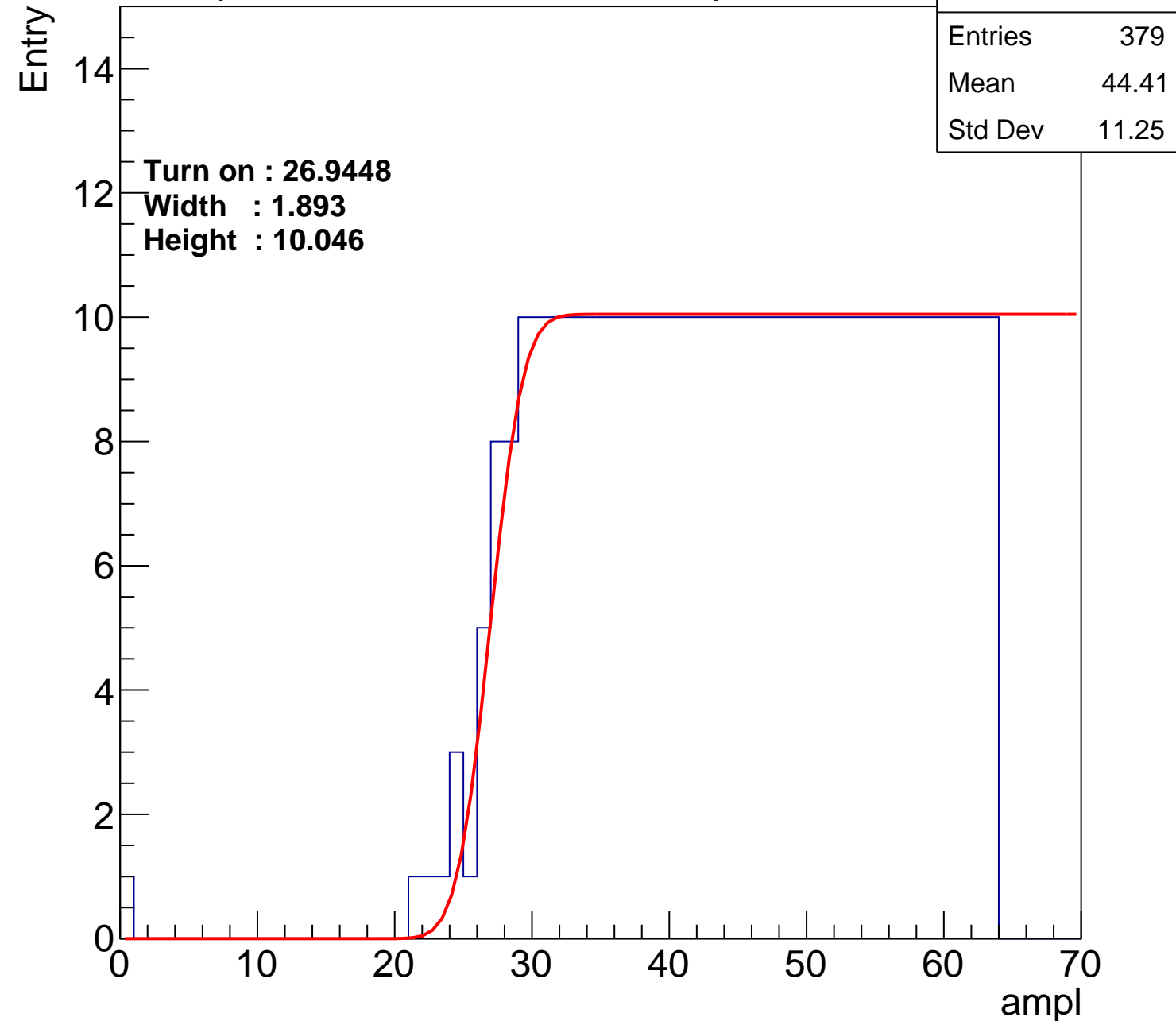
Width : 1.893

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.61
Std Dev	11.32

Turn on : 27.2215

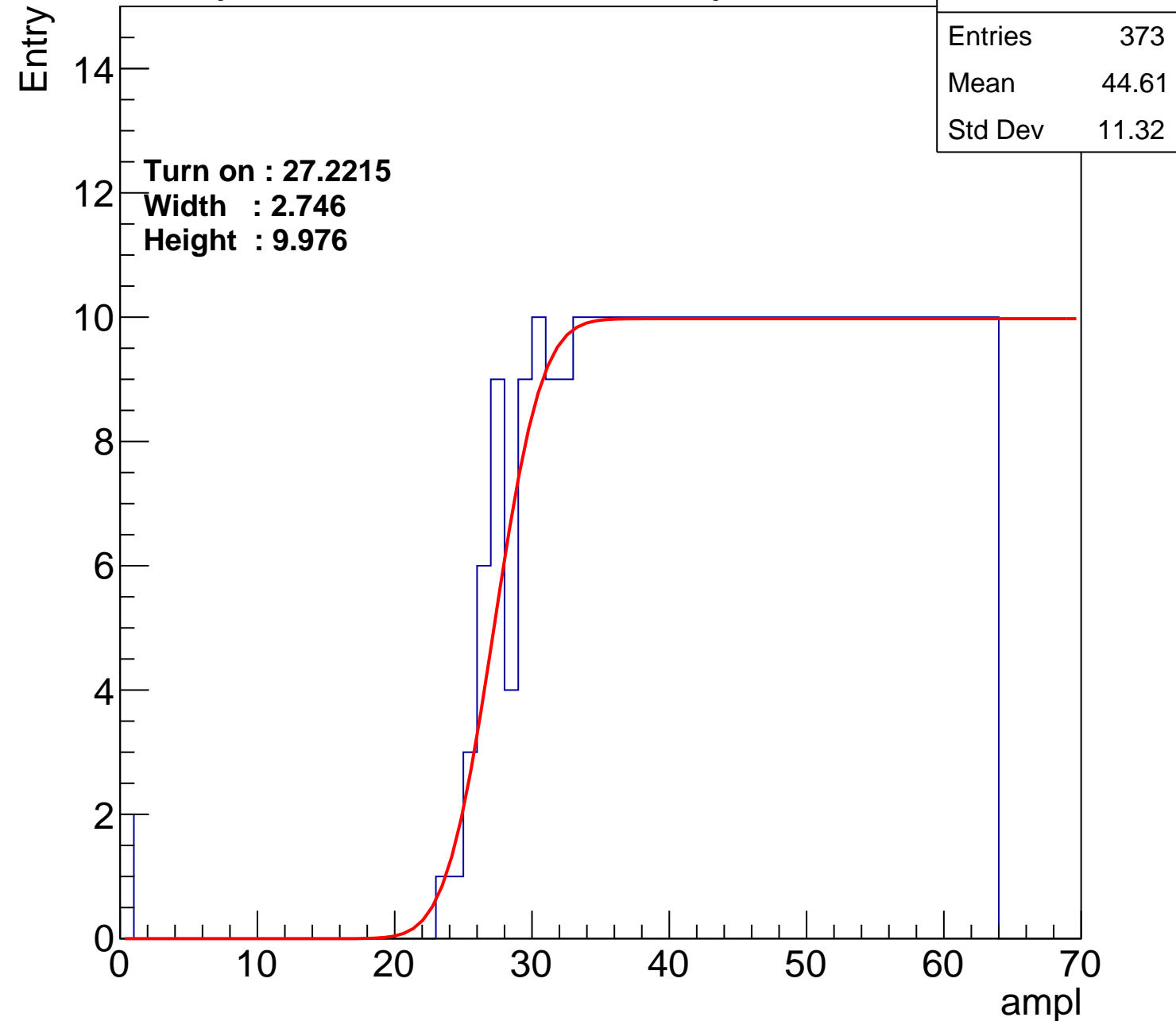
Width : 2.746

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.7041

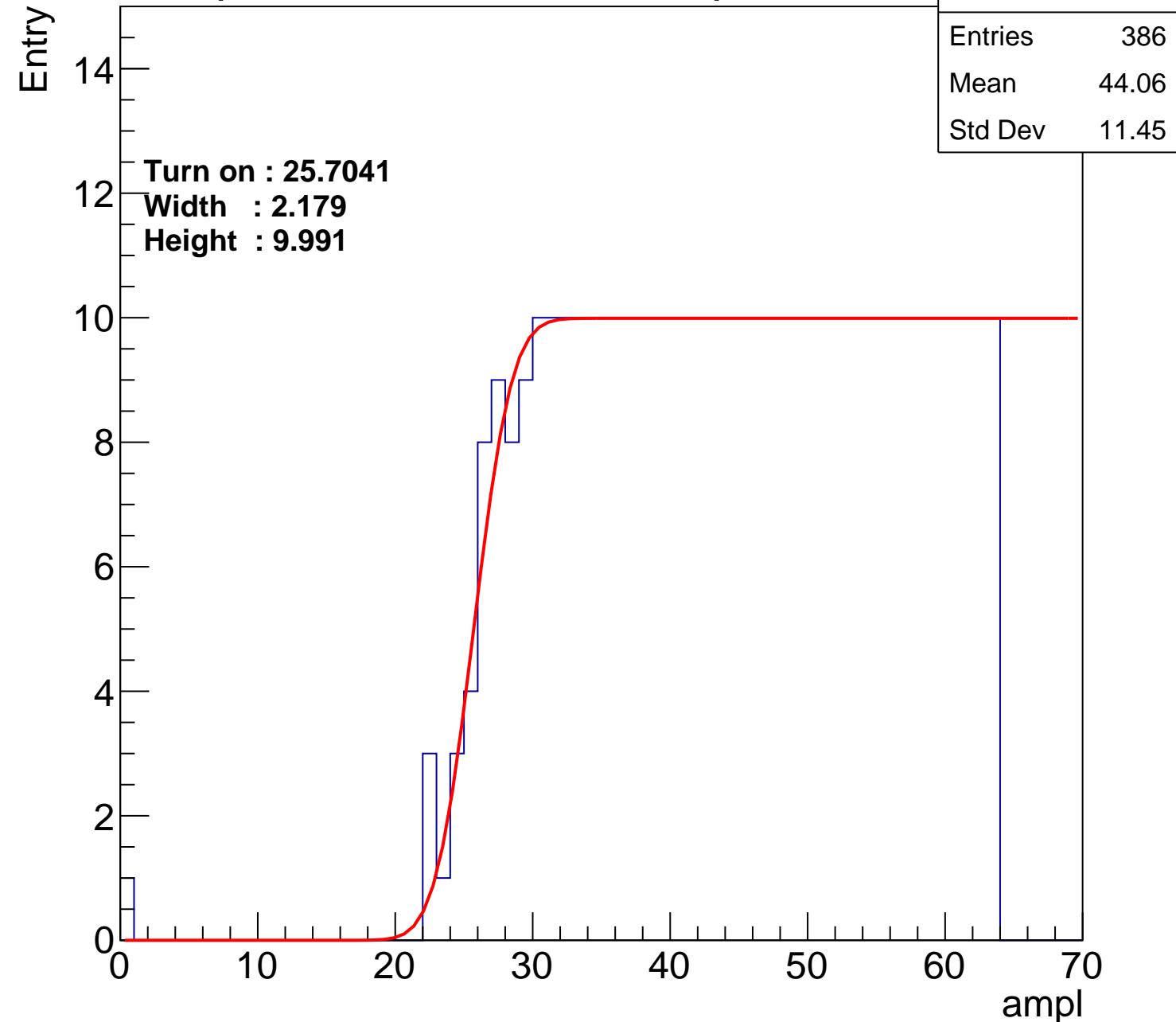
Width : 2.179

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	361
Mean	45.3
Std Dev	10.78

Turn on : 28.0955

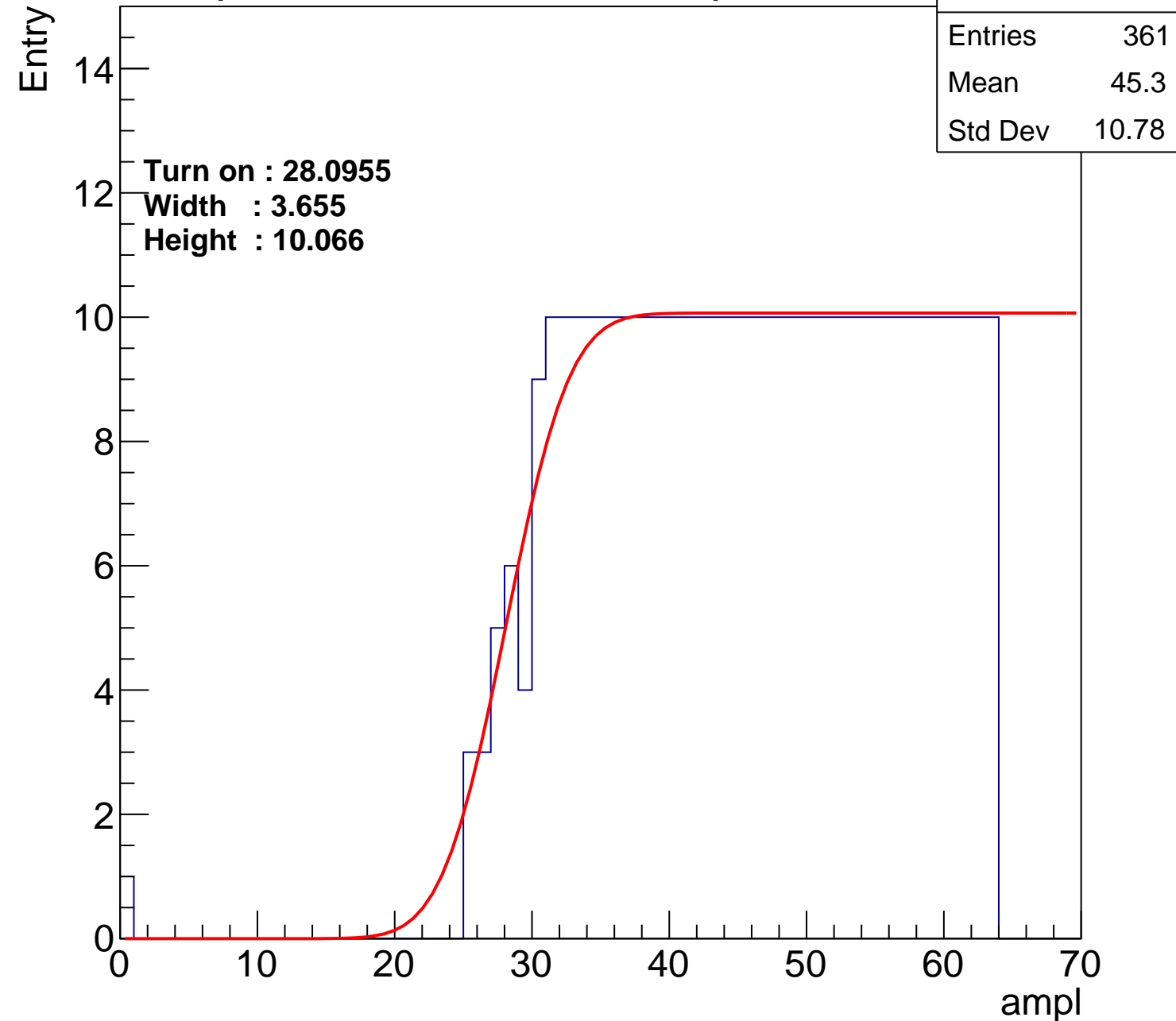
Width : 3.655

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.2050

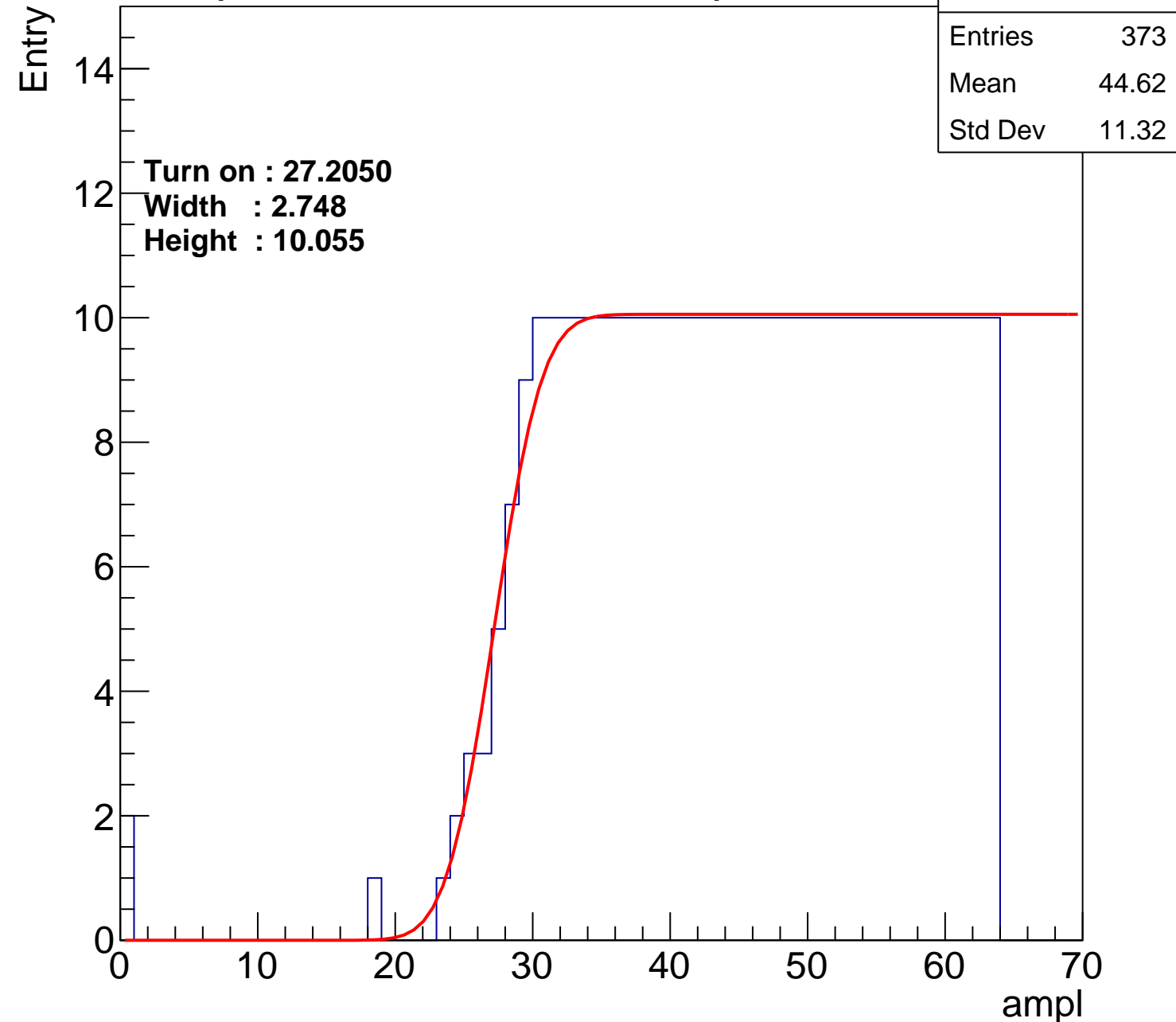
Width : 2.748

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch9

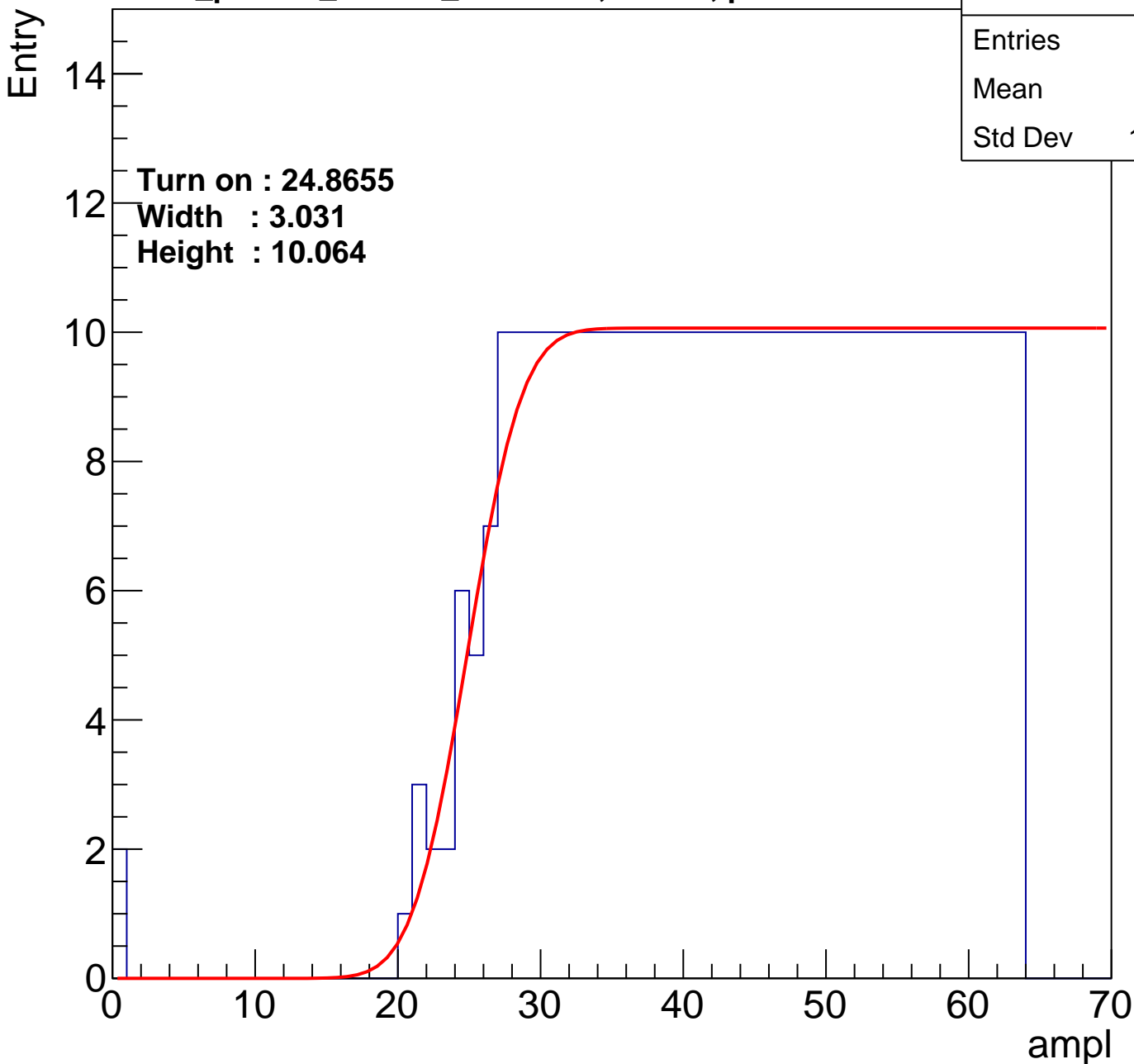
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 24.8655

Width : 3.031

Height : 10.064

Entries	398
Mean	43.4
Std Dev	11.94



B1L002S, U10-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.83

Turn on : 24.8563

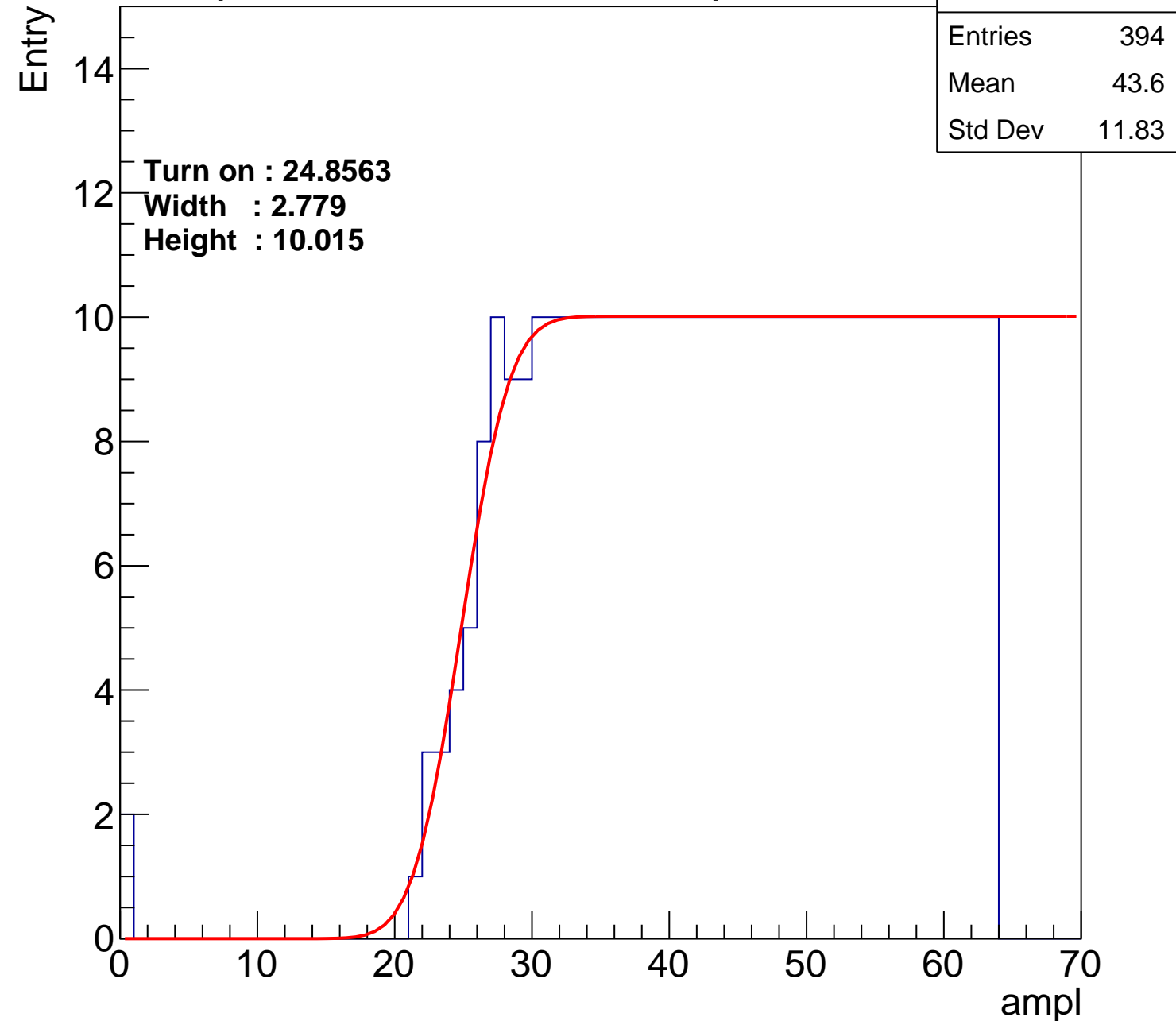
Width : 2.779

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.55

Turn on : 25.7970

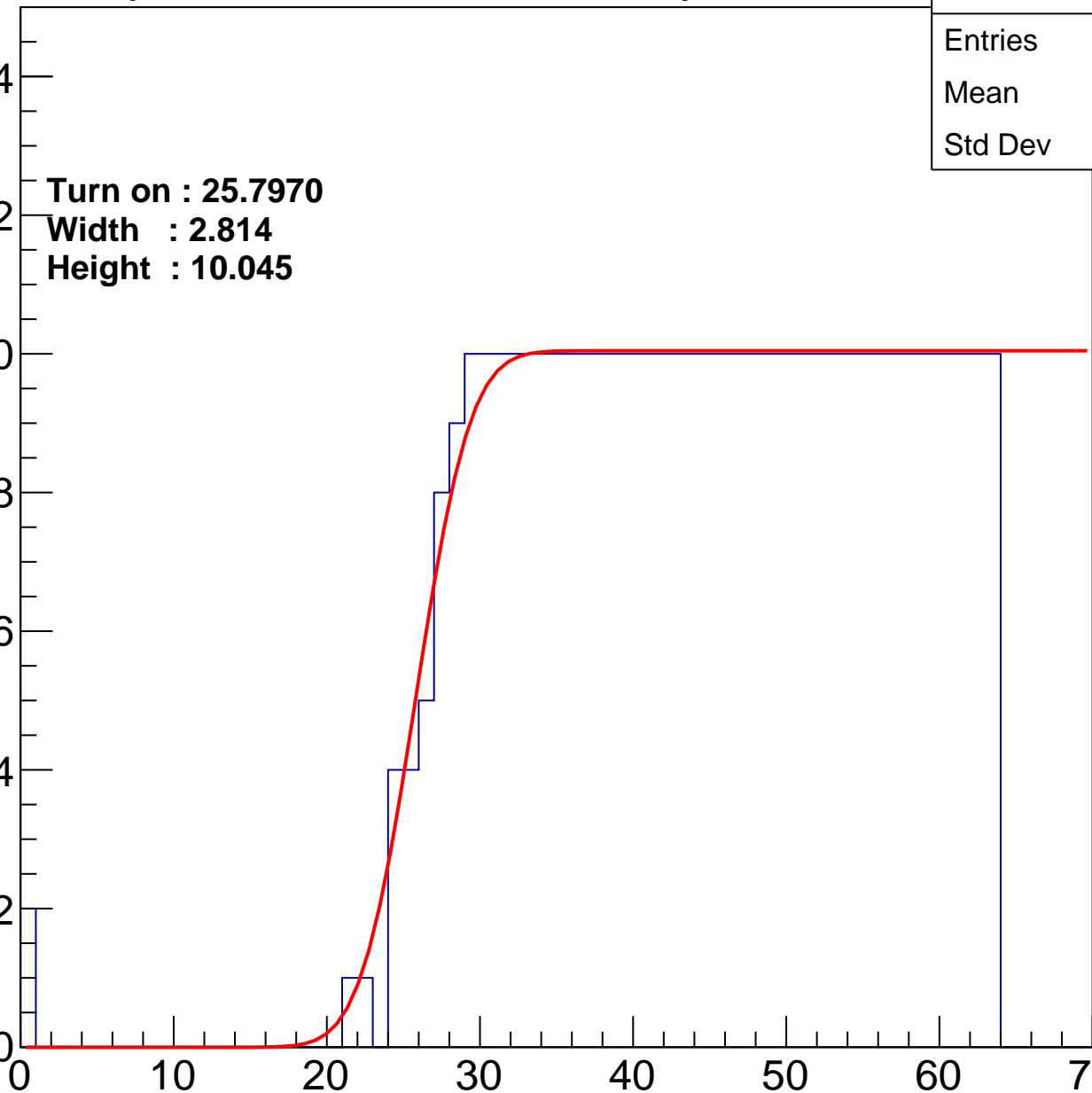
Width : 2.814

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	363
Mean	45.18
Std Dev	10.87

Turn on : 28.2936

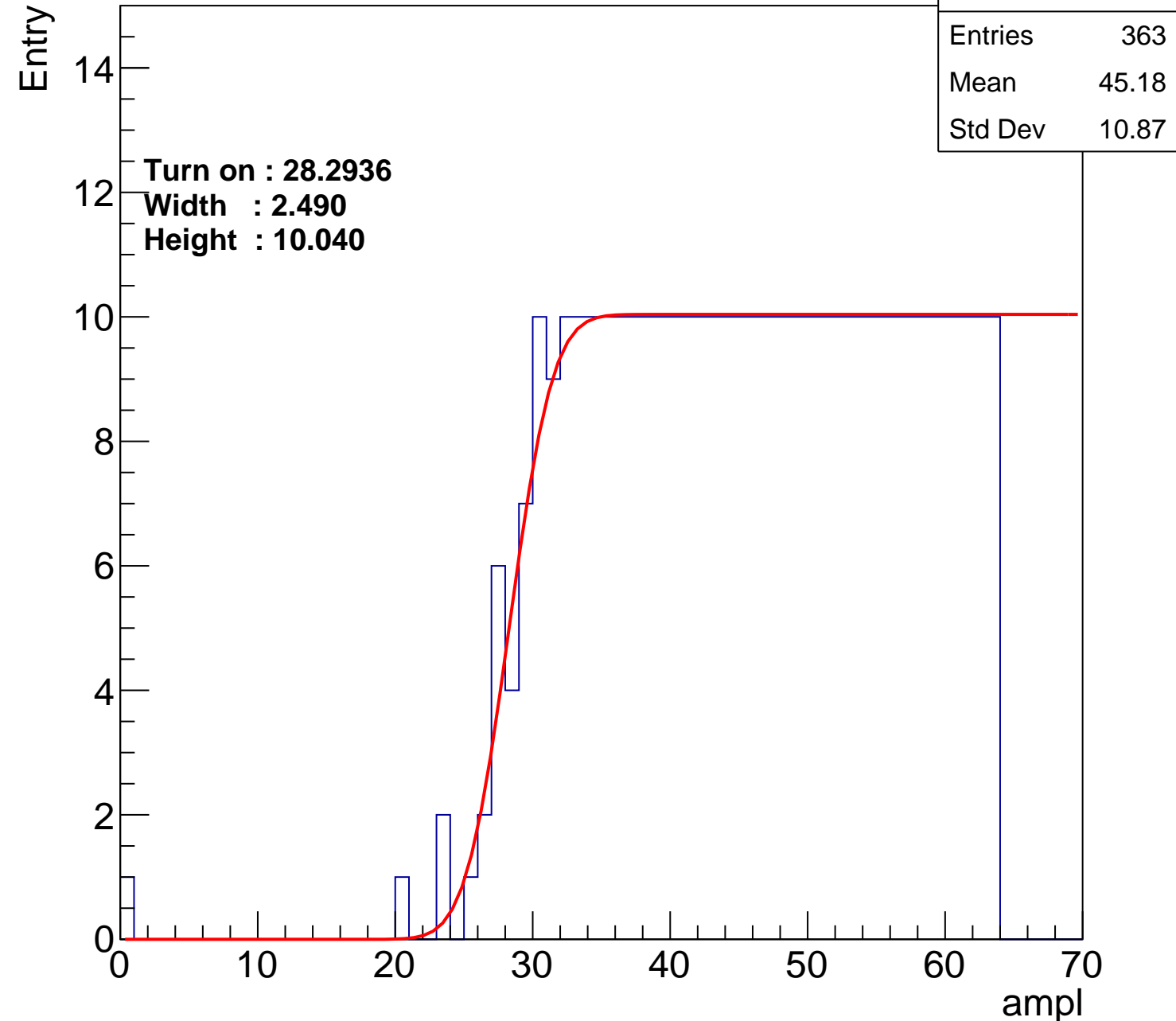
Width : 2.490

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.75

Turn on : 25.6612

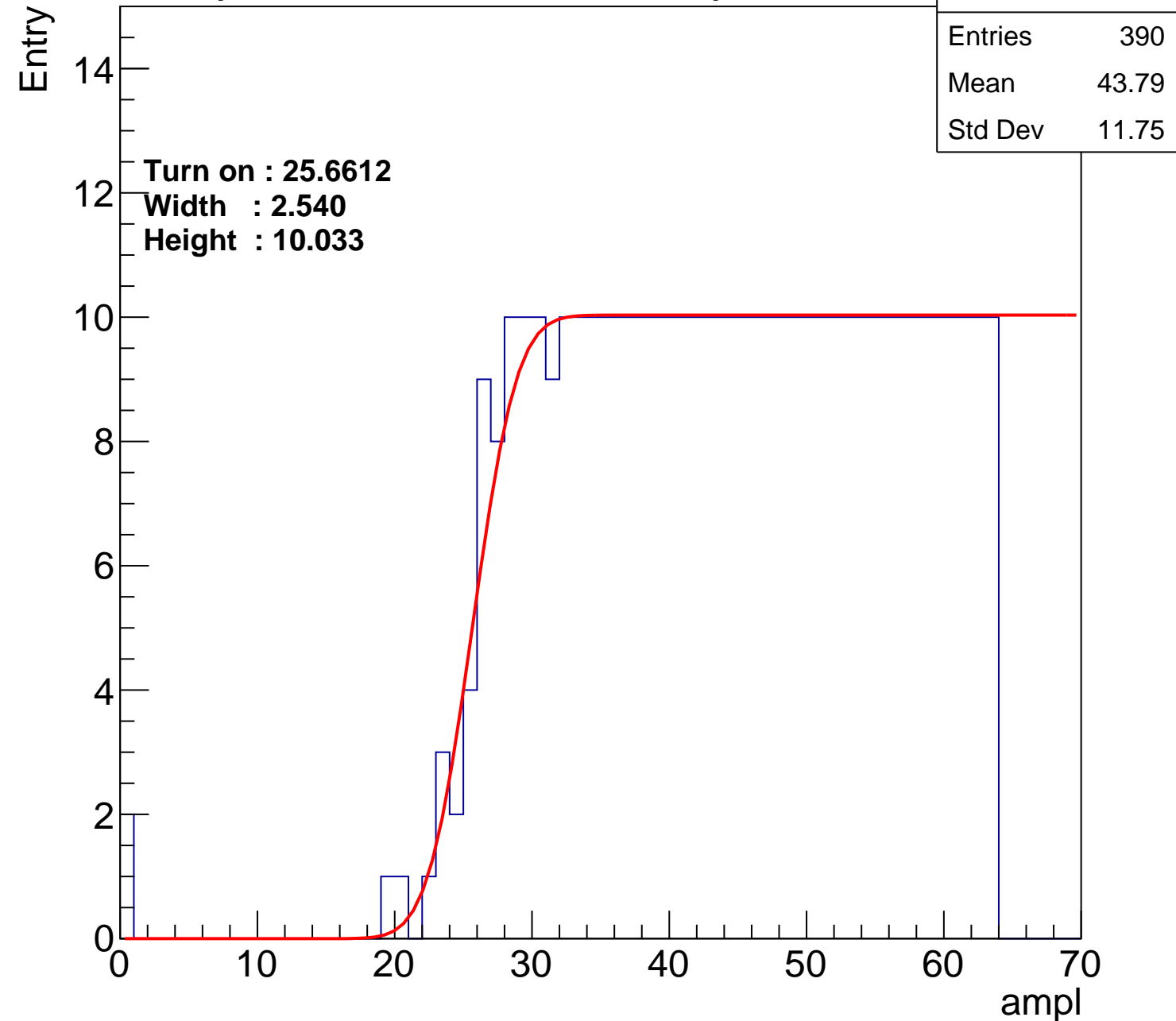
Width : 2.540

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch14

calib_packv5_042523_0143.root, FC#10, port B3

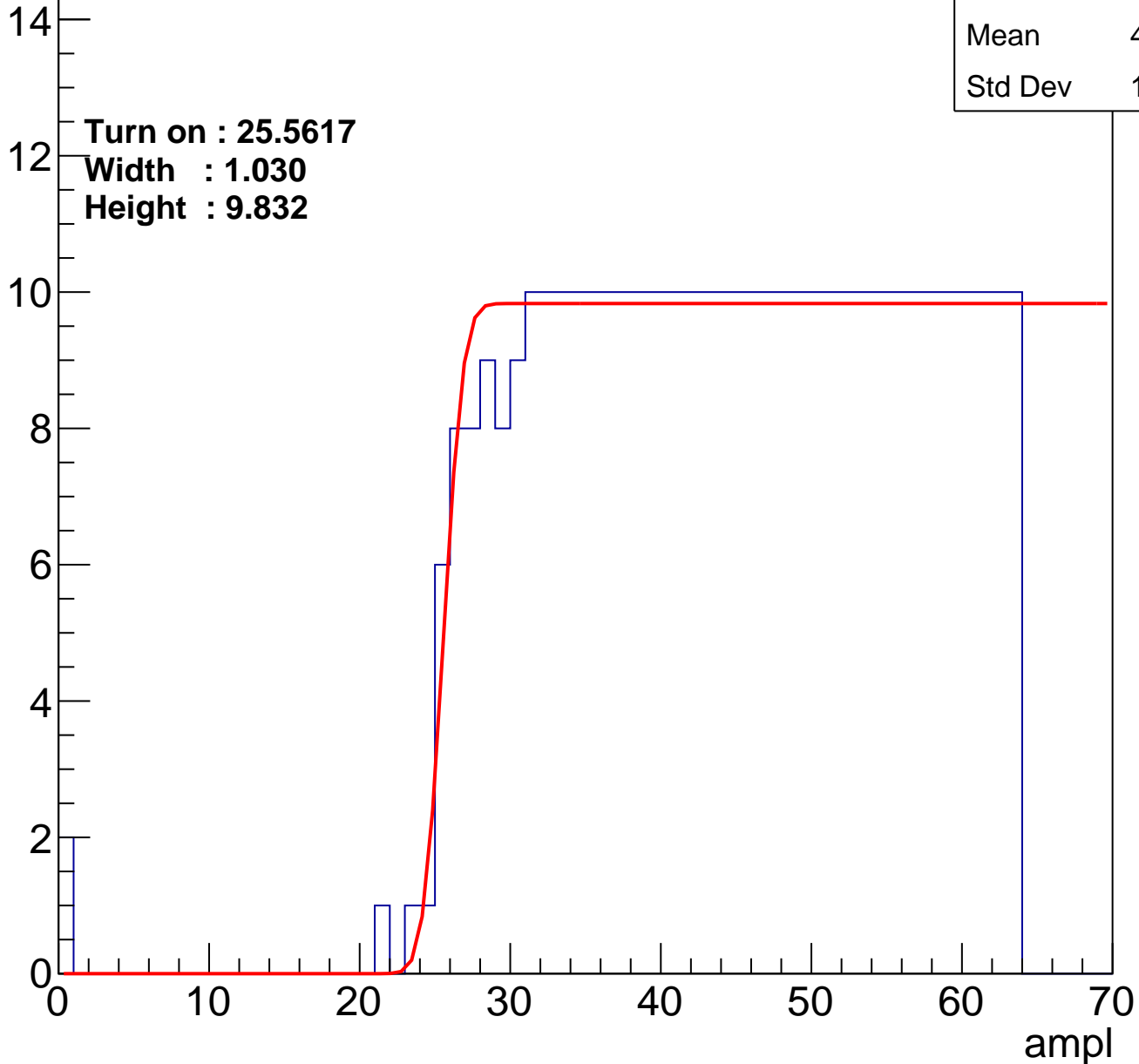
Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 25.5617

Width : 1.030

Height : 9.832

Entry



B1L002S, U10-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 27.2177

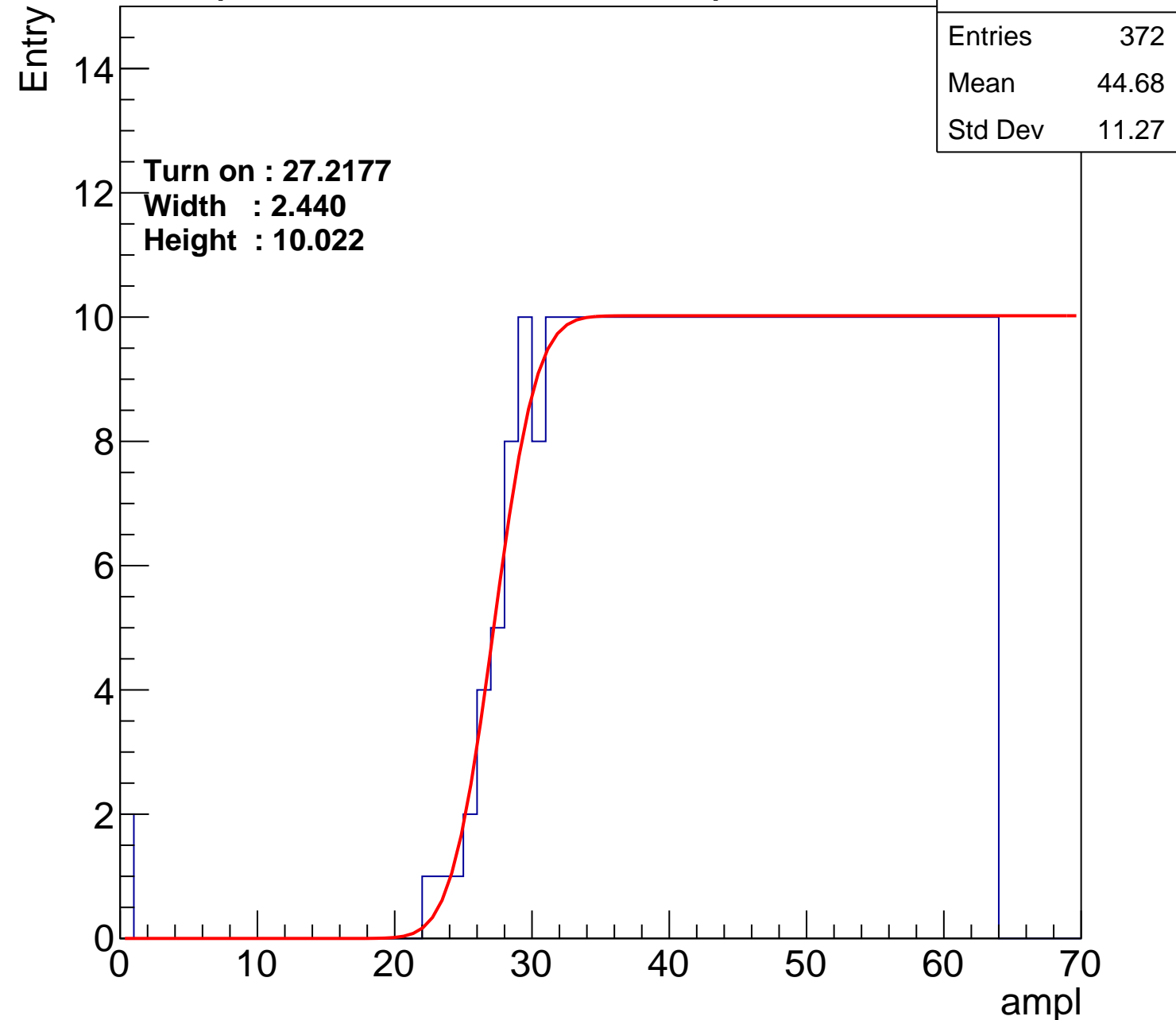
Width : 2.440

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch16

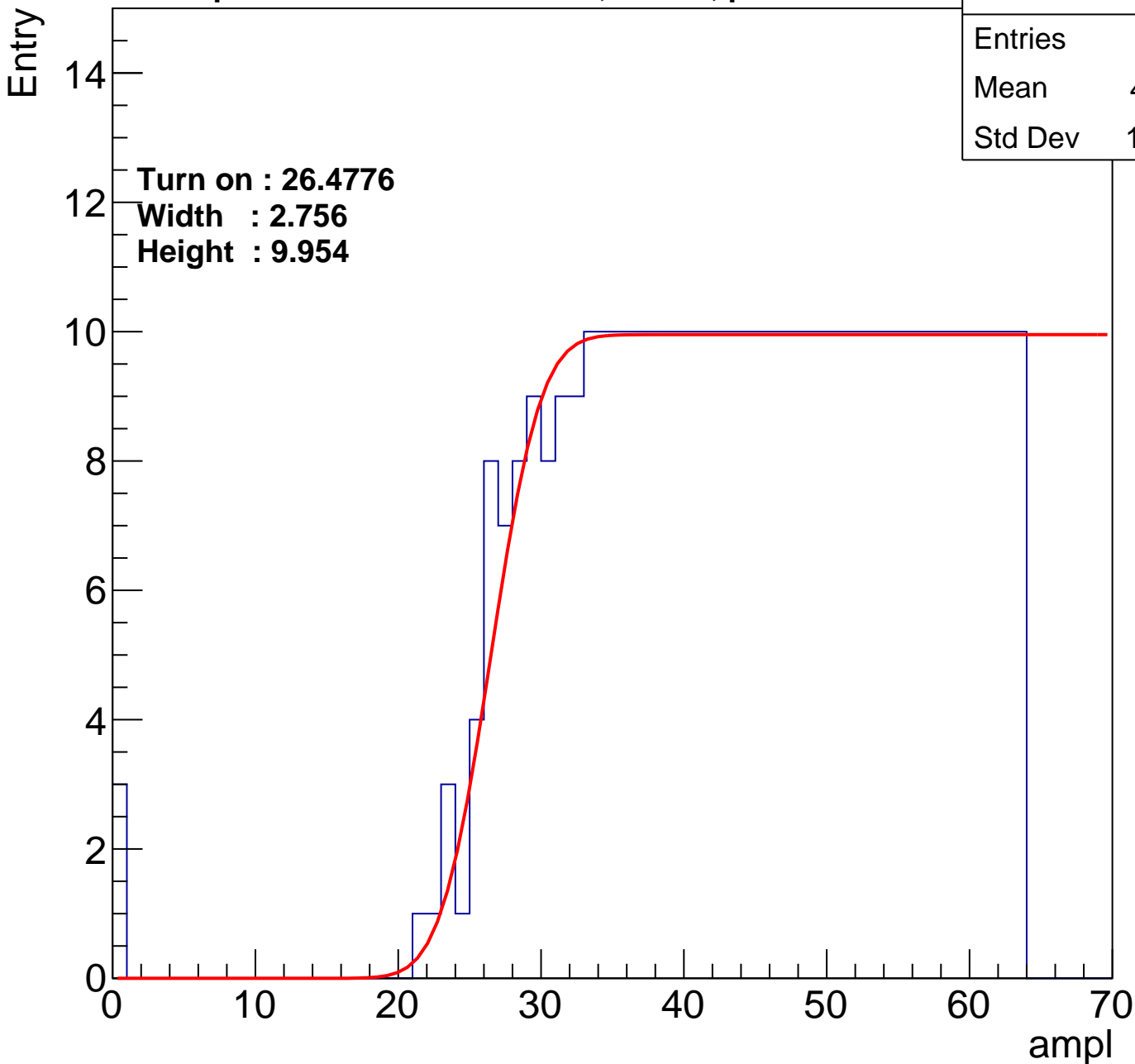
calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.11
Std Dev	11.77

Turn on : 26.4776

Width : 2.756

Height : 9.954



B1L002S, U10-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.71
Std Dev	11.65

Turn on : 24.9426

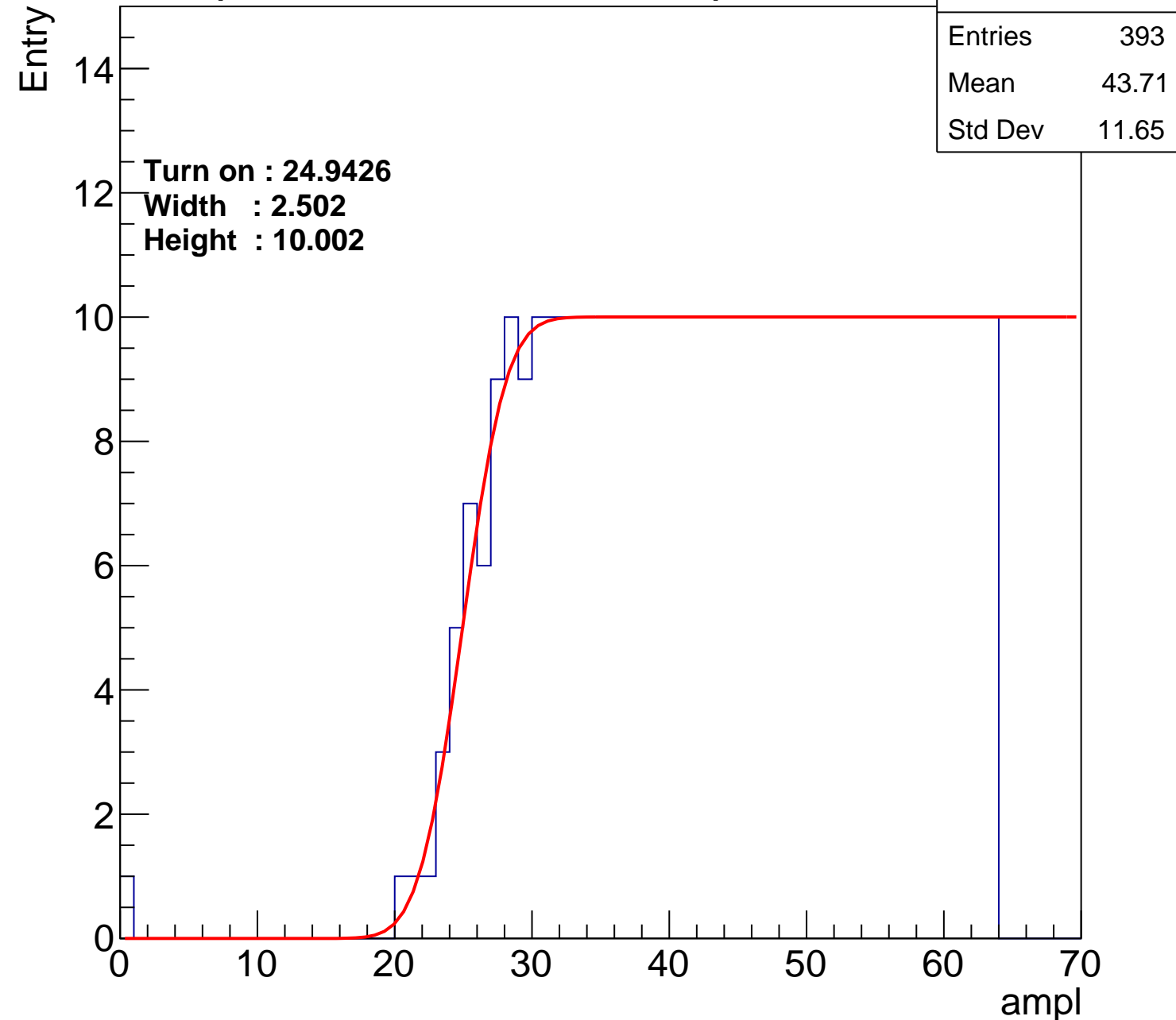
Width : 2.502

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 26.2891

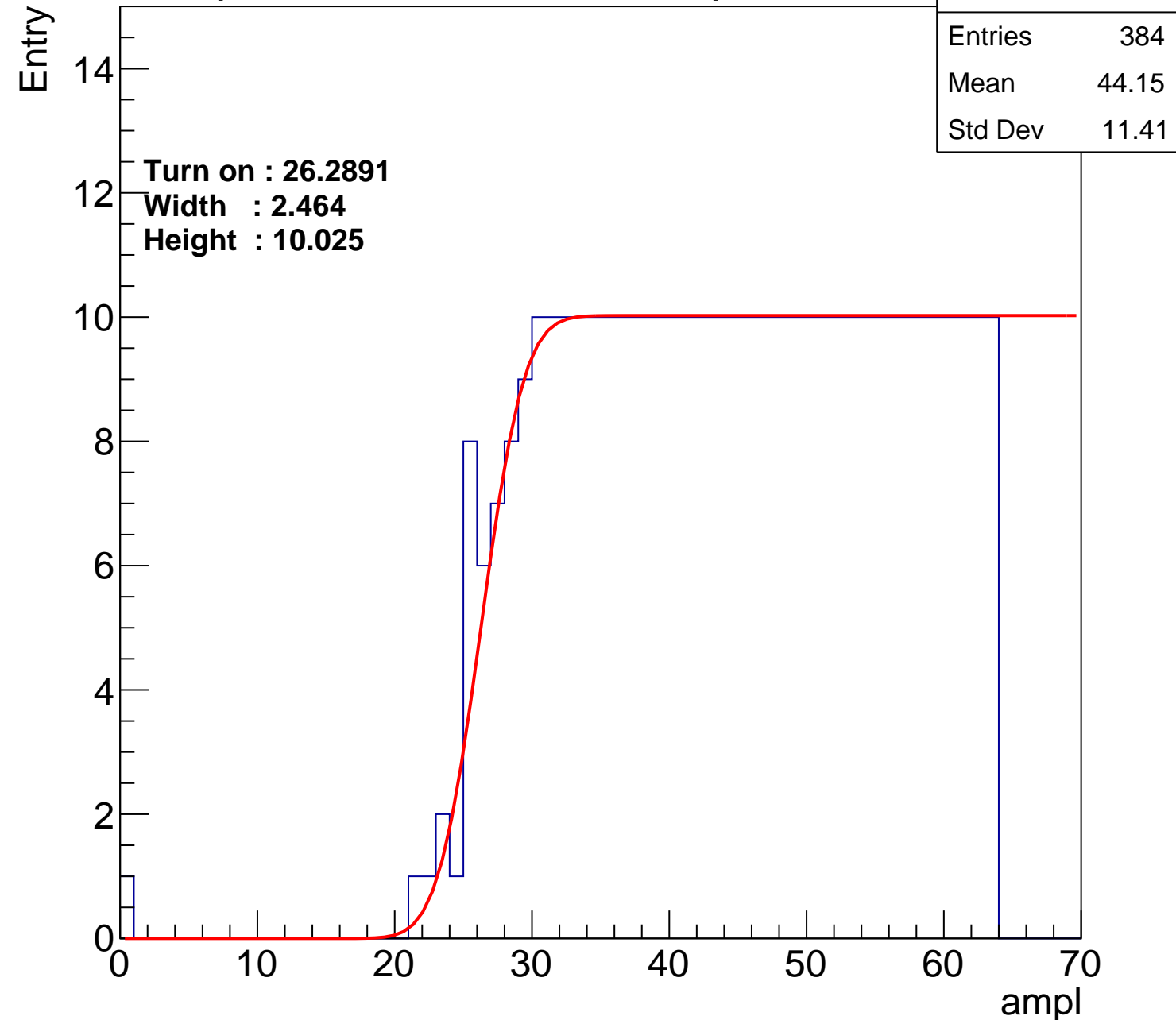
Width : 2.464

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.97
Std Dev	11.71

Turn on : 25.7057

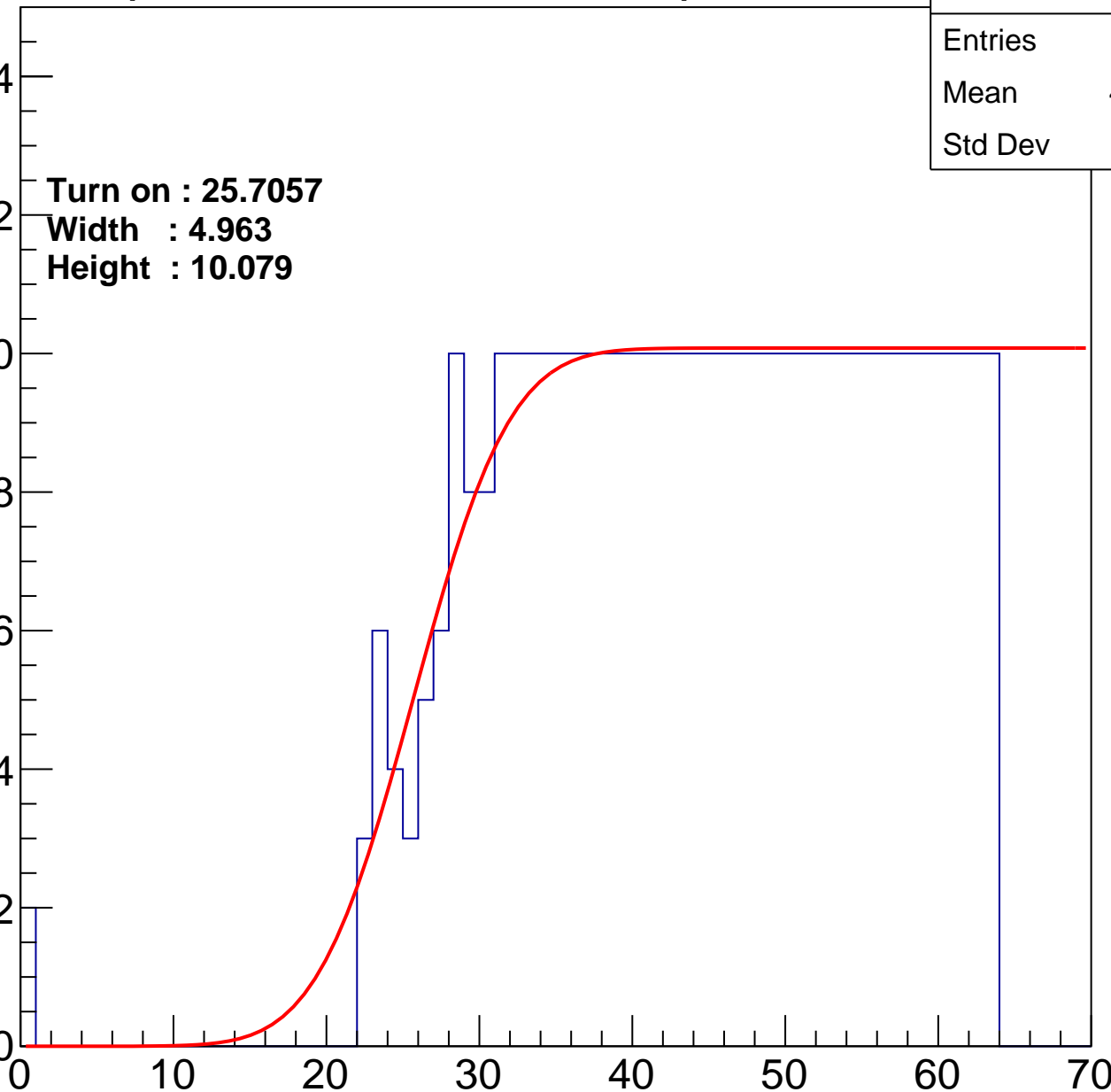
Width : 4.963

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.71

Turn on : 25.2257

Width : 0.586

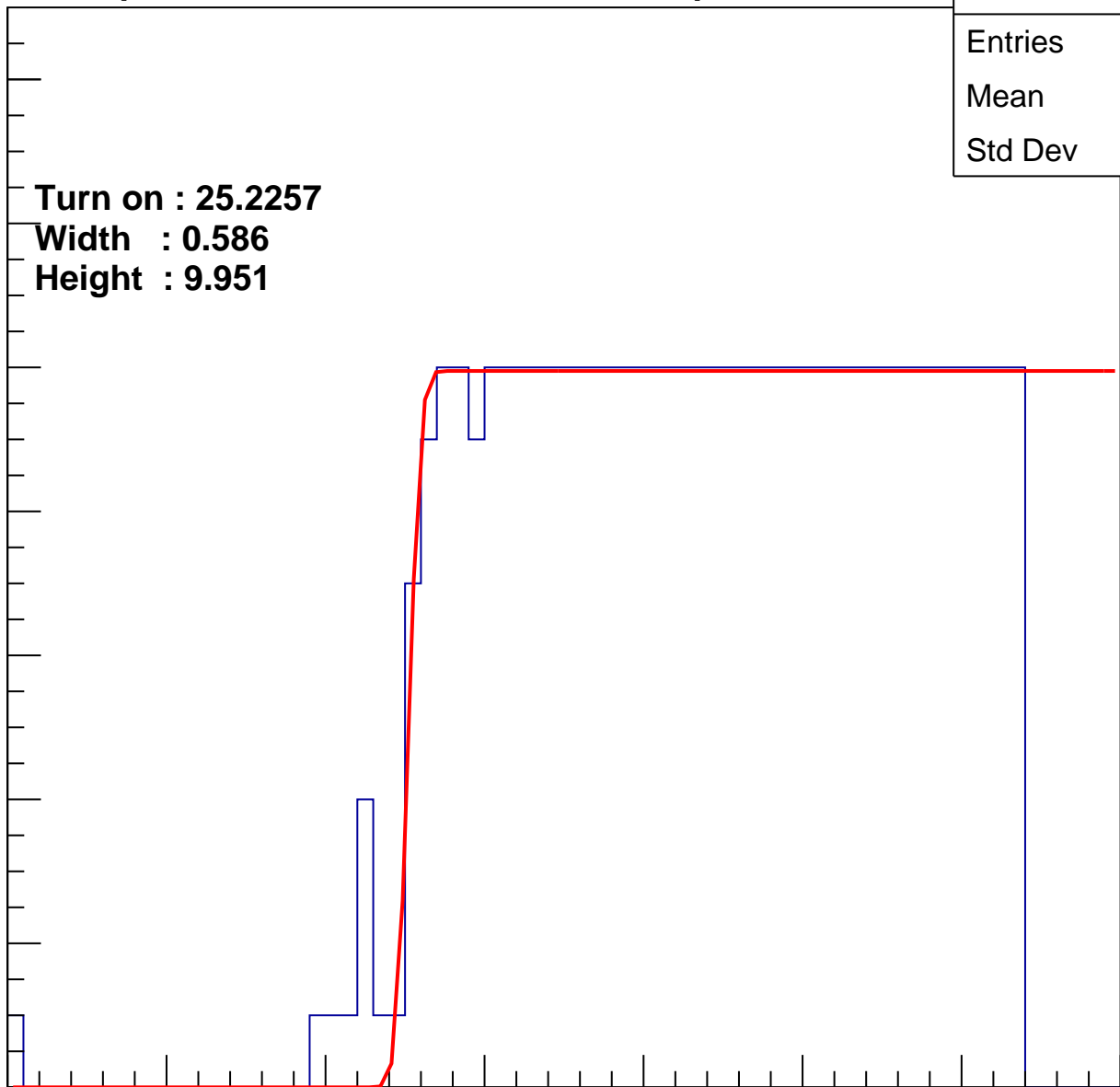
Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.08
Std Dev	11.58

Turn on : 25.6403

Width : 2.392

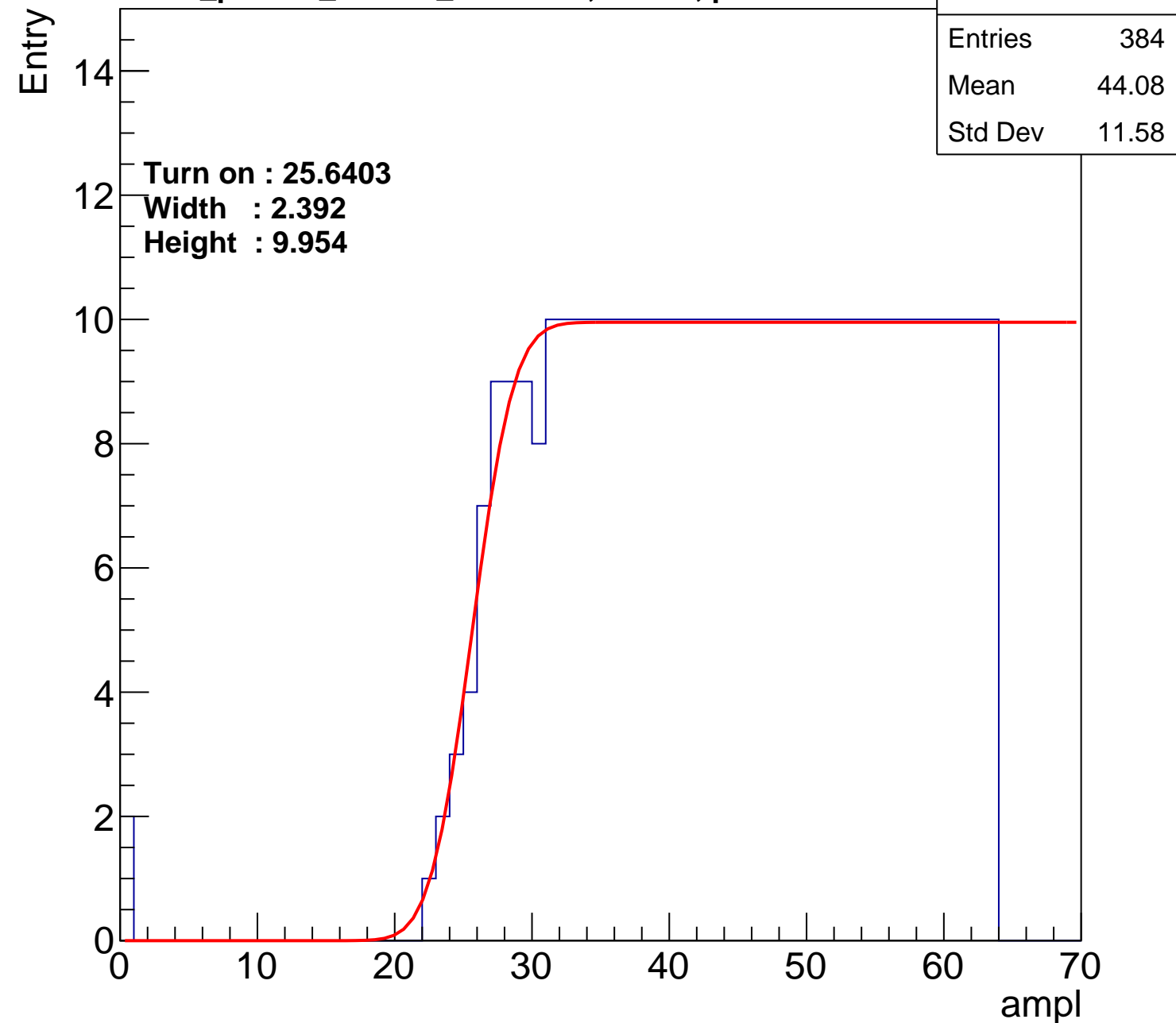
Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch22

calib_packv5_042523_0143.root, FC#10, port B3

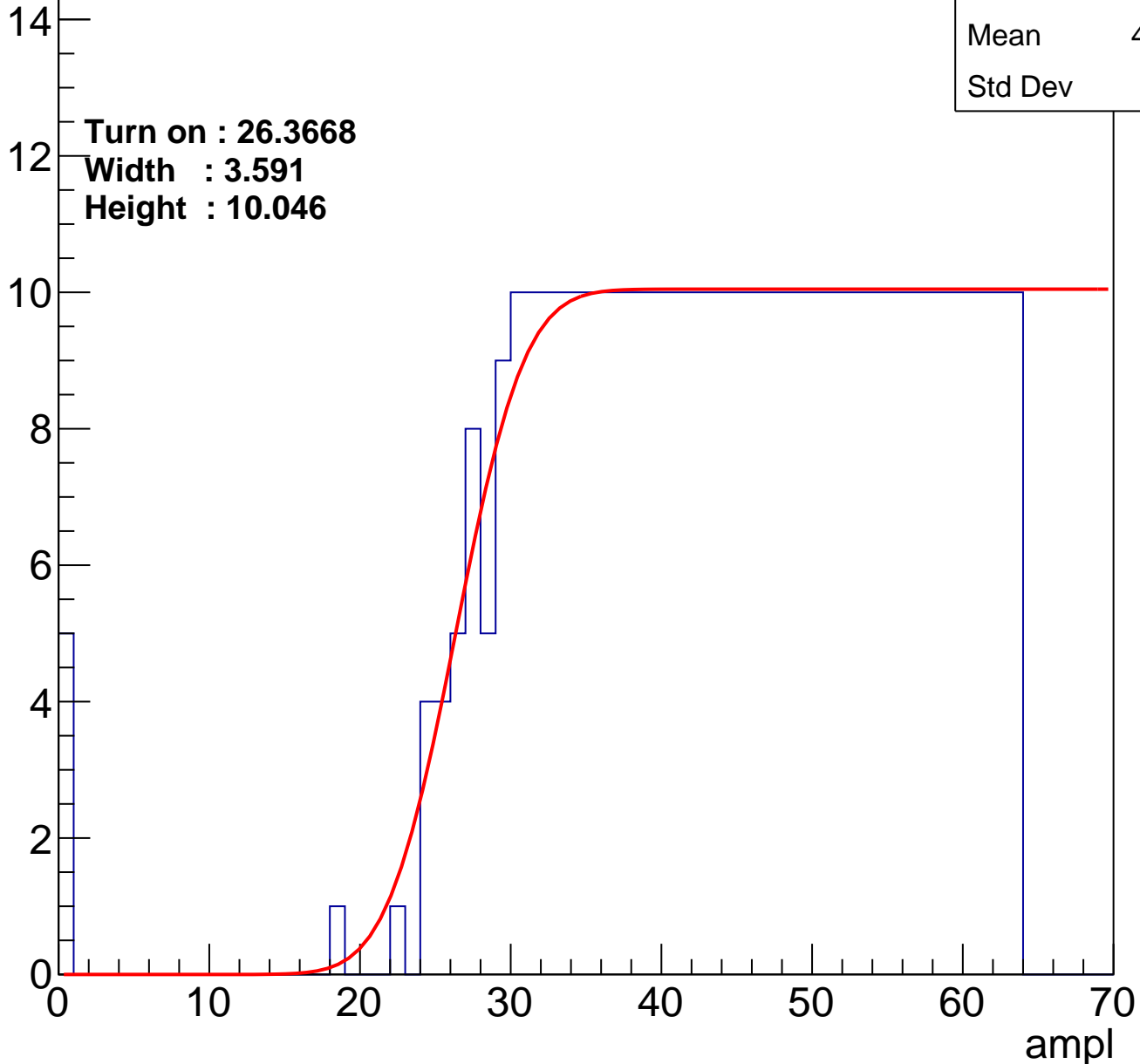
Entries	382
Mean	43.96
Std Dev	12.1

Turn on : 26.3668

Width : 3.591

Height : 10.046

Entry



B1L002S, U10-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.93
Std Dev	11.5

Turn on : 25.0579

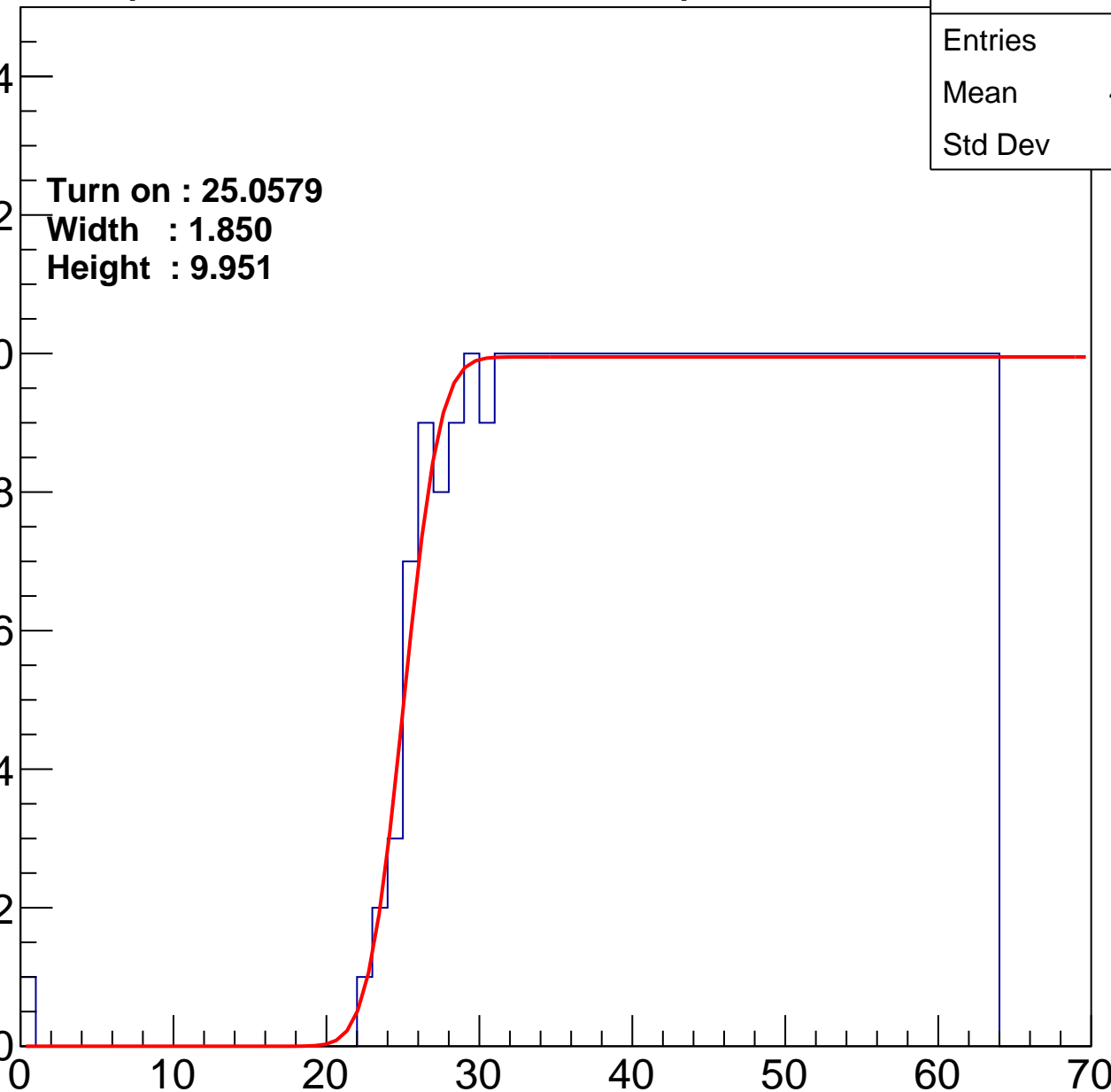
Width : 1.850

Height : 9.951

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch24

calib_packv5_042523_0143.root, FC#10, port B3

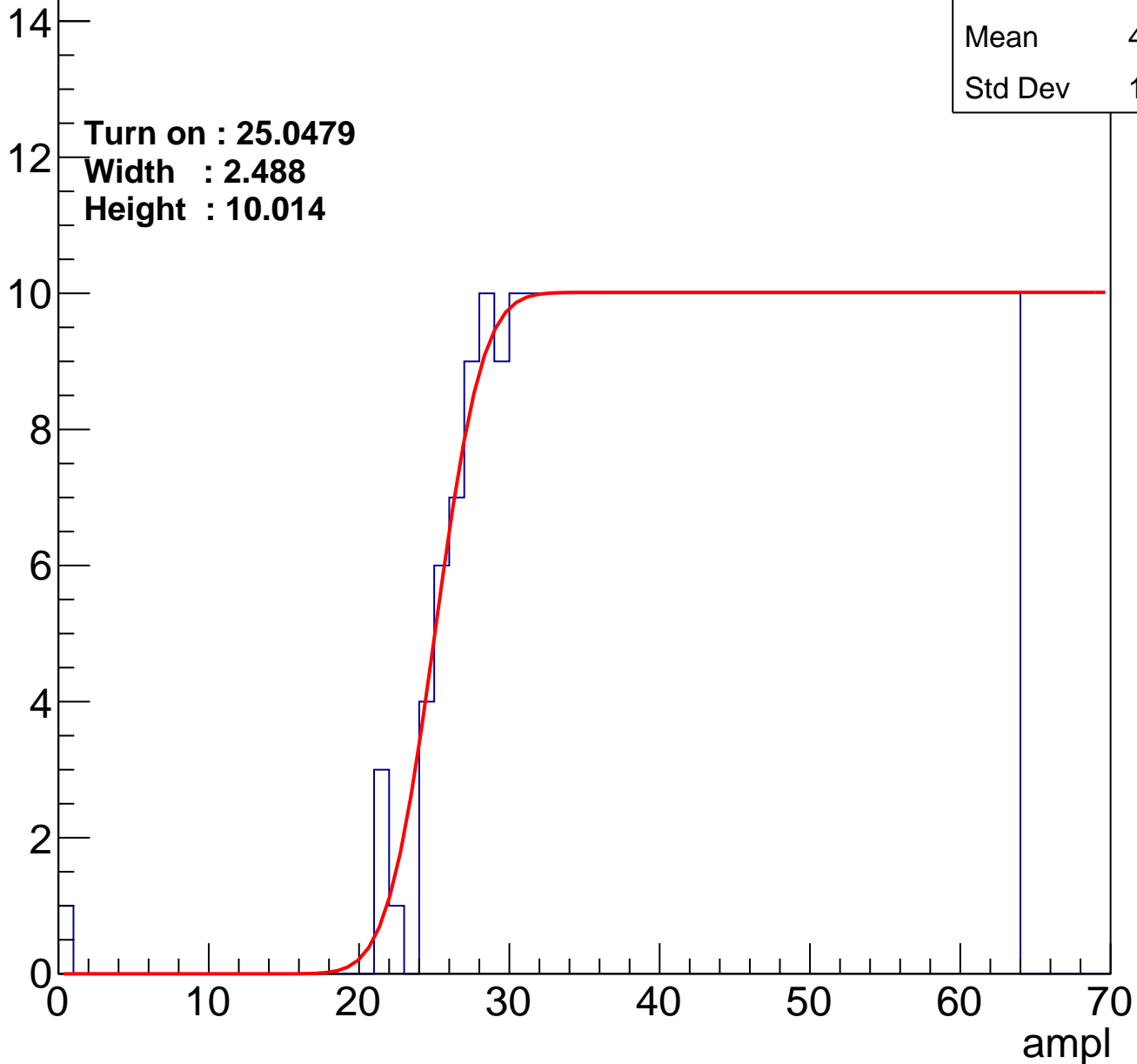
Entries	390
Mean	43.86
Std Dev	11.55

Turn on : 25.0479

Width : 2.488

Height : 10.014

Entry



B1L002S, U10-ch25

calib_packv5_042523_0143.root, FC#10, port B3

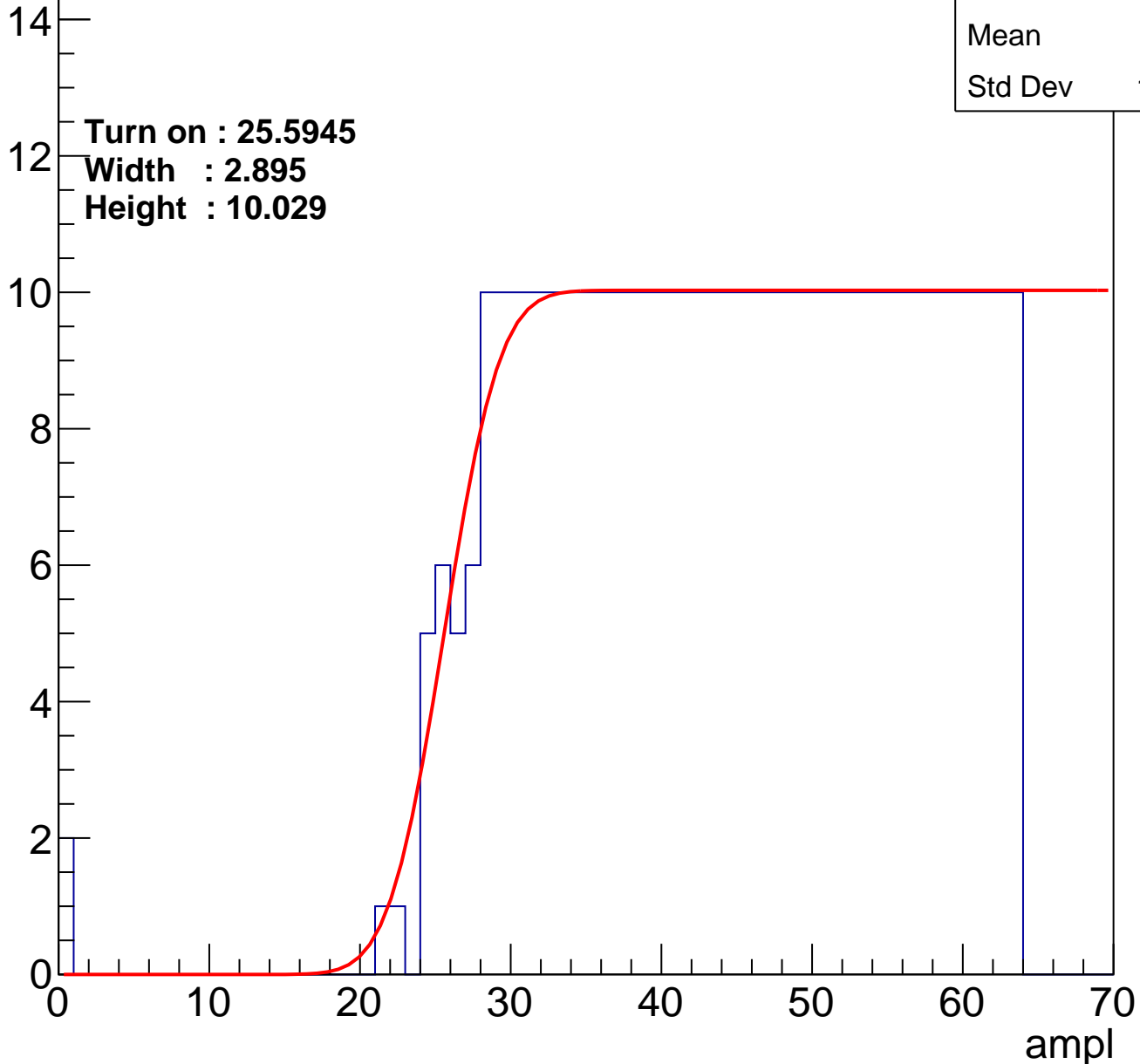
Entries	386
Mean	44
Std Dev	11.61

Turn on : 25.5945

Width : 2.895

Height : 10.029

Entry



B1L002S, U10-ch26

calib_packv5_042523_0143.root, FC#10, port B3

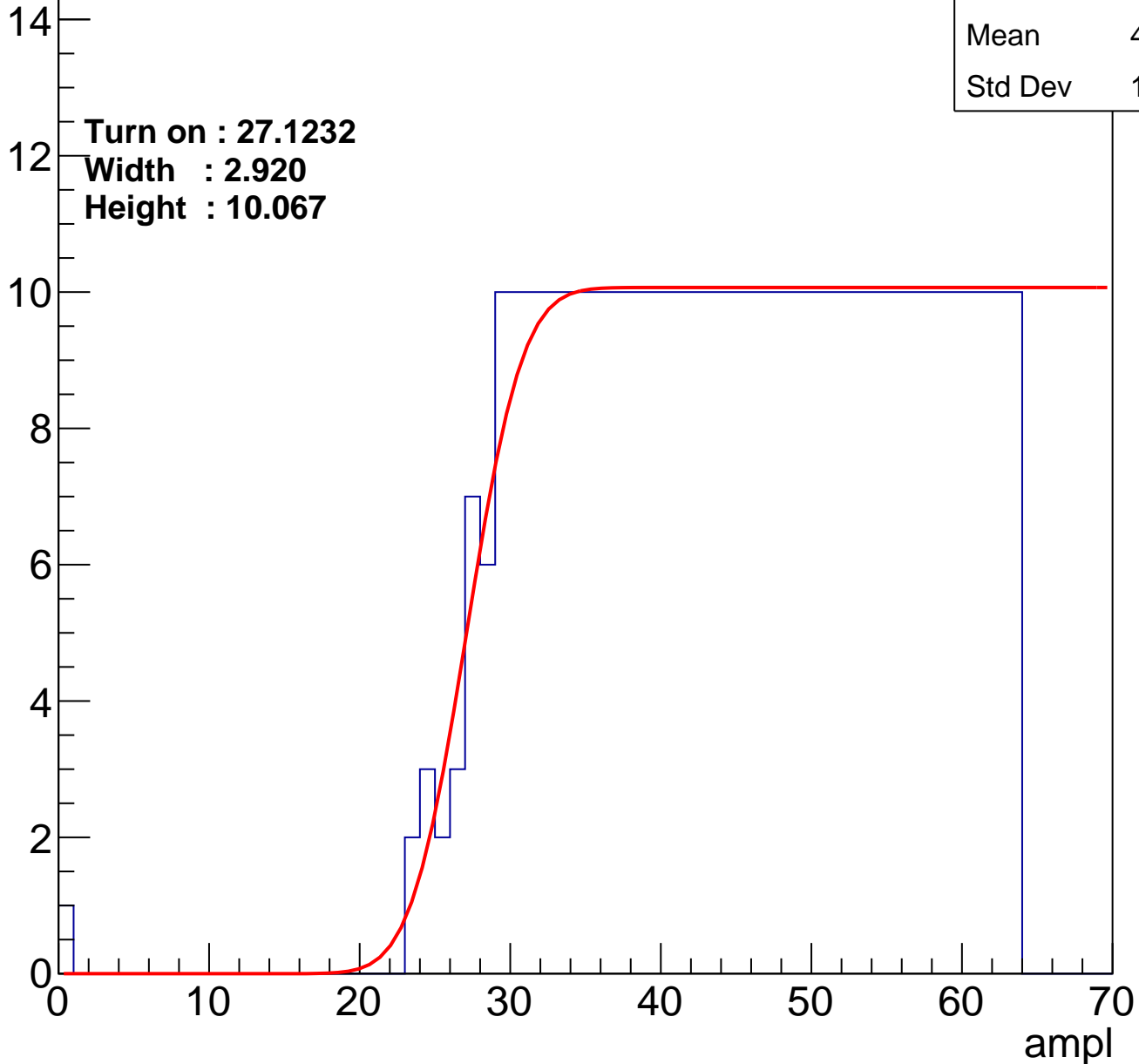
Entries	374
Mean	44.66
Std Dev	11.12

Turn on : 27.1232

Width : 2.920

Height : 10.067

Entry



B1L002S, U10-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.89
Std Dev	11

Turn on : 27.5847

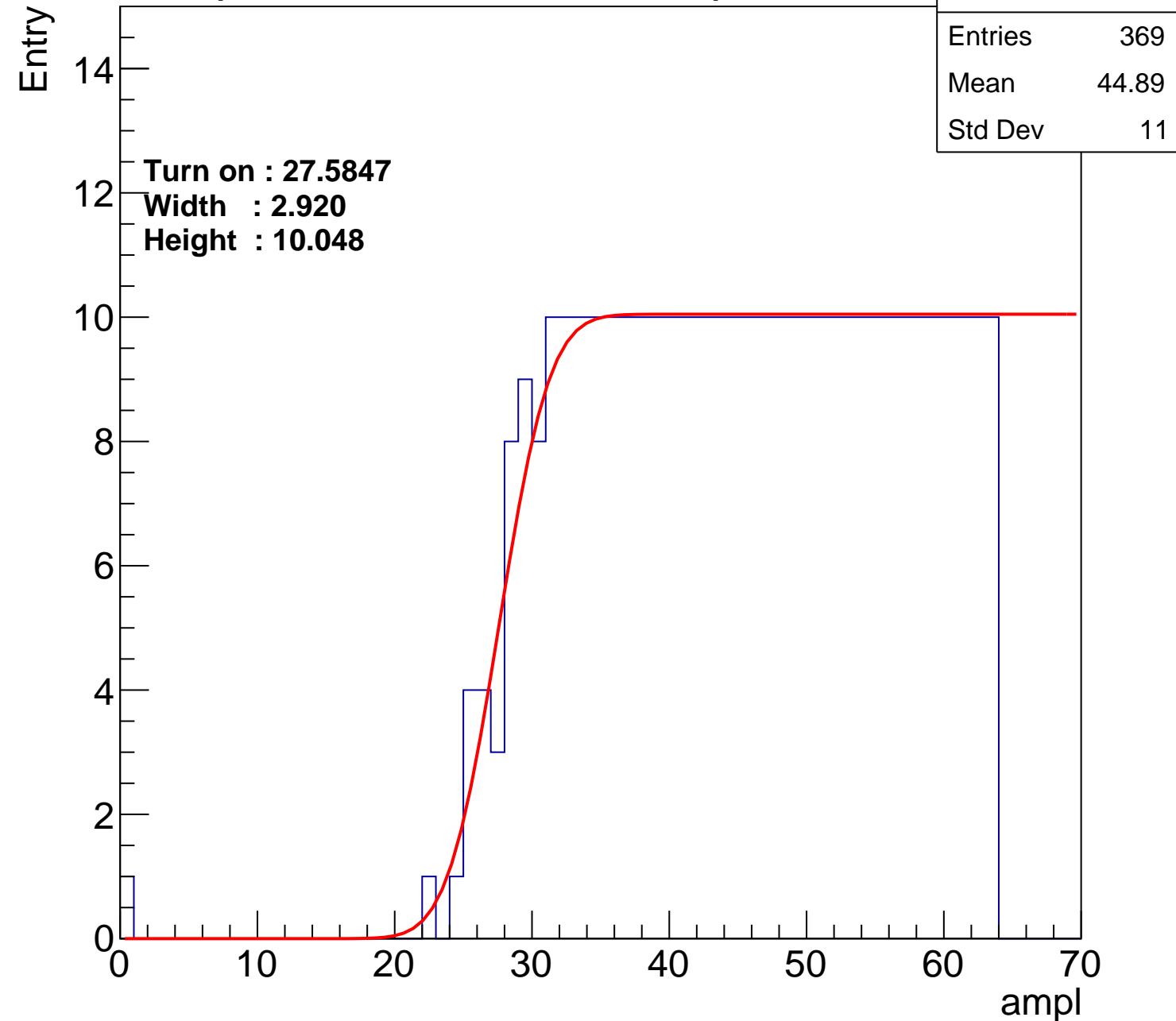
Width : 2.920

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.6976

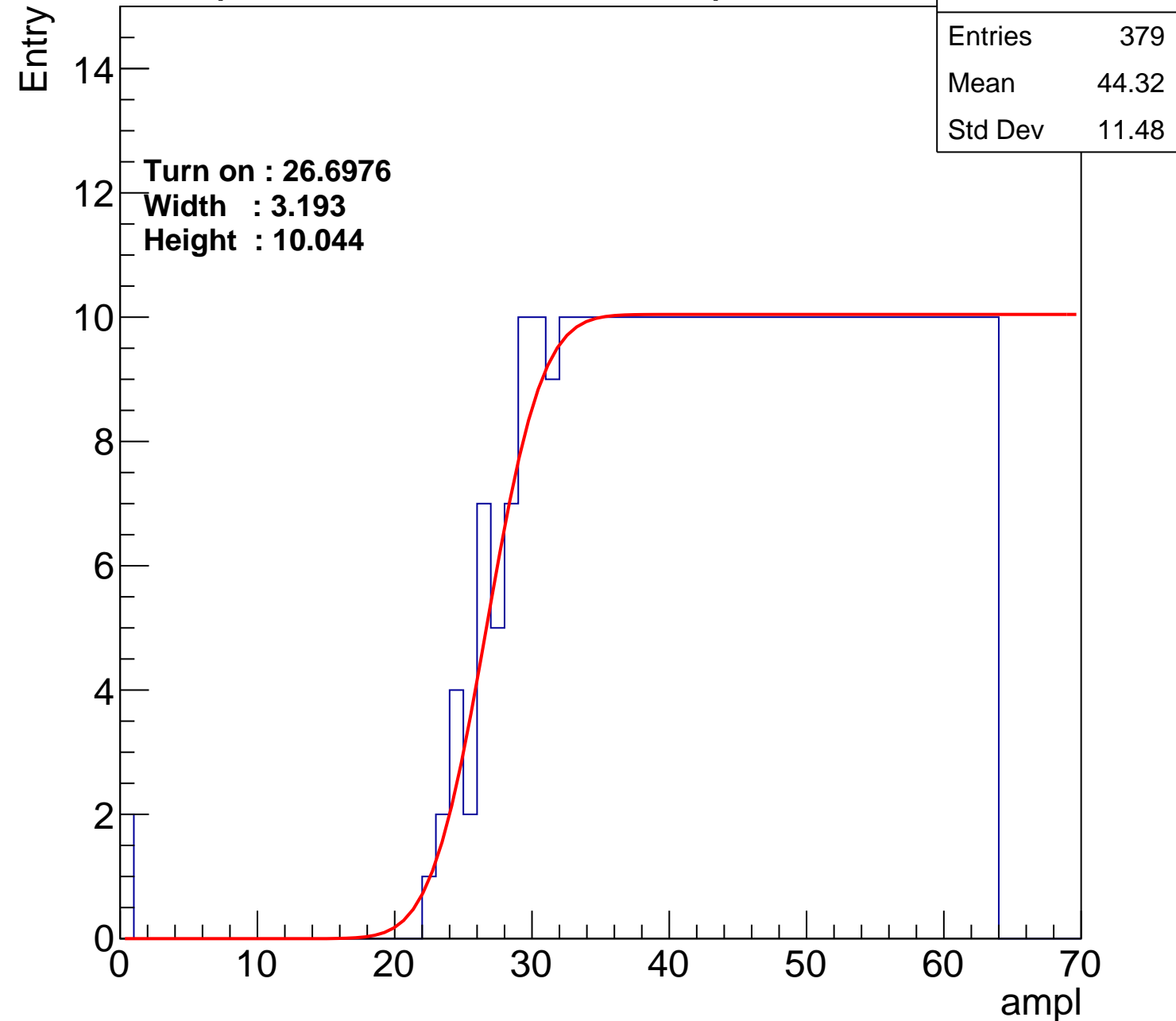
Width : 3.193

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 25.9389

Width : 1.394

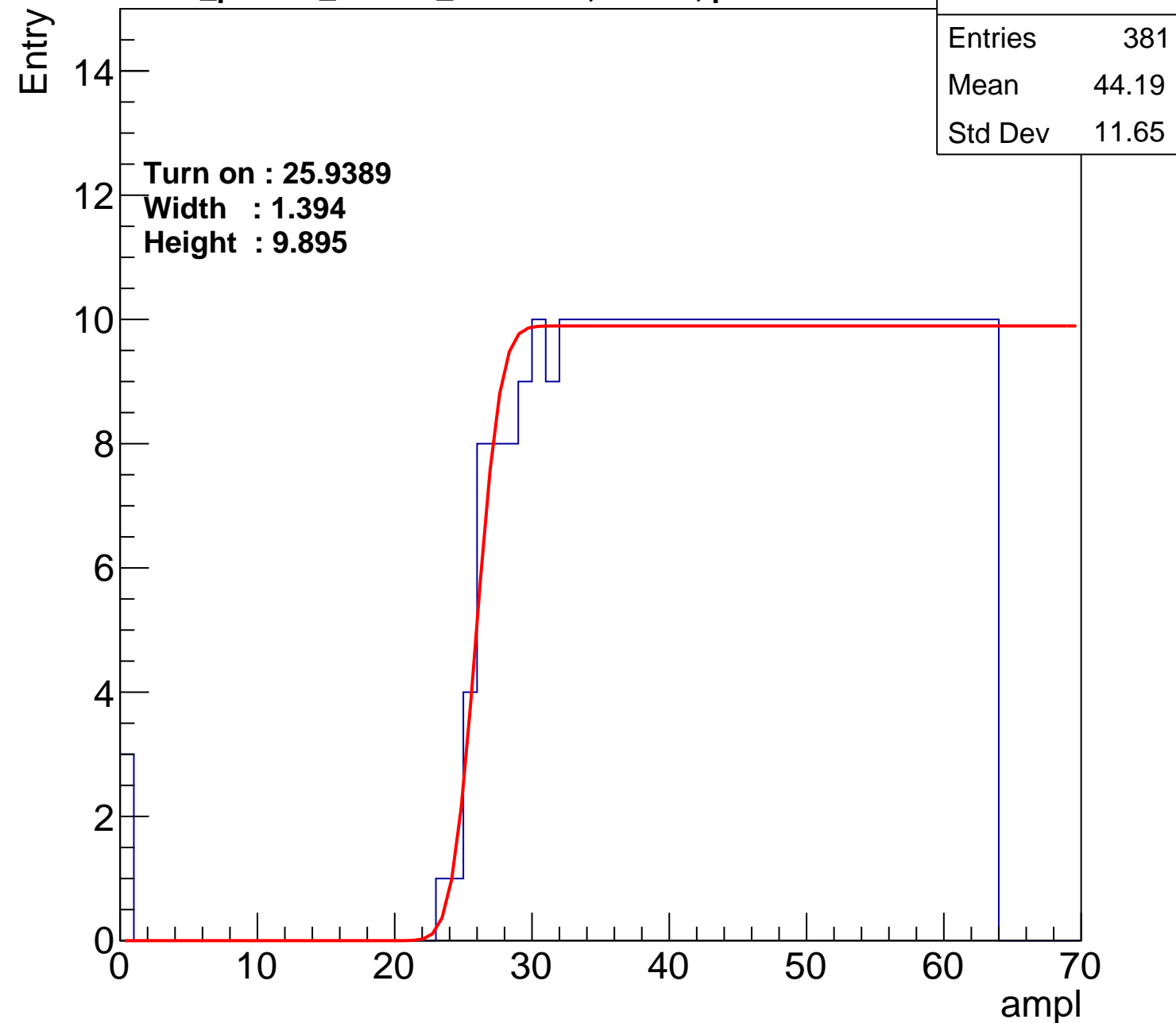
Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.23
Std Dev	12

Turn on : 24.0648

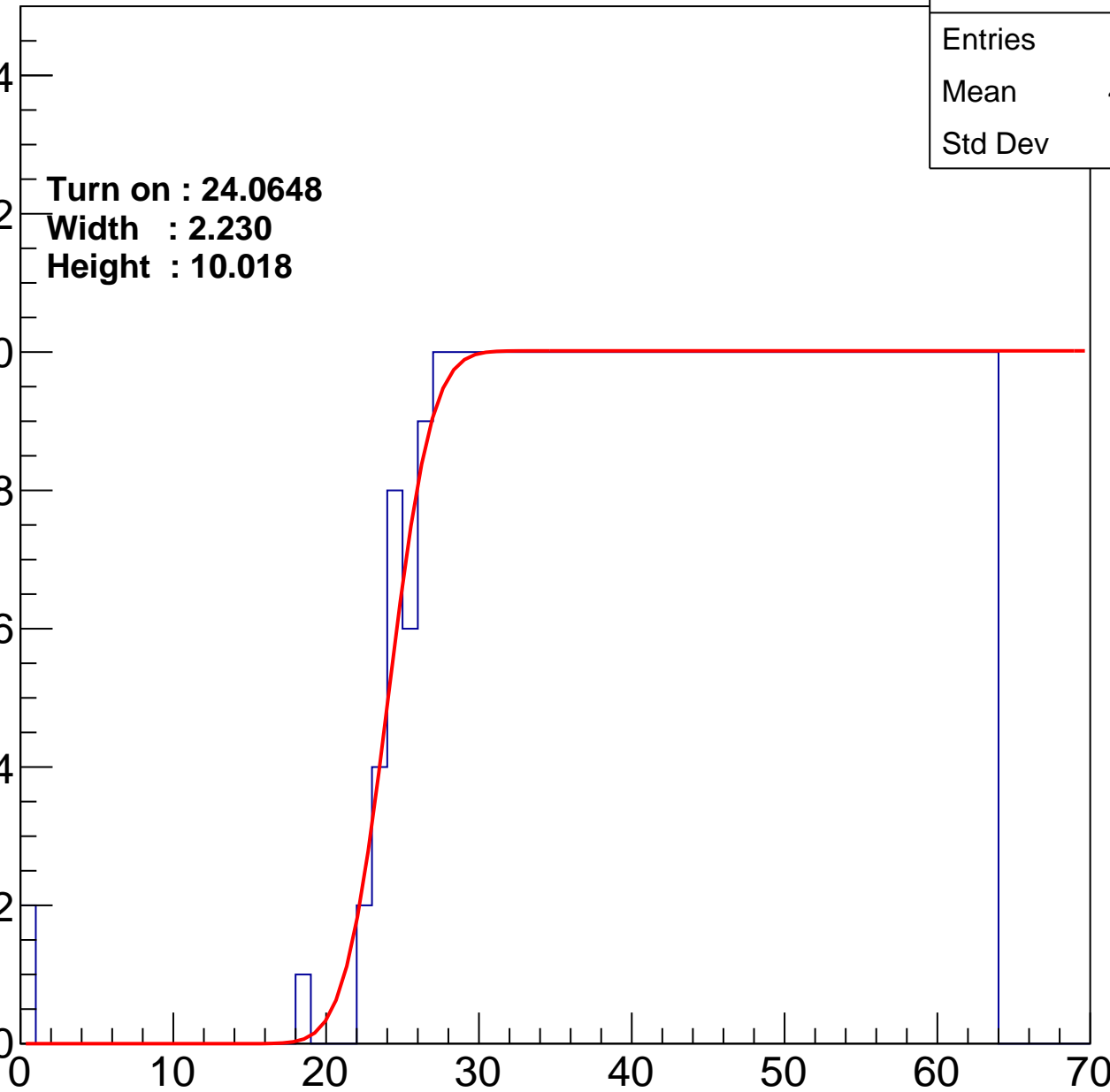
Width : 2.230

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.14
Std Dev	11.44

Turn on : 26.7266

Width : 3.145

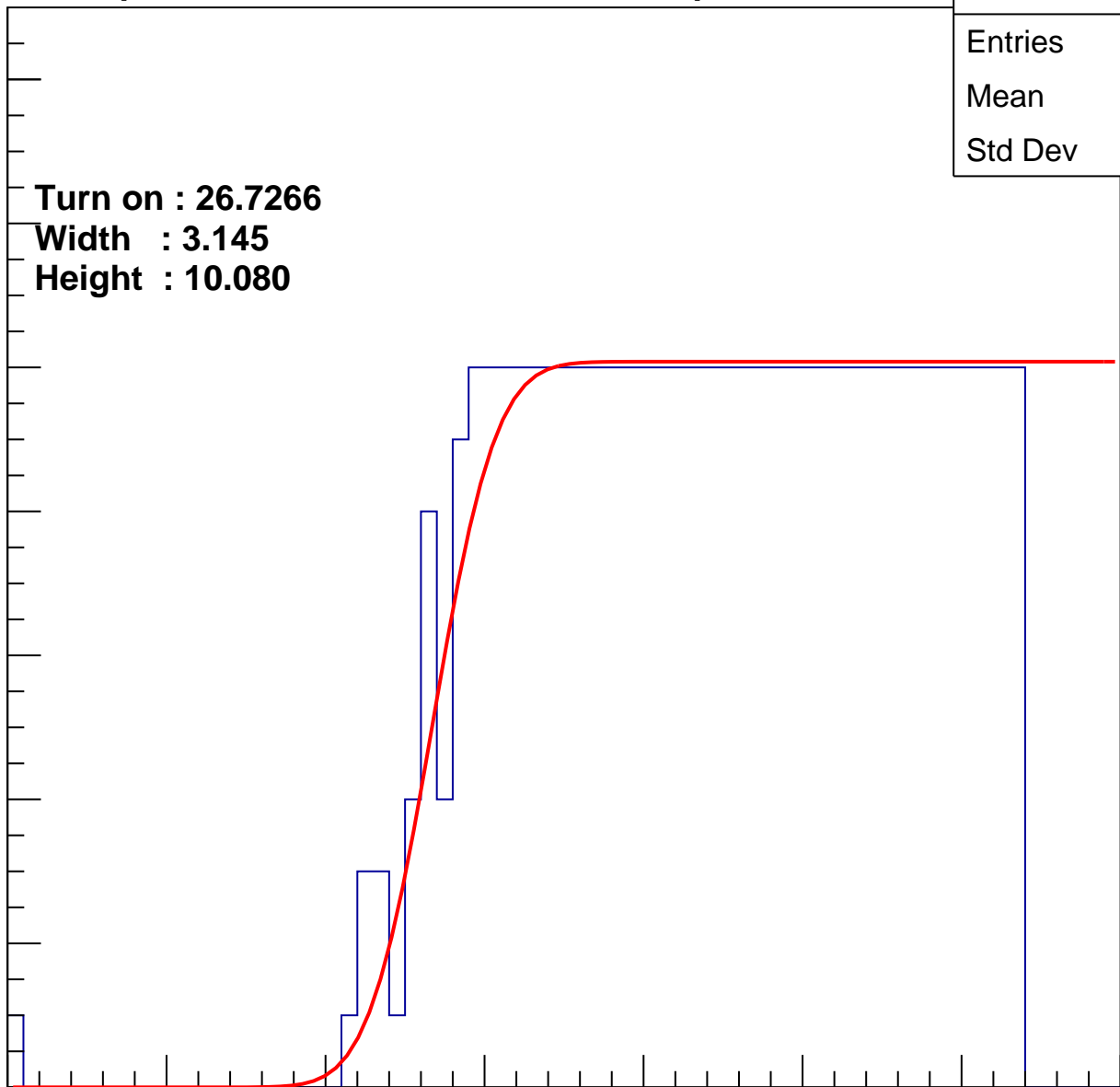
Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch32

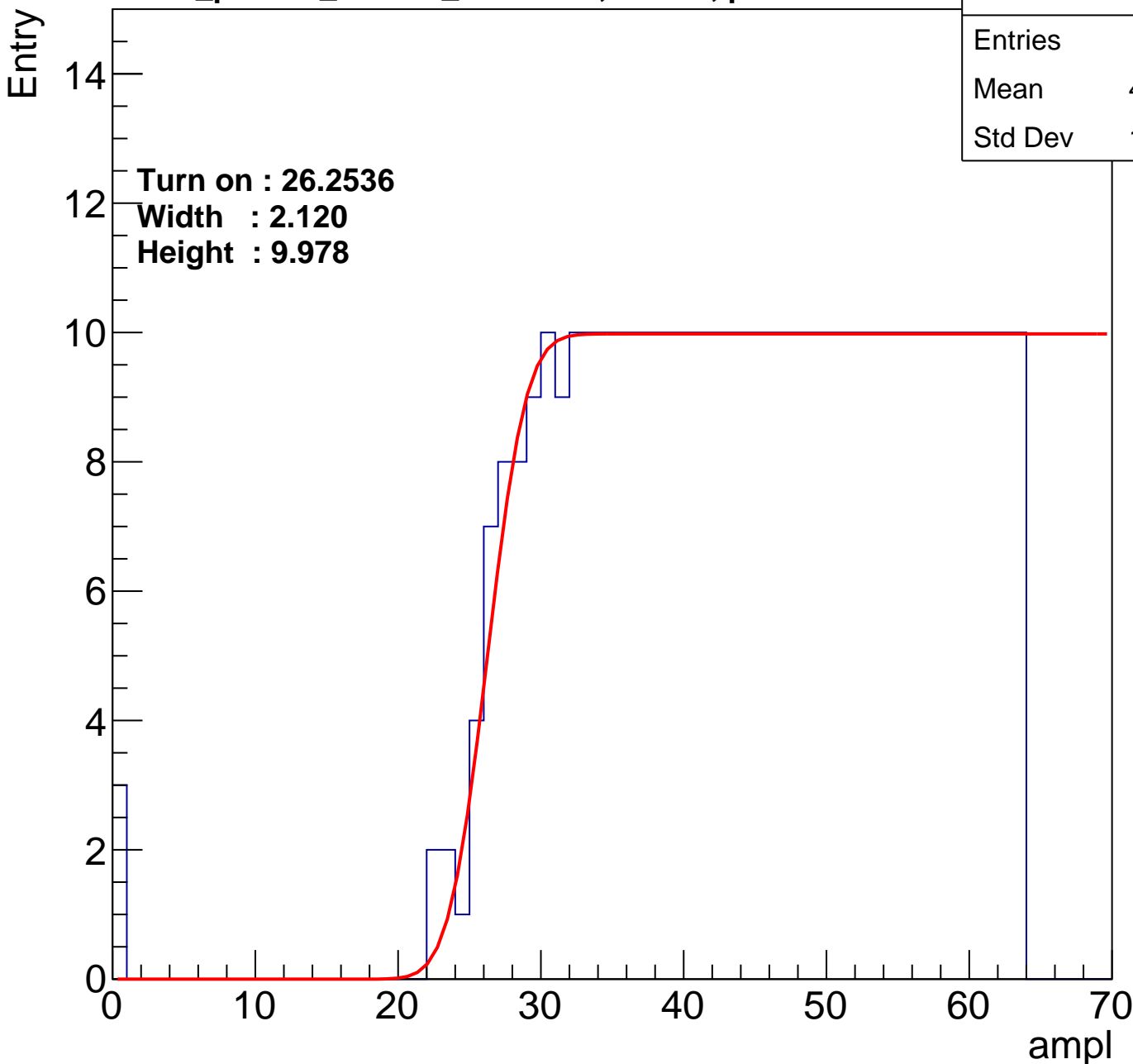
calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.06
Std Dev	11.75

Turn on : 26.2536

Width : 2.120

Height : 9.978



B1L002S, U10-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.4359

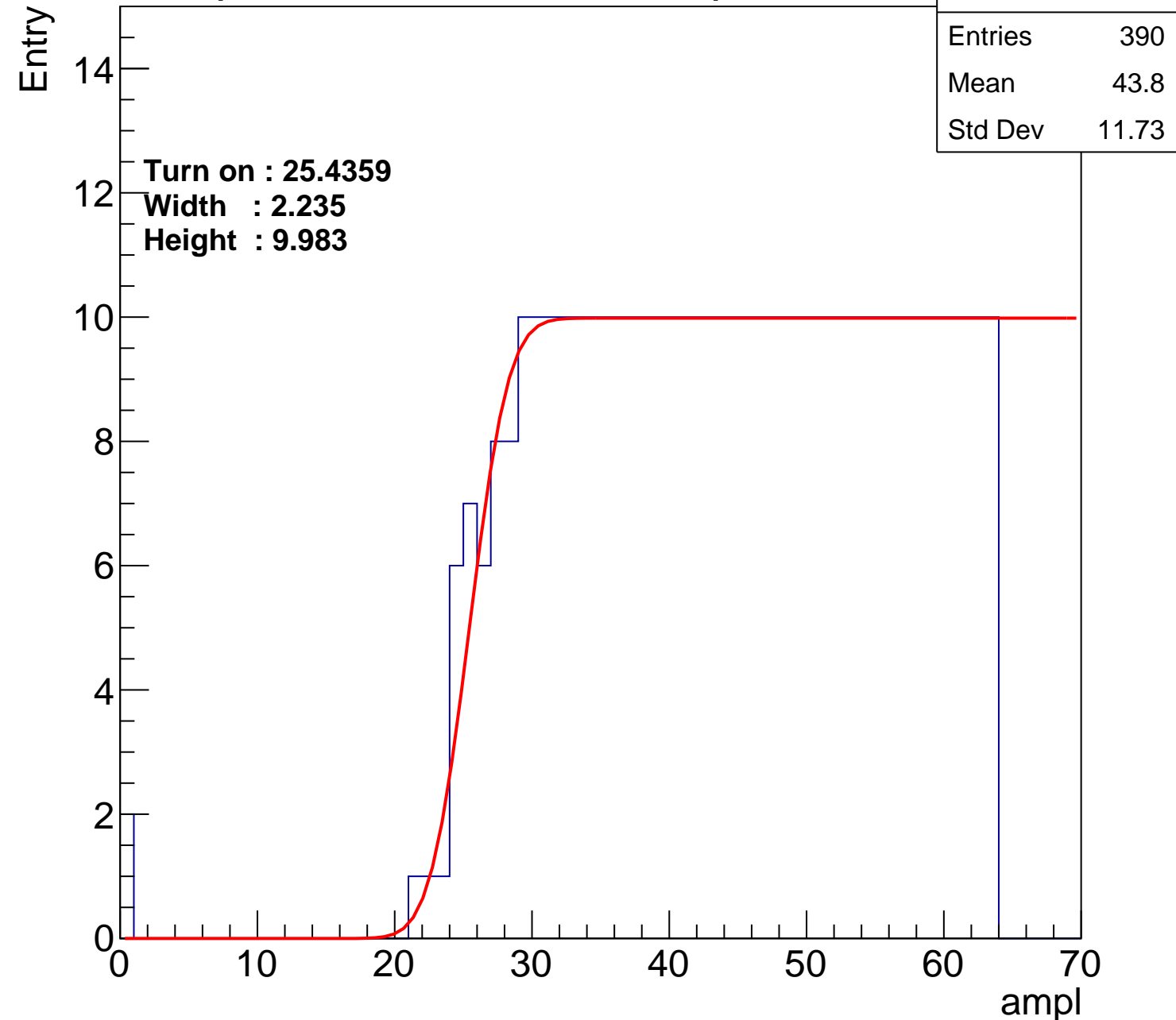
Width : 2.235

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.59
Std Dev	11.36

Turn on : 27.0454

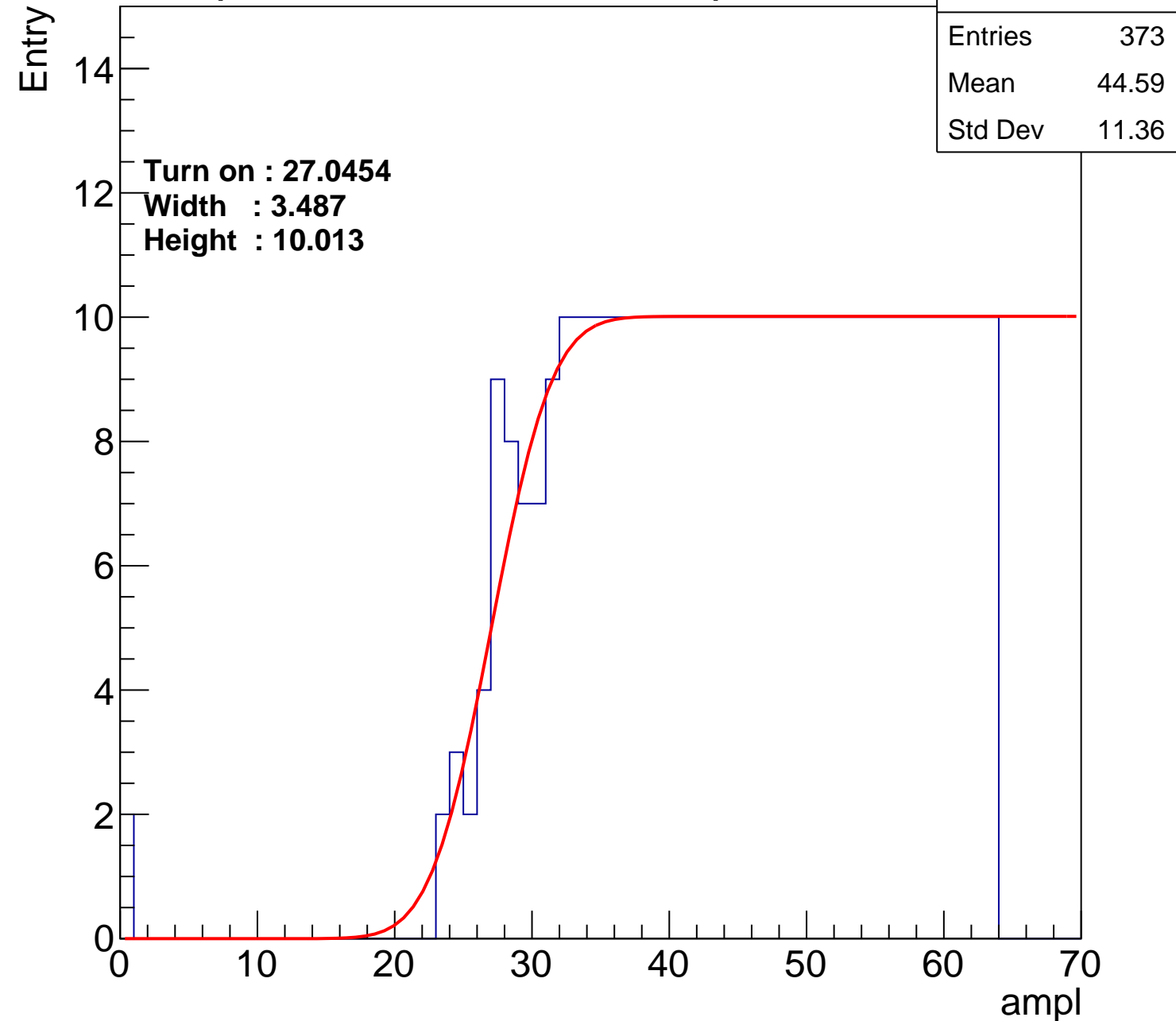
Width : 3.487

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch35

calib_packv5_042523_0143.root, FC#10, port B3

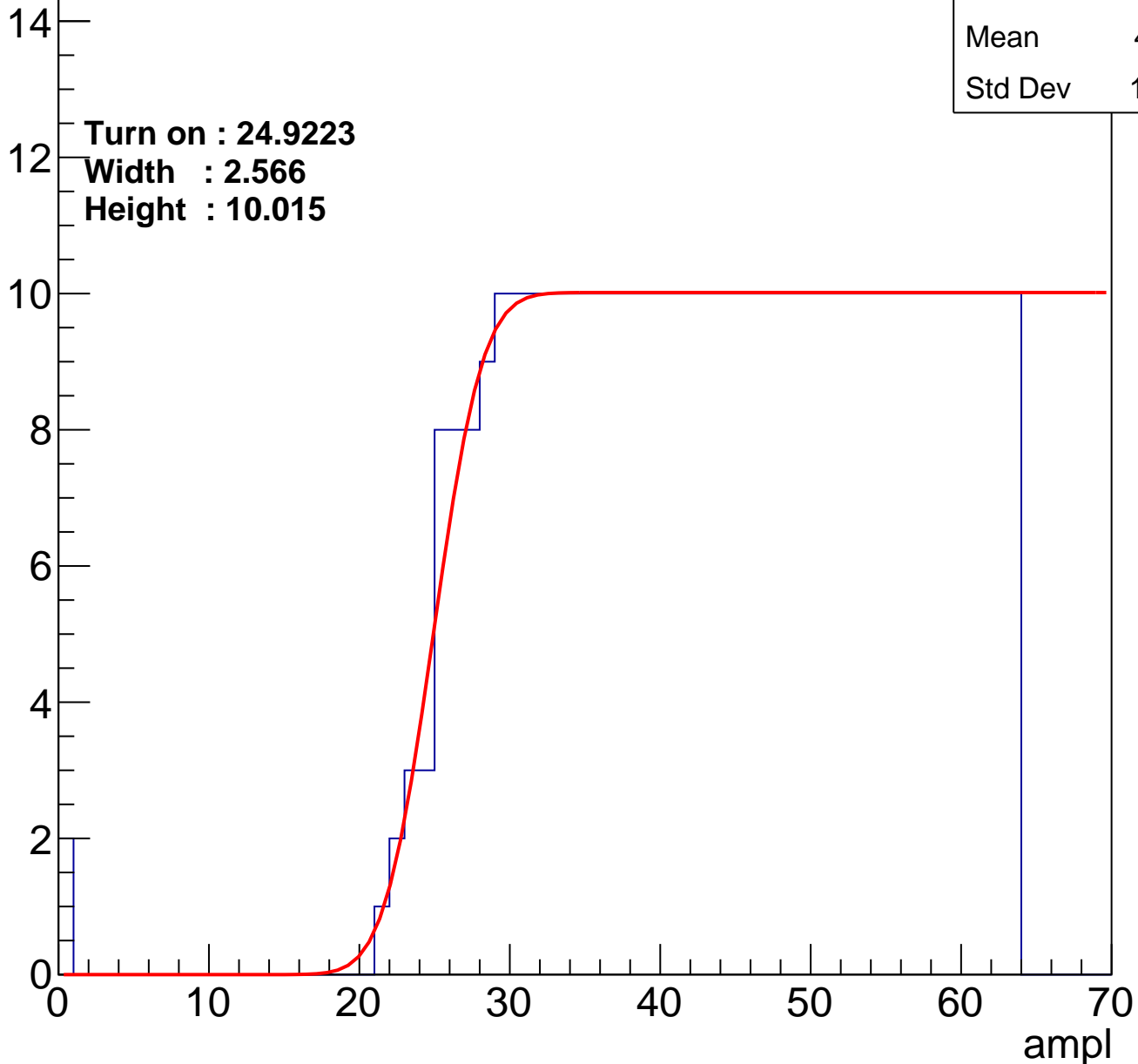
Entries	394
Mean	43.61
Std Dev	11.82

Turn on : 24.9223

Width : 2.566

Height : 10.015

Entry



B1L002S, U10-ch36

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.69

Turn on : 25.7025

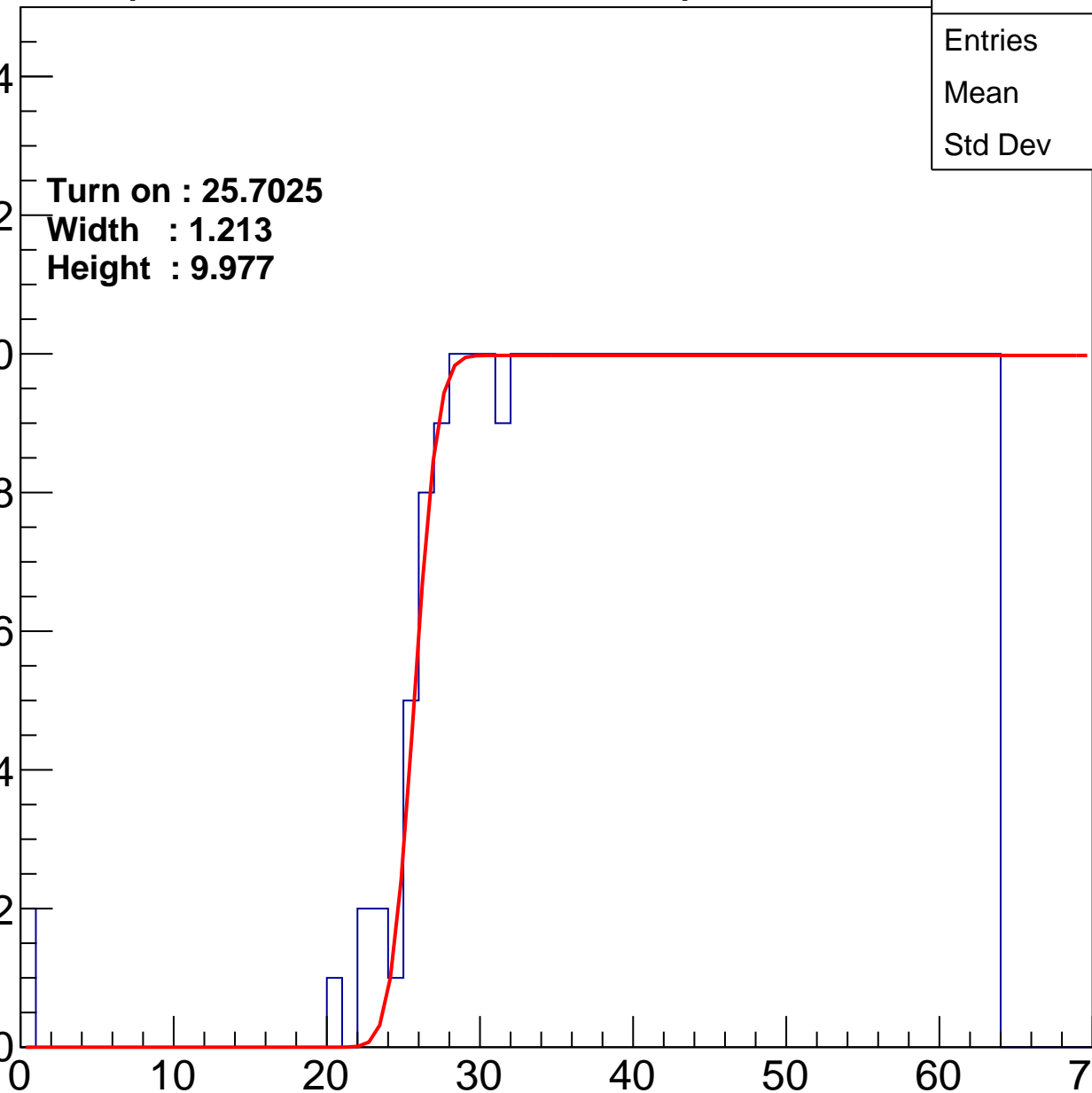
Width : 1.213

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 25.9140

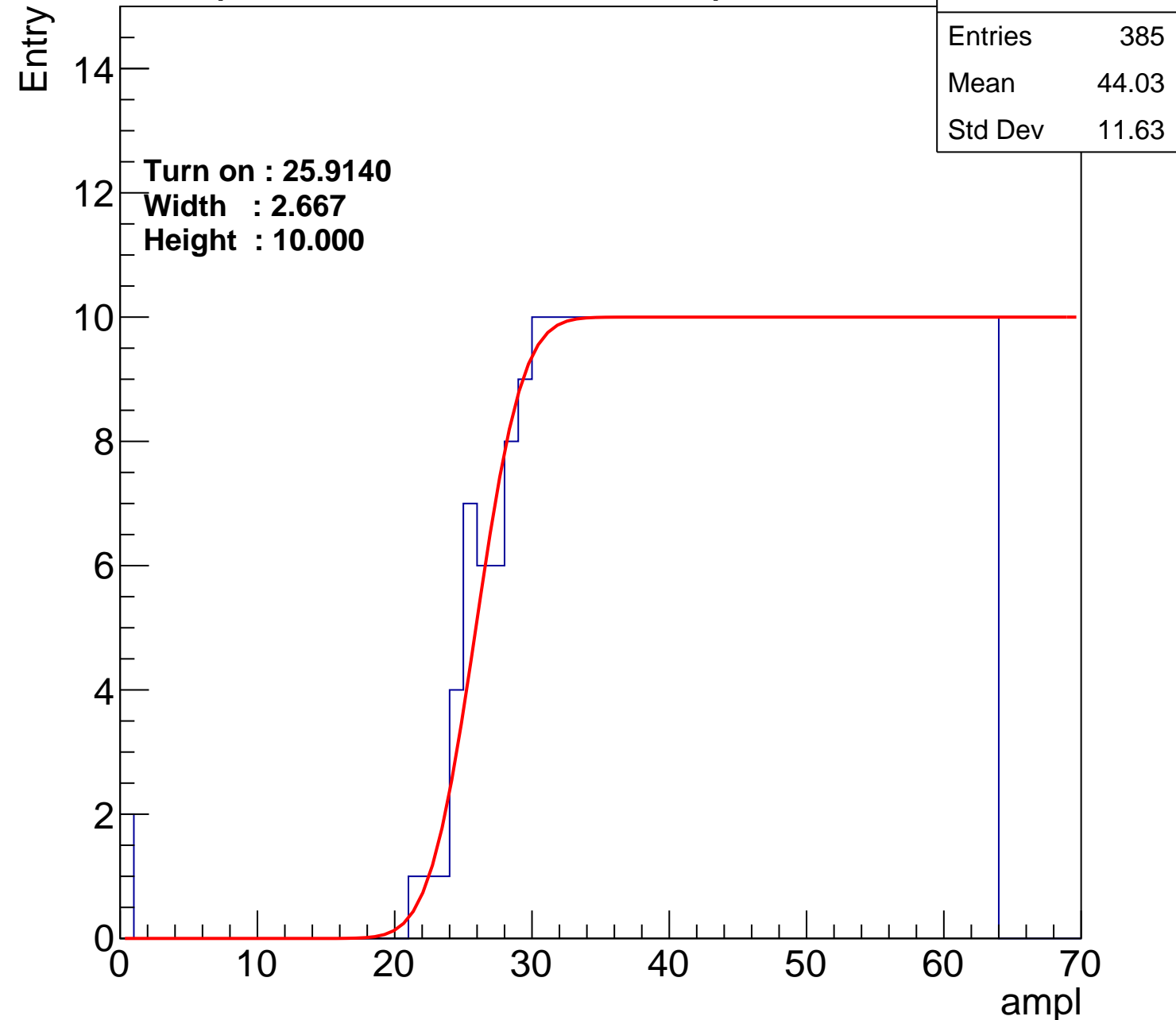
Width : 2.667

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.5589

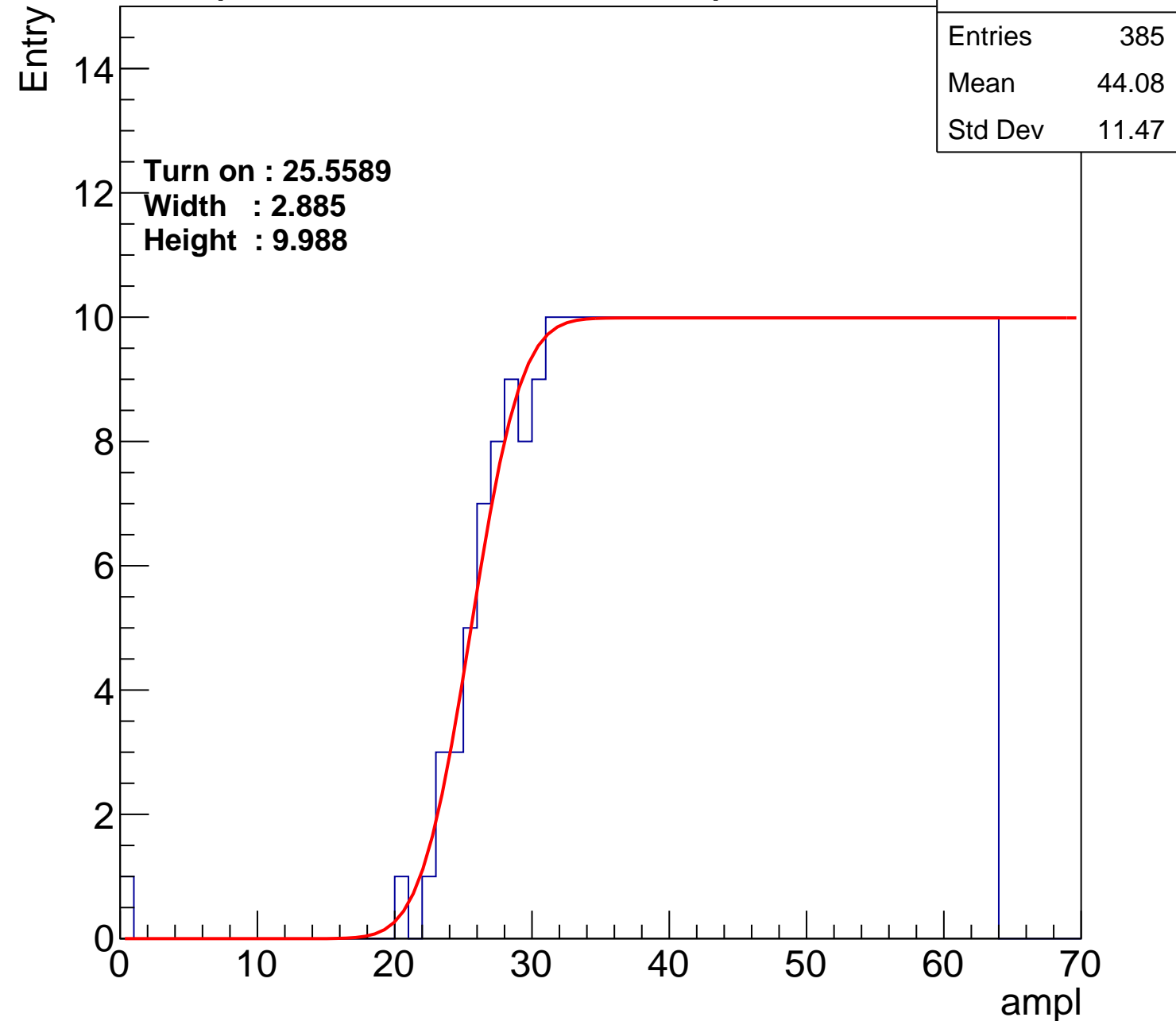
Width : 2.885

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch39

calib_packv5_042523_0143.root, FC#10, port B3

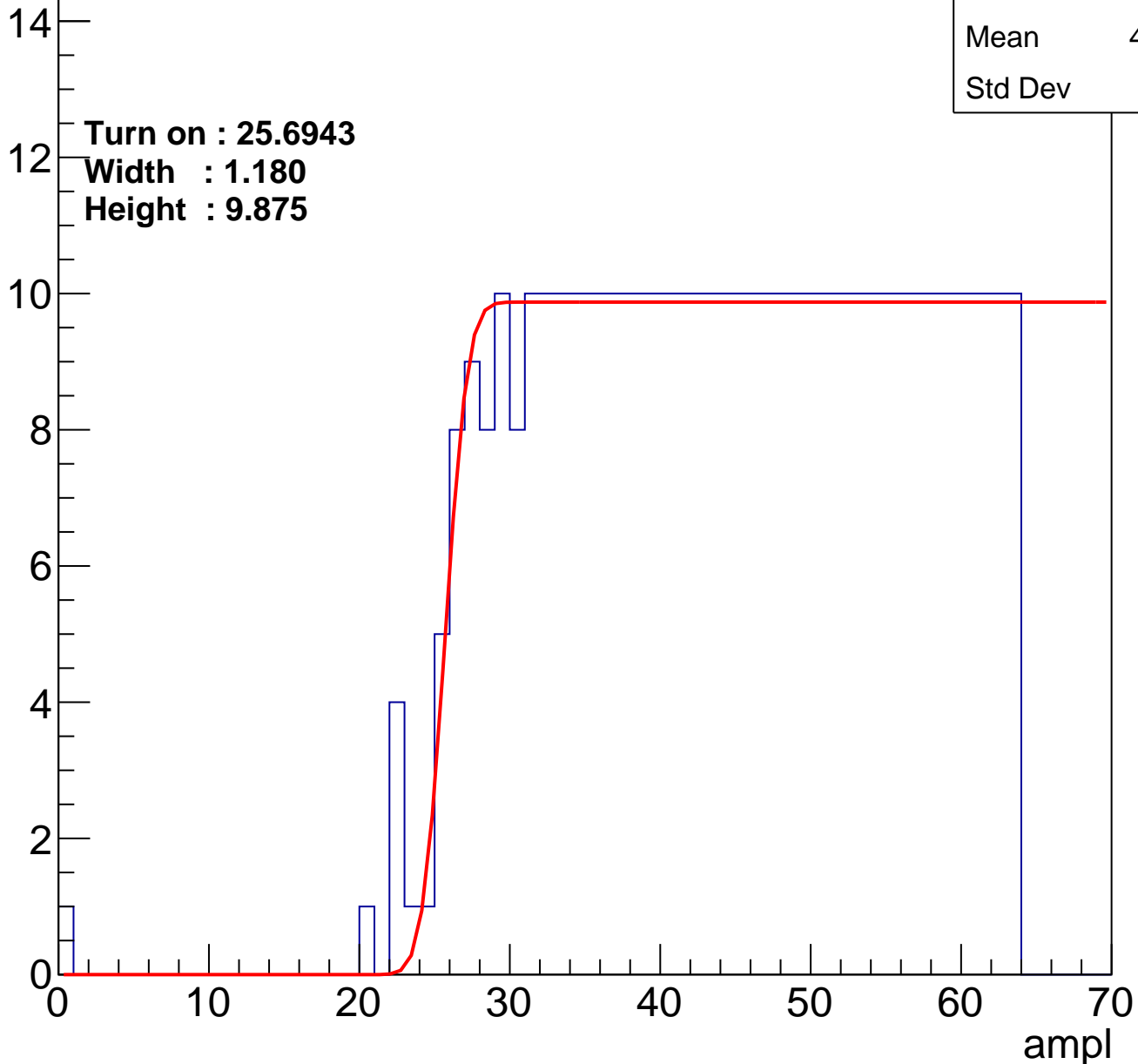
Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 25.6943

Width : 1.180

Height : 9.875

Entry



B1L002S, U10-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.38
Std Dev	11.31

Turn on : 26.5062

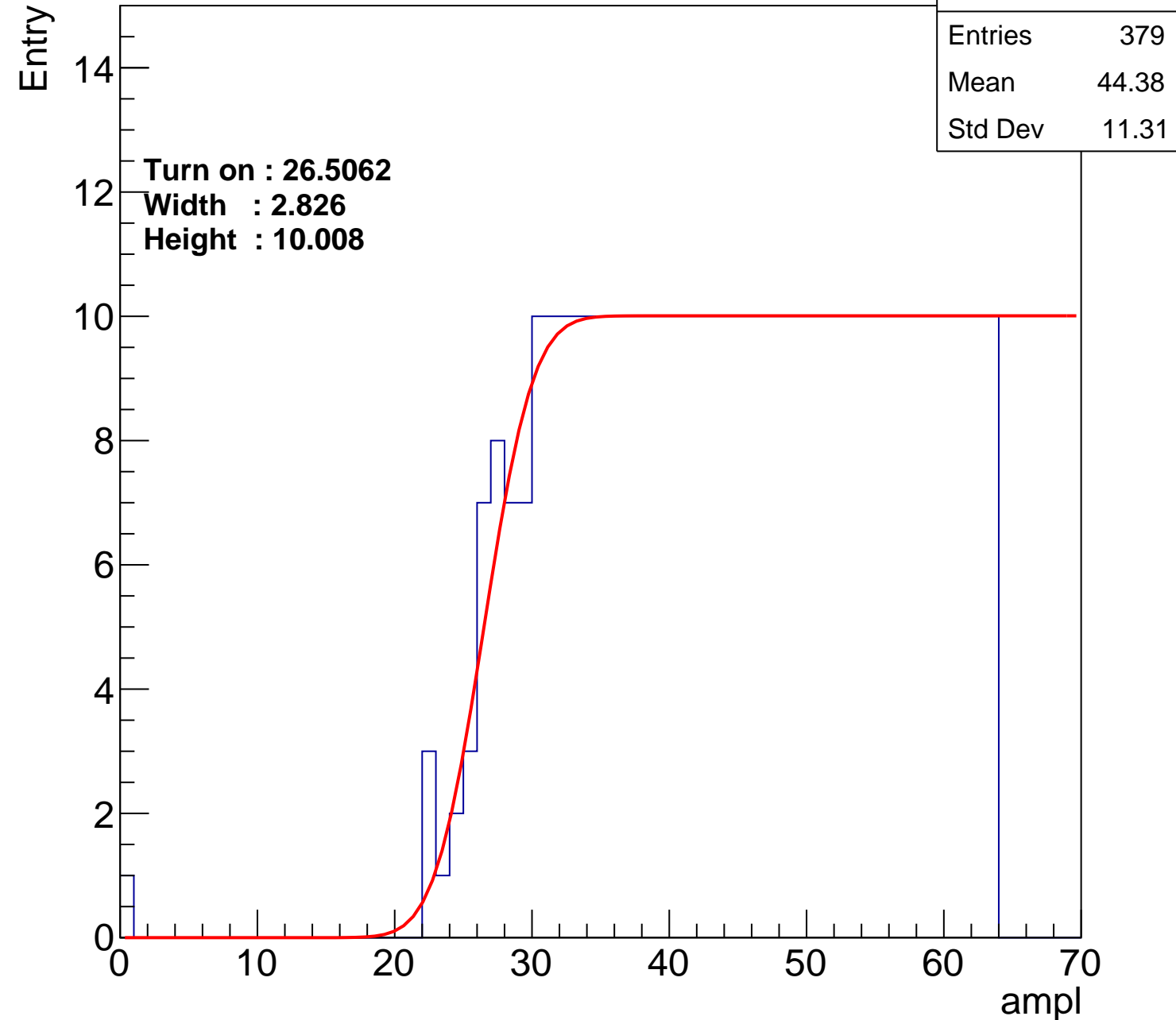
Width : 2.826

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch41

calib_packv5_042523_0143.root, FC#10, port B3

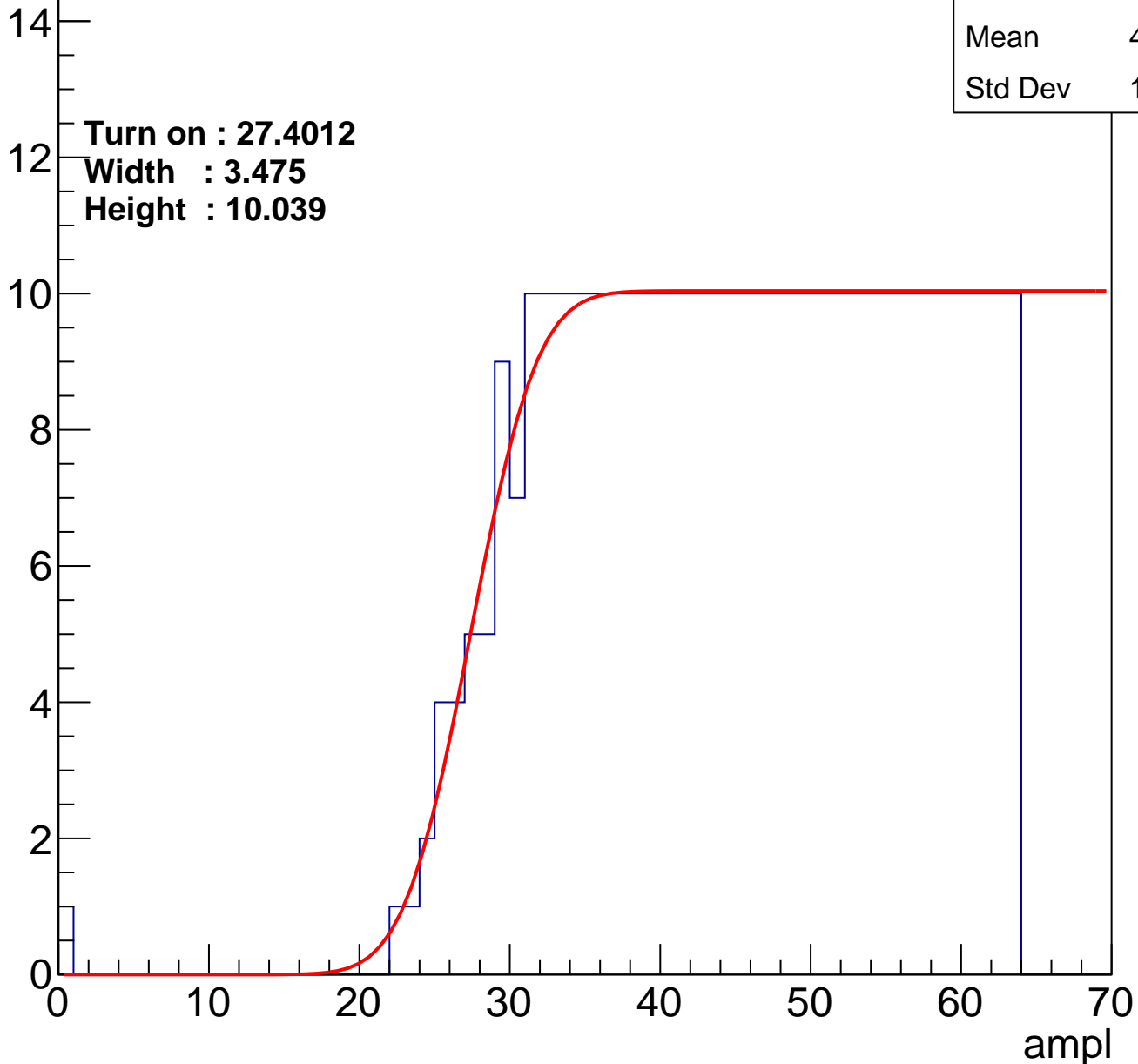
Entries	369
Mean	44.86
Std Dev	11.06

Turn on : 27.4012

Width : 3.475

Height : 10.039

Entry



B1L002S, U10-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.1031

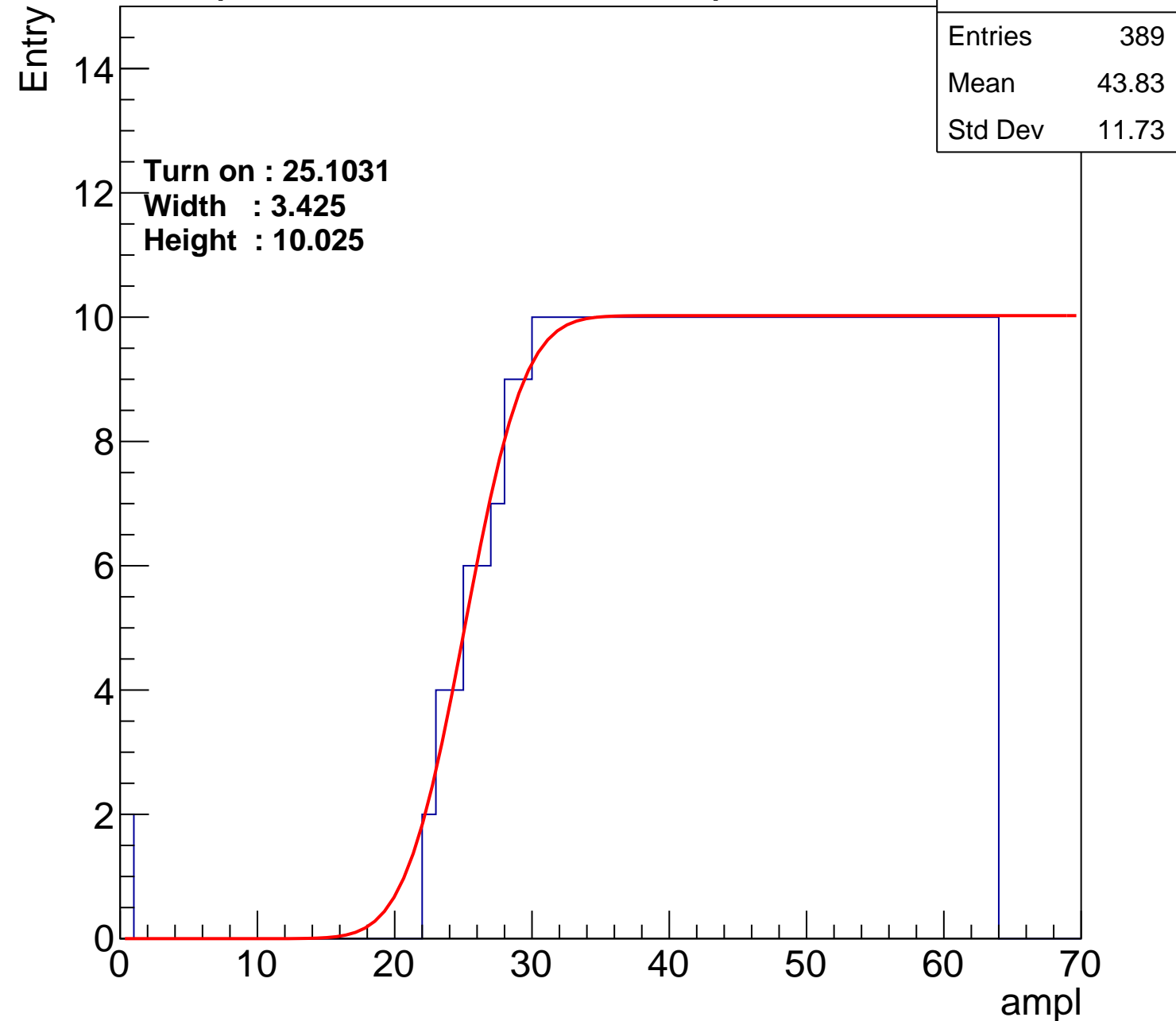
Width : 3.425

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.41
Std Dev	11.93

Turn on : 23.7657

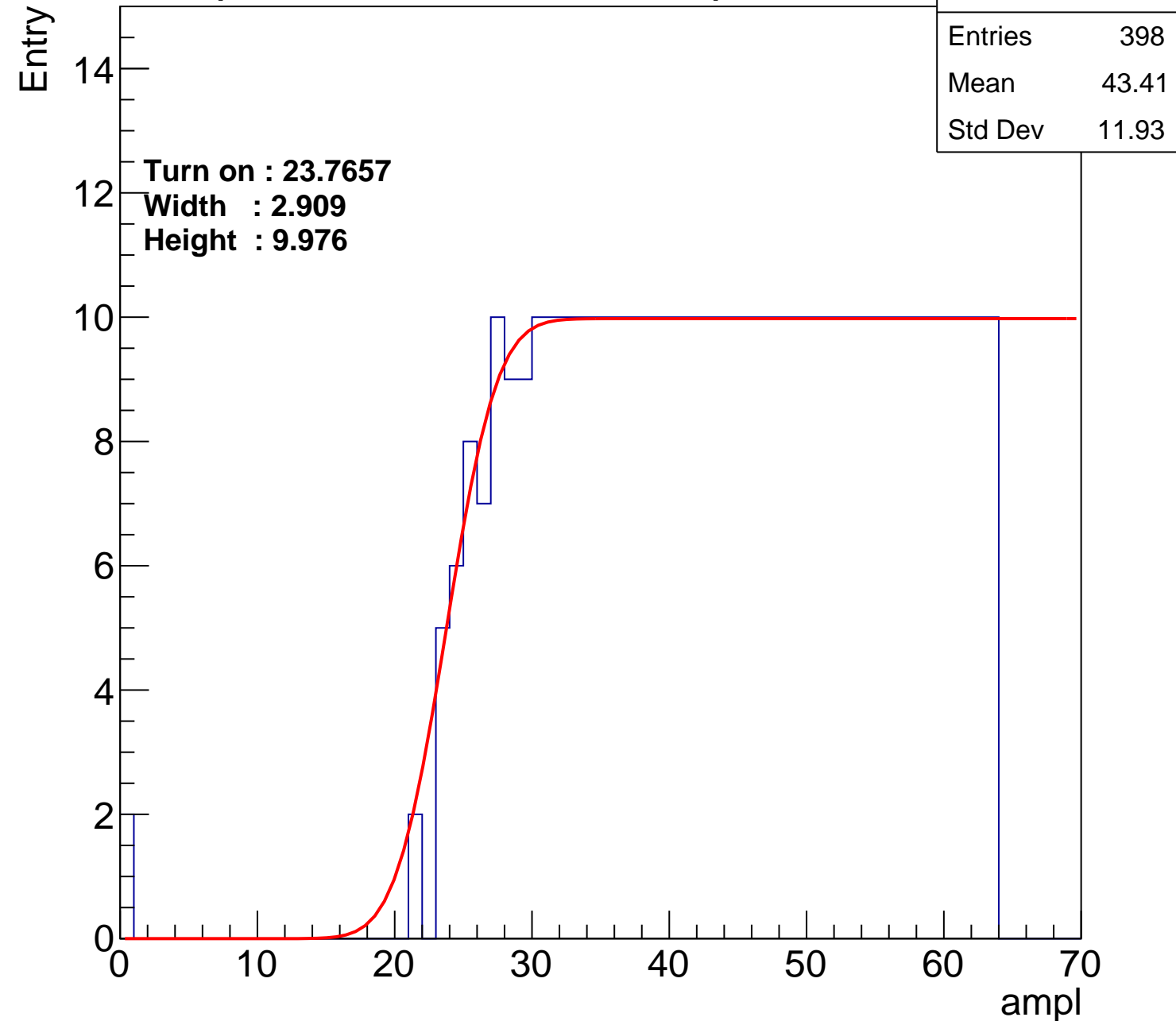
Width : 2.909

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.92
Std Dev	11.52

Turn on : 25.1800

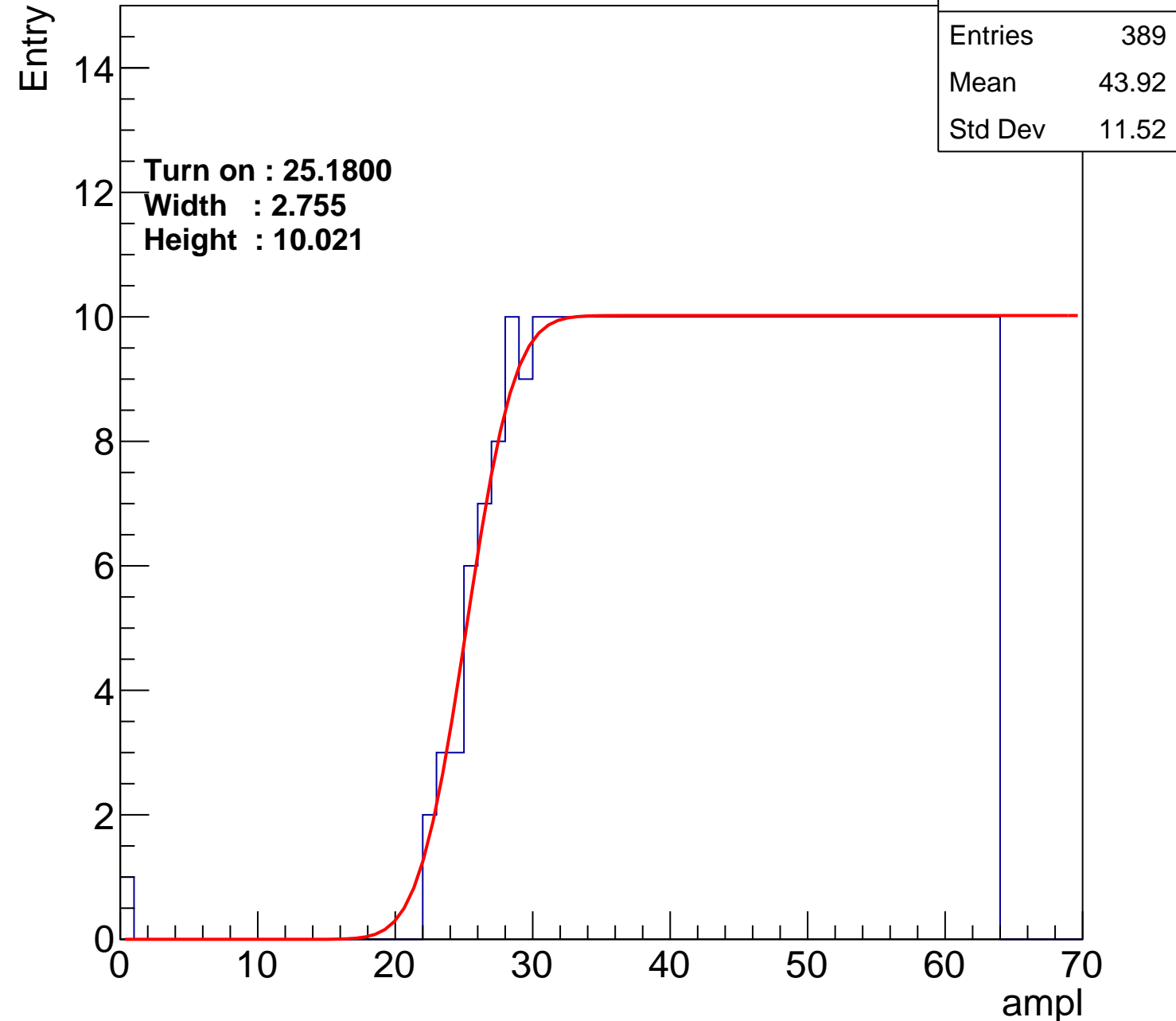
Width : 2.755

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.12
Std Dev	11.46

Turn on : 26.3184

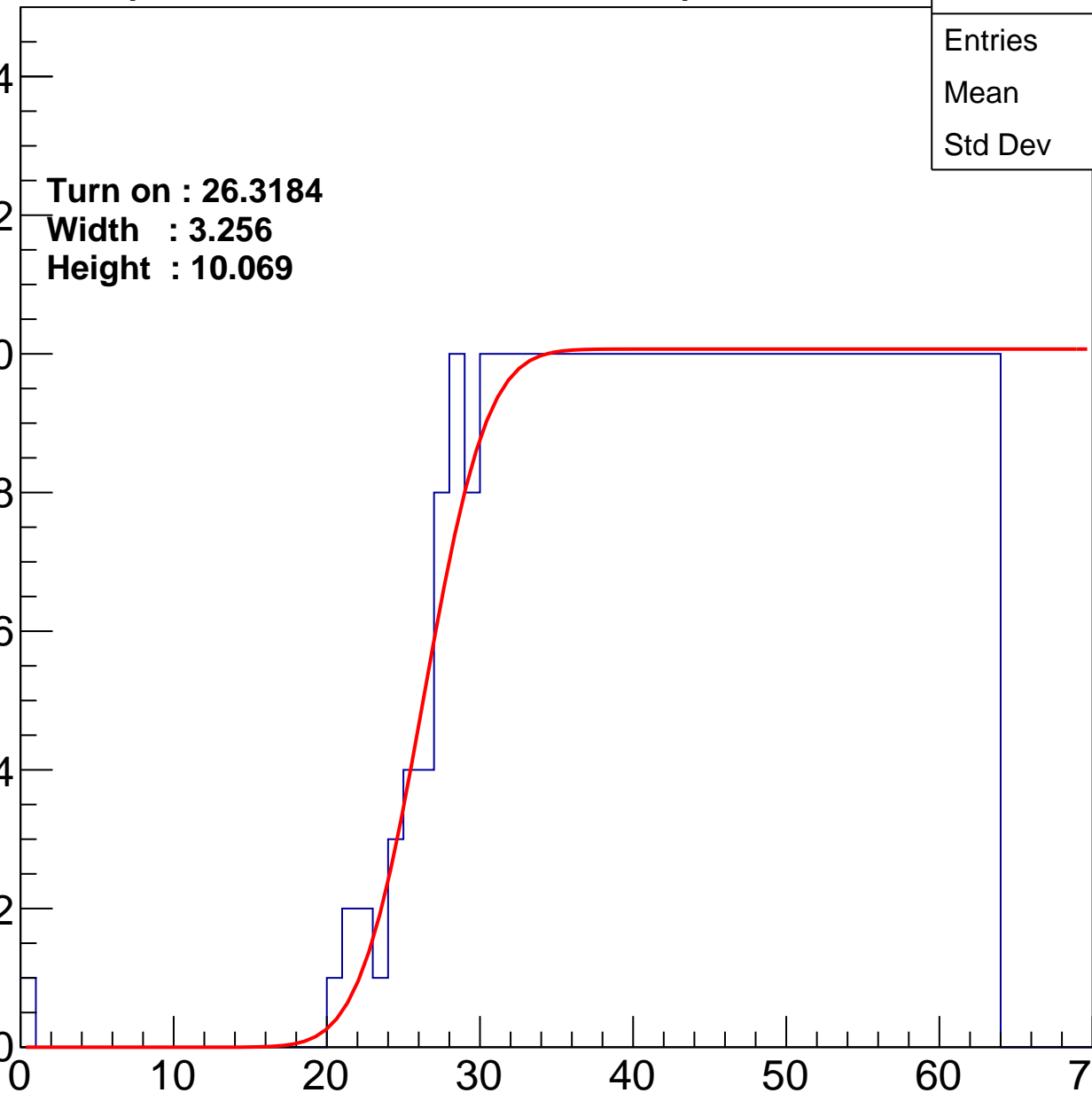
Width : 3.256

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch46

calib_packv5_042523_0143.root, FC#10, port B3

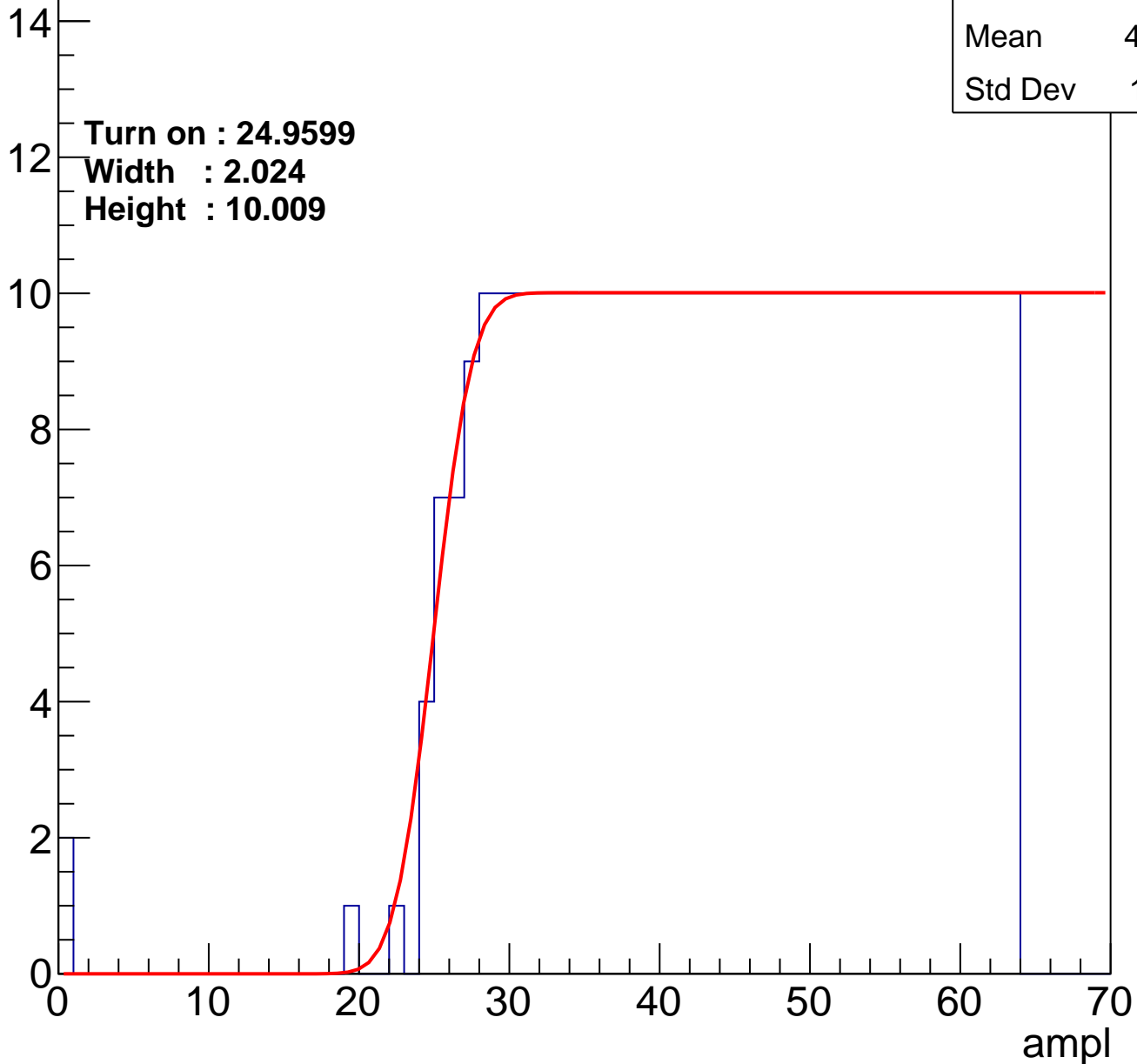
Entries	391
Mean	43.78
Std Dev	11.71

Turn on : 24.9599

Width : 2.024

Height : 10.009

Entry



B1L002S, U10-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.37
Std Dev	11.99

Turn on : 24.4833

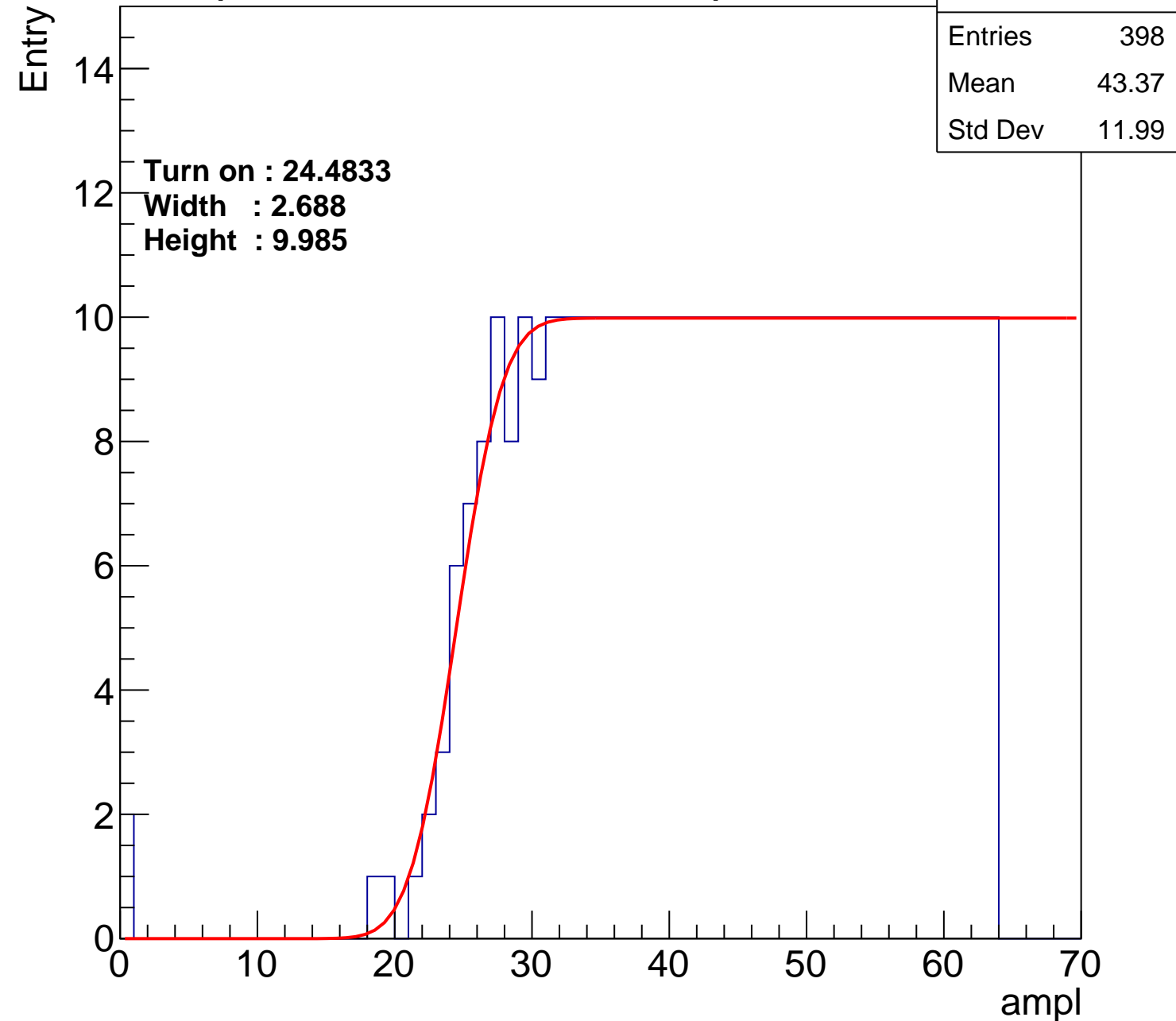
Width : 2.688

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.36
Std Dev	11.95

Turn on : 24.5217

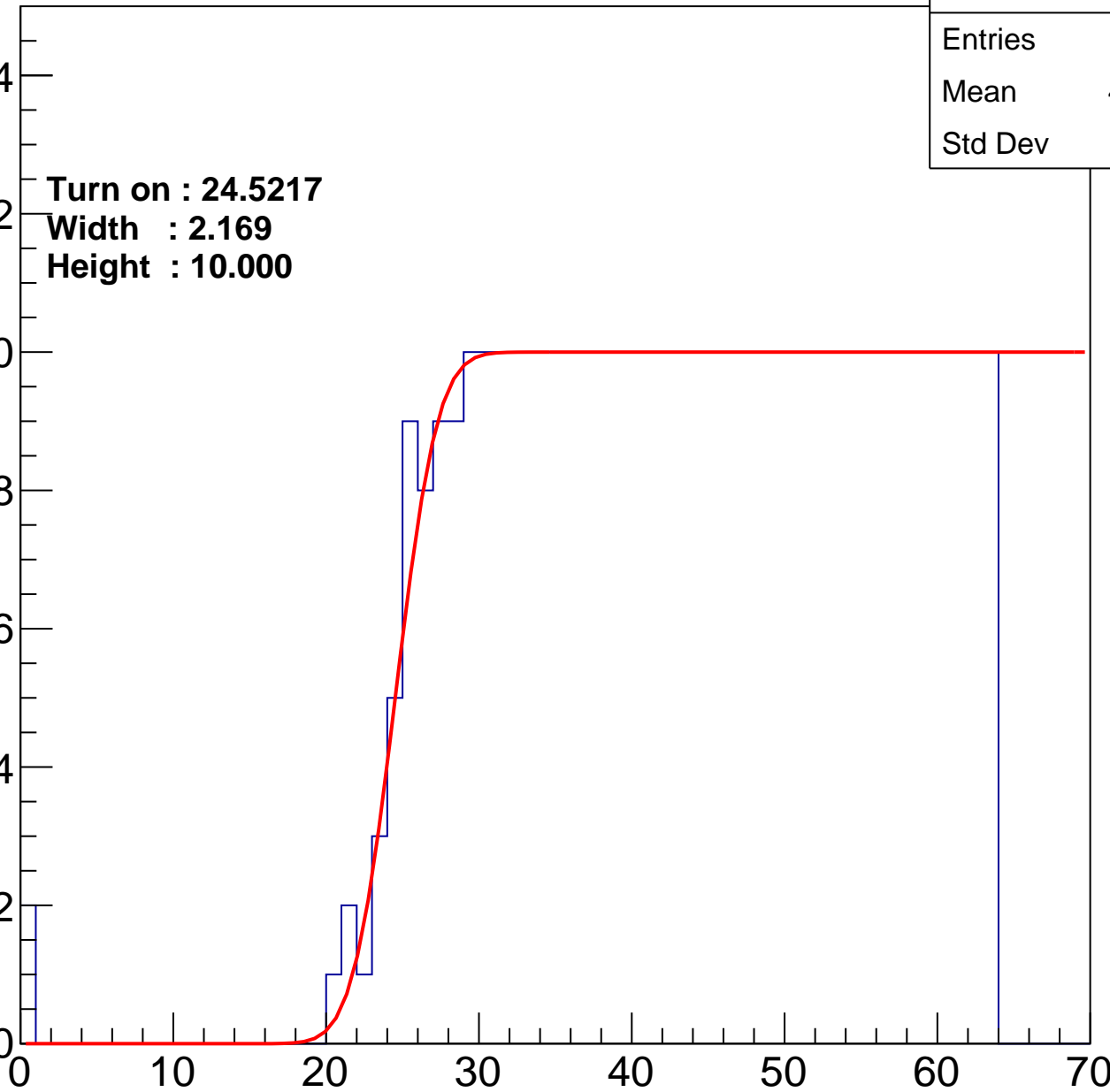
Width : 2.169

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch49

calib_packv5_042523_0143.root, FC#10, port B3

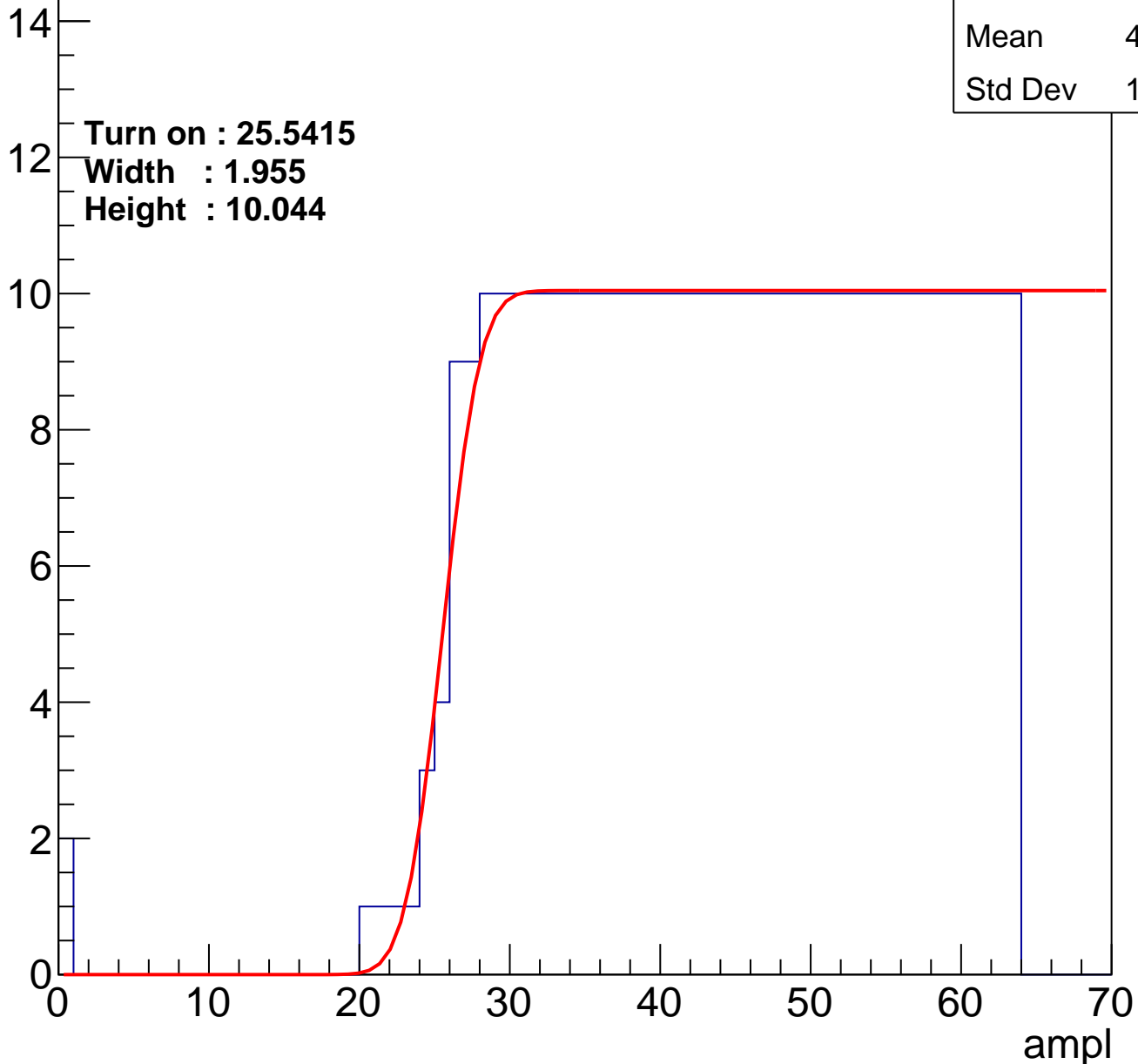
Entries	391
Mean	43.77
Std Dev	11.72

Turn on : 25.5415

Width : 1.955

Height : 10.044

Entry



B1L002S, U10-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	43.93
Std Dev	12.1

Turn on : 26.6777

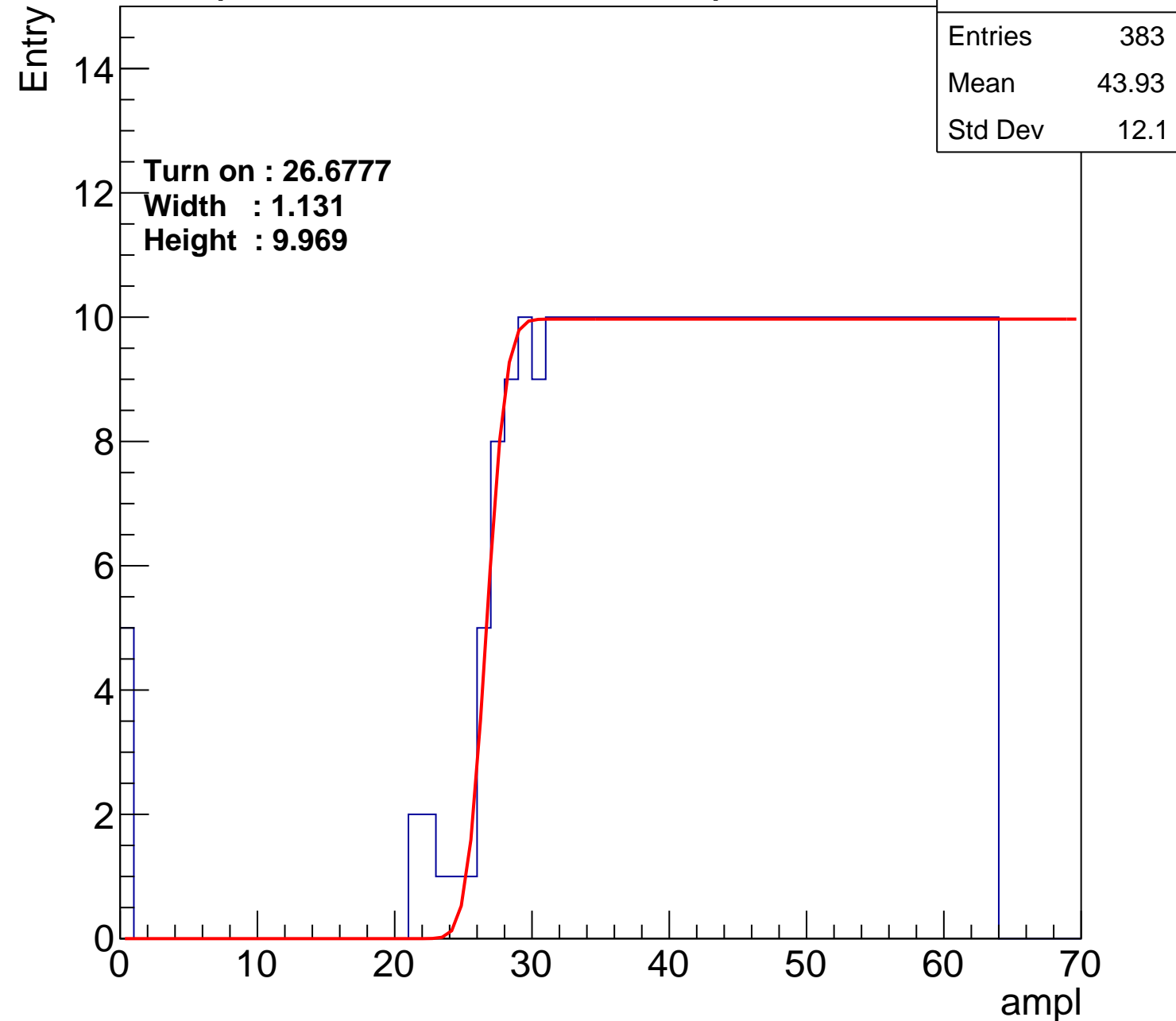
Width : 1.131

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch51

calib_packv5_042523_0143.root, FC#10, port B3

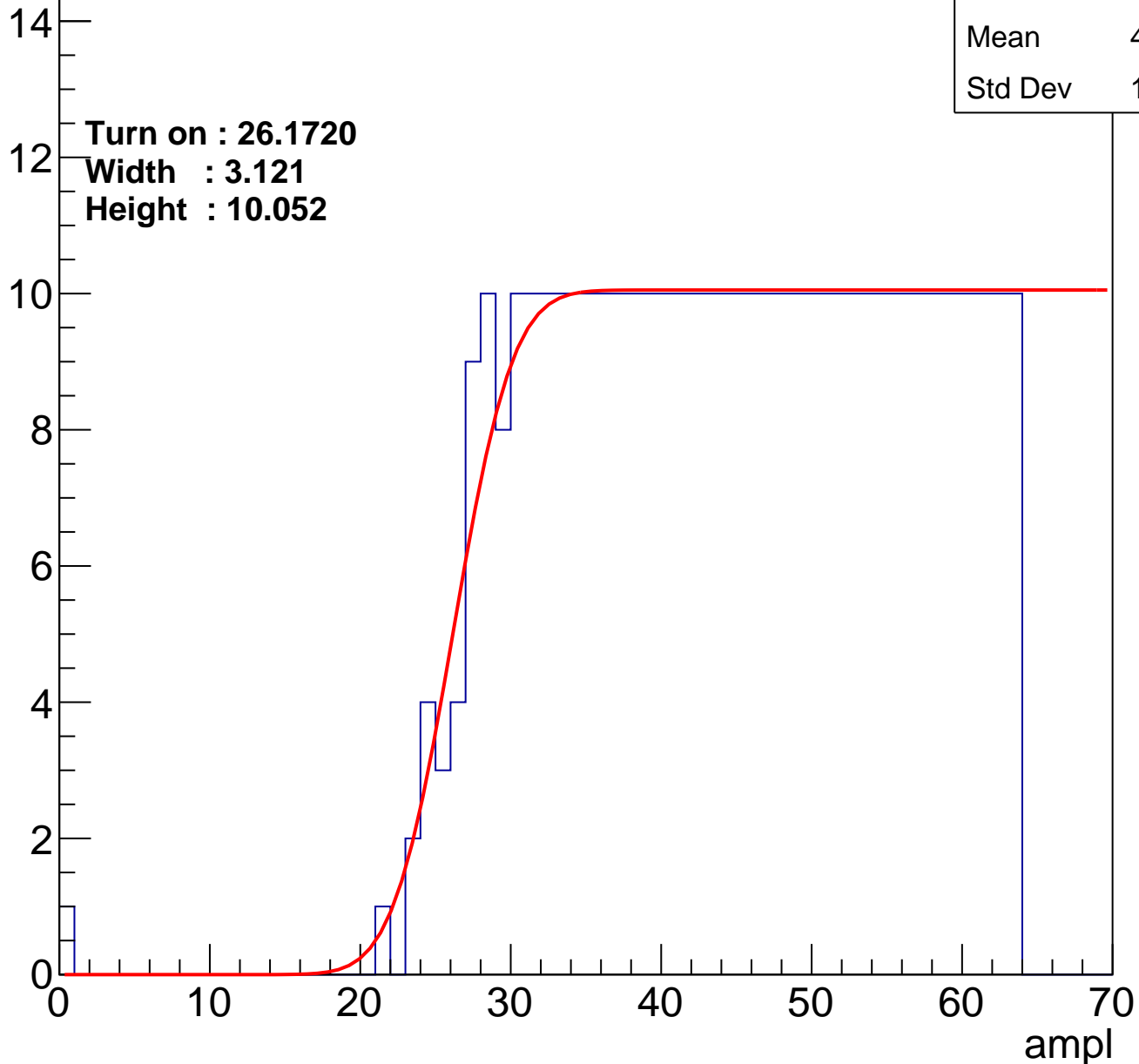
Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 26.1720

Width : 3.121

Height : 10.052

Entry



B1L002S, U10-ch52

calib_packv5_042523_0143.root, FC#10, port B3

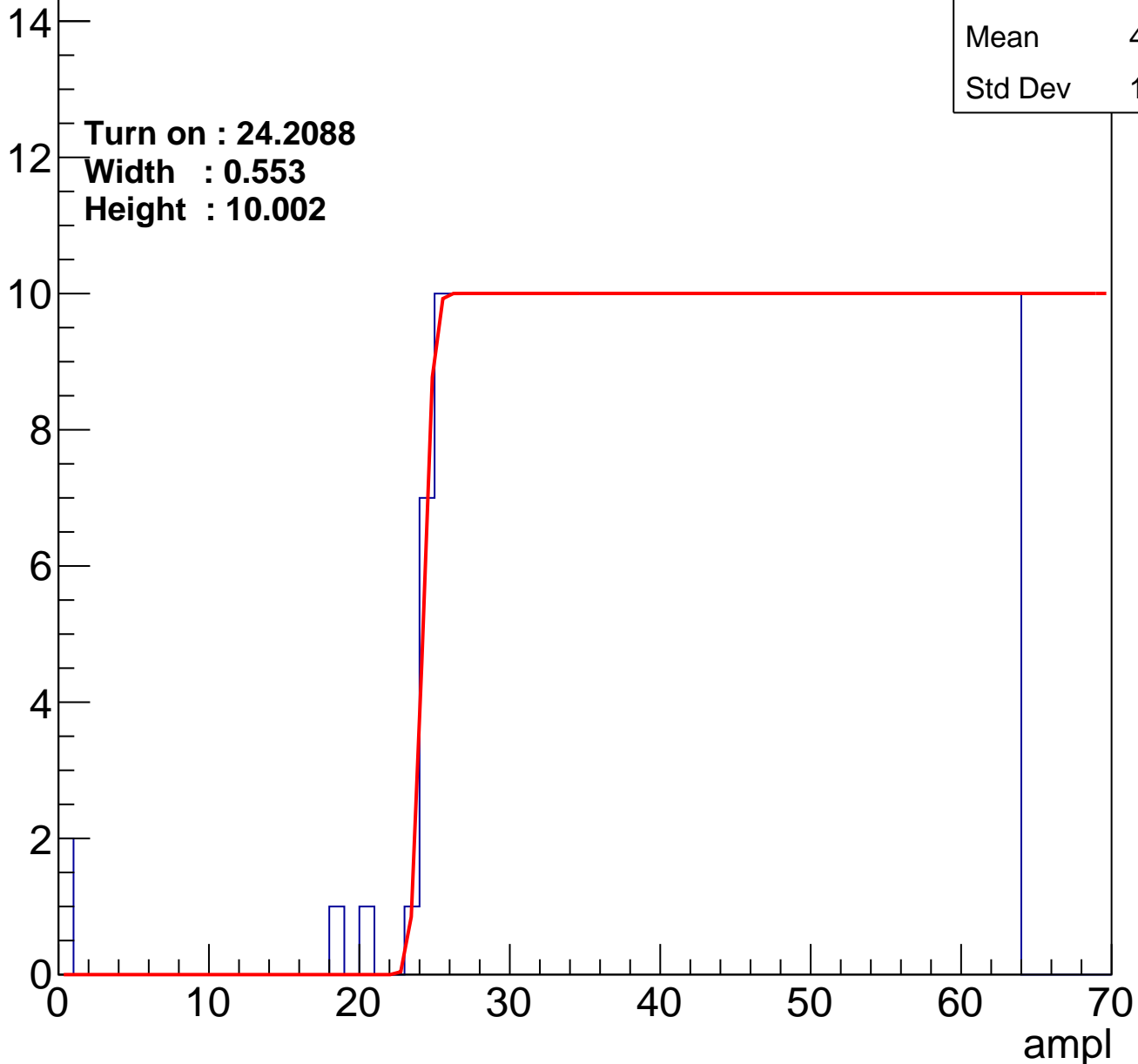
Entries	402
Mean	43.26
Std Dev	11.96

Turn on : 24.2088

Width : 0.553

Height : 10.002

Entry



B1L002S, U10-ch53

calib_packv5_042523_0143.root, FC#10, port B3

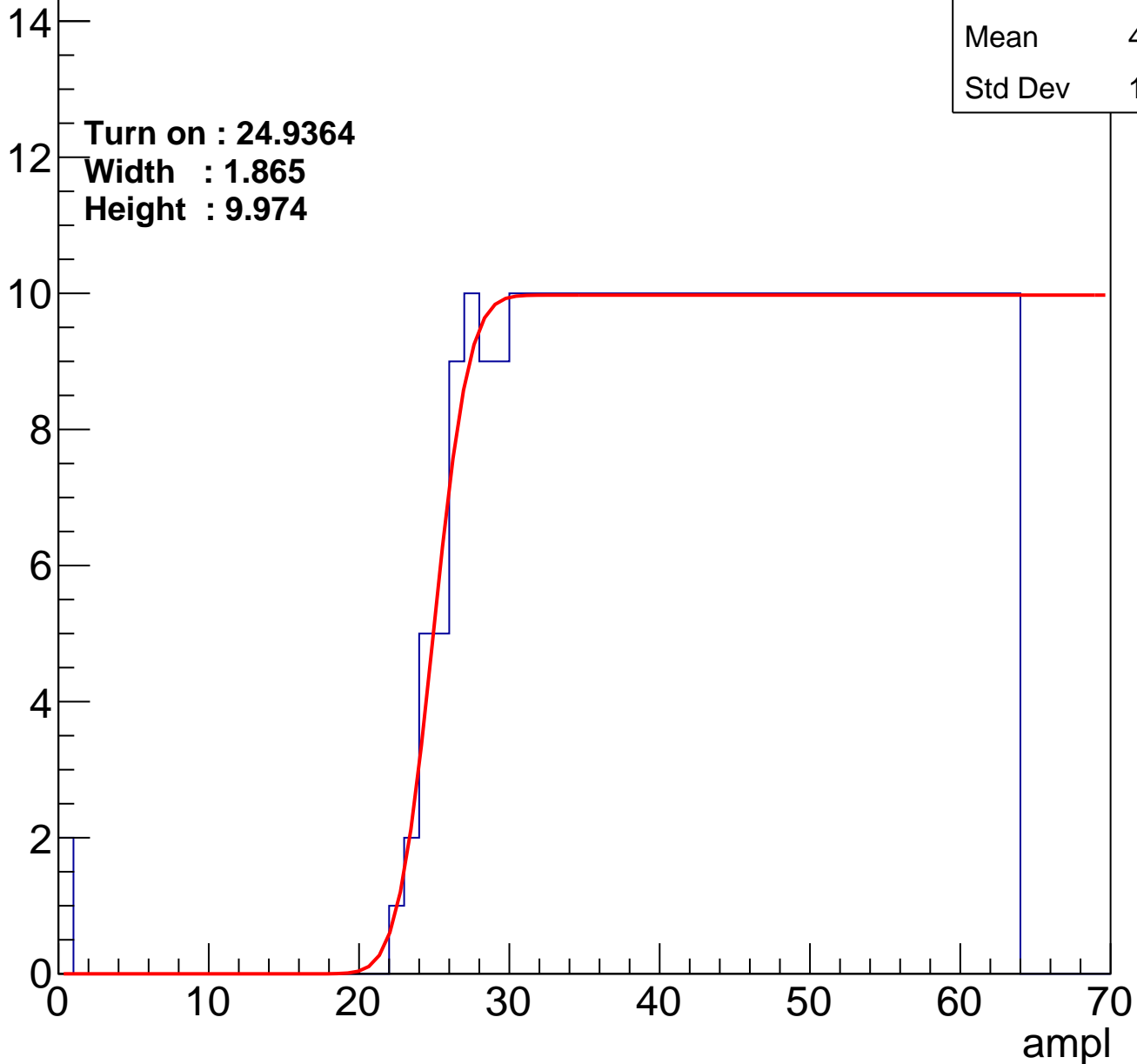
Entries	392
Mean	43.72
Std Dev	11.74

Turn on : 24.9364

Width : 1.865

Height : 9.974

Entry



B1L002S, U10-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 26.9767

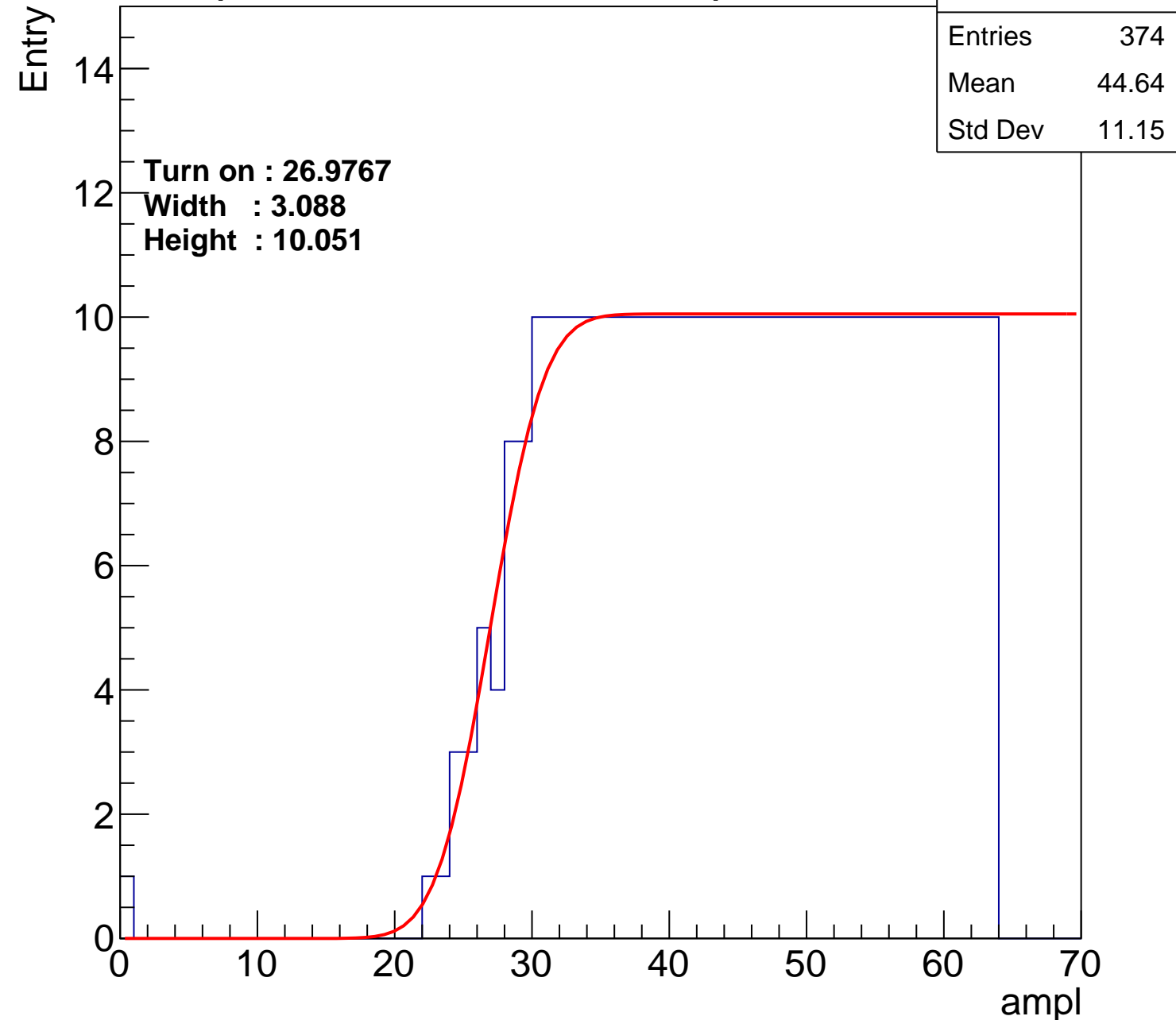
Width : 3.088

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 25.8569

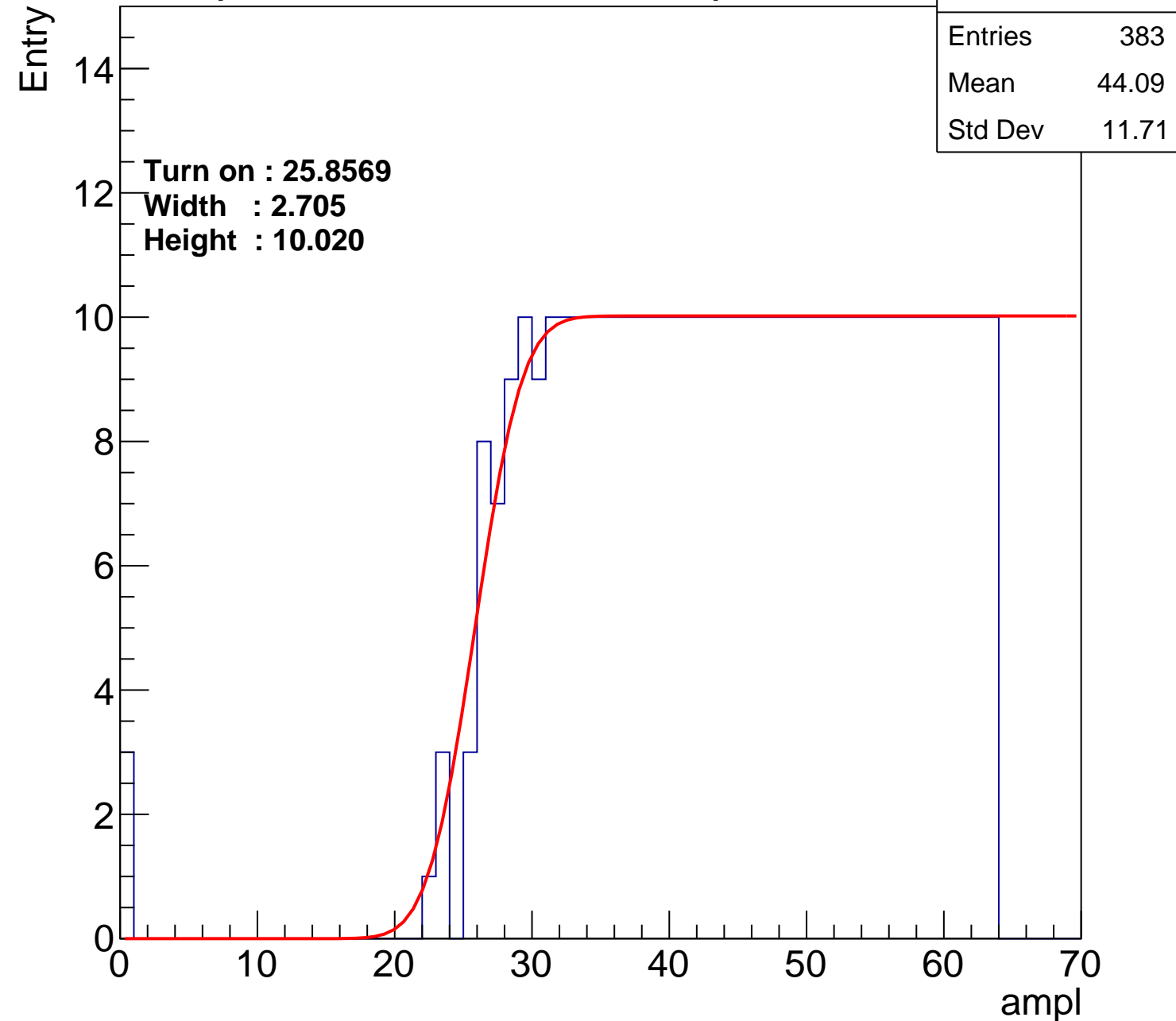
Width : 2.705

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	43.02
Std Dev	12.01

Turn on : 23.4966

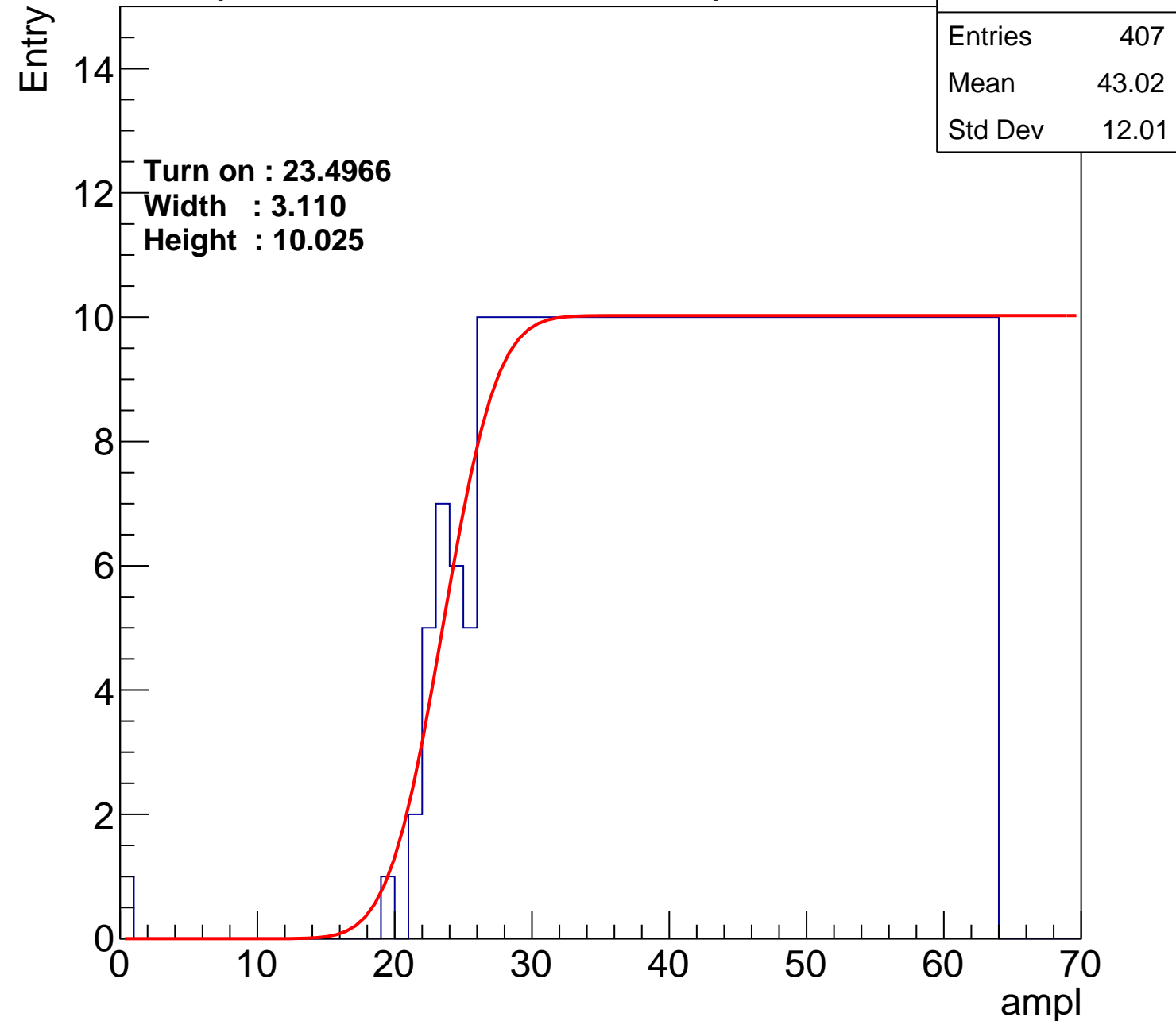
Width : 3.110

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.97
Std Dev	11.61

Turn on : 25.6939

Width : 2.482

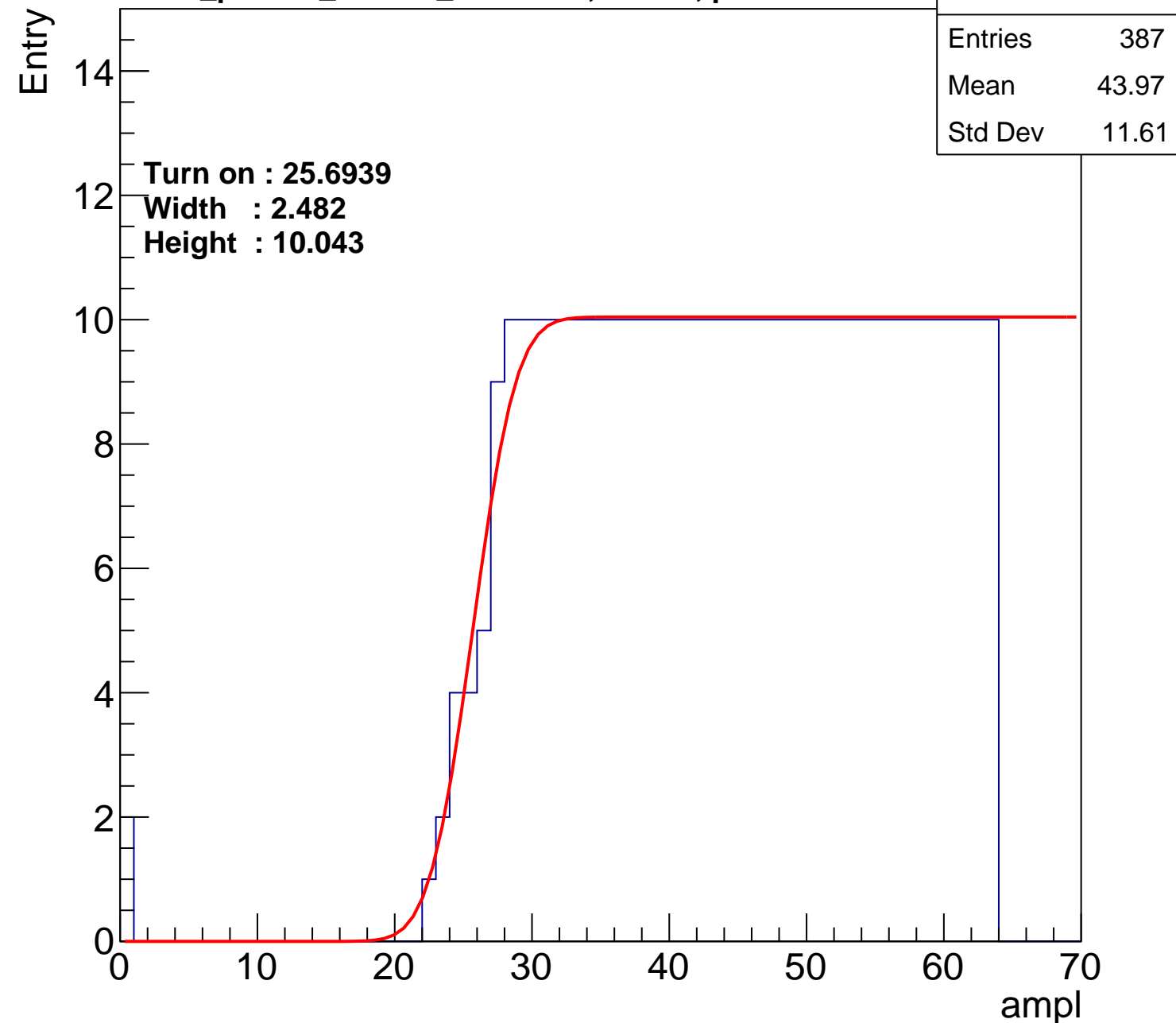
Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch58

calib_packv5_042523_0143.root, FC#10, port B3

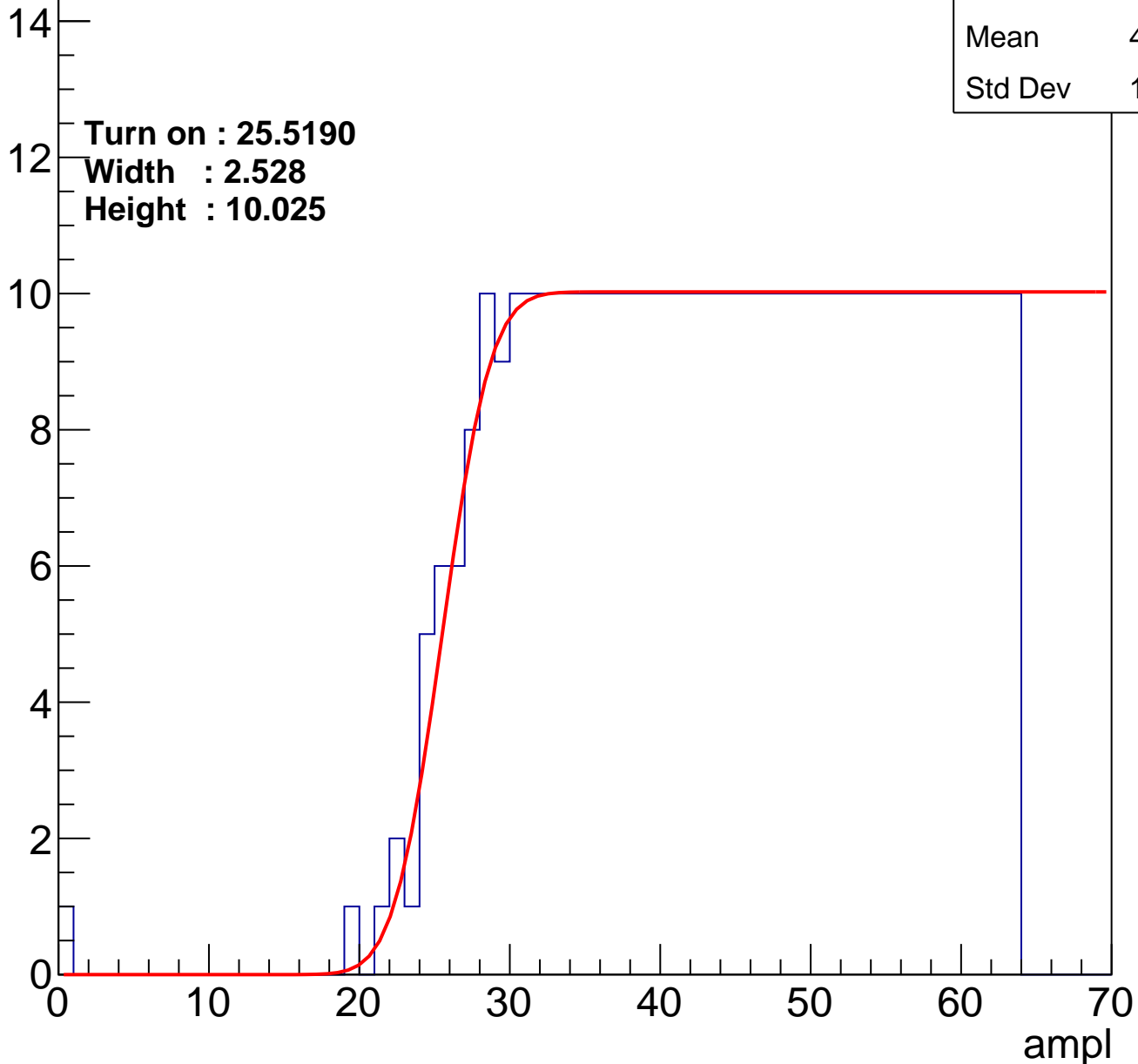
Entries	390
Mean	43.85
Std Dev	11.59

Turn on : 25.5190

Width : 2.528

Height : 10.025

Entry



B1L002S, U10-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.92
Std Dev	11.64

Turn on : 25.8008

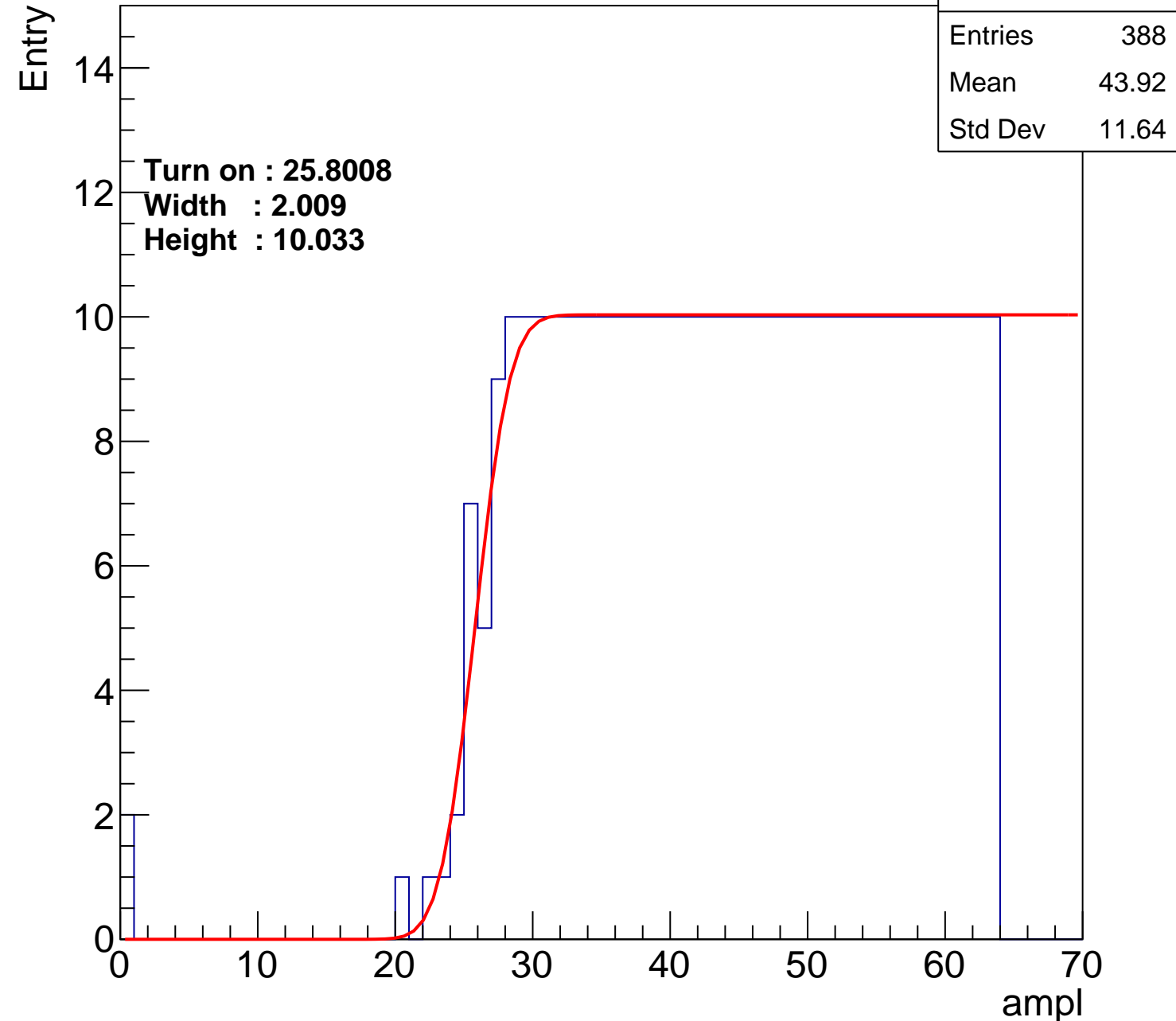
Width : 2.009

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.78

Turn on : 24.9859

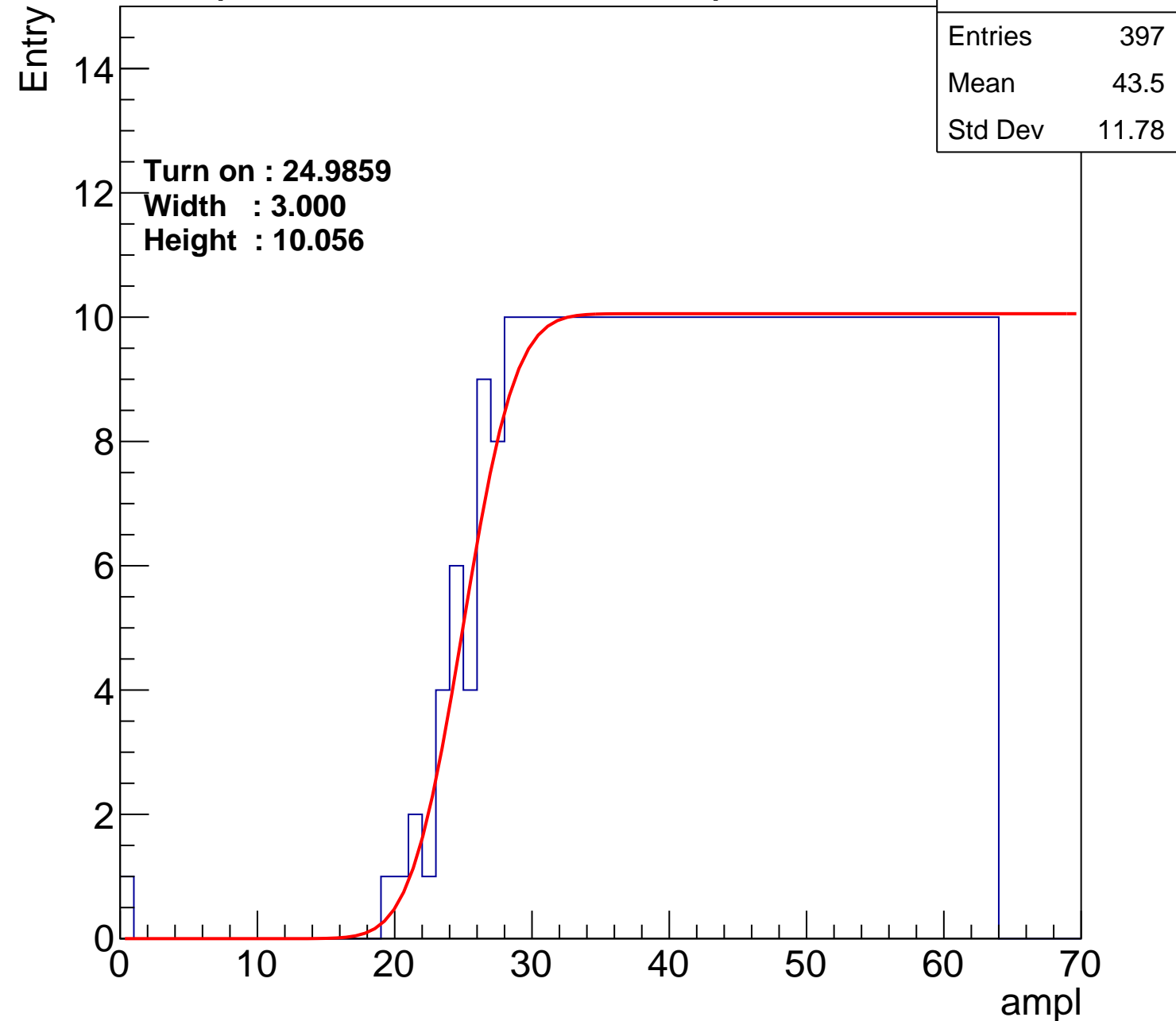
Width : 3.000

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.1488

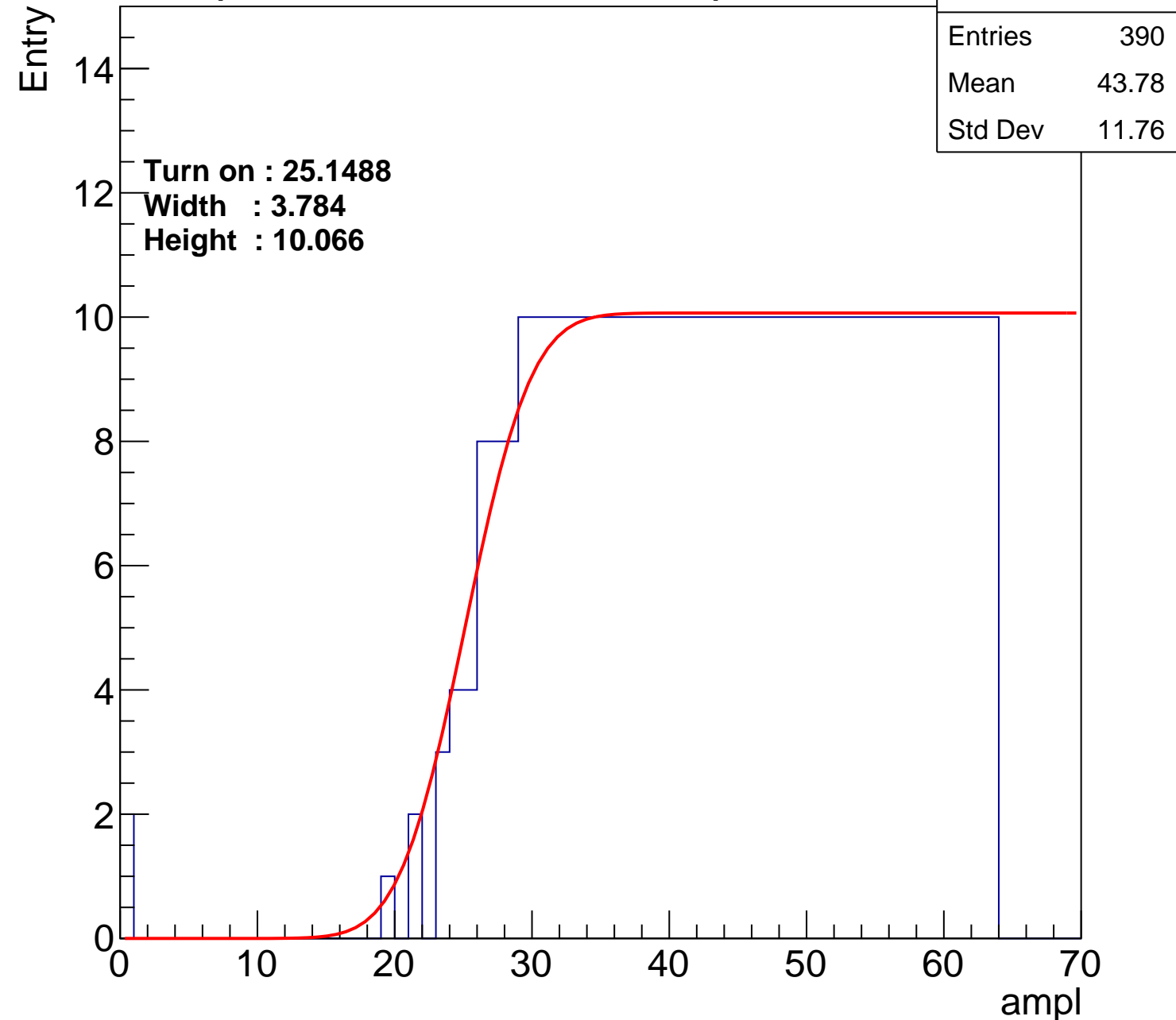
Width : 3.784

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch62

calib_packv5_042523_0143.root, FC#10, port B3

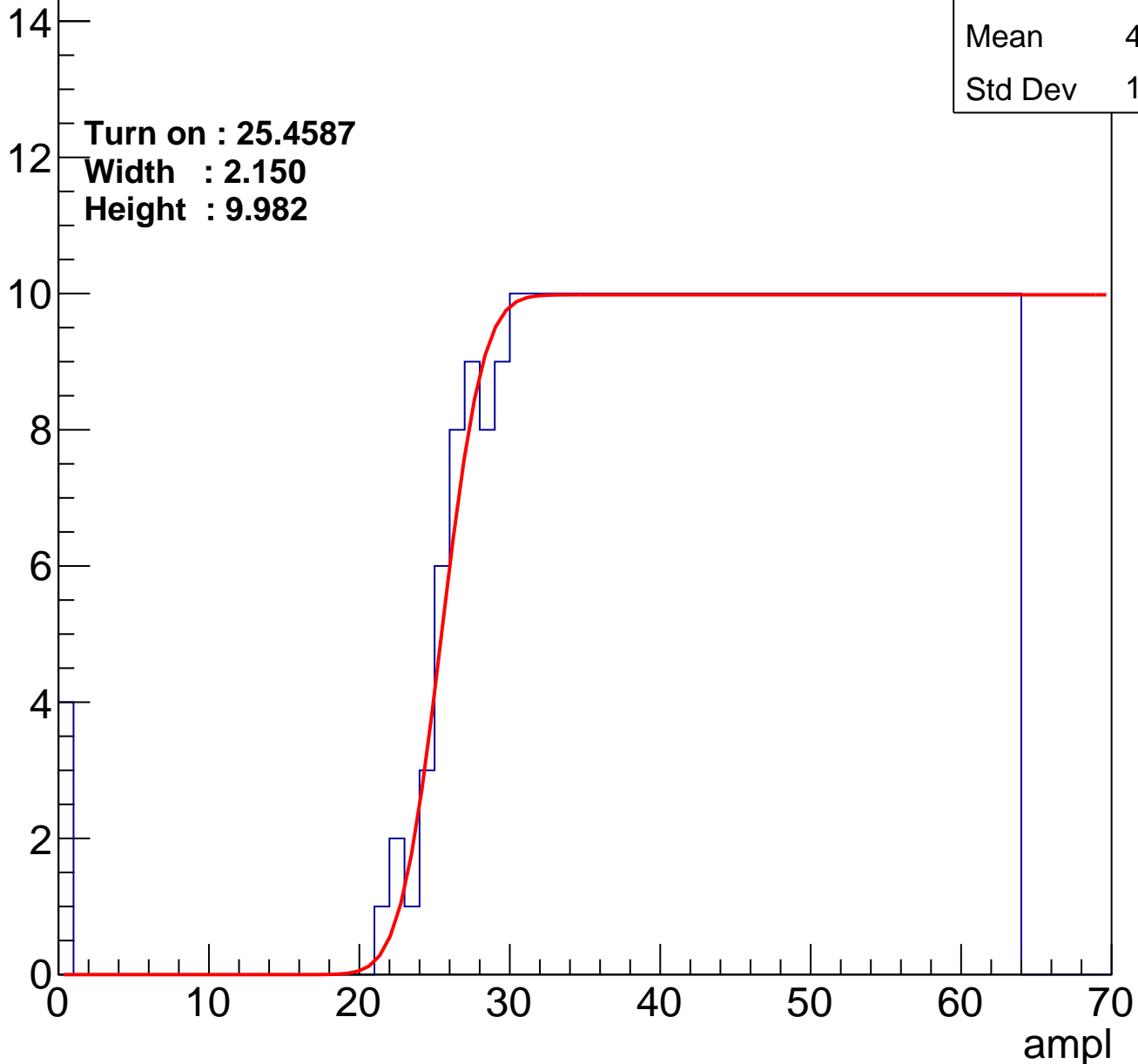
Entries	391
Mean	43.62
Std Dev	12.09

Turn on : 25.4587

Width : 2.150

Height : 9.982

Entry



B1L002S, U10-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.4
Std Dev	11.77

Turn on : 27.6339

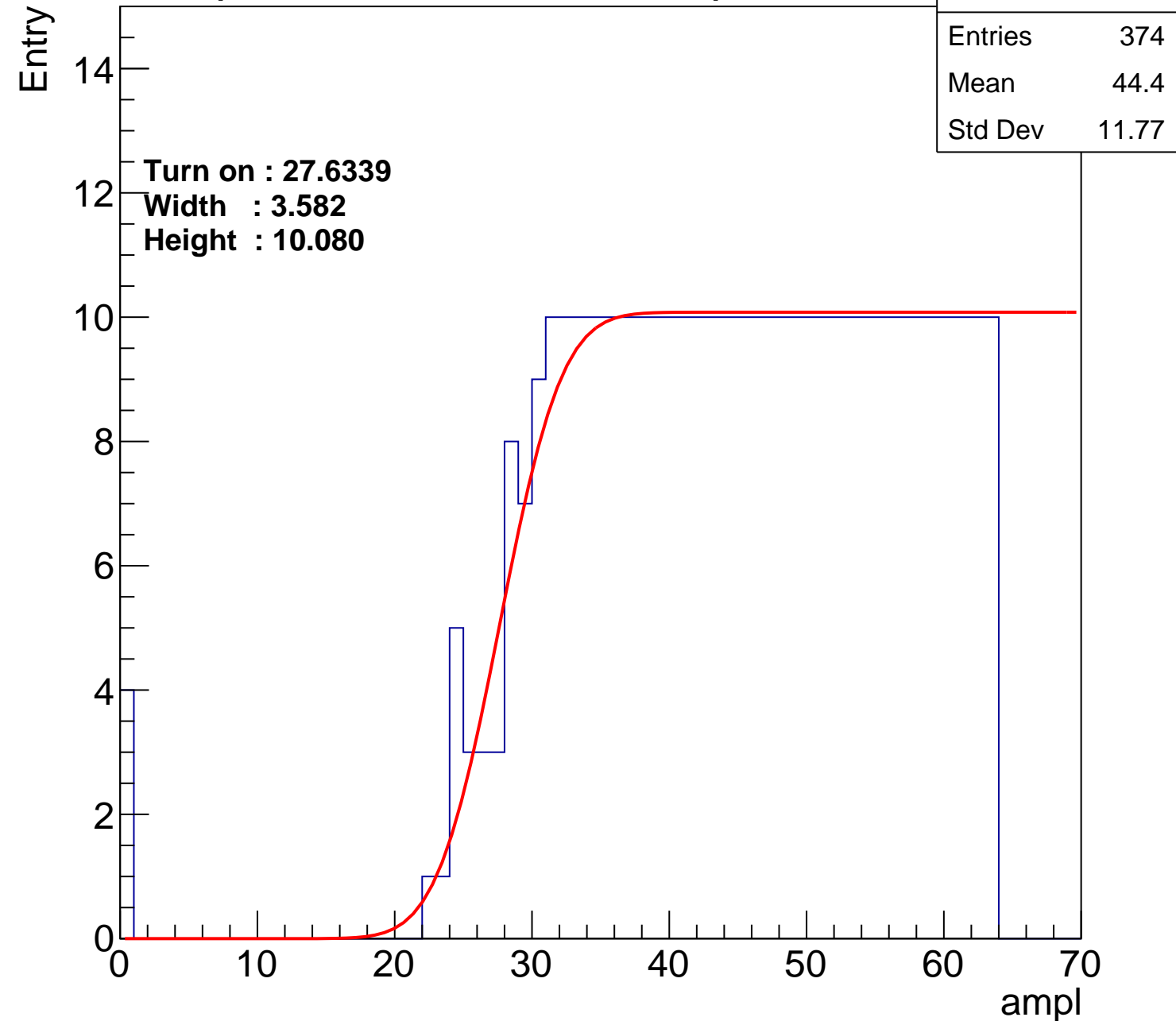
Width : 3.582

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.0534

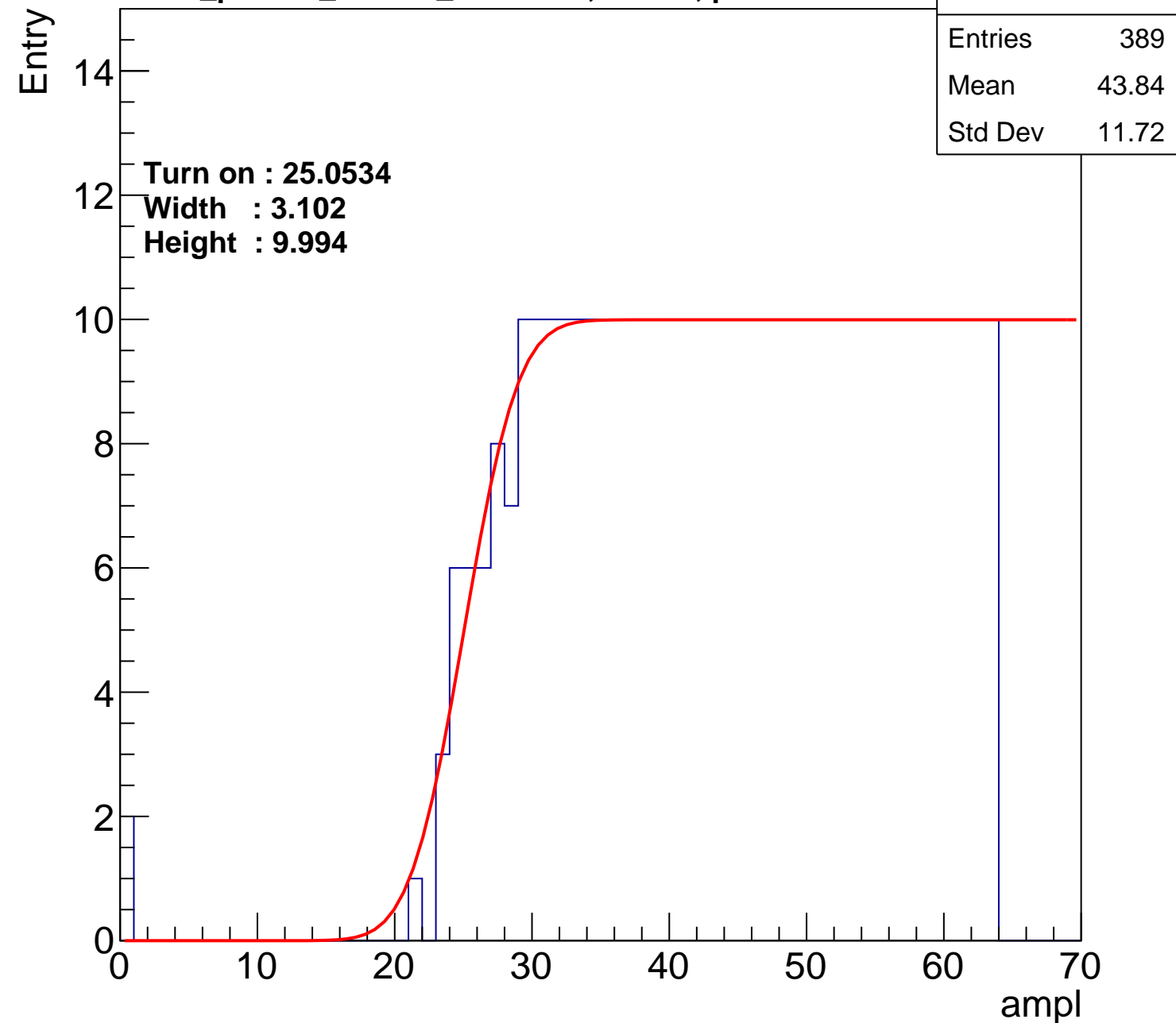
Width : 3.102

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.63
Std Dev	11.11

Turn on : 26.6582

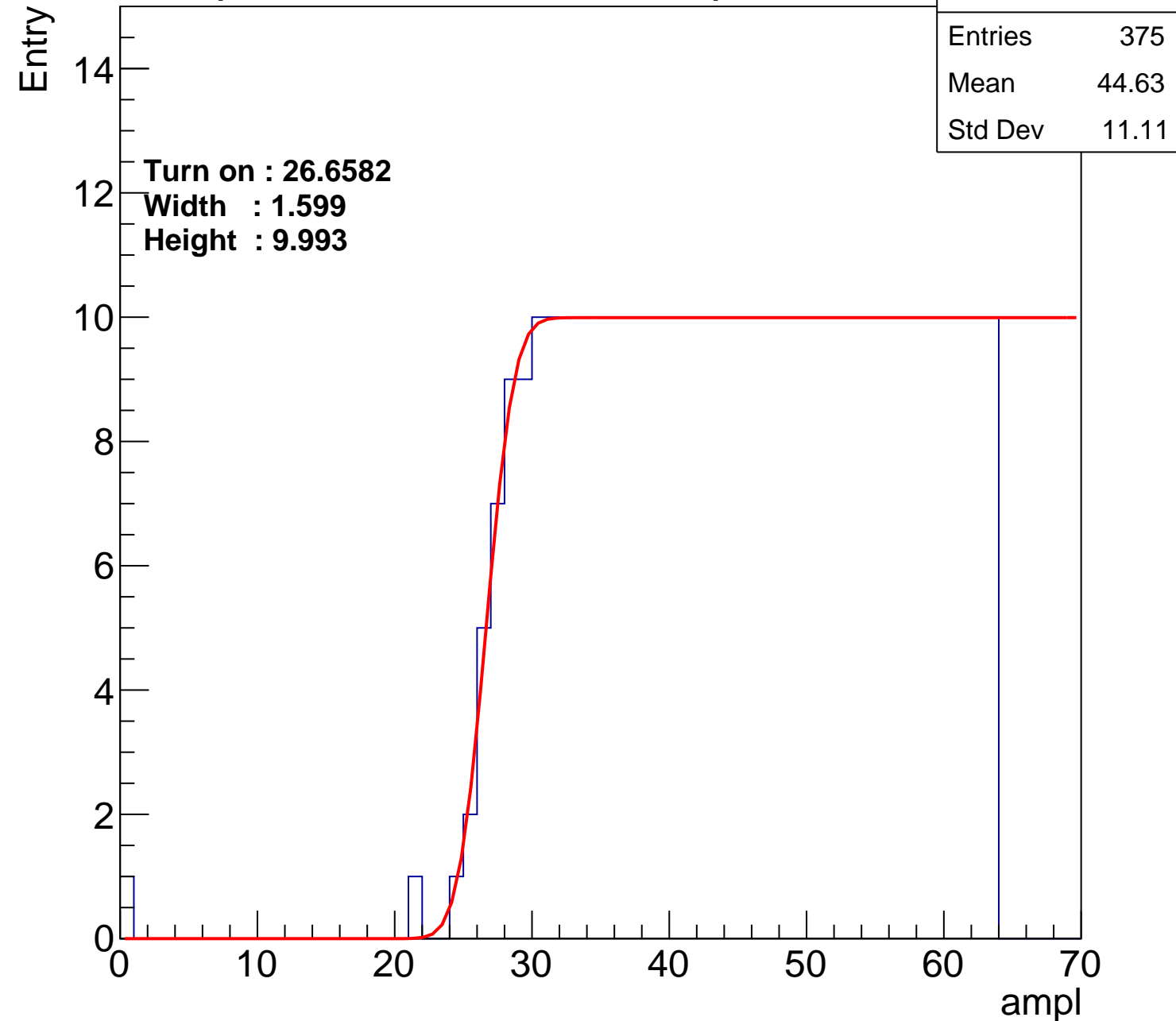
Width : 1.599

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch66

calib_packv5_042523_0143.root, FC#10, port B3

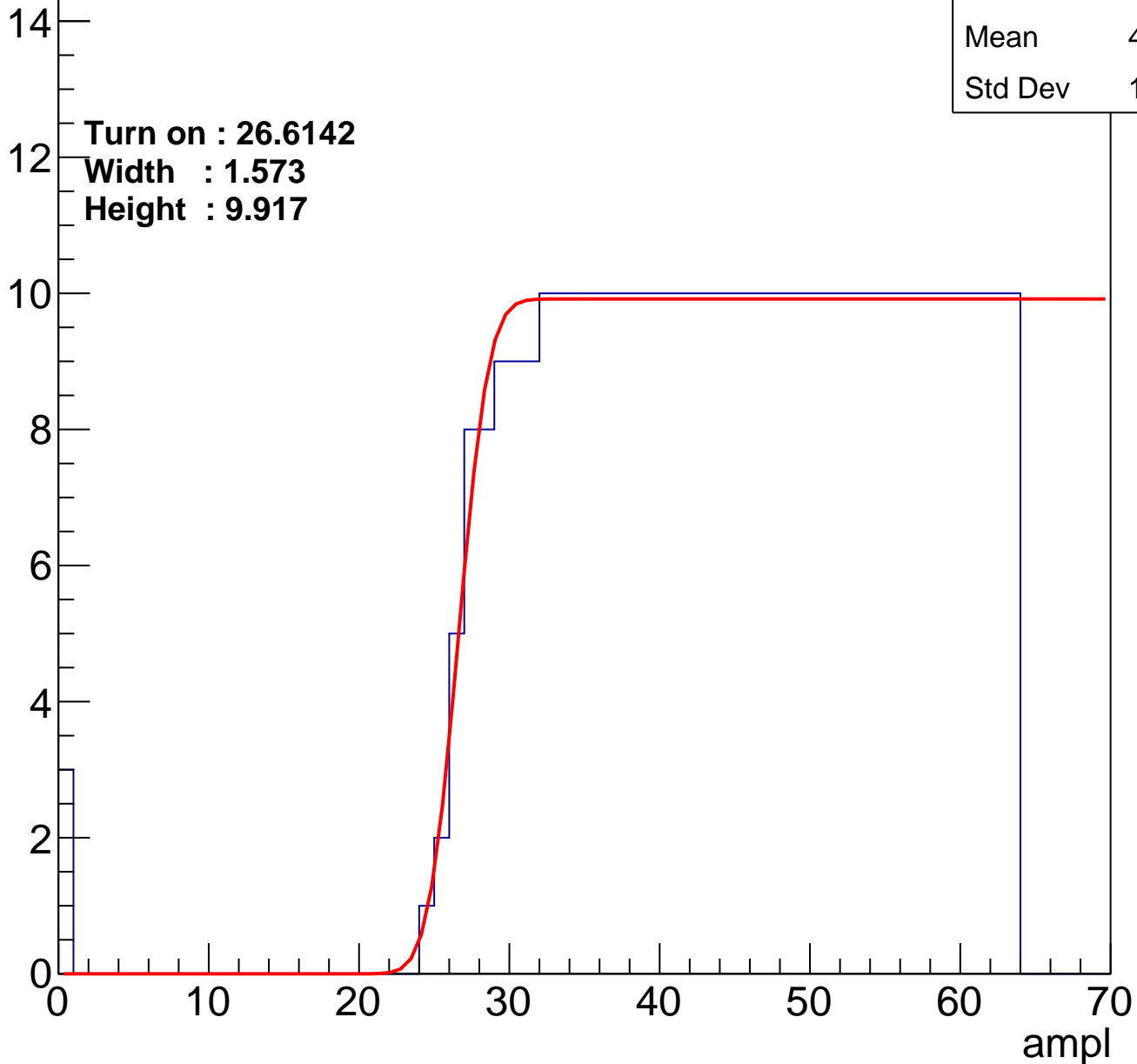
Entries	374
Mean	44.53
Std Dev	11.48

Turn on : 26.6142

Width : 1.573

Height : 9.917

Entry



B1L002S, U10-ch67

calib_packv5_042523_0143.root, FC#10, port B3

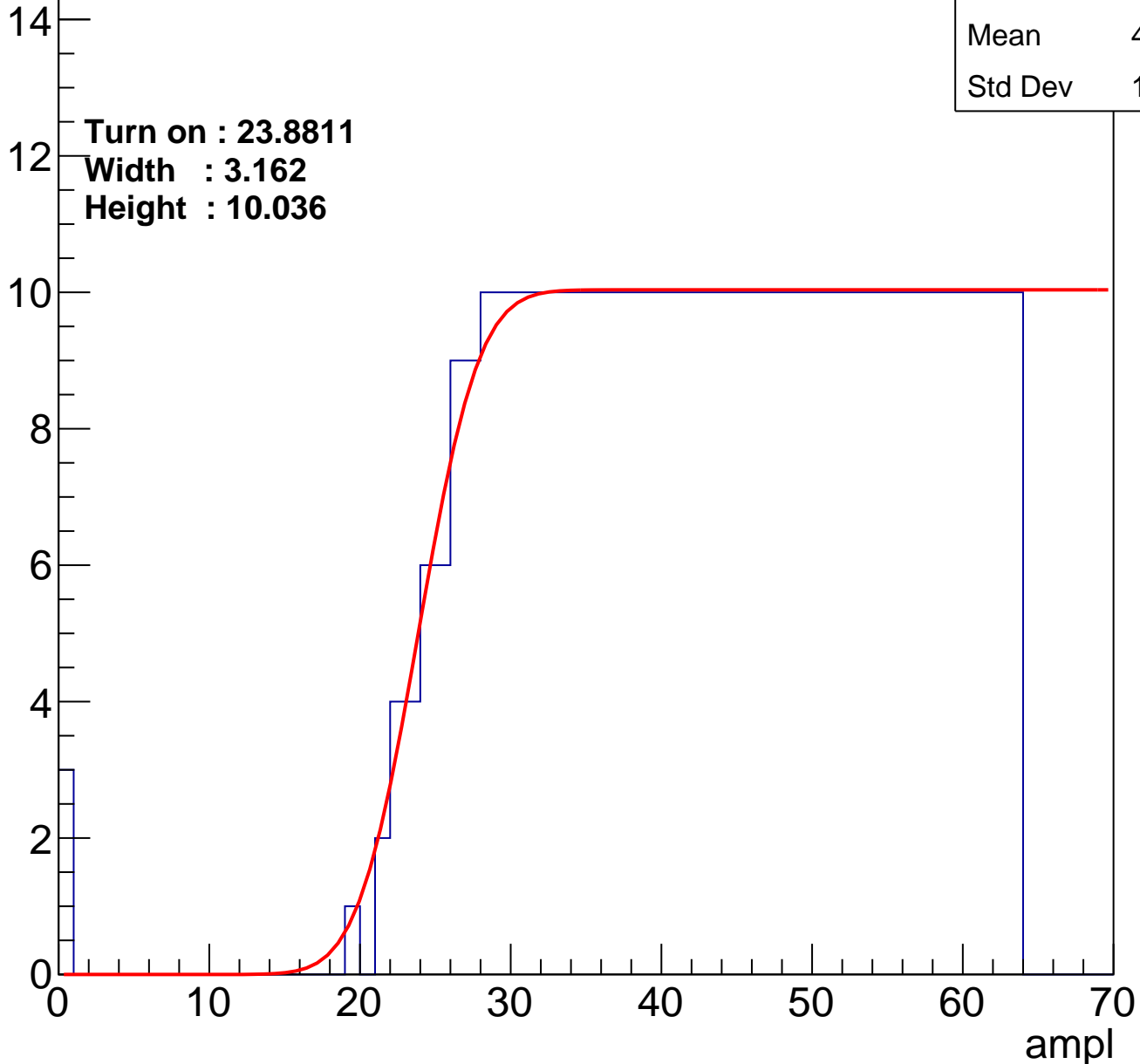
Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 23.8811

Width : 3.162

Height : 10.036

Entry



B1L002S, U10-ch68

calib_packv5_042523_0143.root, FC#10, port B3

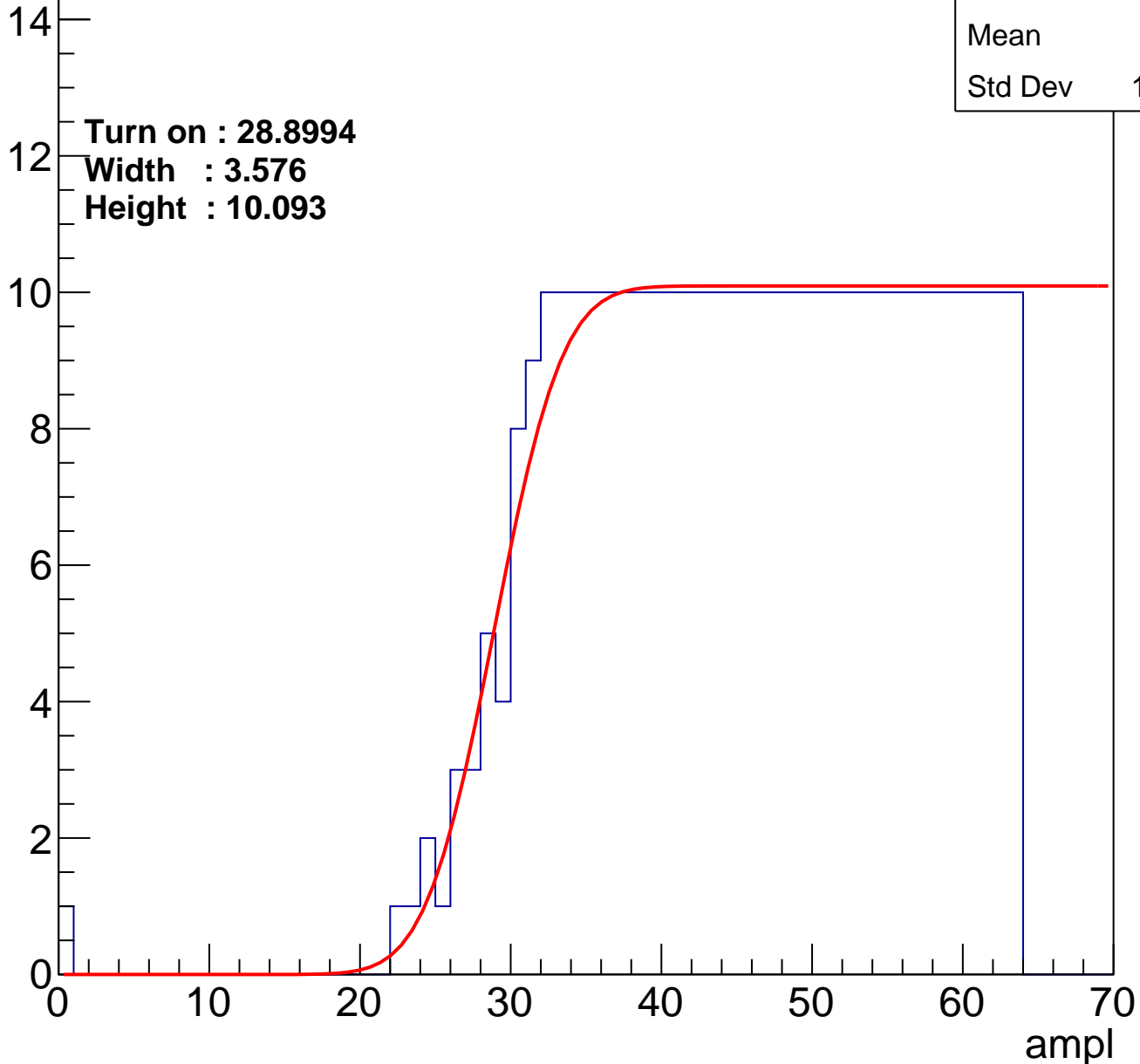
Entries	358
Mean	45.4
Std Dev	10.78

Turn on : 28.8994

Width : 3.576

Height : 10.093

Entry



B1L002S, U10-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.73
Std Dev	11.91

Turn on : 25.0830

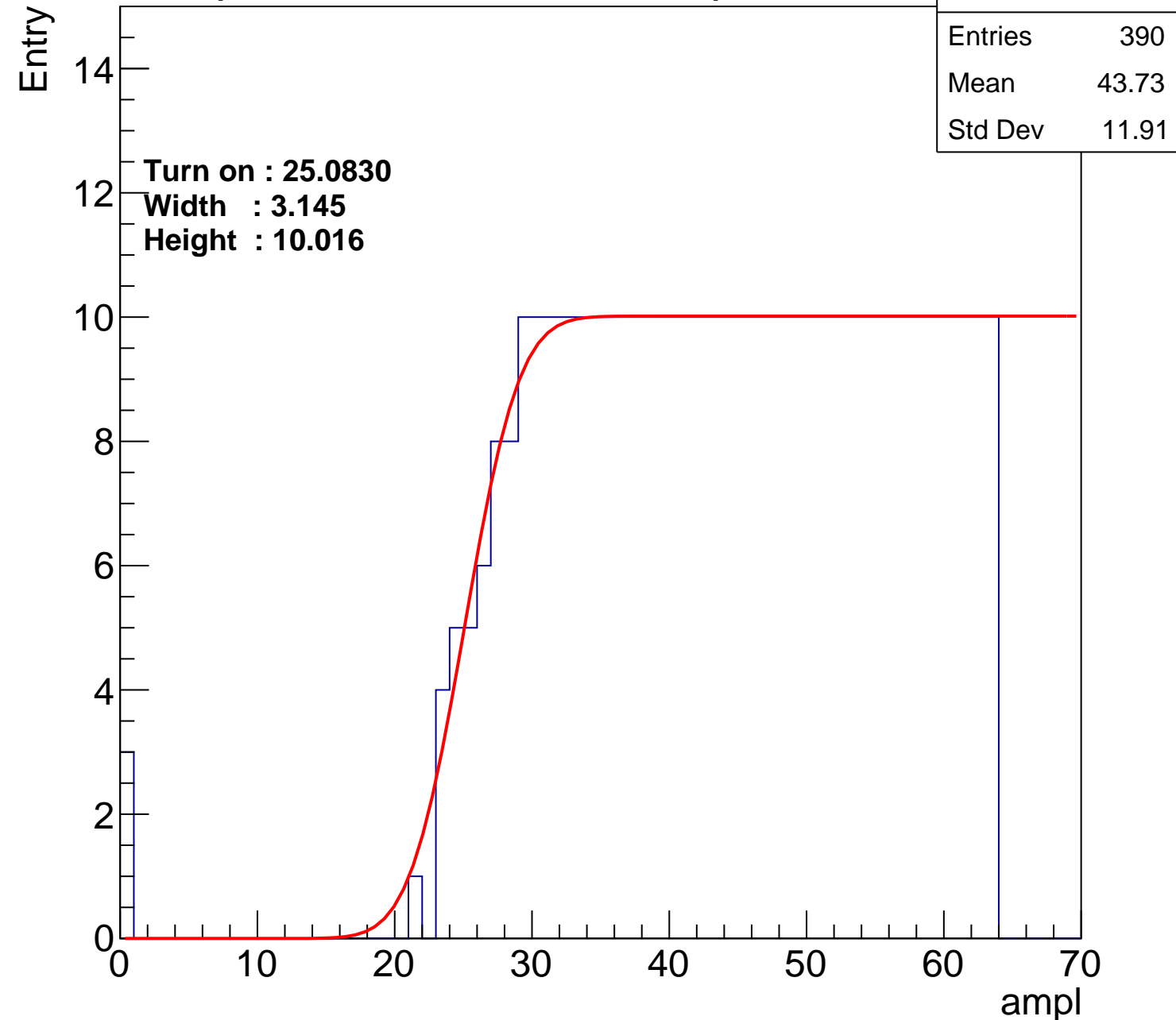
Width : 3.145

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.4
Std Dev	12.22

Turn on : 25.0686

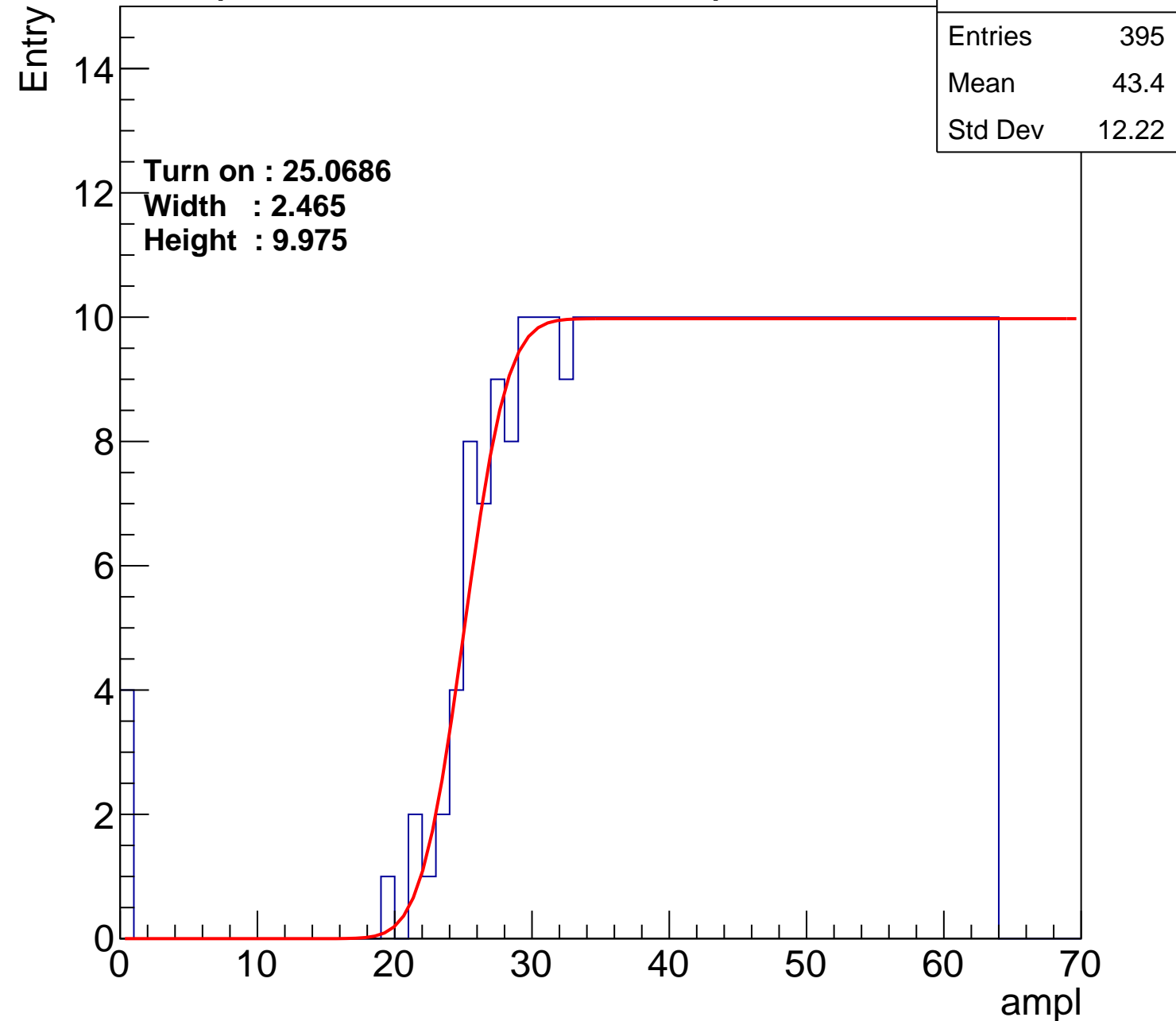
Width : 2.465

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.21
Std Dev	12.04

Turn on : 23.8561

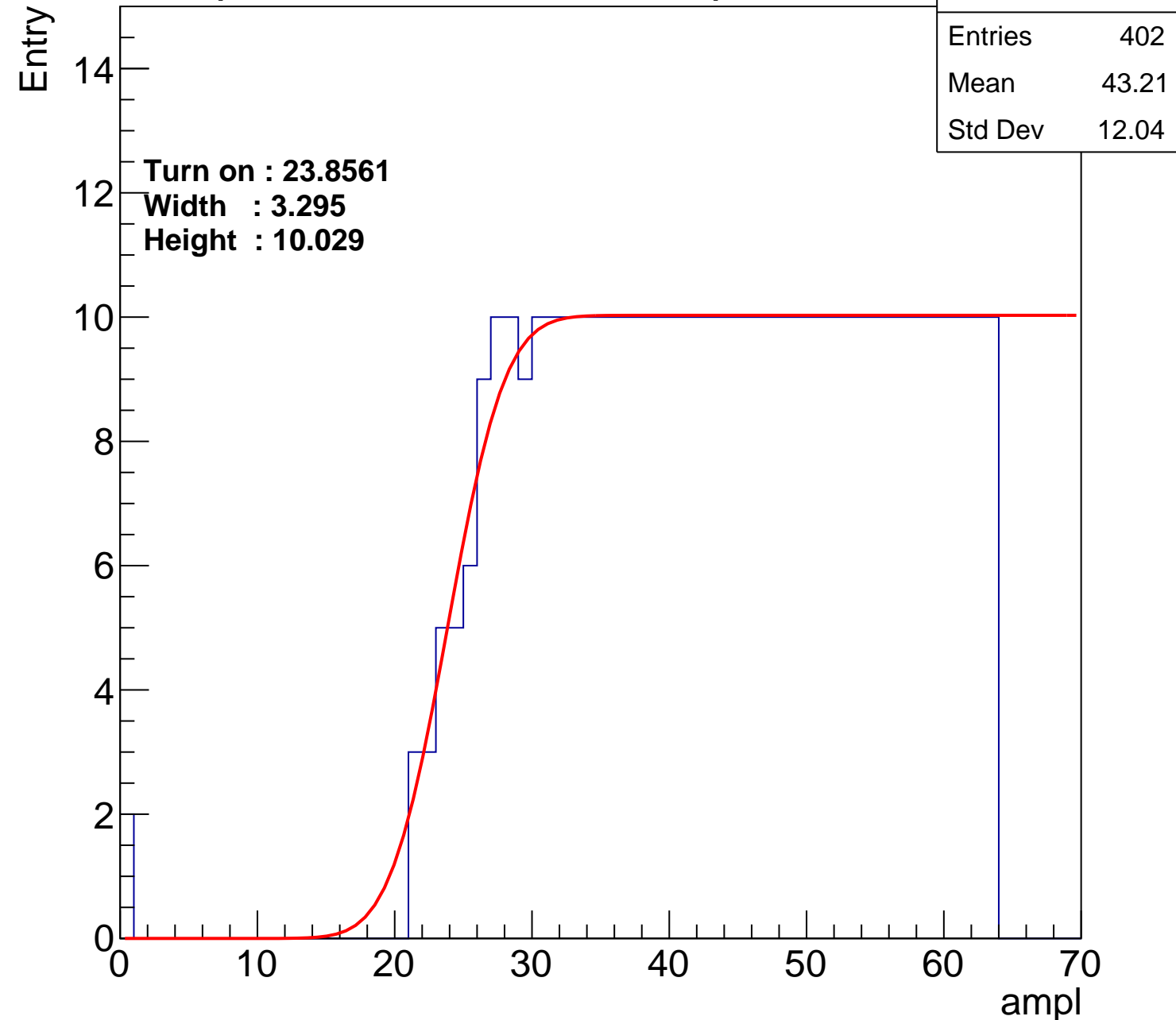
Width : 3.295

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch72

calib_packv5_042523_0143.root, FC#10, port B3

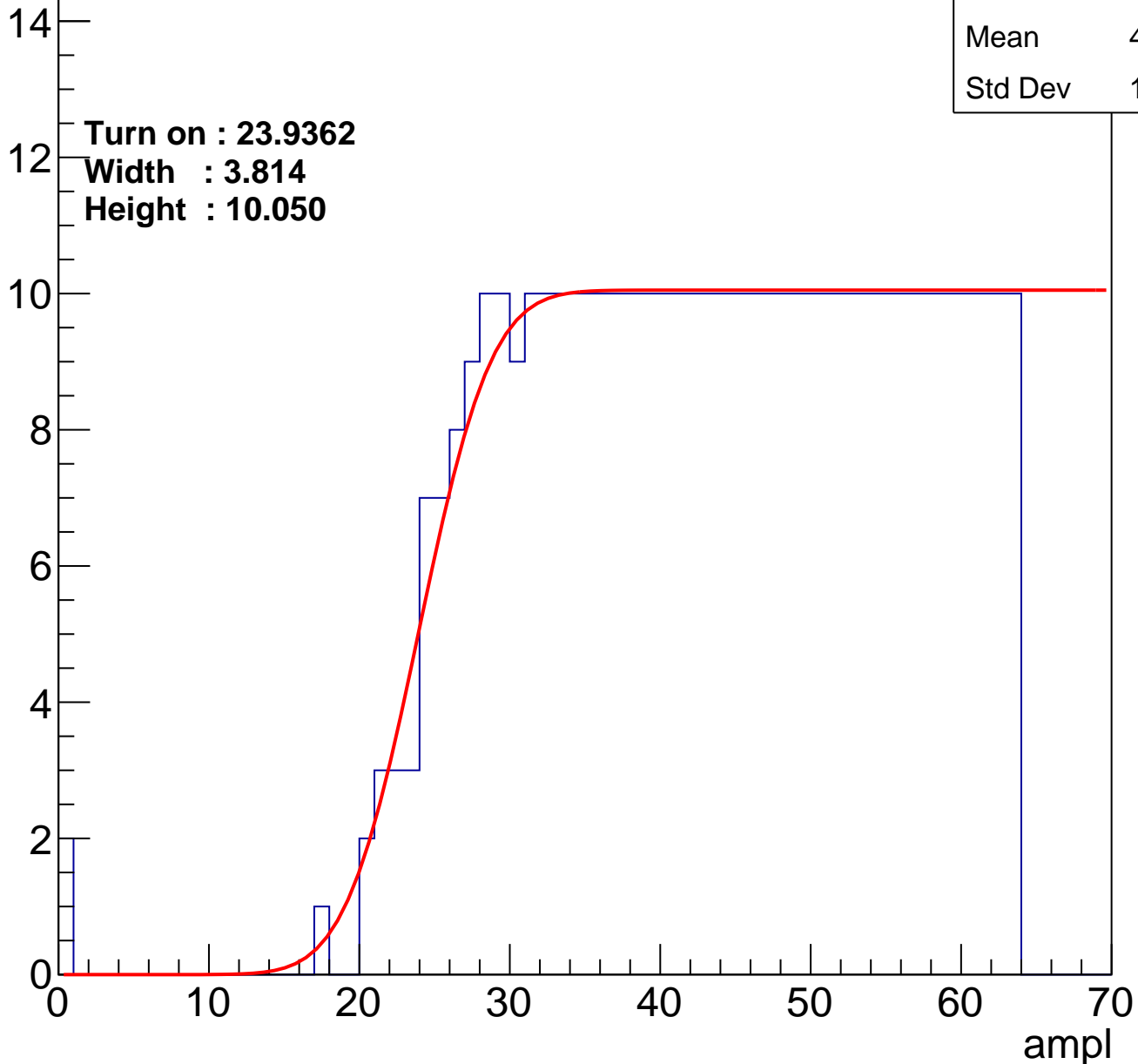
Entries	404
Mean	43.07
Std Dev	12.16

Turn on : 23.9362

Width : 3.814

Height : 10.050

Entry



B1L002S, U10-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.75
Std Dev	11.75

Turn on : 25.2492

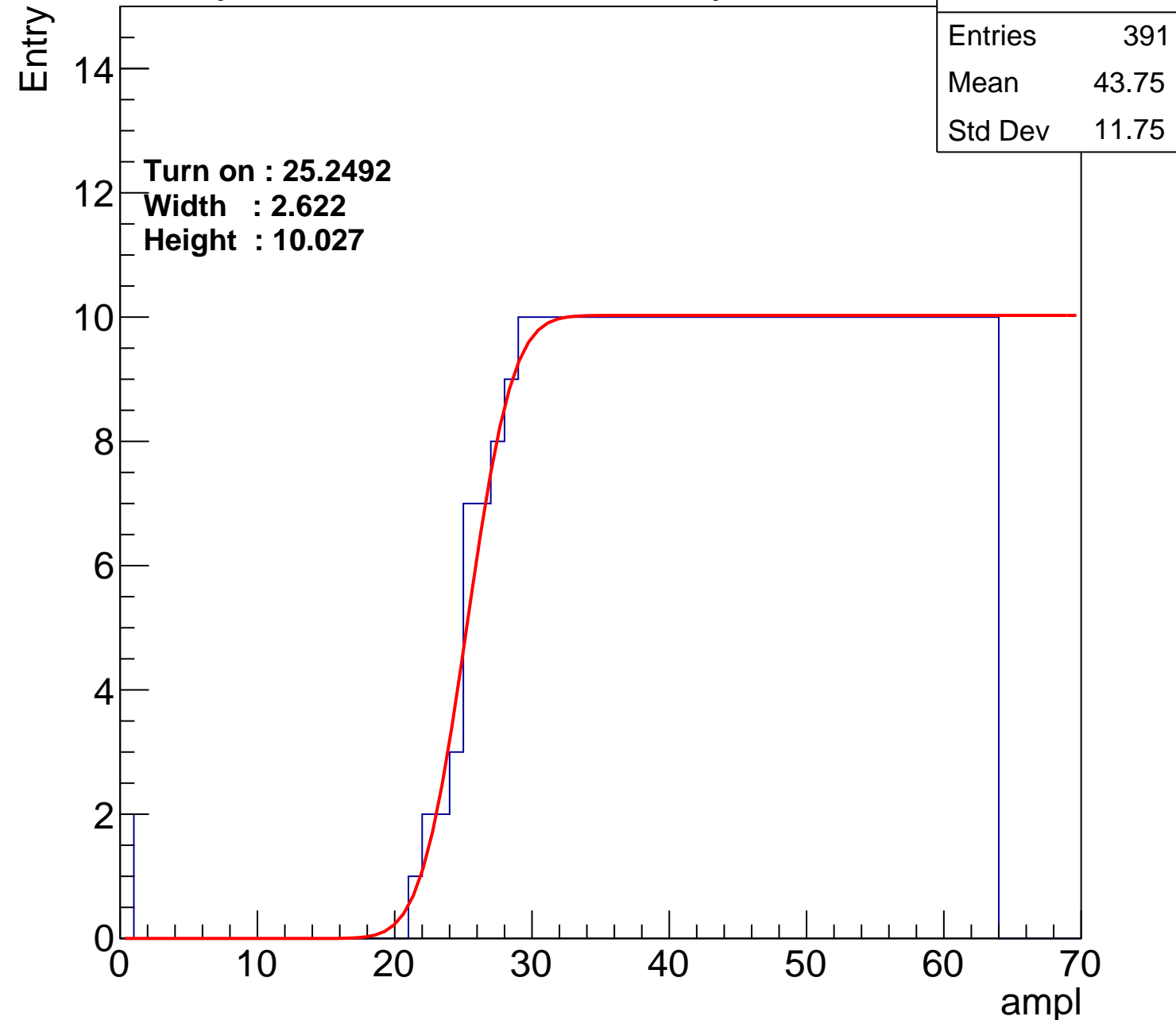
Width : 2.622

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 24.7158

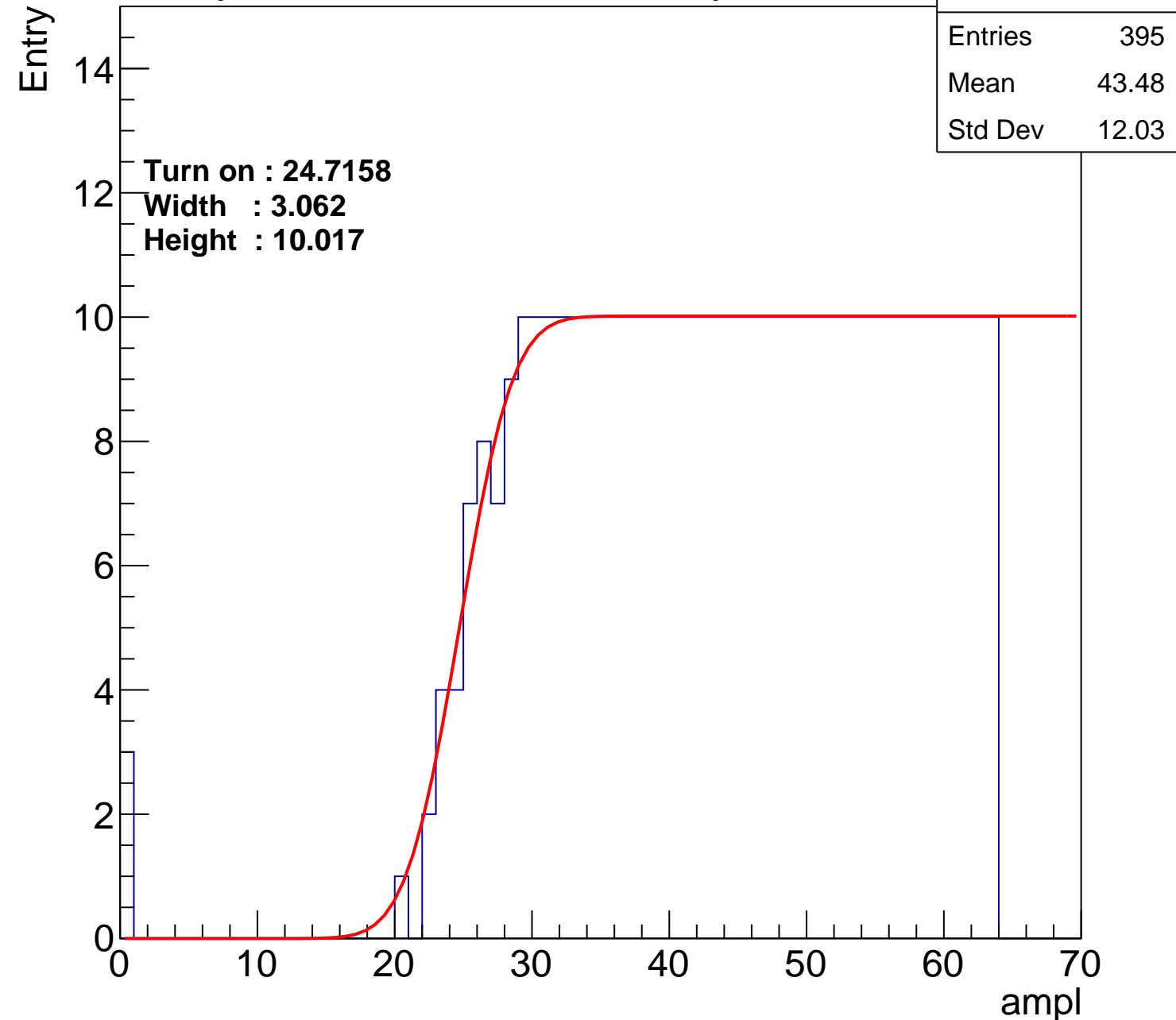
Width : 3.062

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.05
Std Dev	11.88

Turn on : 26.1039

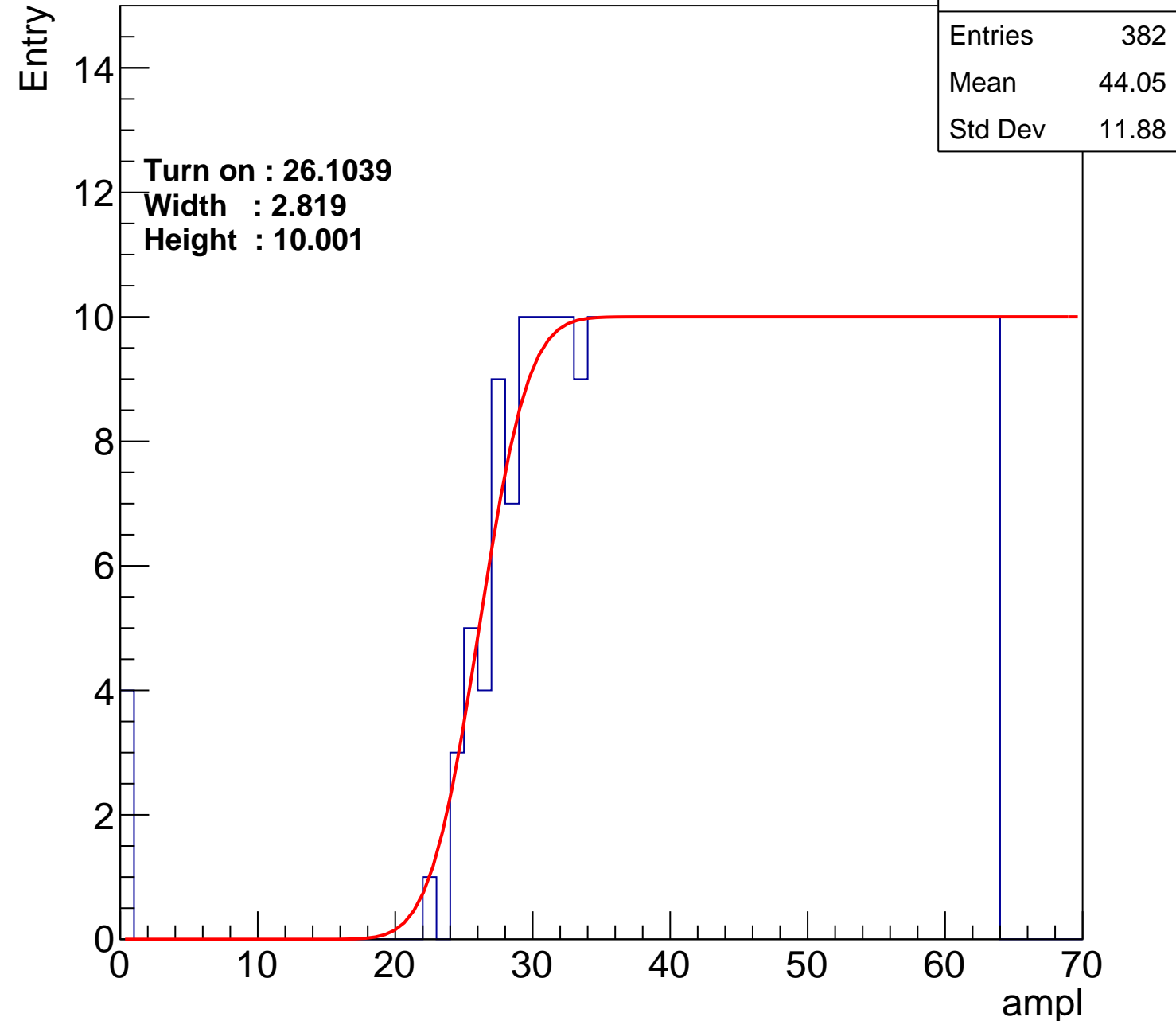
Width : 2.819

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch76

calib_packv5_042523_0143.root, FC#10, port B3

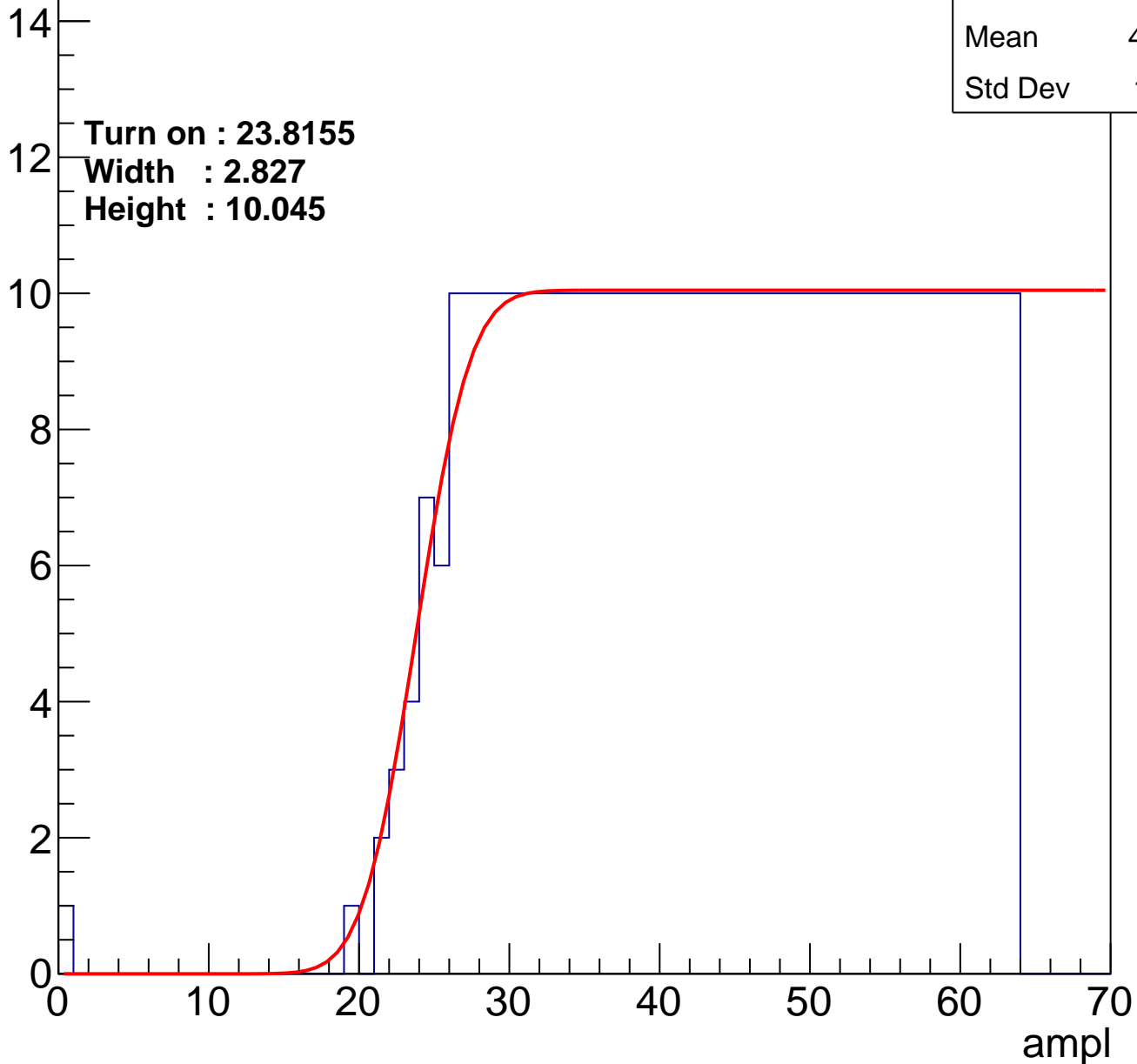
Entries	404
Mean	43.19
Std Dev	11.91

Turn on : 23.8155

Width : 2.827

Height : 10.045

Entry



B1L002S, U10-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.72
Std Dev	11.67

Turn on : 24.8794

Width : 2.741

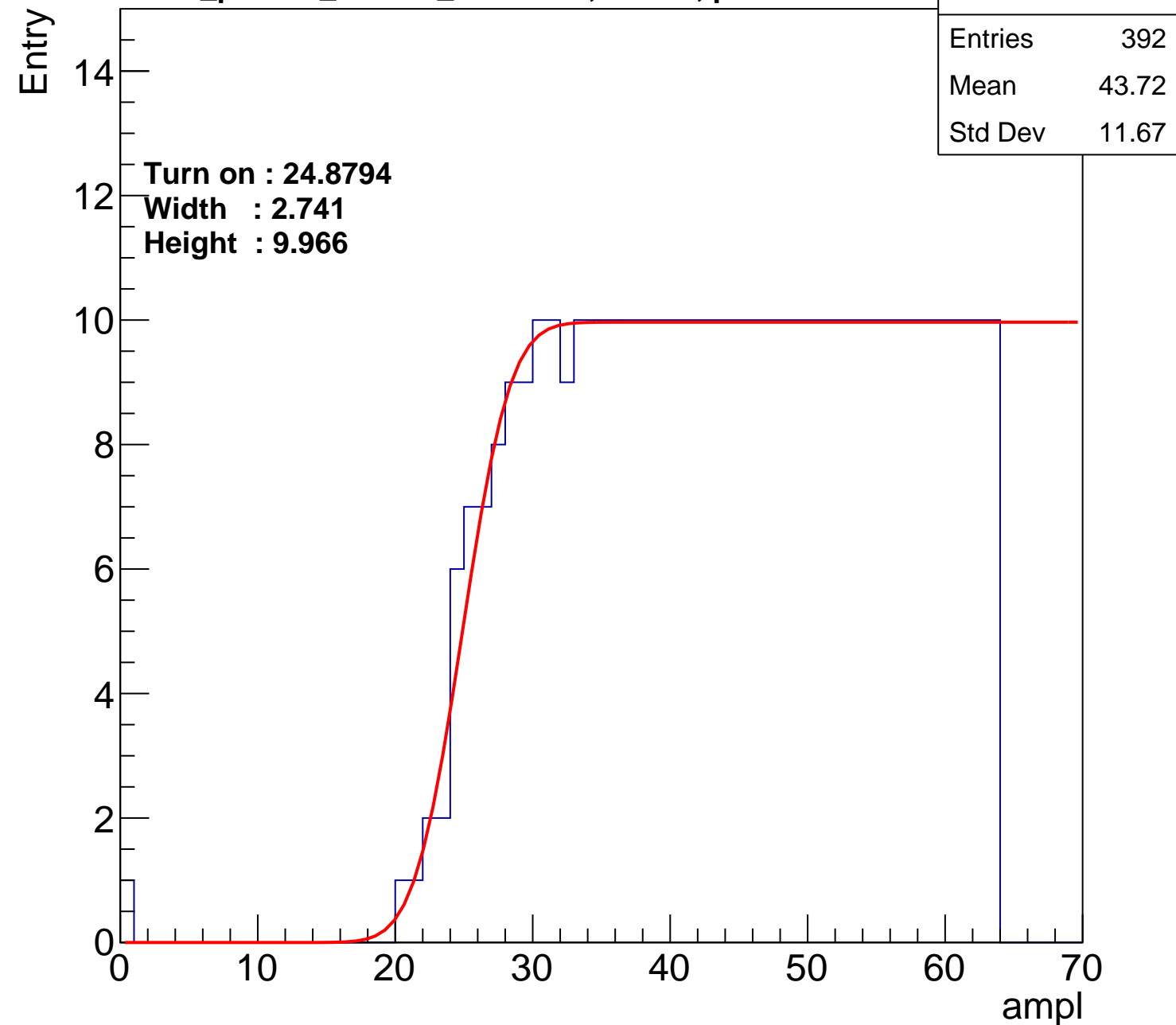
Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.26
Std Dev	11.95

Turn on : 27.0227

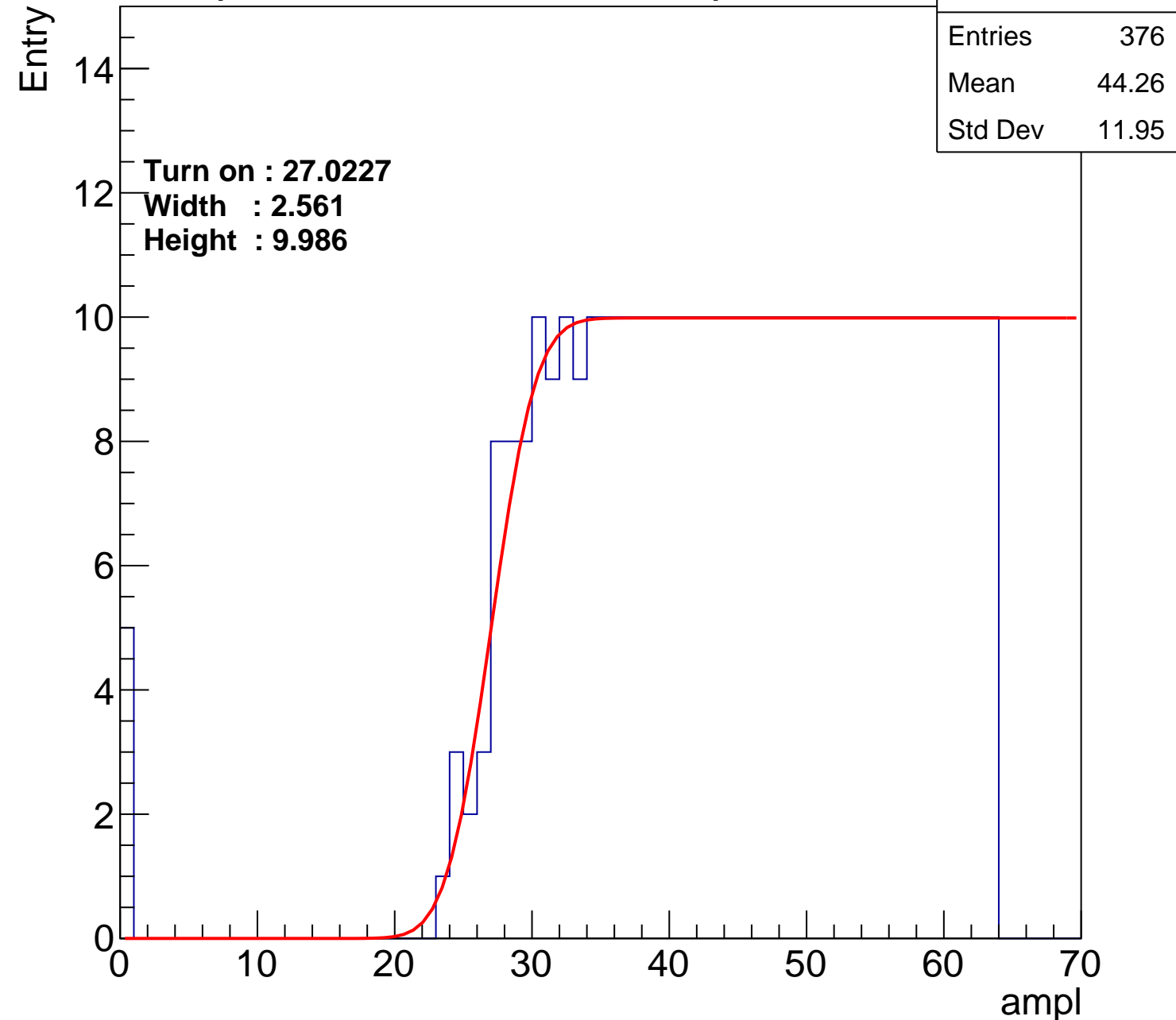
Width : 2.561

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.57
Std Dev	11.76

Turn on : 24.5748

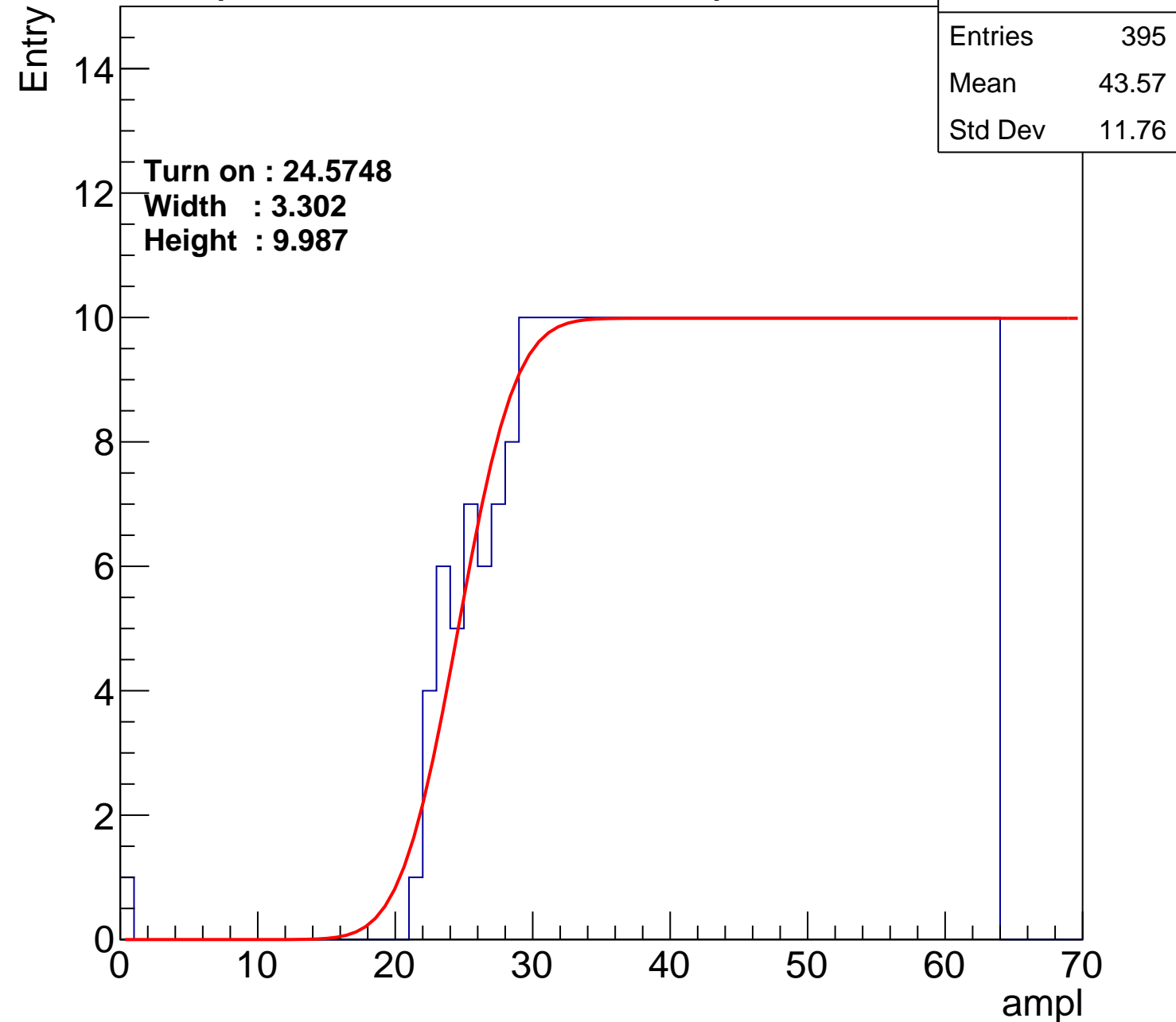
Width : 3.302

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.4
Std Dev	11.72

Turn on : 27.3403

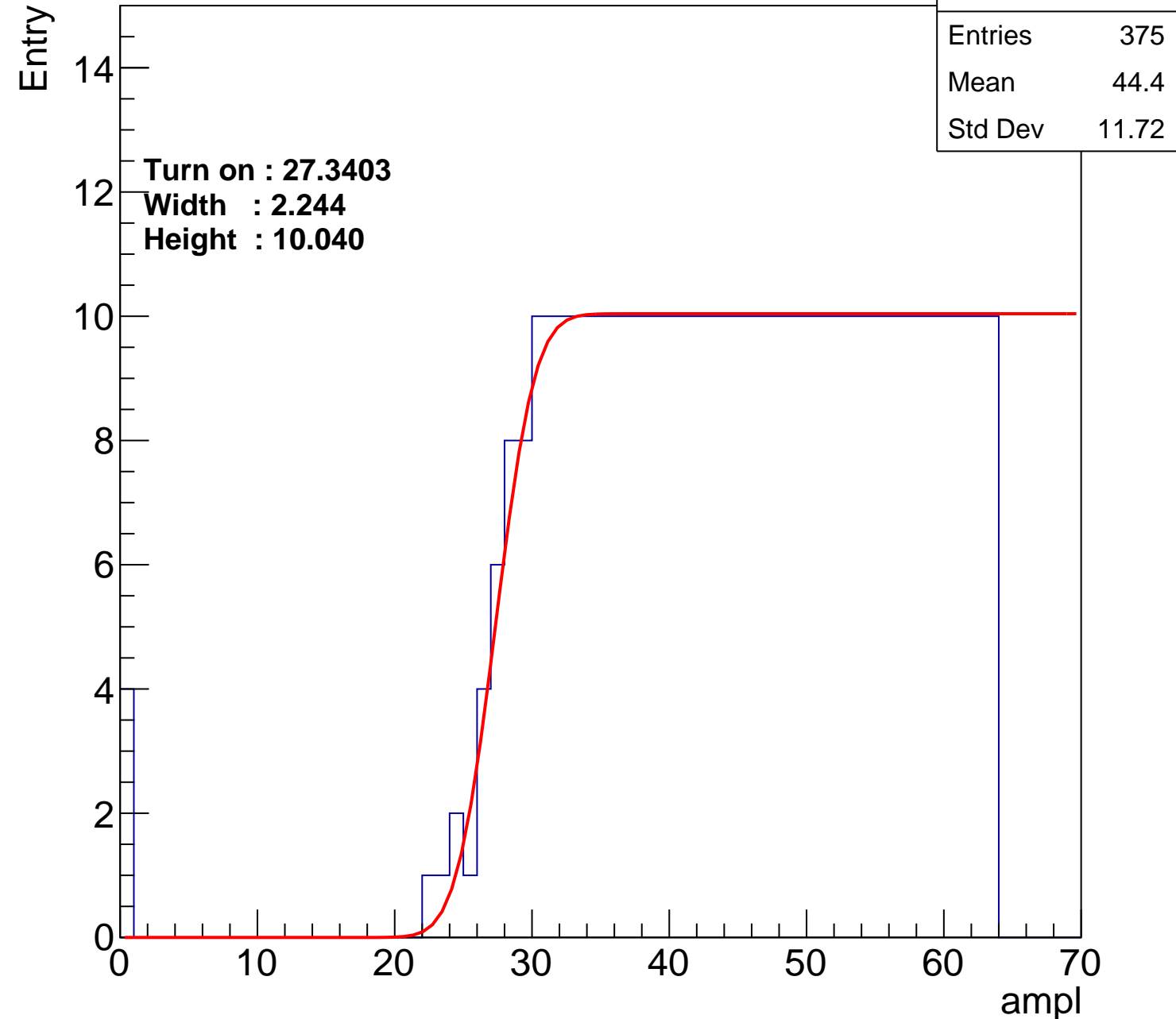
Width : 2.244

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.53

Turn on : 25.5383

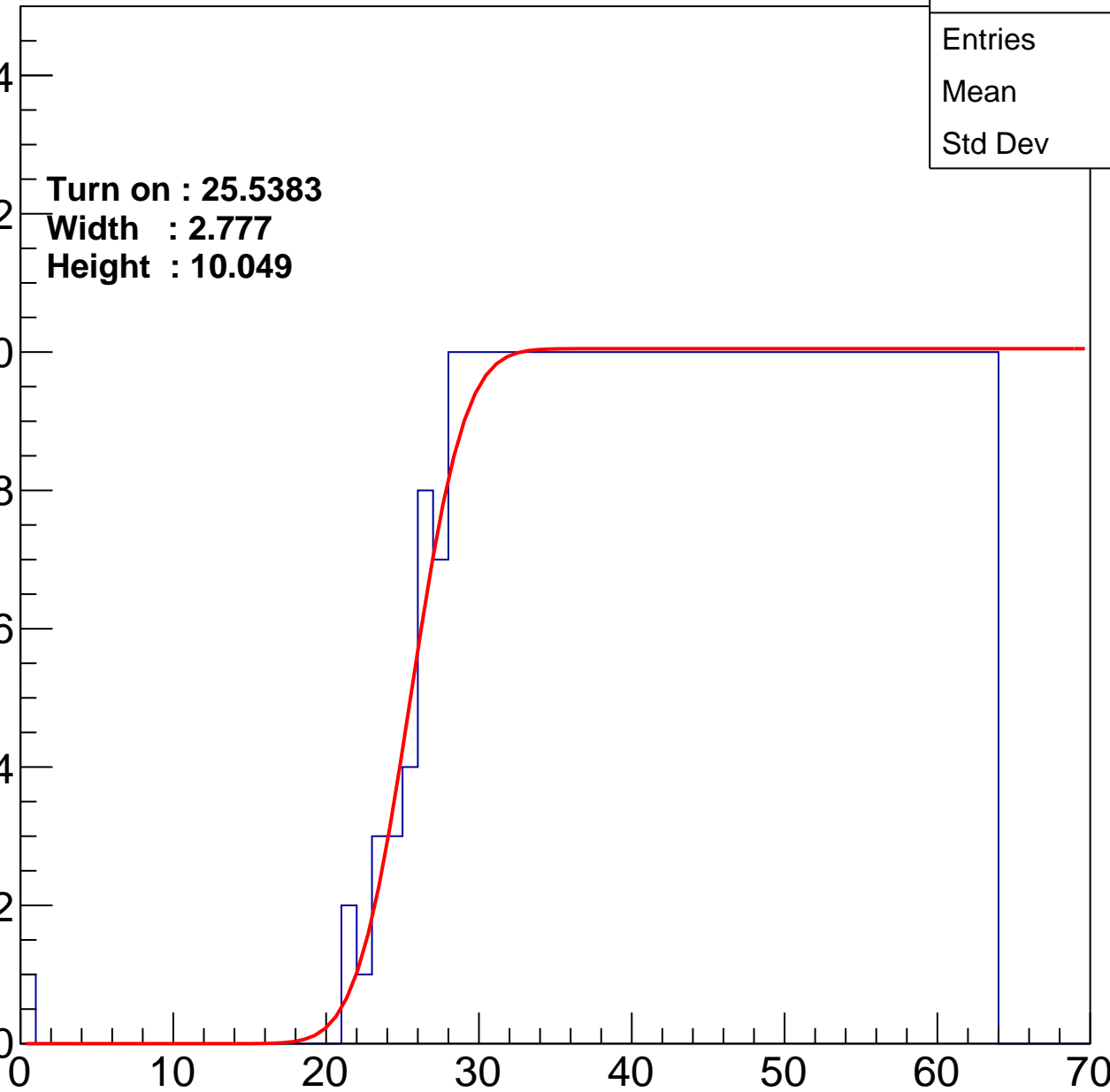
Width : 2.777

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch82

calib_packv5_042523_0143.root, FC#10, port B3

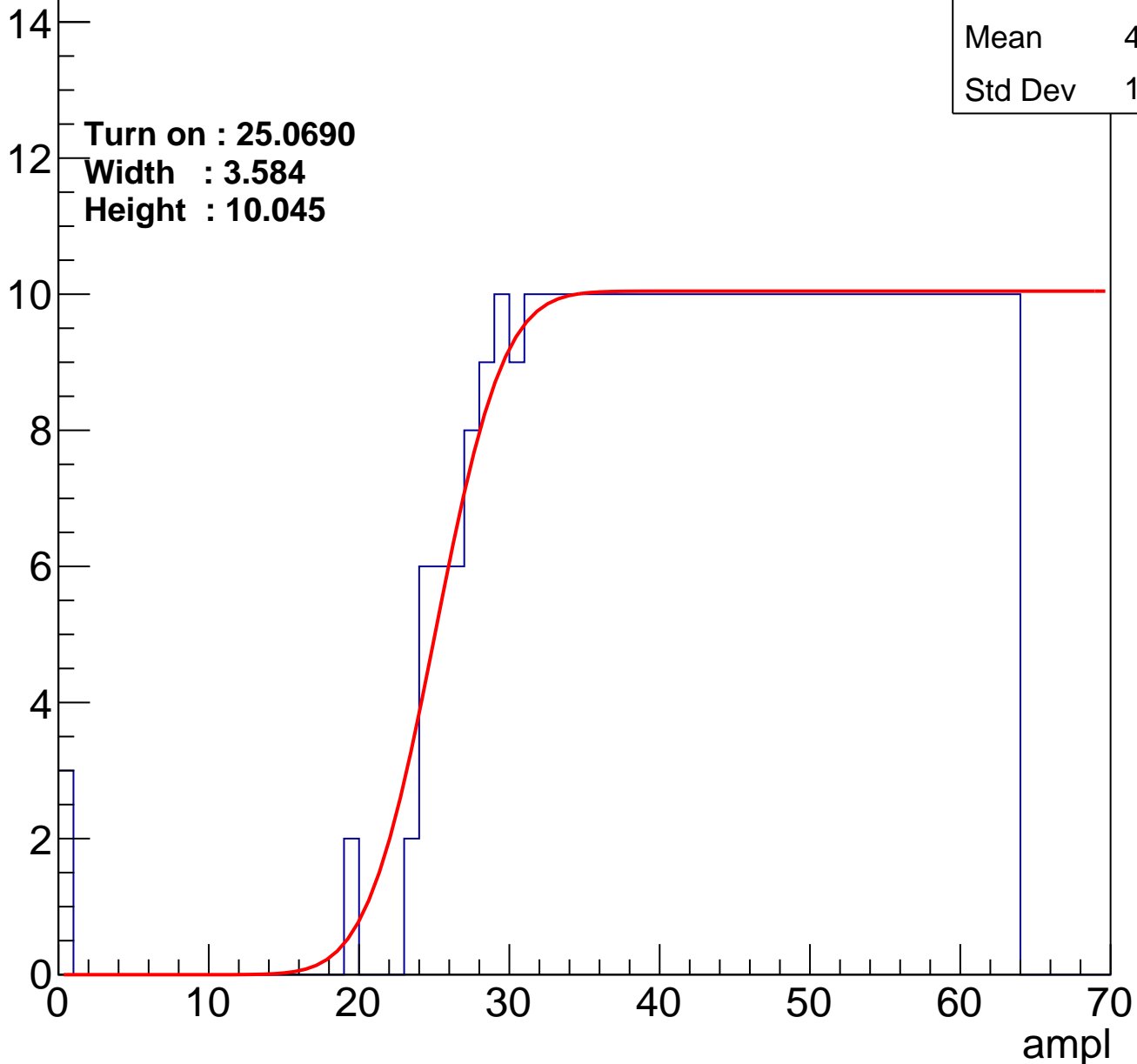
Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.0690

Width : 3.584

Height : 10.045

Entry



B1L002S, U10-ch83

calib_packv5_042523_0143.root, FC#10, port B3

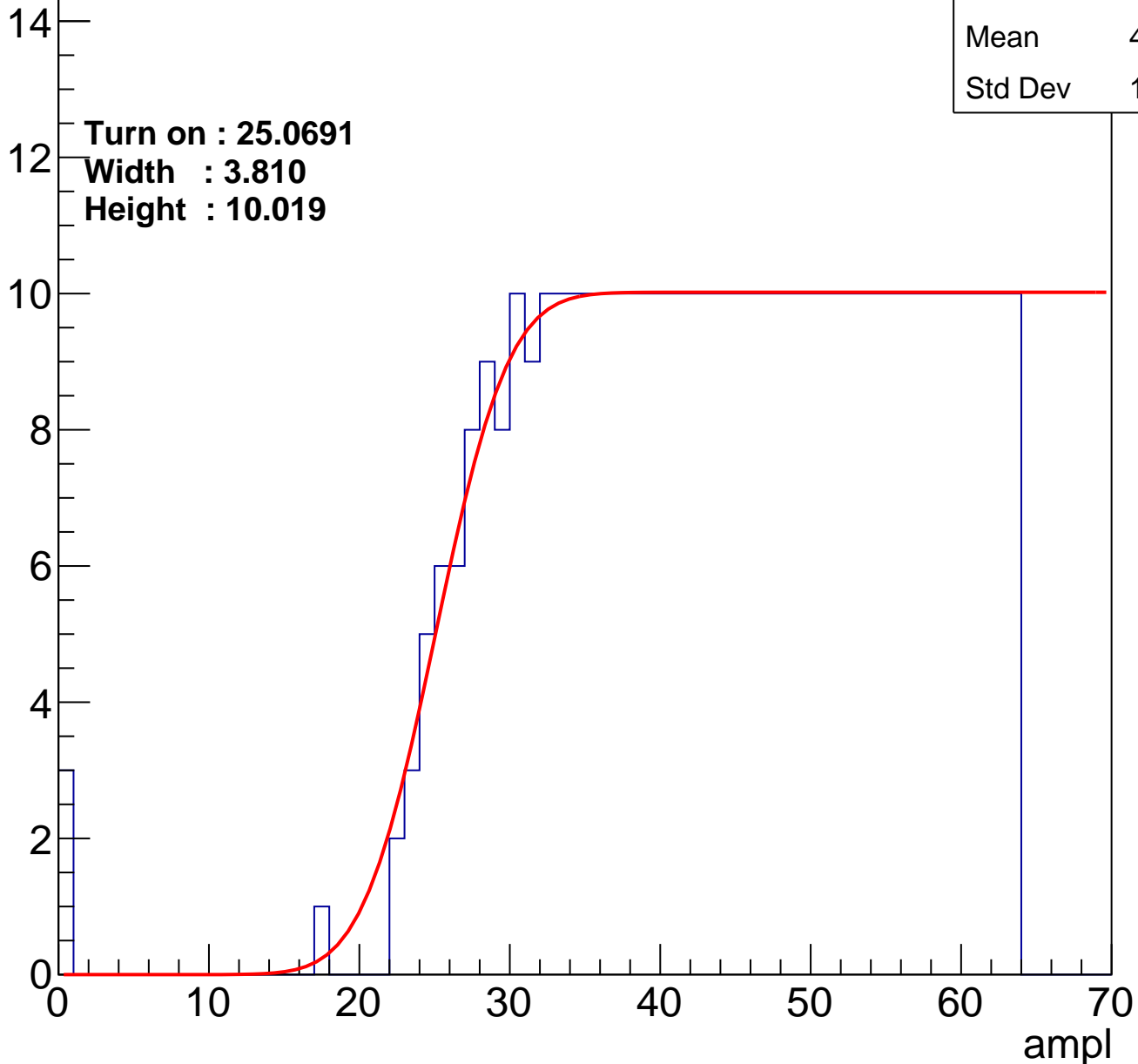
Entries	390
Mean	43.68
Std Dev	11.98

Turn on : 25.0691

Width : 3.810

Height : 10.019

Entry



B1L002S, U10-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.34
Std Dev	11.87

Turn on : 24.9374

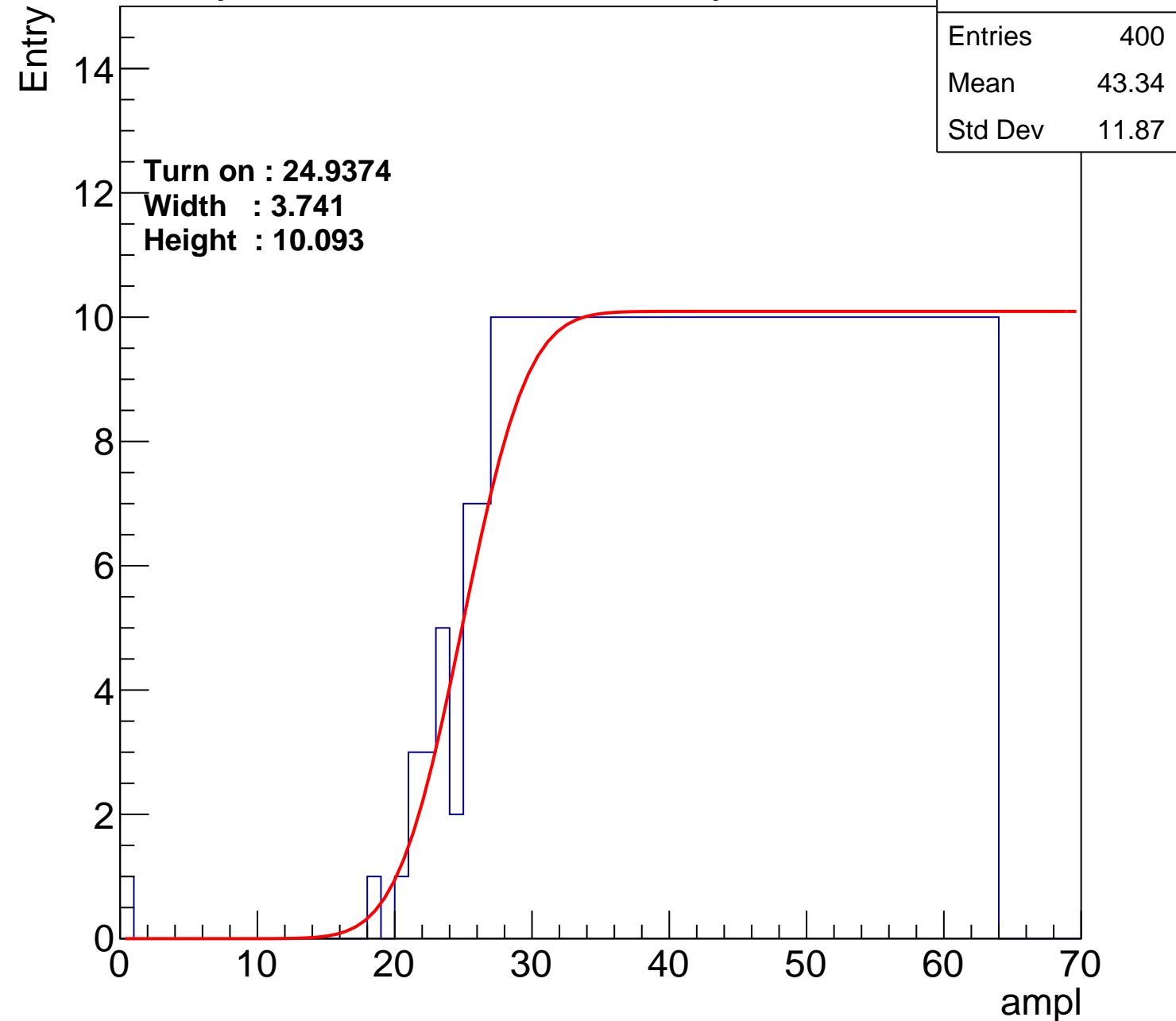
Width : 3.741

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.87
Std Dev	11.84

Turn on : 25.8008

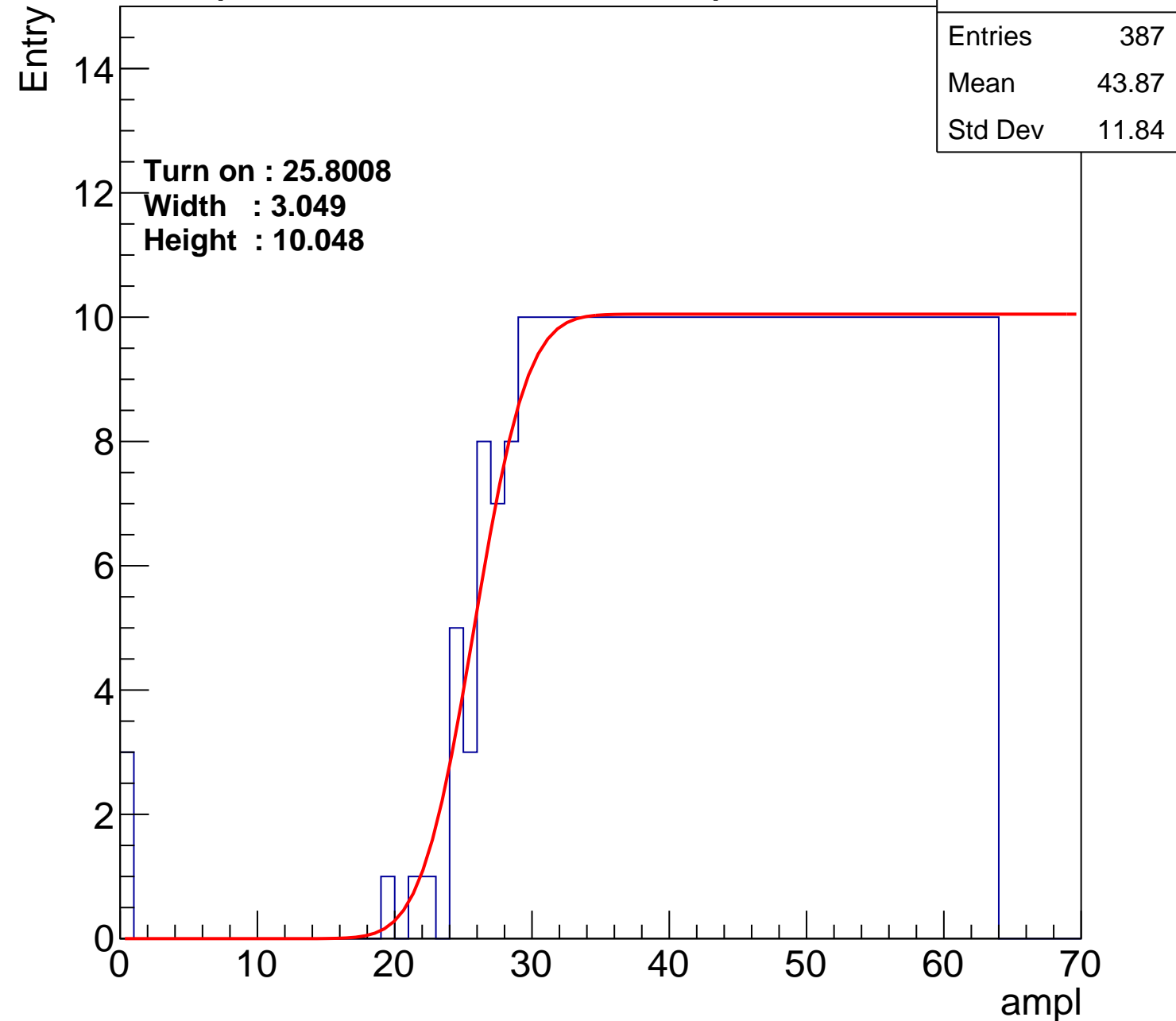
Width : 3.049

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.6922

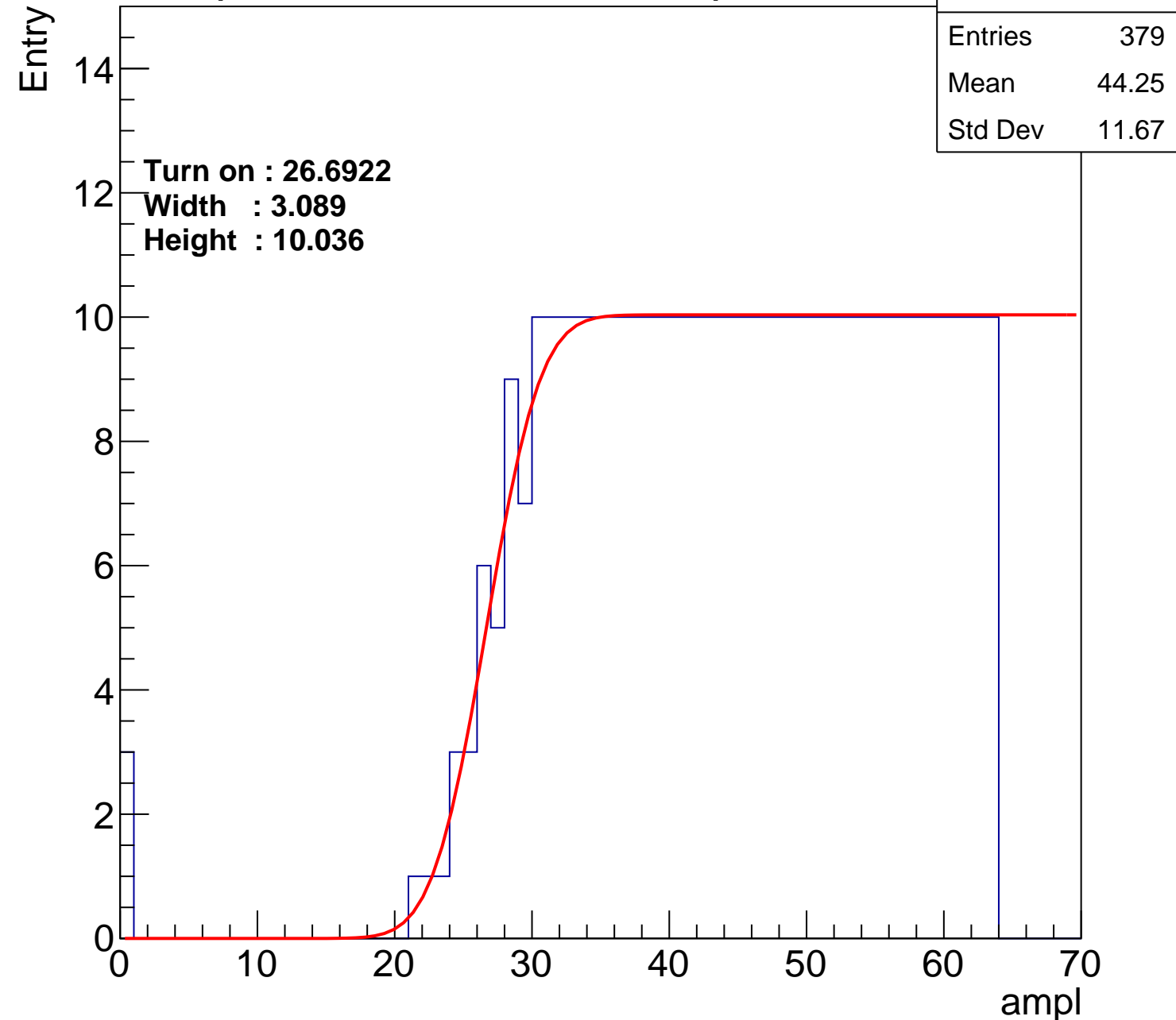
Width : 3.089

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch87

calib_packv5_042523_0143.root, FC#10, port B3

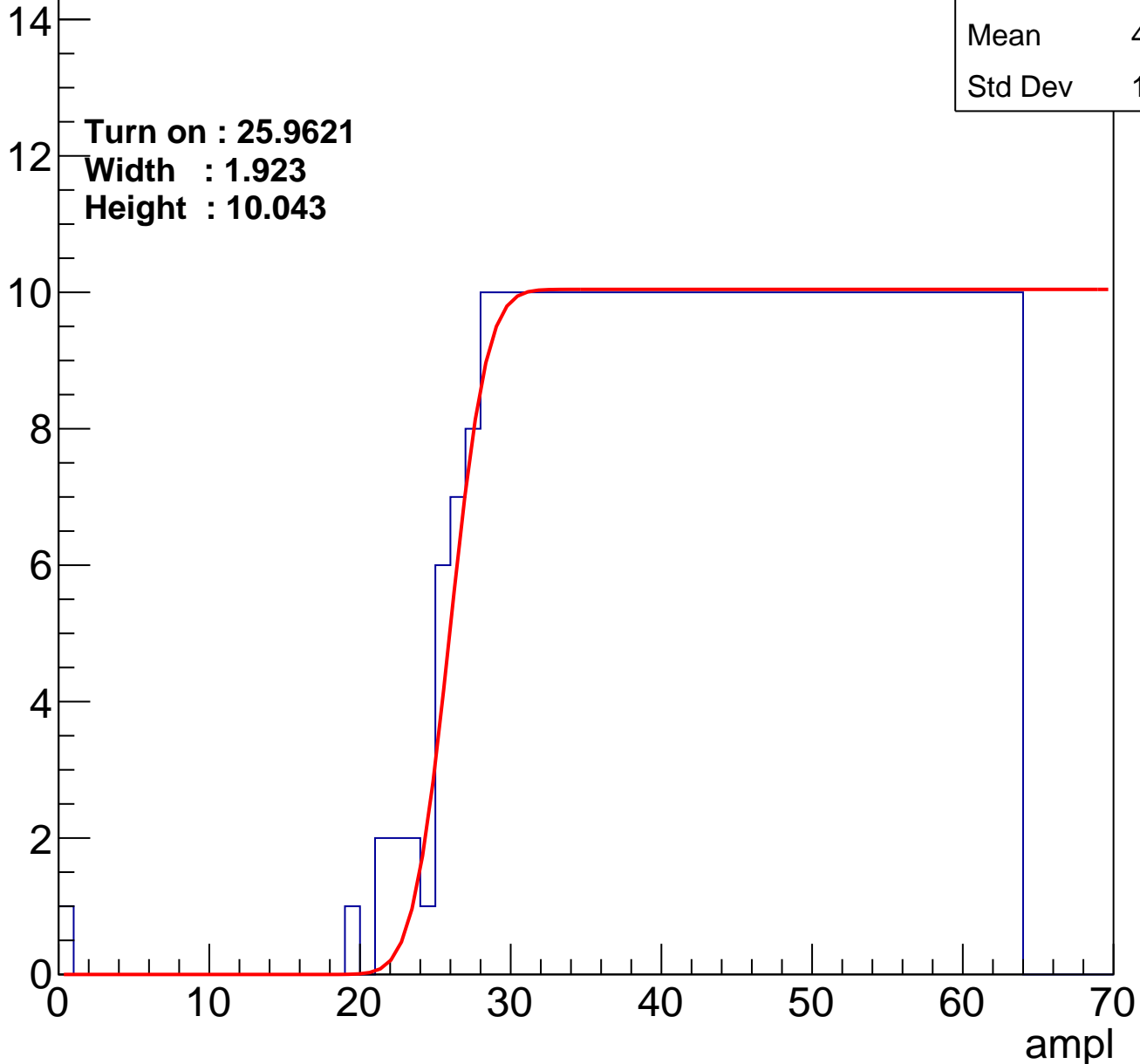
Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.9621

Width : 1.923

Height : 10.043

Entry



B1L002S, U10-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.19
Std Dev	11.96

Turn on : 26.7219

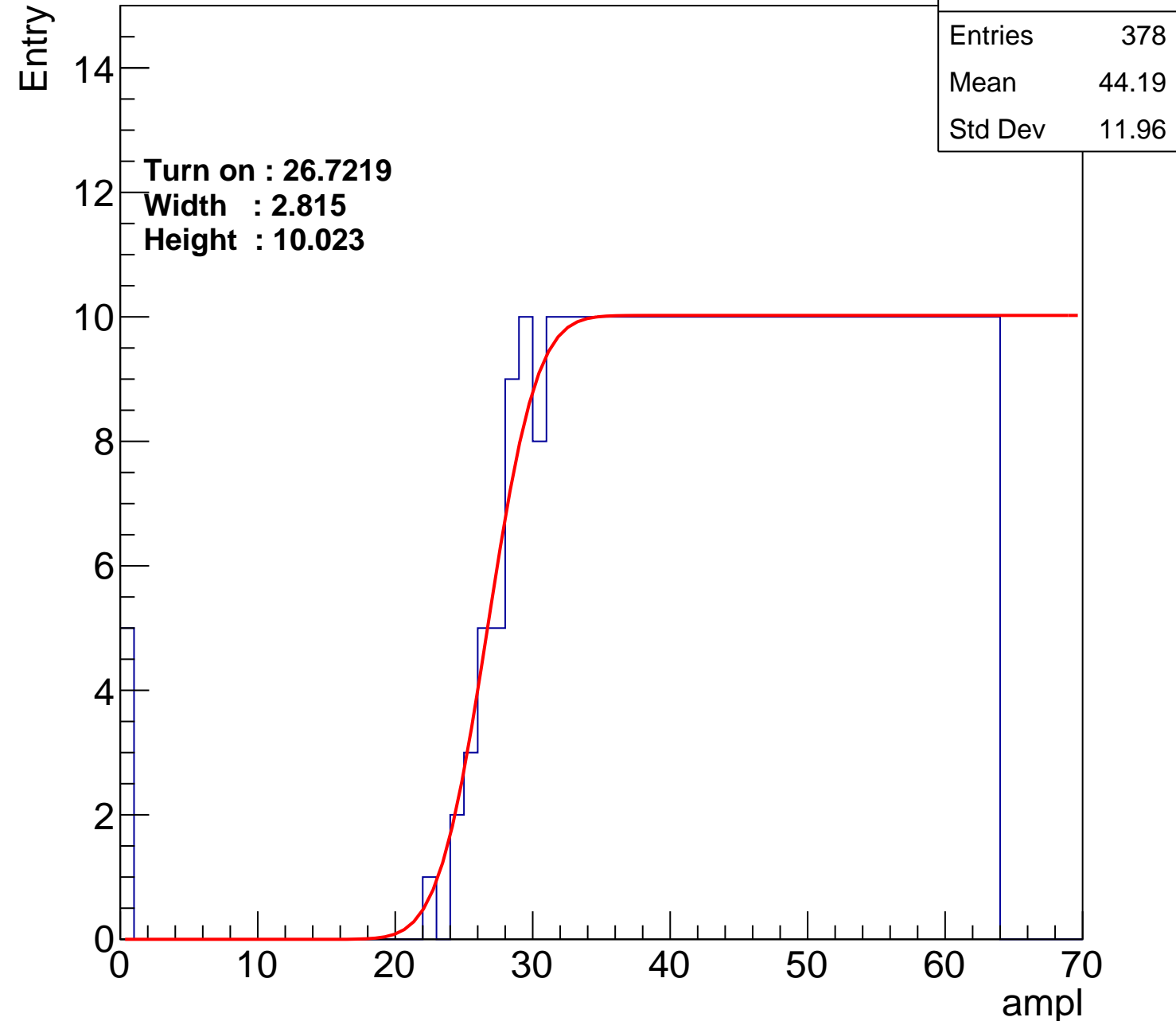
Width : 2.815

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.57
Std Dev	11.2

Turn on : 26.8945

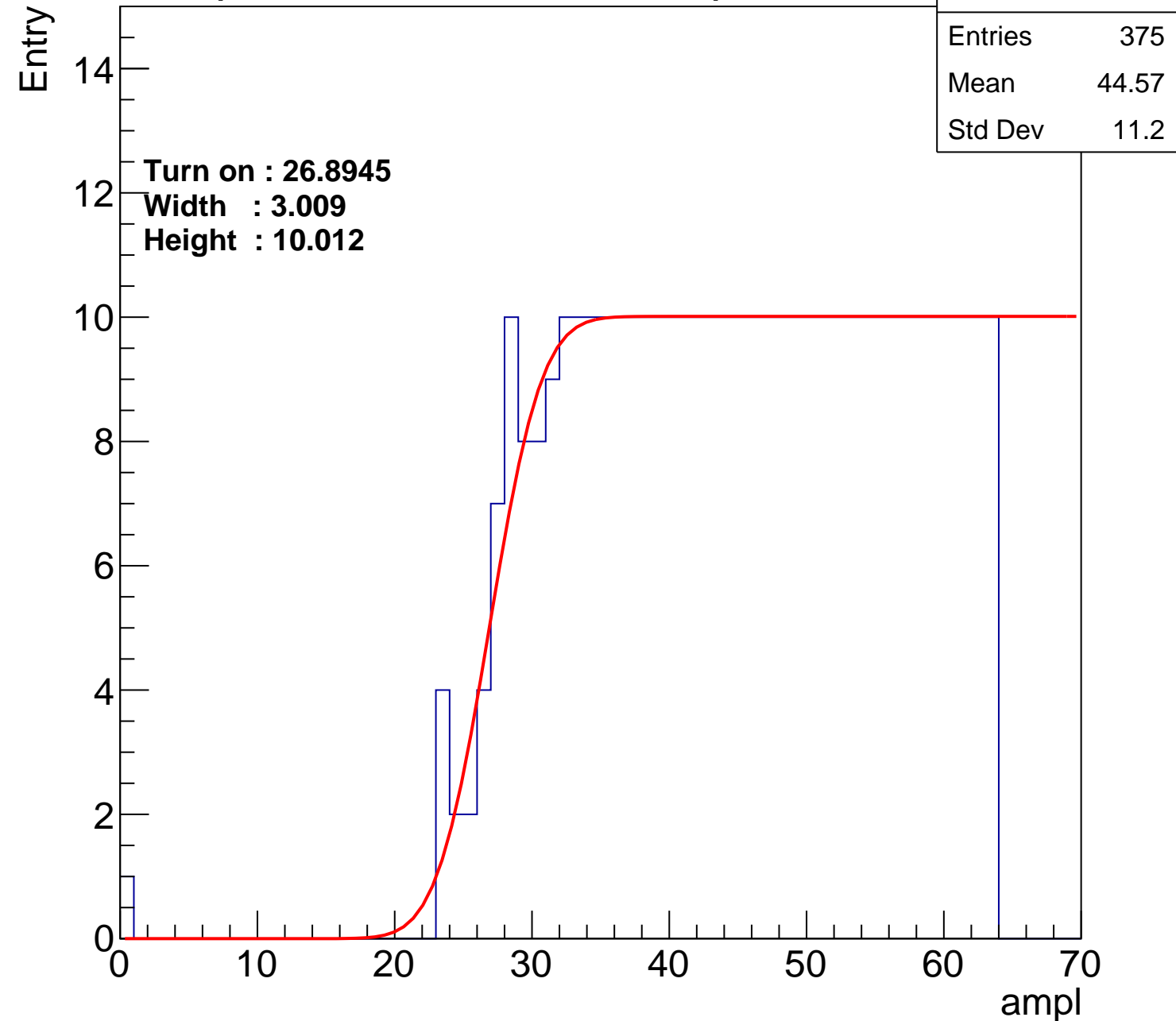
Width : 3.009

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch90

calib_packv5_042523_0143.root, FC#10, port B3

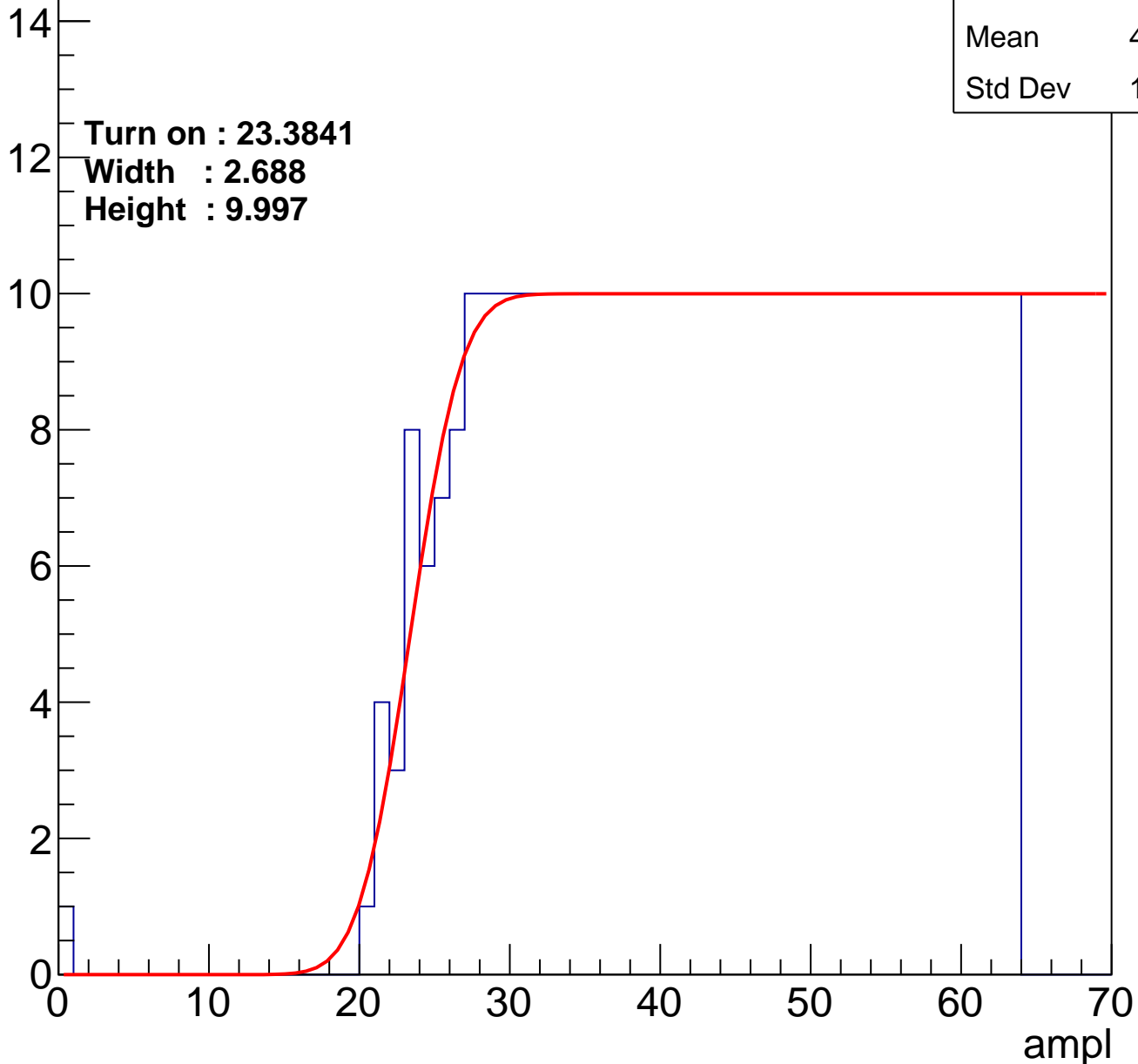
Entries	408
Mean	42.97
Std Dev	12.05

Turn on : 23.3841

Width : 2.688

Height : 9.997

Entry



B1L002S, U10-ch91

calib_packv5_042523_0143.root, FC#10, port B3

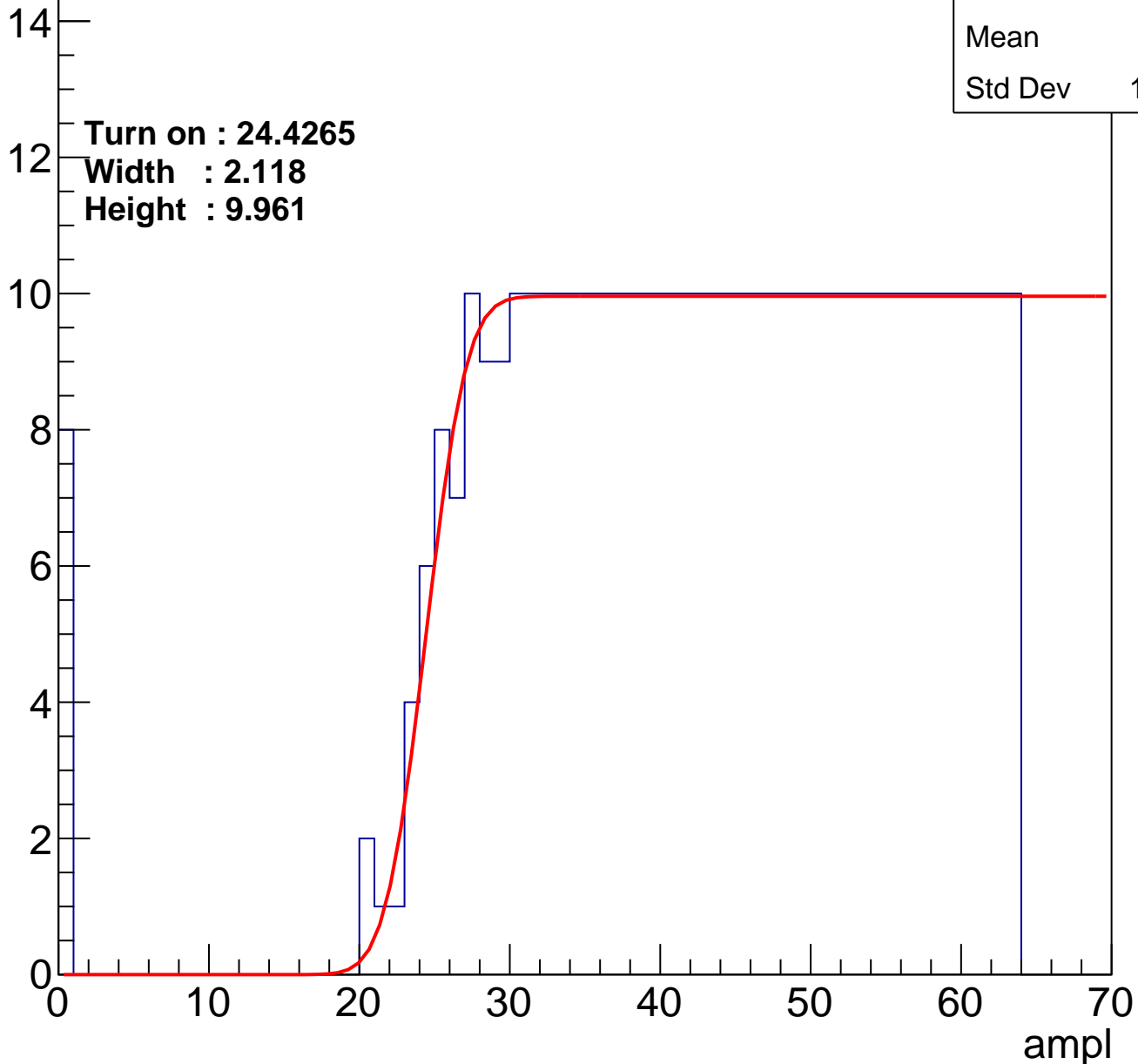
Entries	405
Mean	42.7
Std Dev	12.99

Turn on : 24.4265

Width : 2.118

Height : 9.961

Entry



B1L002S, U10-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.4870

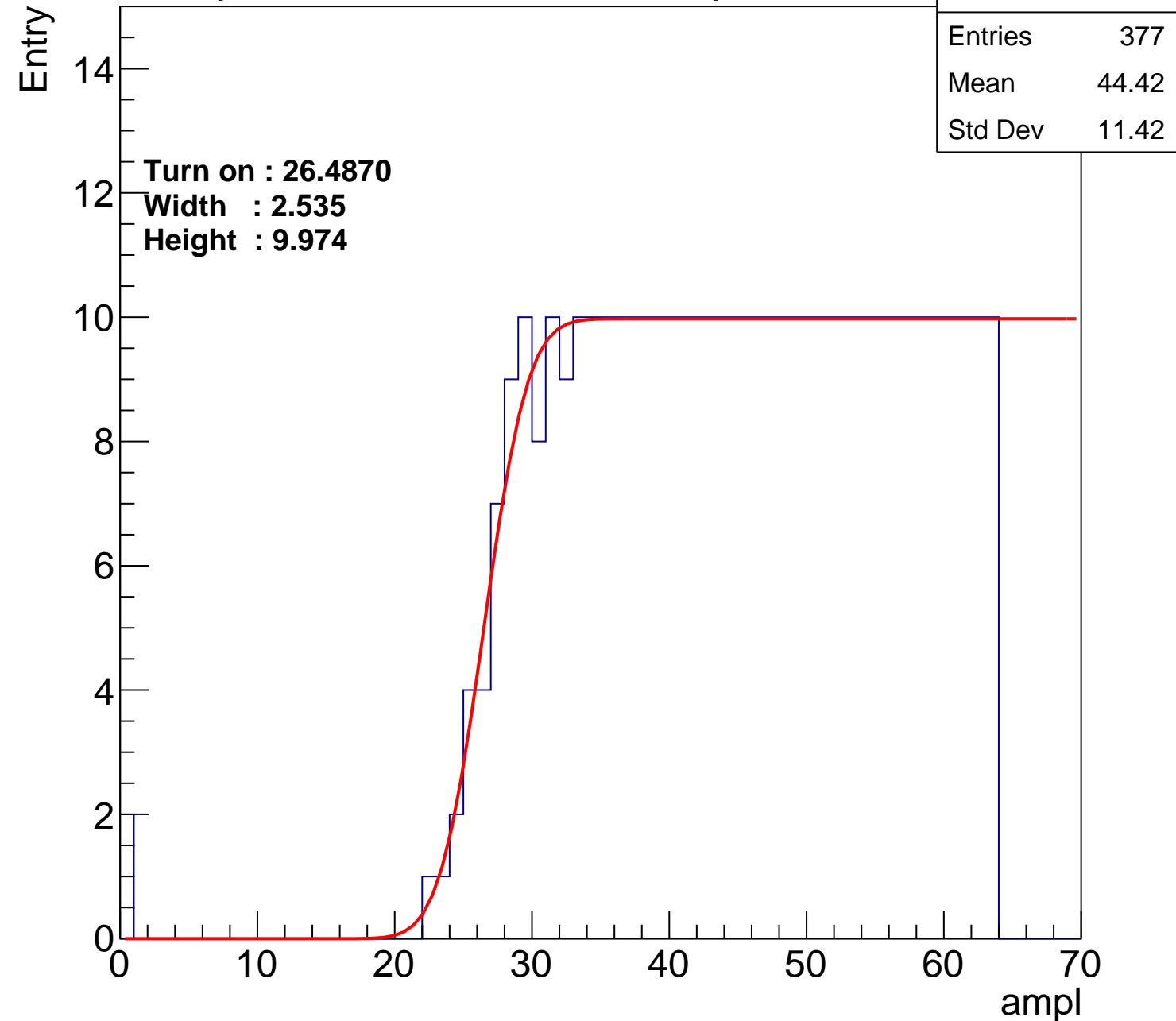
Width : 2.535

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.35
Std Dev	11.74

Turn on : 26.7323

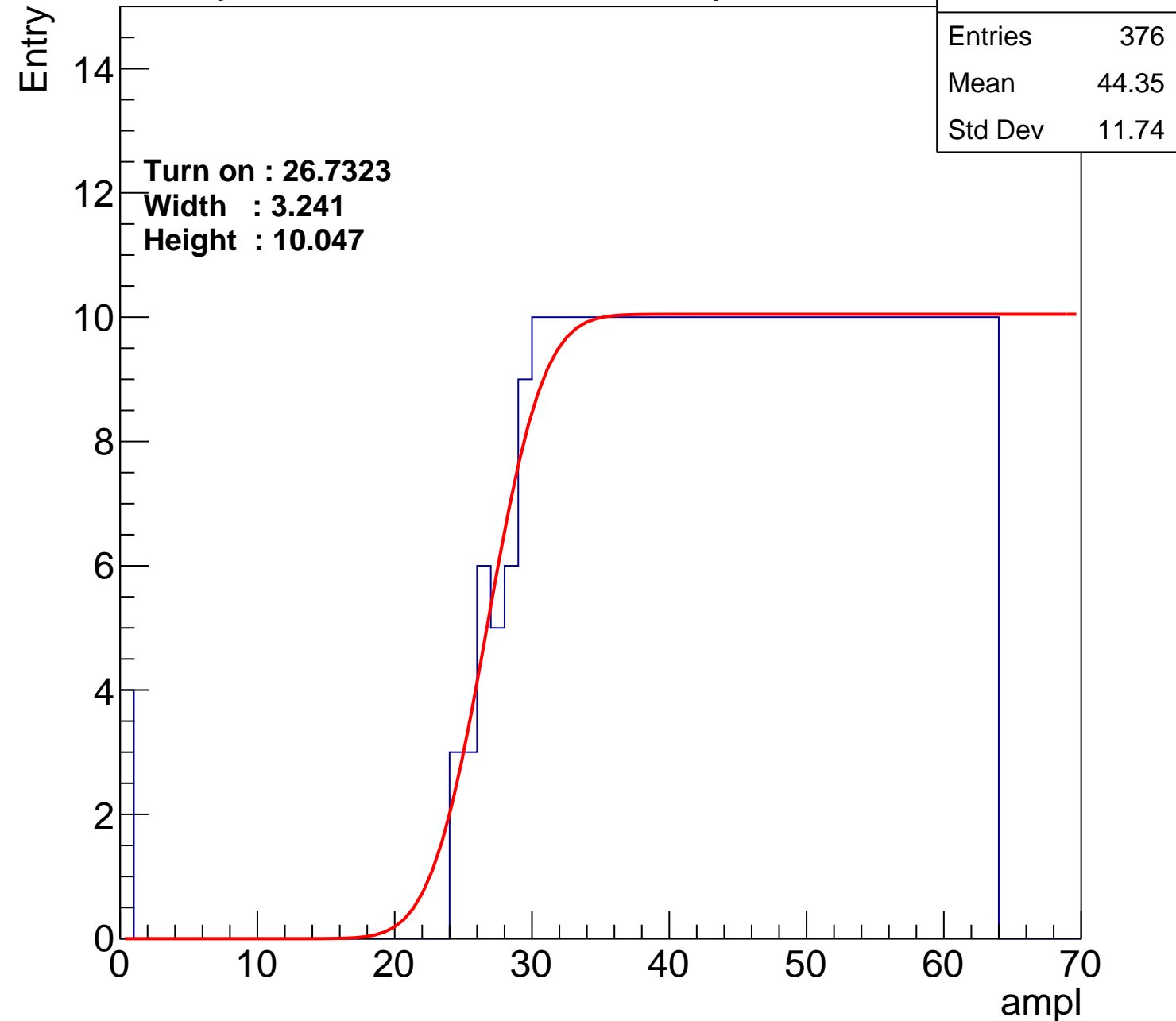
Width : 3.241

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.93
Std Dev	11.92

Turn on : 25.5594

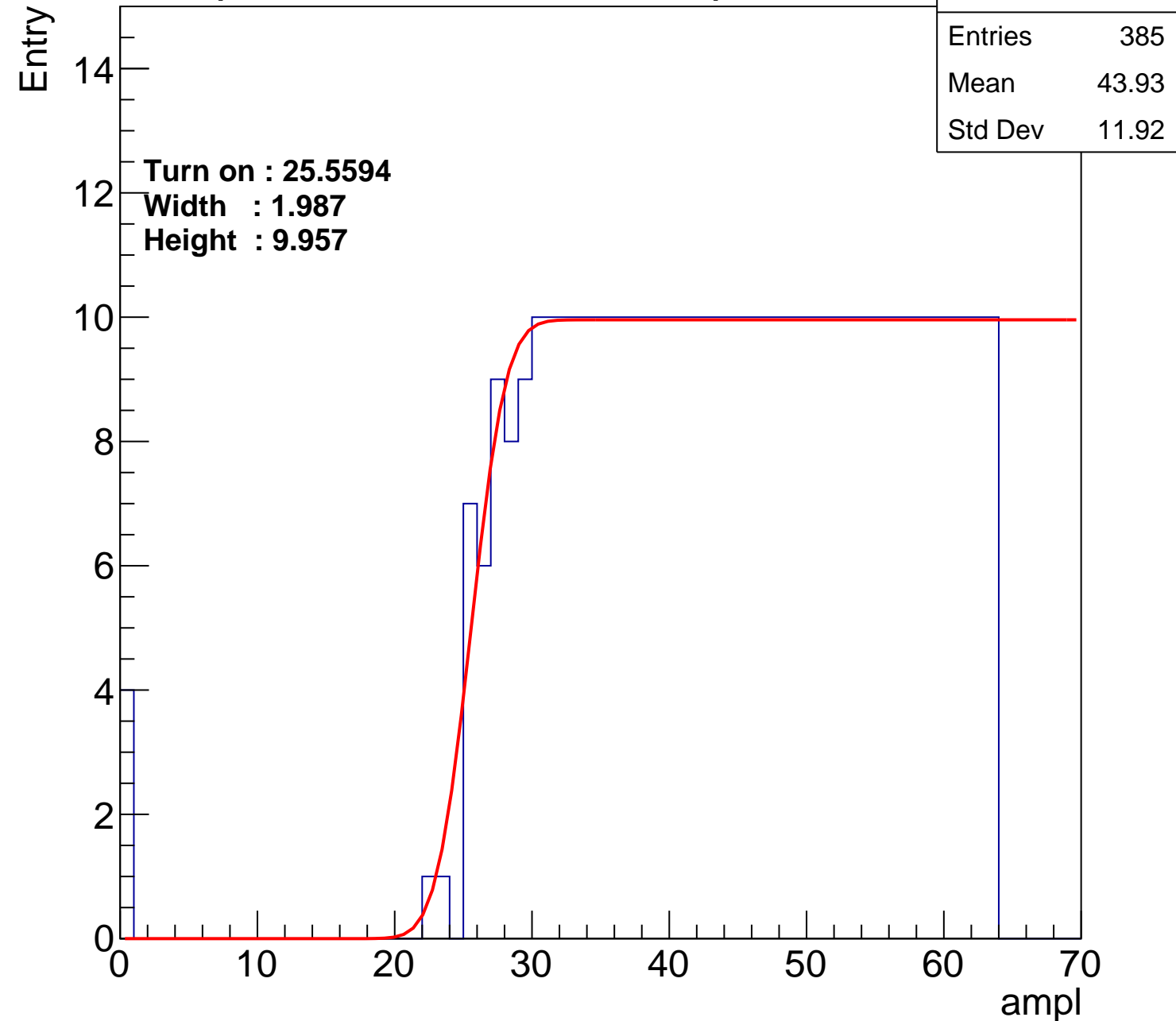
Width : 1.987

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.17
Std Dev	11.38

Turn on : 25.3085

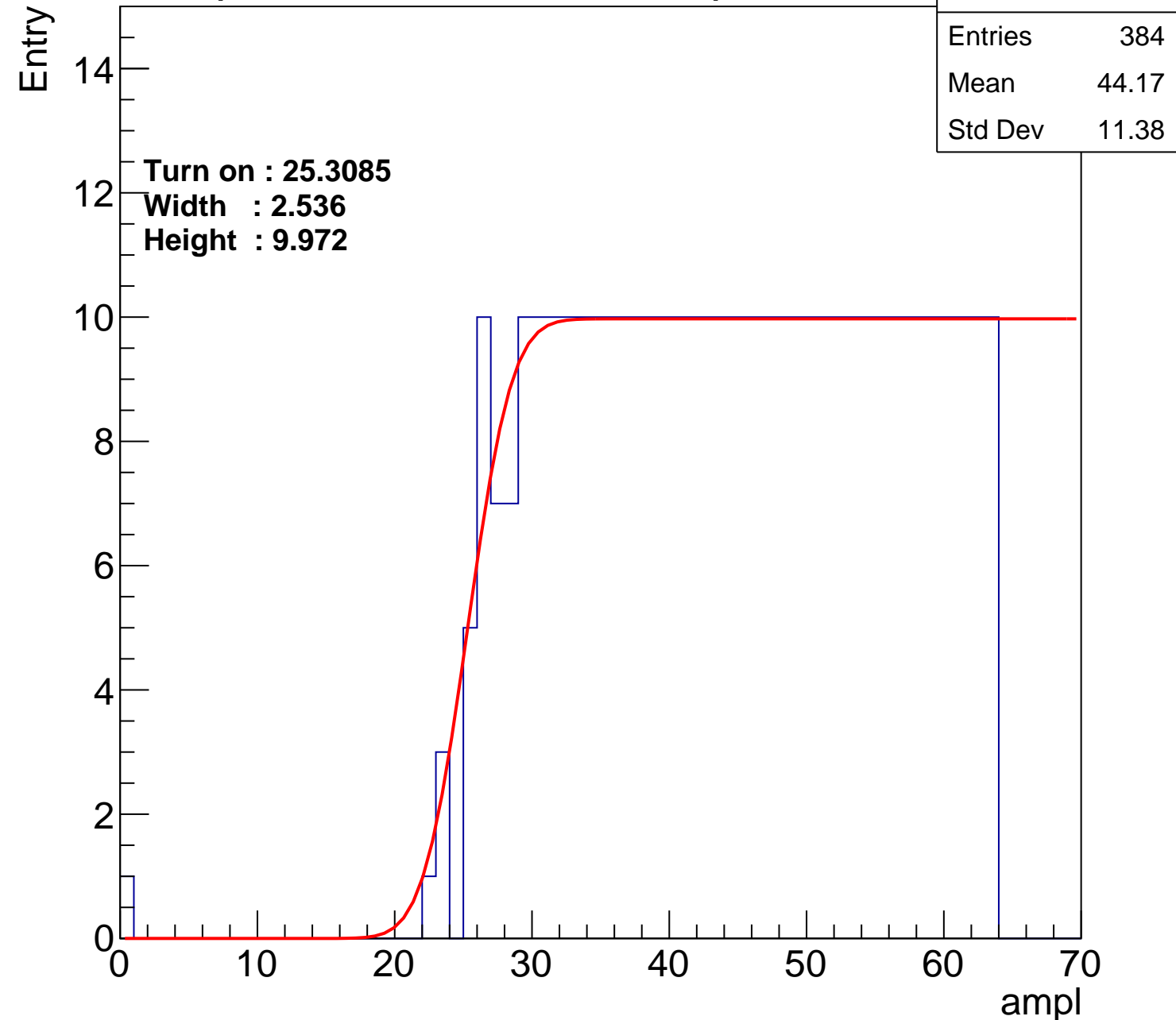
Width : 2.536

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch96

calib_packv5_042523_0143.root, FC#10, port B3

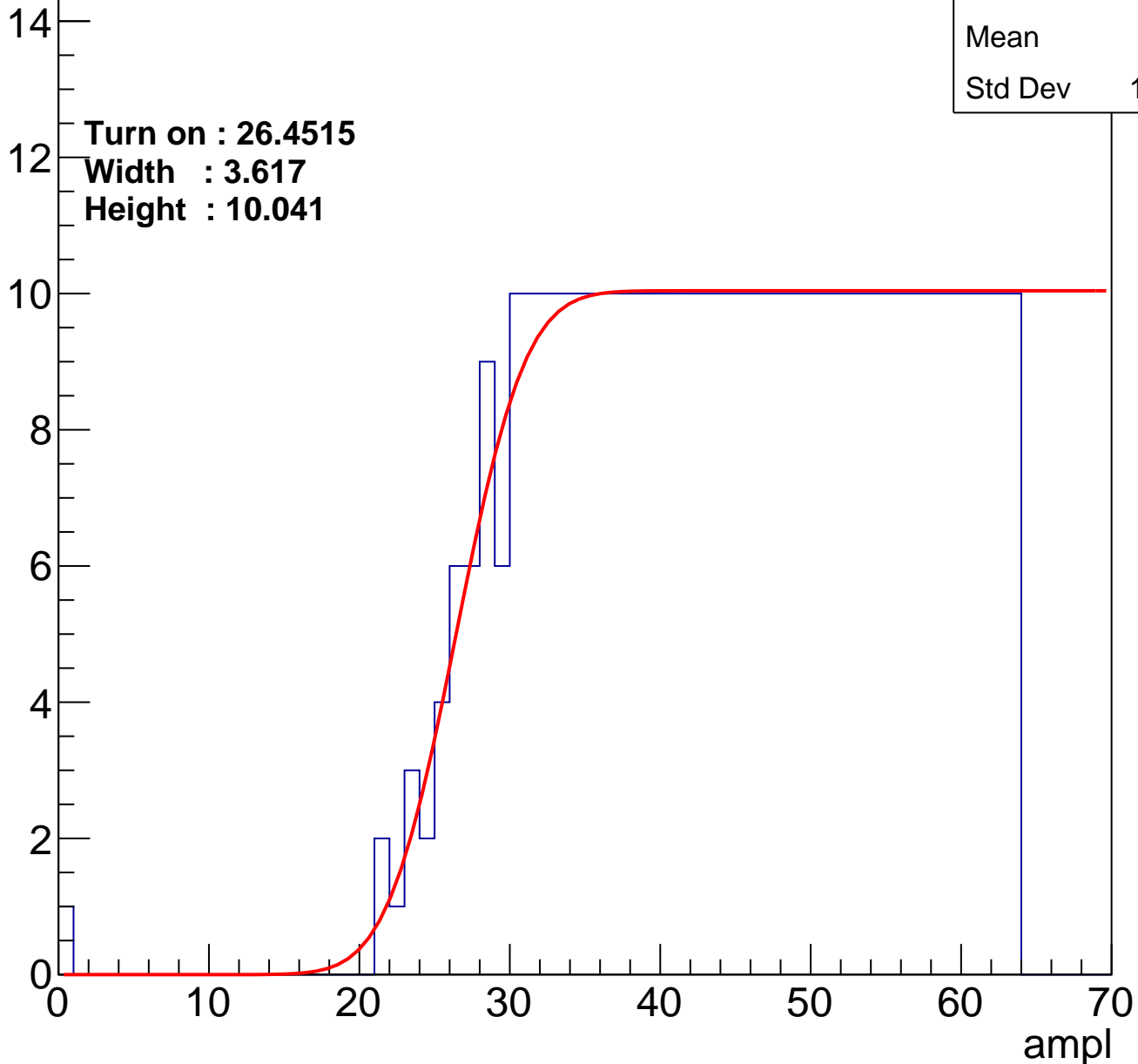
Entries	380
Mean	44.3
Std Dev	11.38

Turn on : 26.4515

Width : 3.617

Height : 10.041

Entry



B1L002S, U10-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.55
Std Dev	11.97

Turn on : 24.8261

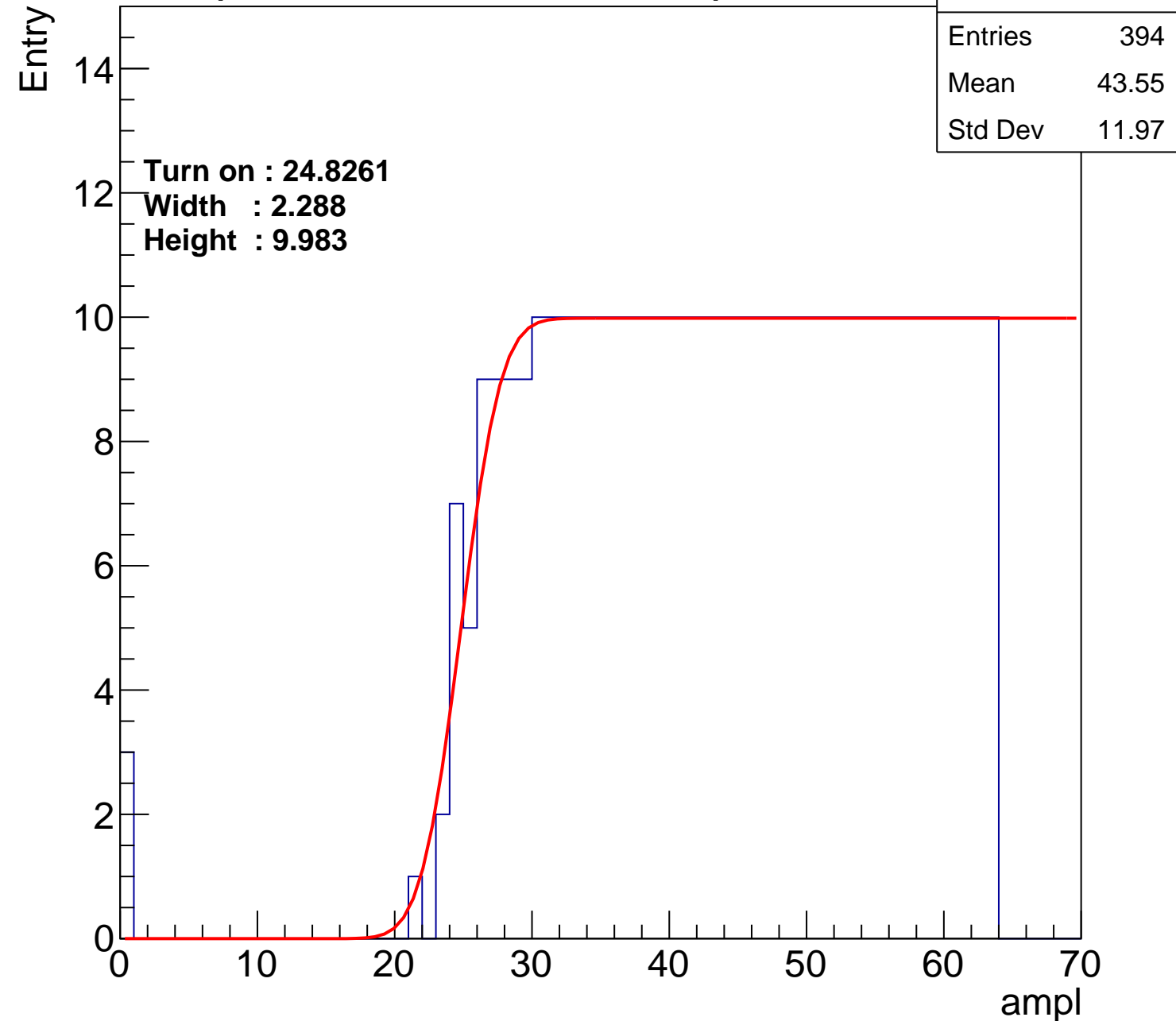
Width : 2.288

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch98

calib_packv5_042523_0143.root, FC#10, port B3

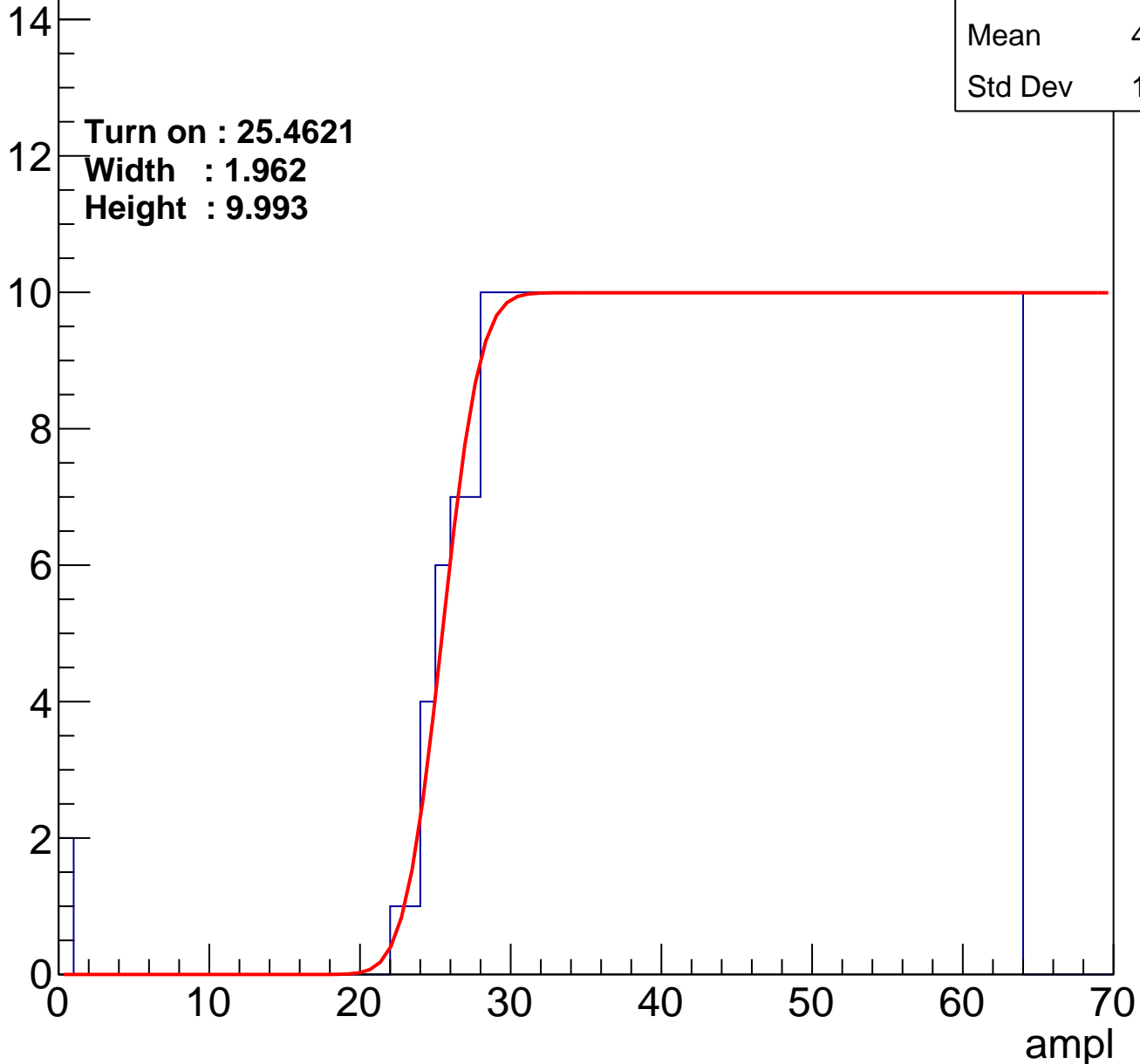
Entries	388
Mean	43.92
Std Dev	11.63

Turn on : 25.4621

Width : 1.962

Height : 9.993

Entry



B1L002S, U10-ch99

calib_packv5_042523_0143.root, FC#10, port B3

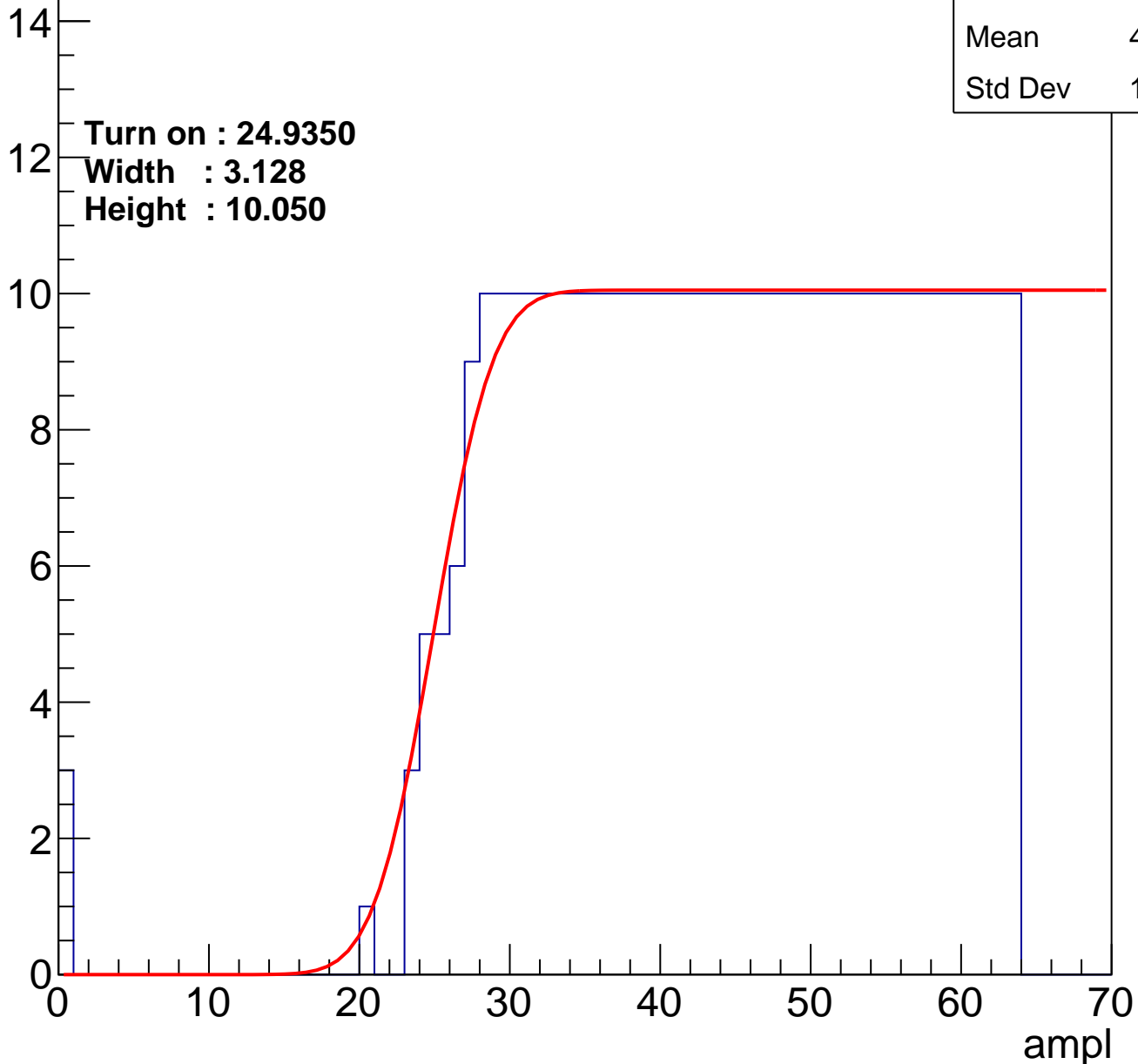
Entries	392
Mean	43.66
Std Dev	11.92

Turn on : 24.9350

Width : 3.128

Height : 10.050

Entry



B1L002S, U10-ch100

calib_packv5_042523_0143.root, FC#10, port B3

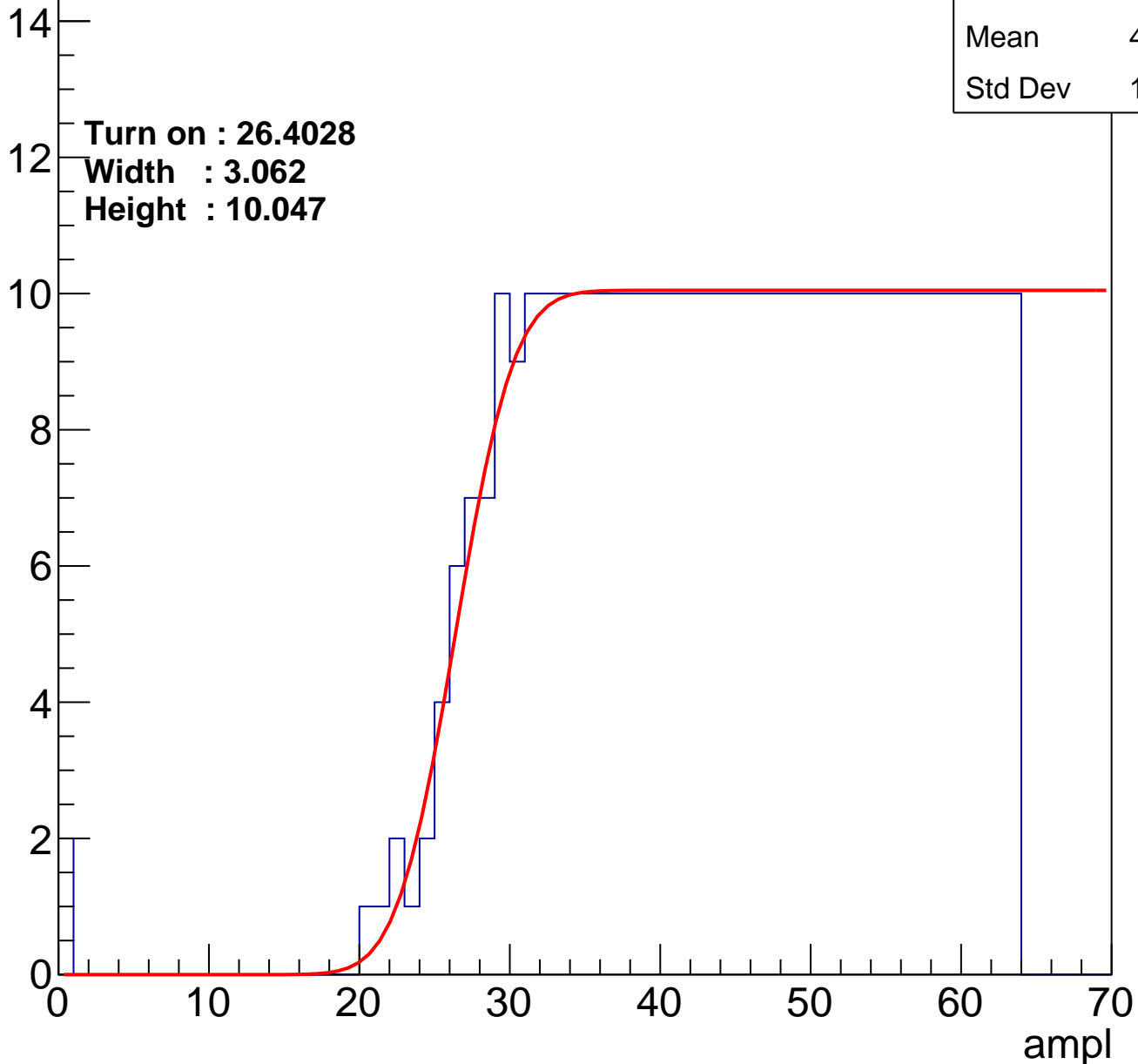
Entries	382
Mean	44.15
Std Dev	11.59

Turn on : 26.4028

Width : 3.062

Height : 10.047

Entry



B1L002S, U10-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.59
Std Dev	11.74

Turn on : 24.7700

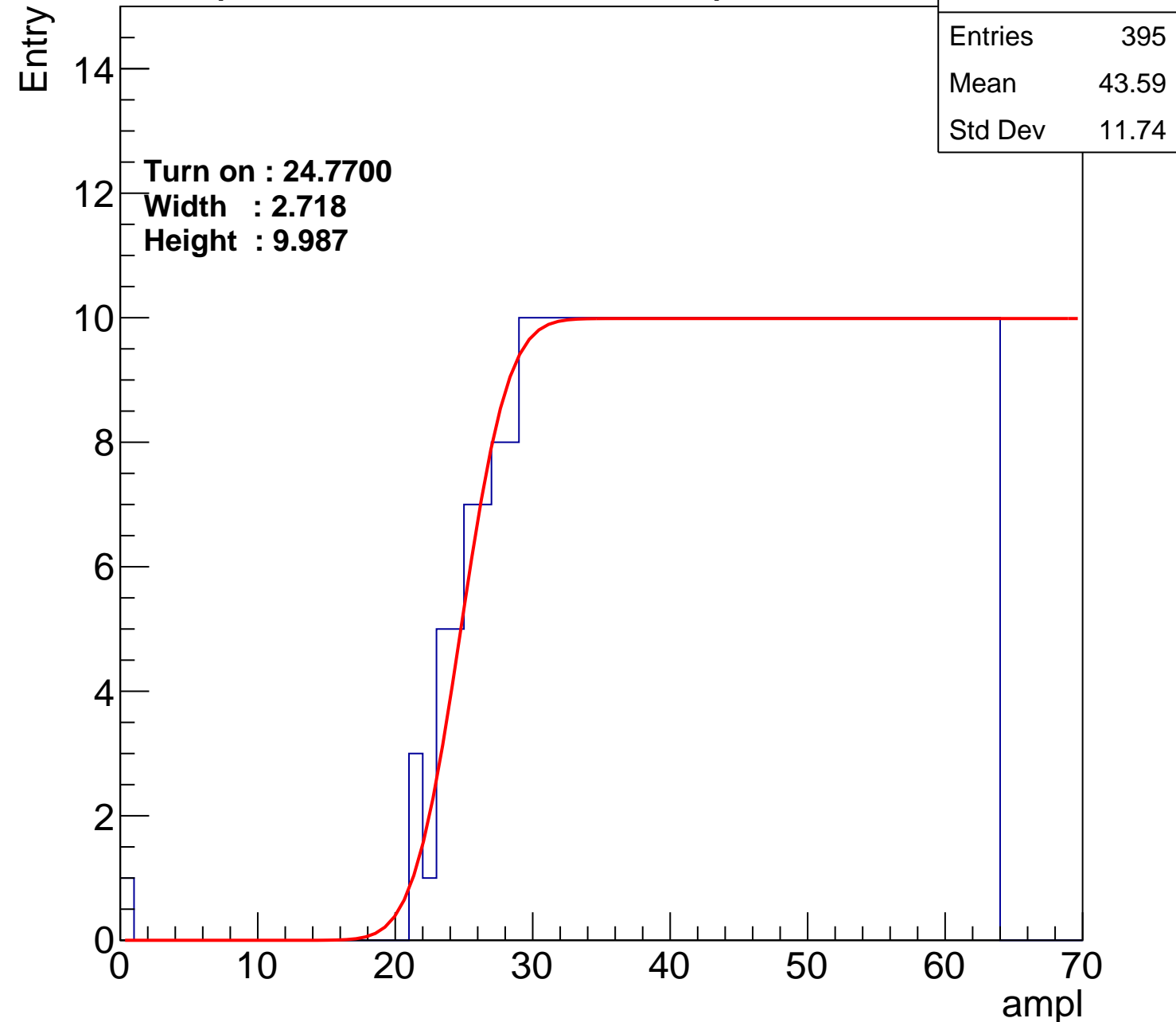
Width : 2.718

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch102

calib_packv5_042523_0143.root, FC#10, port B3

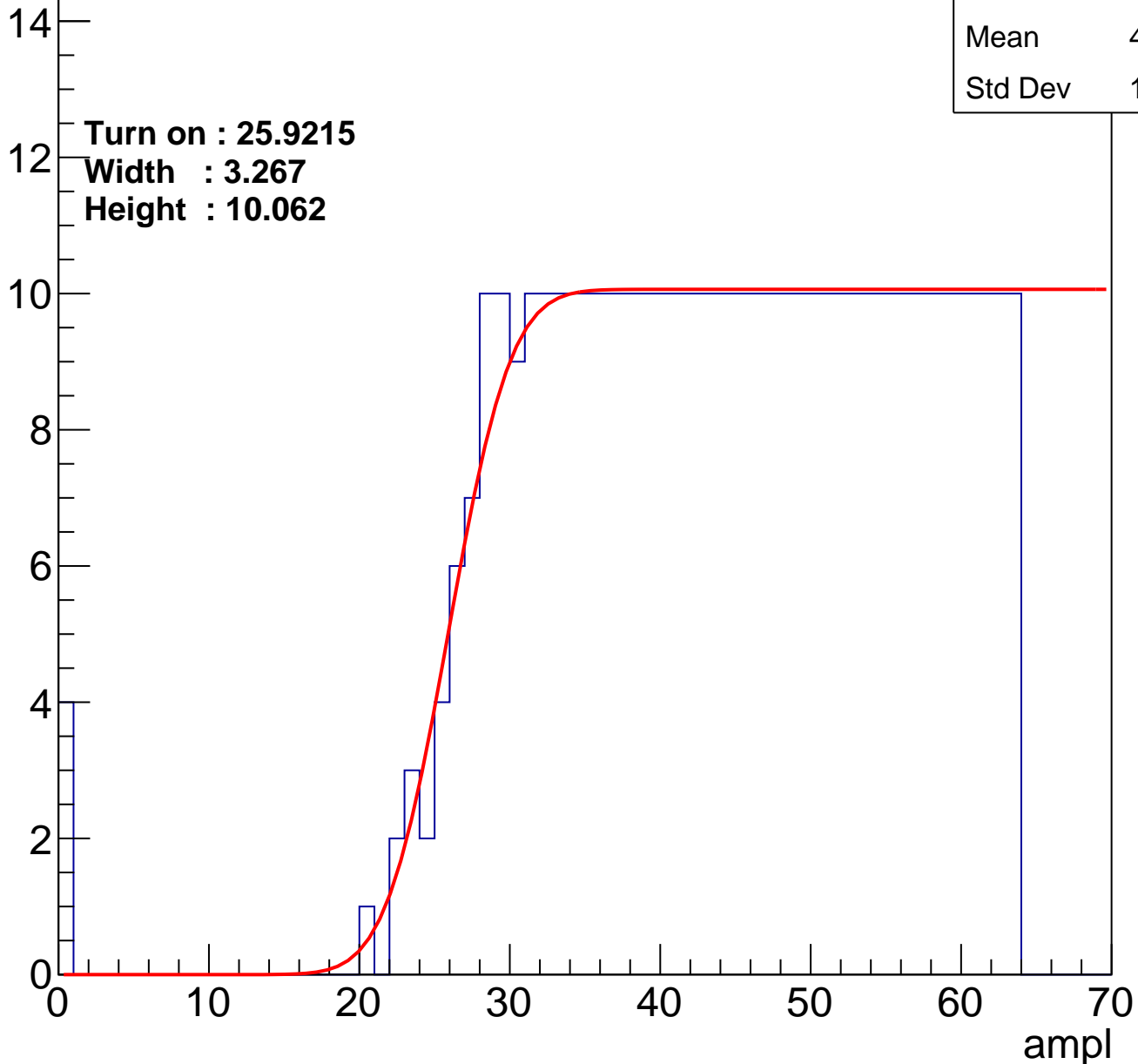
Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.9215

Width : 3.267

Height : 10.062

Entry



B1L002S, U10-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.91
Std Dev	12.47

Turn on : 23.6352

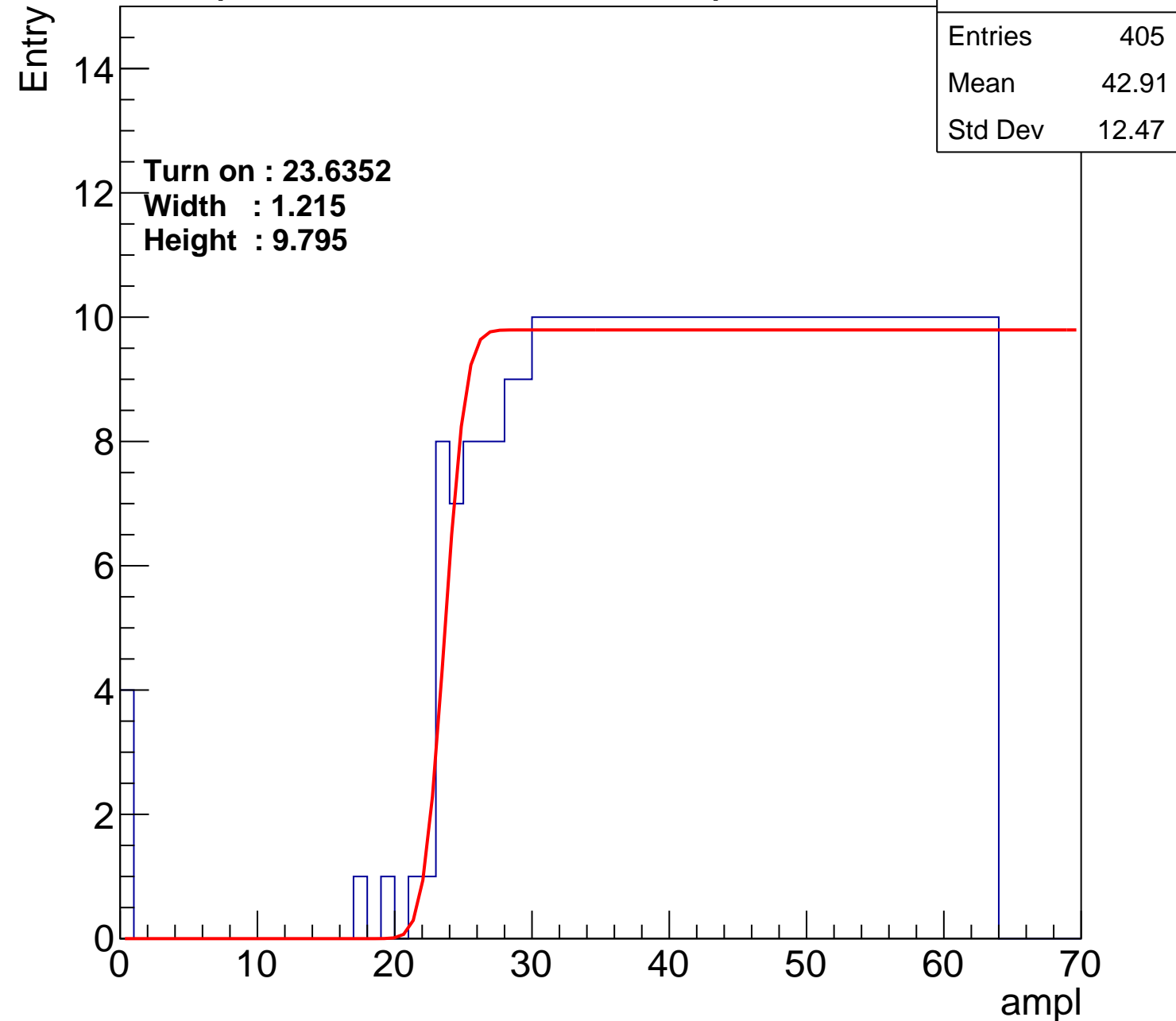
Width : 1.215

Height : 9.795

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.77
Std Dev	11.9

Turn on : 25.7834

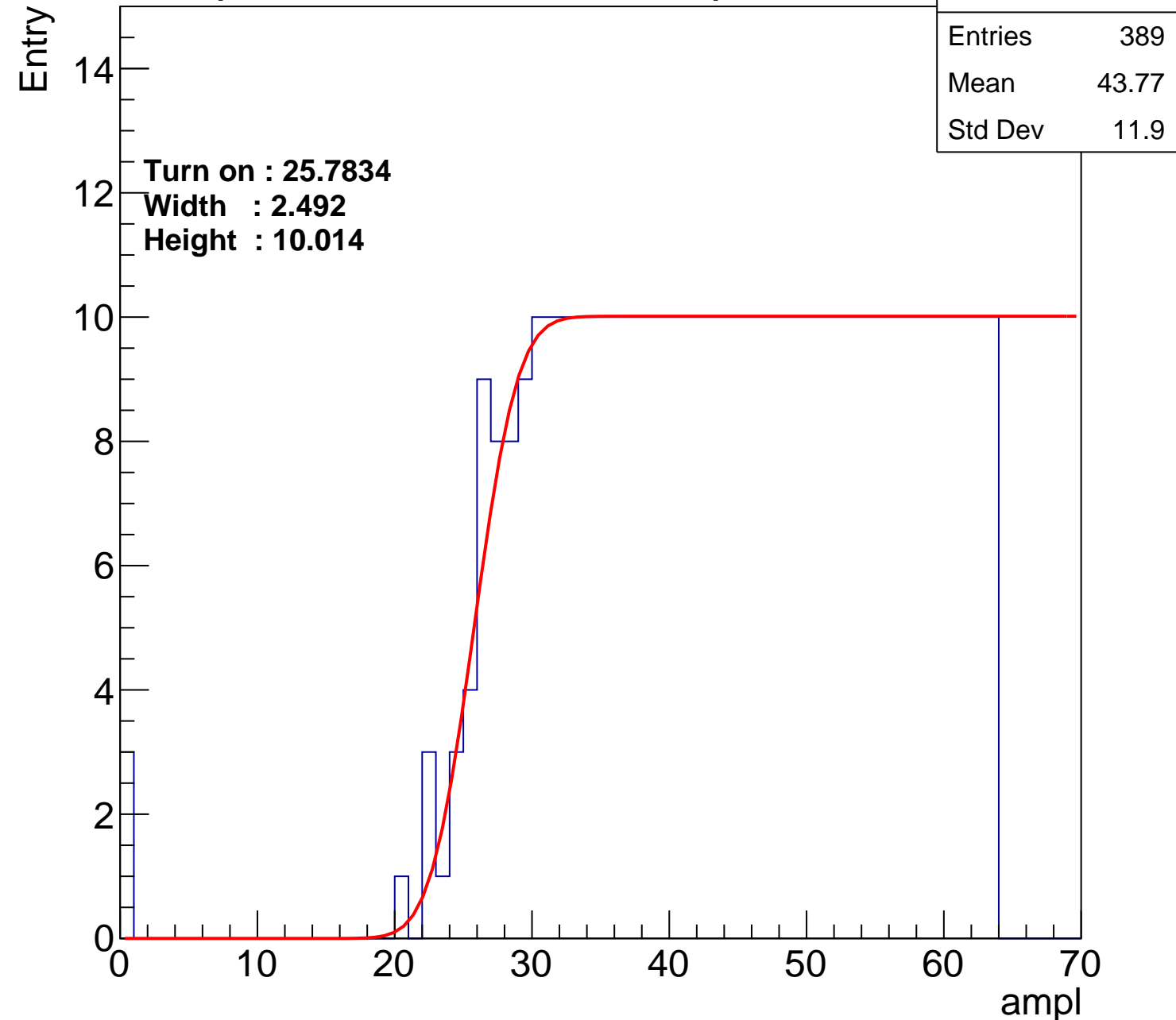
Width : 2.492

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.1135

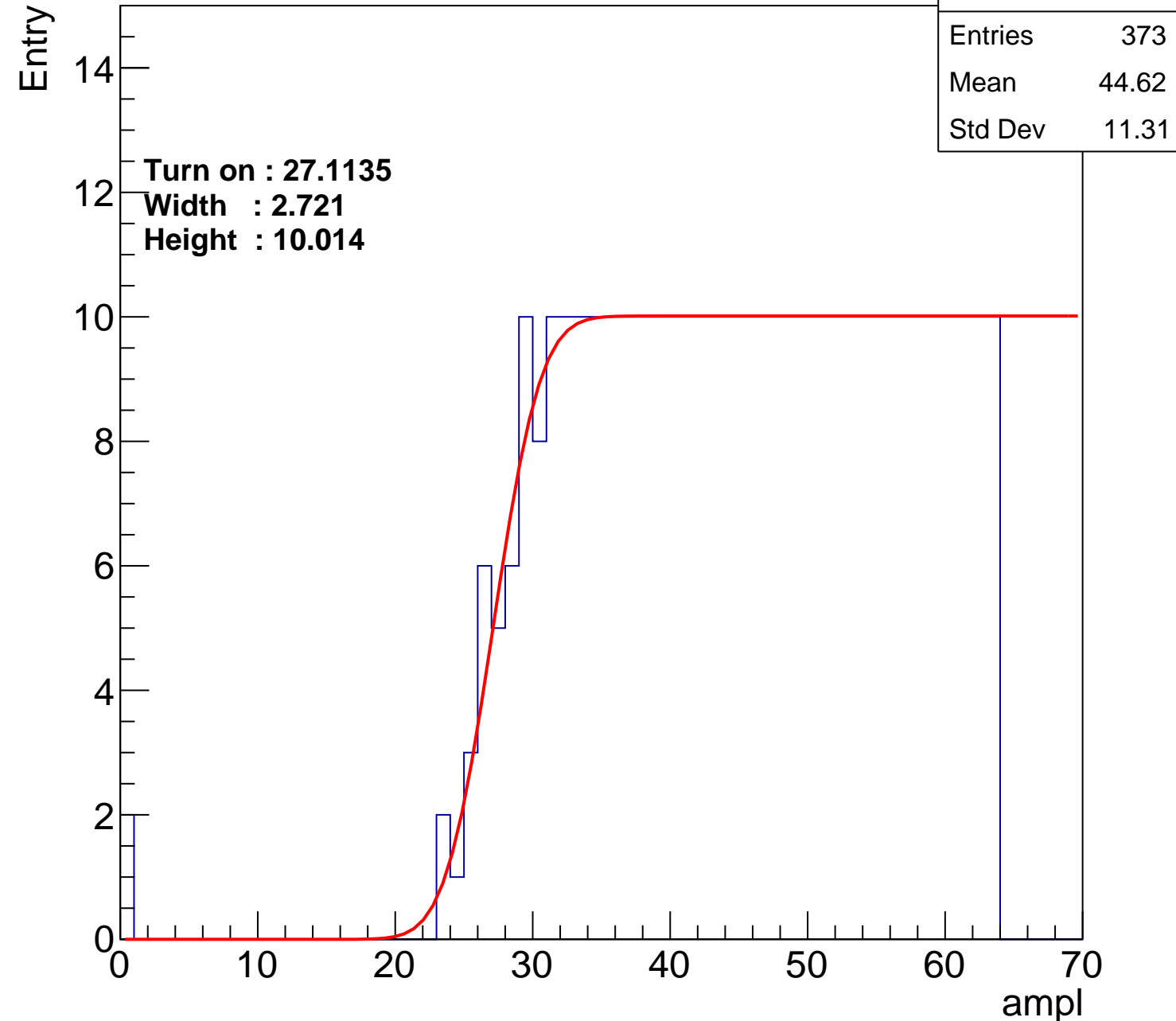
Width : 2.721

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch106

calib_packv5_042523_0143.root, FC#10, port B3

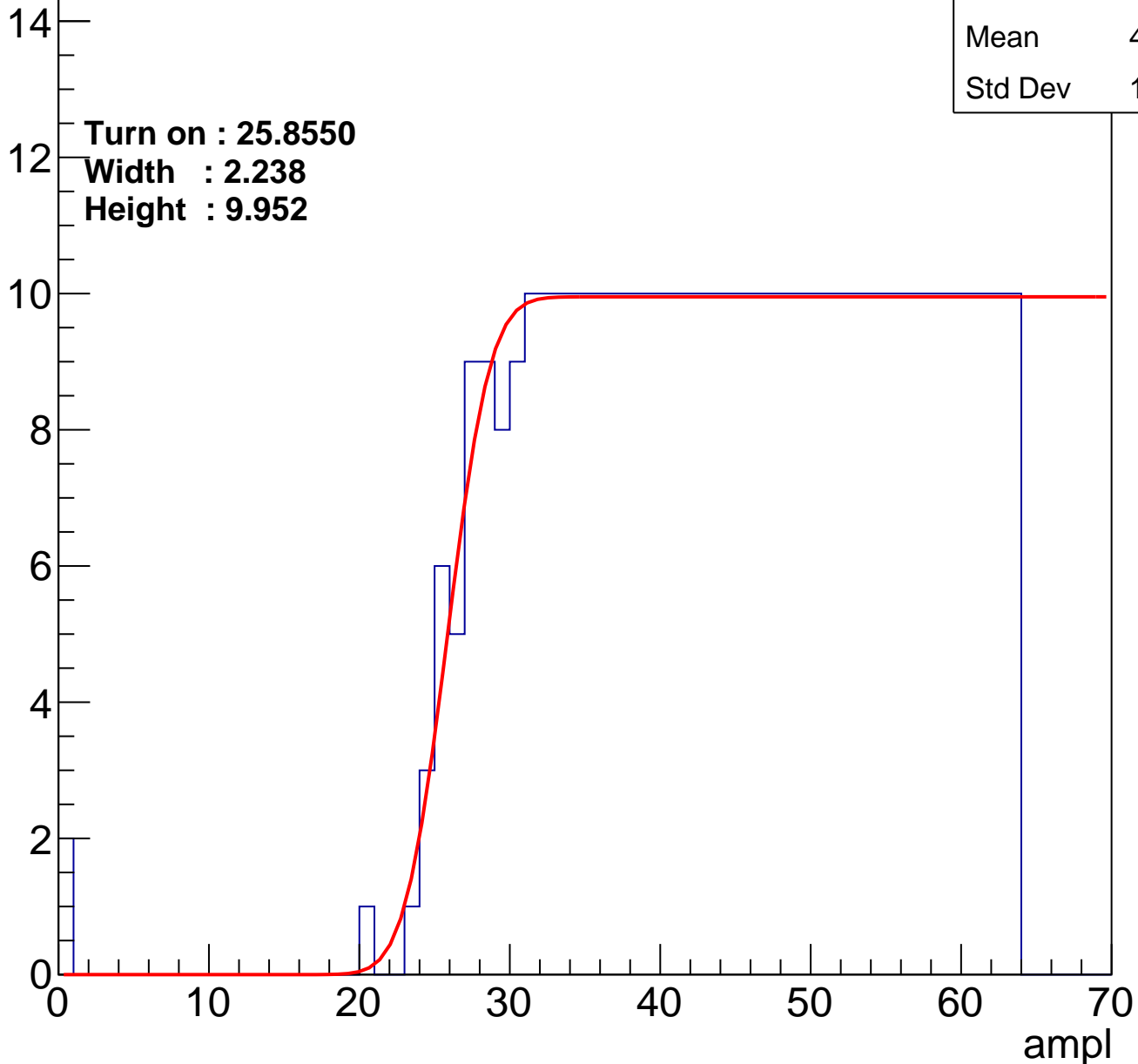
Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.8550

Width : 2.238

Height : 9.952

Entry



B1L002S, U10-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.54
Std Dev	11.33

Turn on : 26.9089

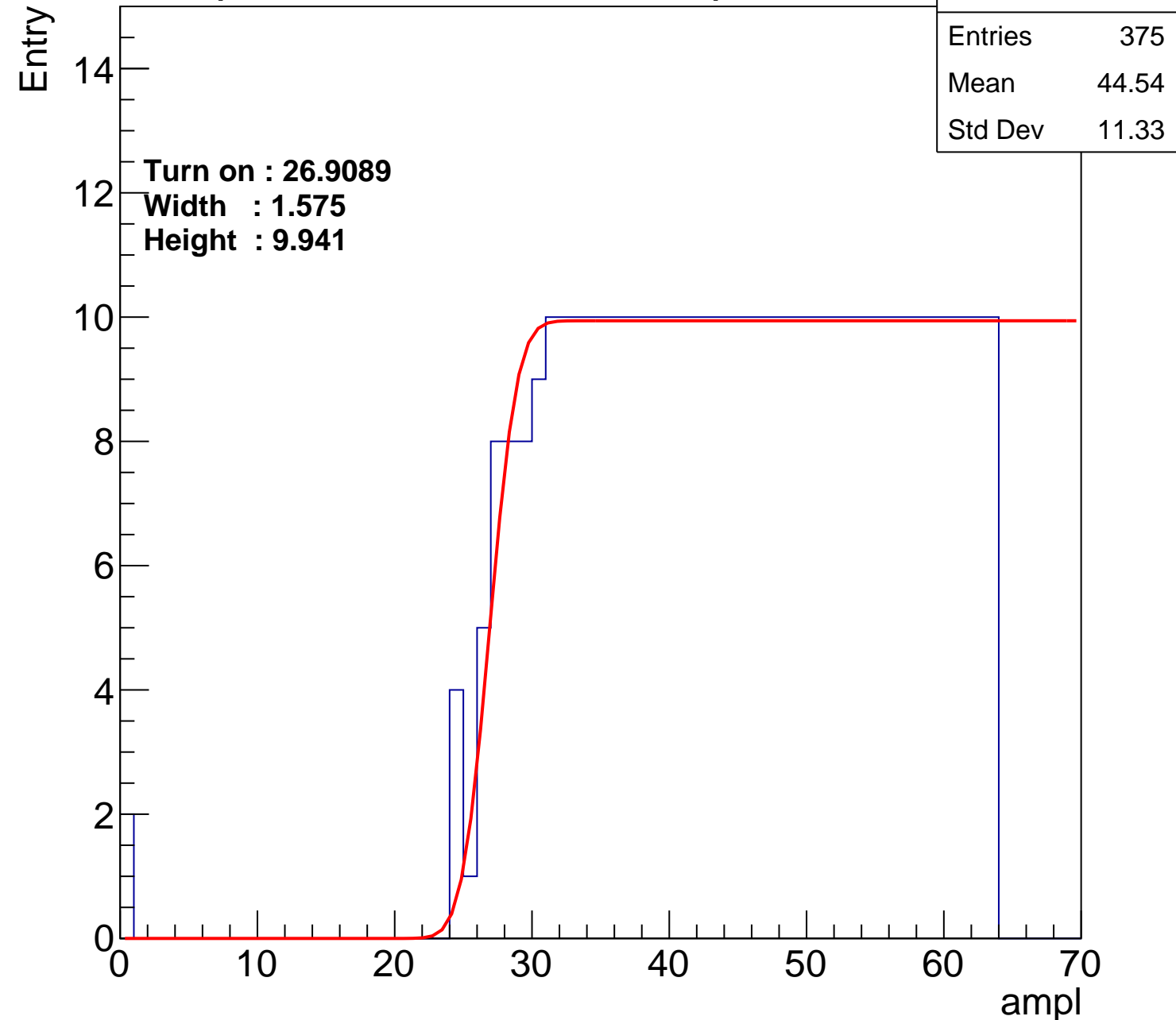
Width : 1.575

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.3
Std Dev	11.63

Turn on : 26.8217

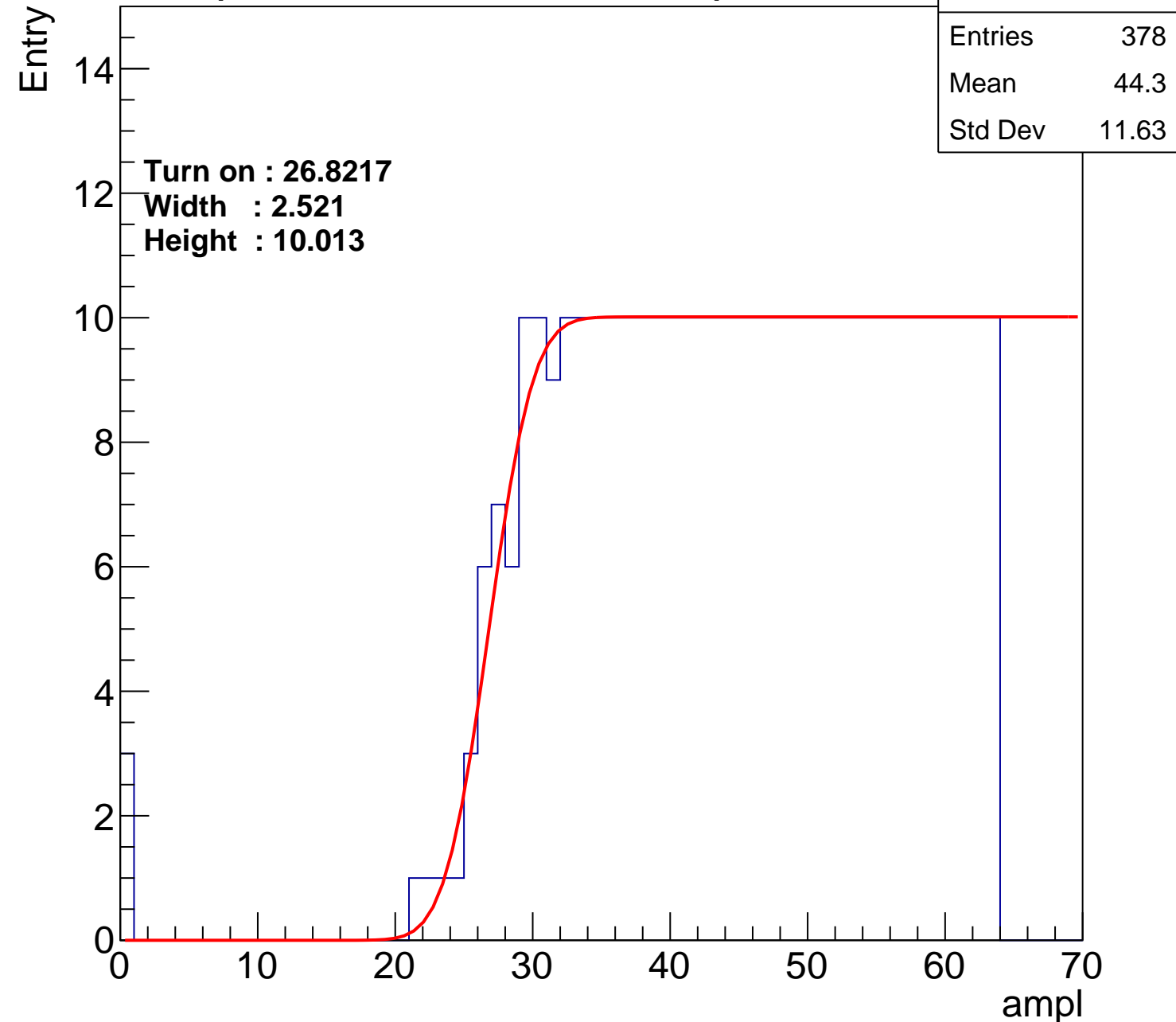
Width : 2.521

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.39
Std Dev	11.47

Turn on : 27.3163

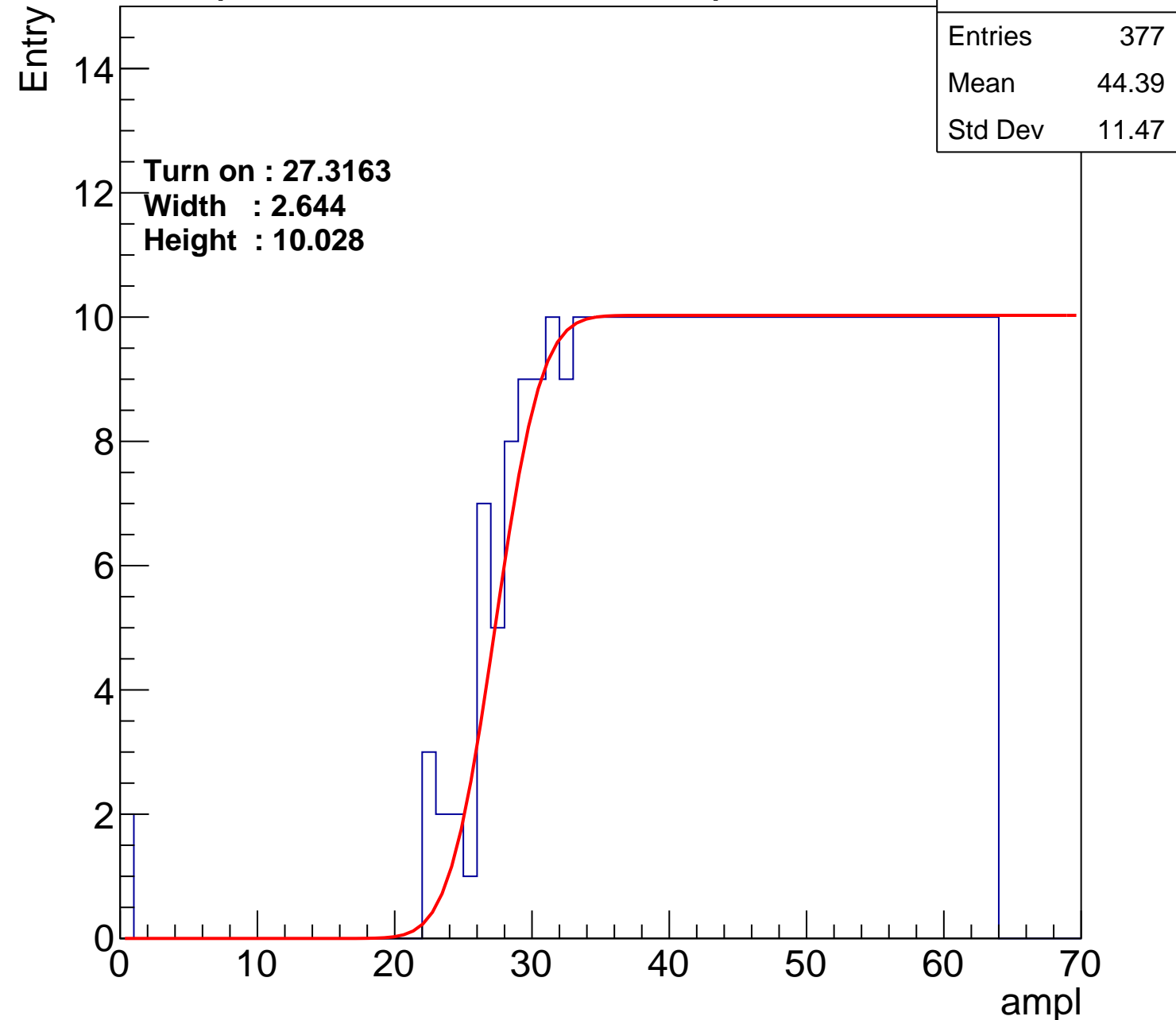
Width : 2.644

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.7950

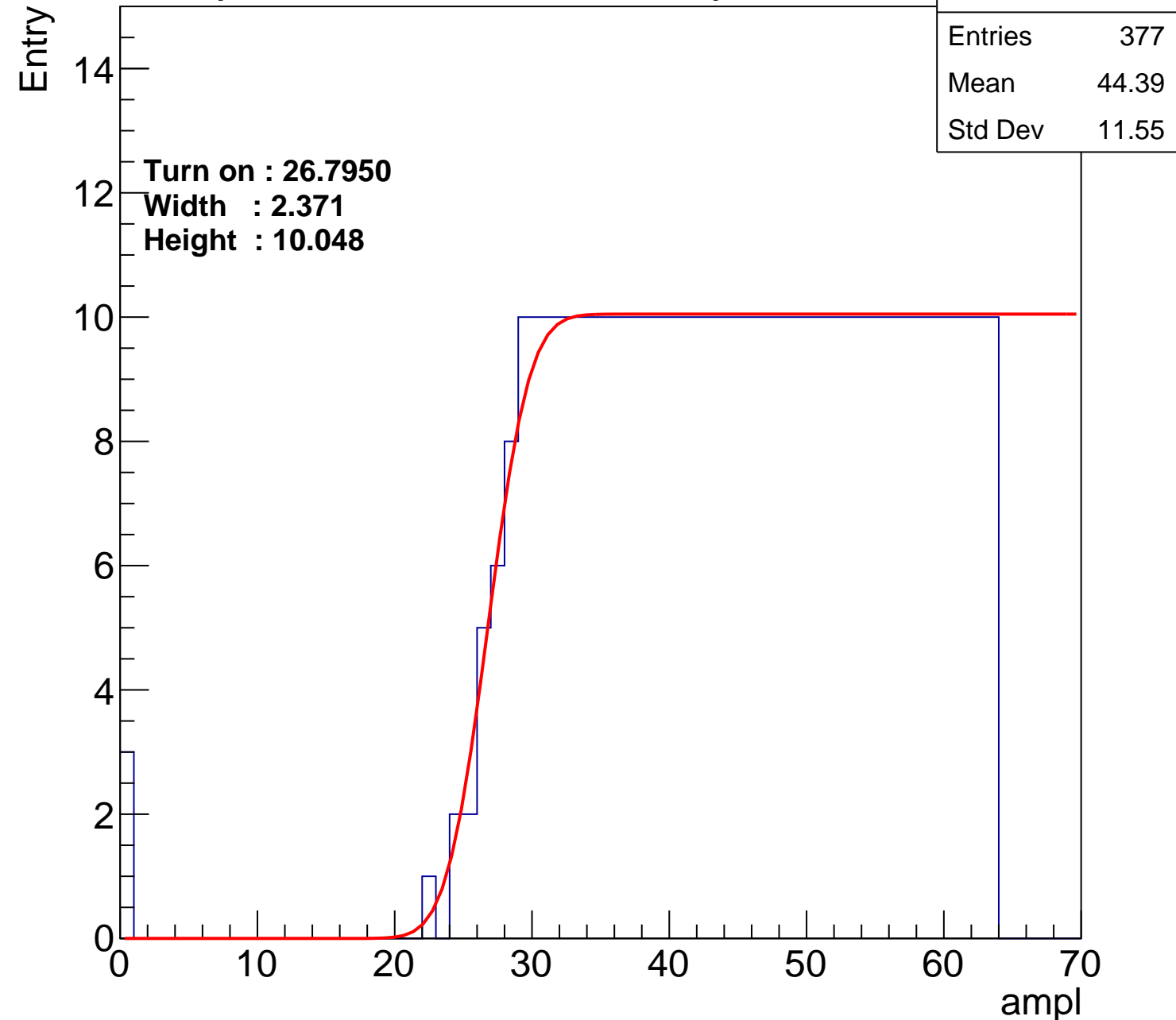
Width : 2.371

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.3337

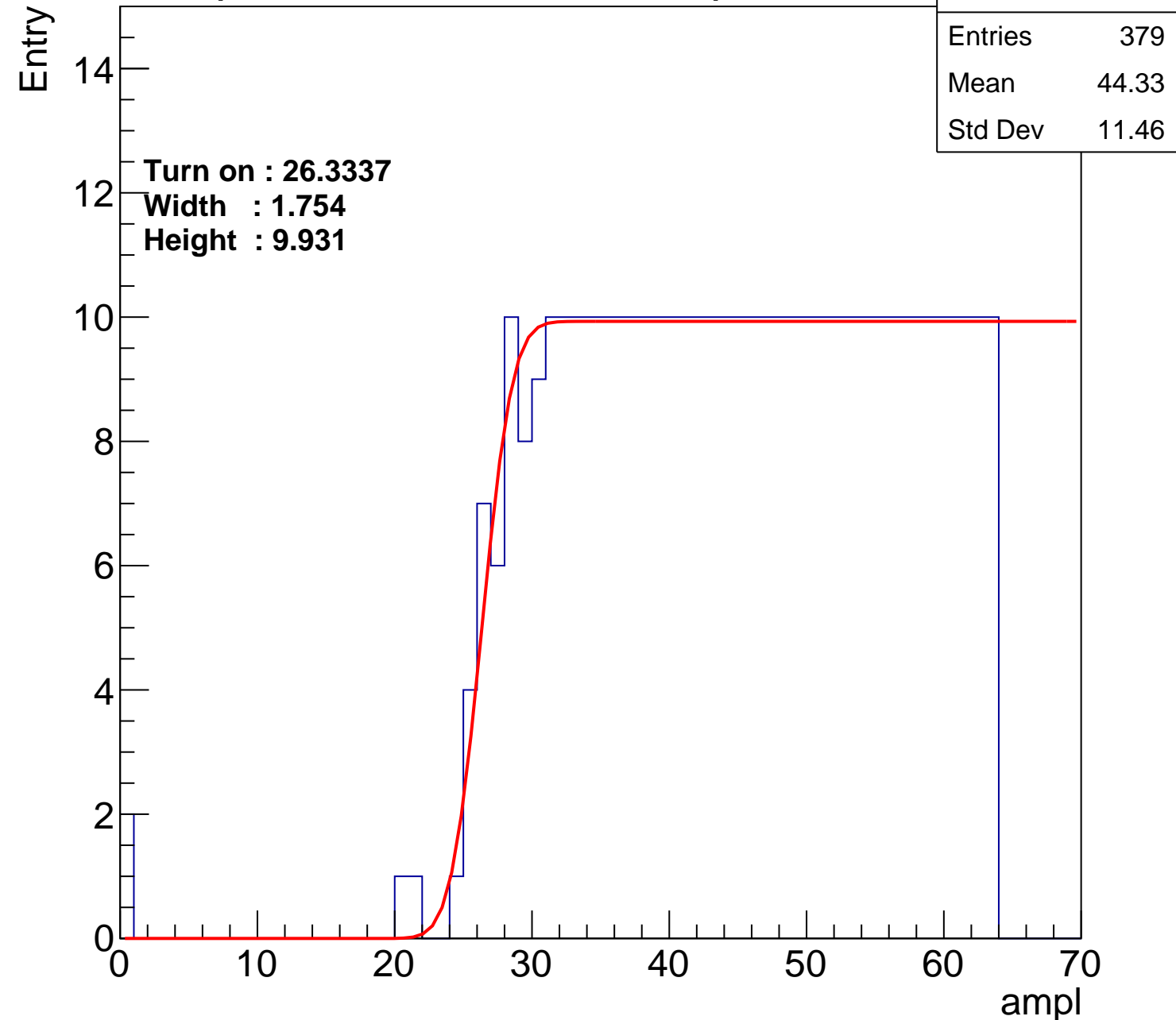
Width : 1.754

Height : 9.931

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	365
Mean	45.12
Std Dev	10.84

Turn on : 27.7131

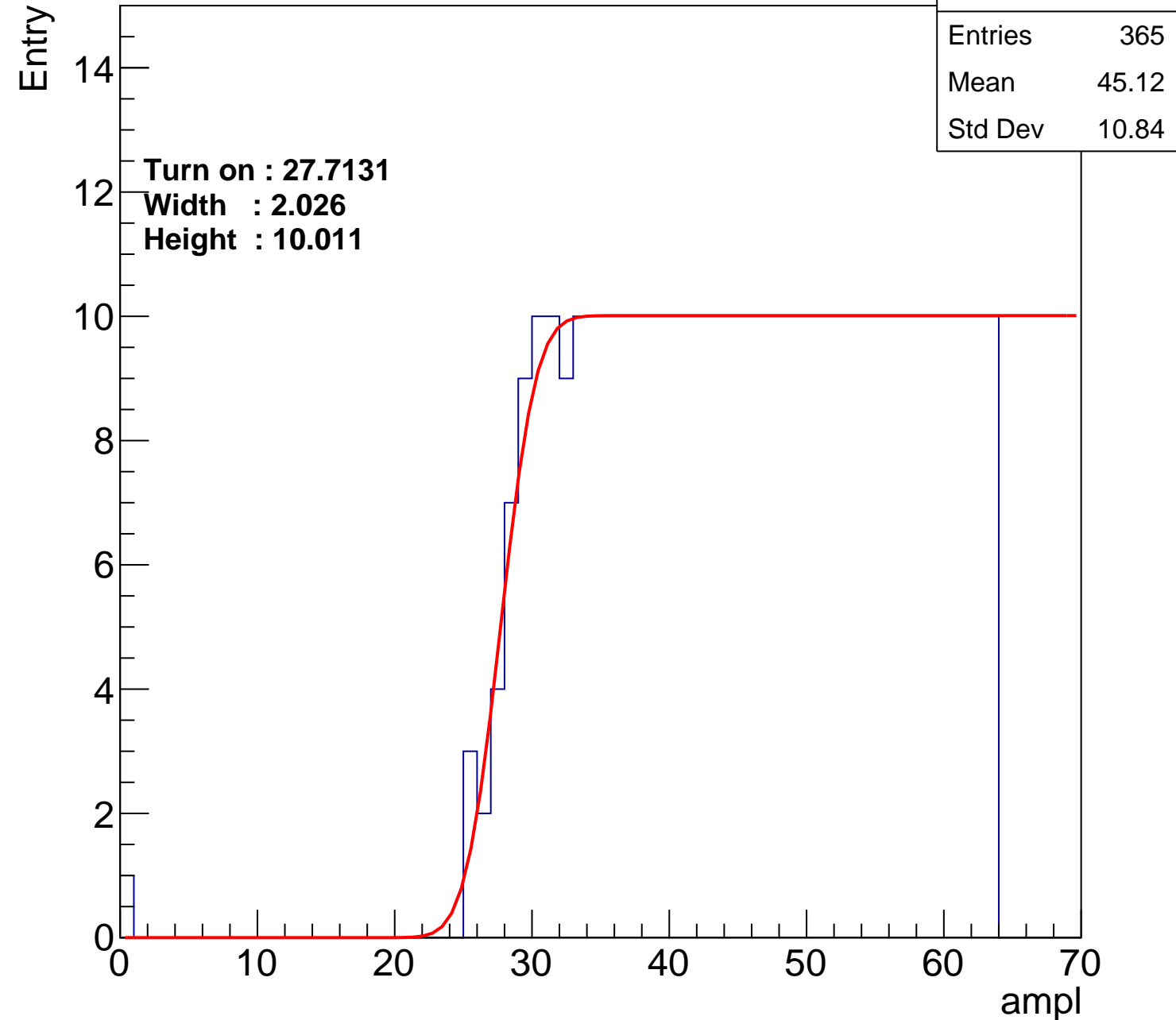
Width : 2.026

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.95
Std Dev	12.3

Turn on : 24.2170

Width : 3.149

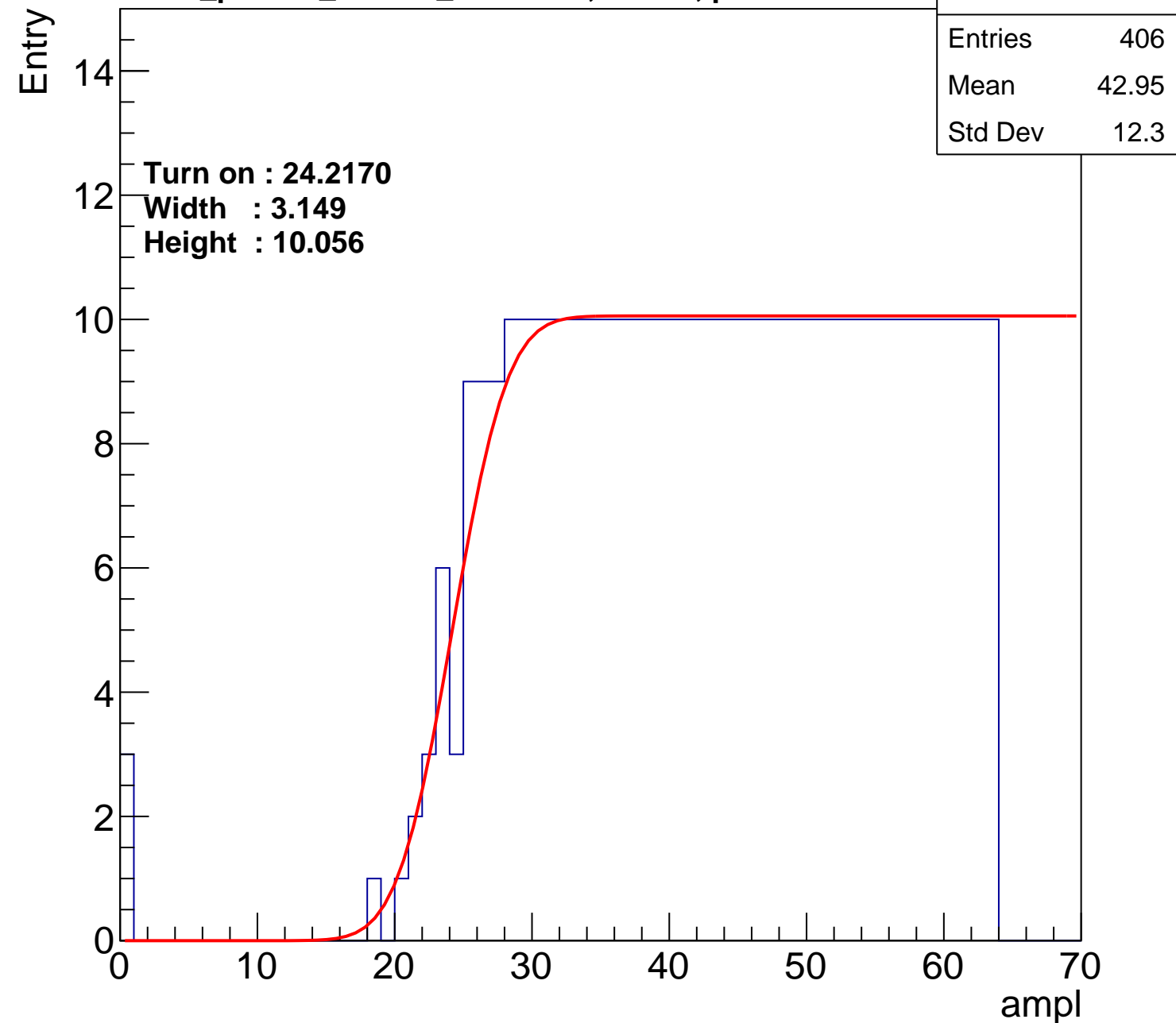
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U10-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.14
Std Dev	11.6

Turn on : 25.6117

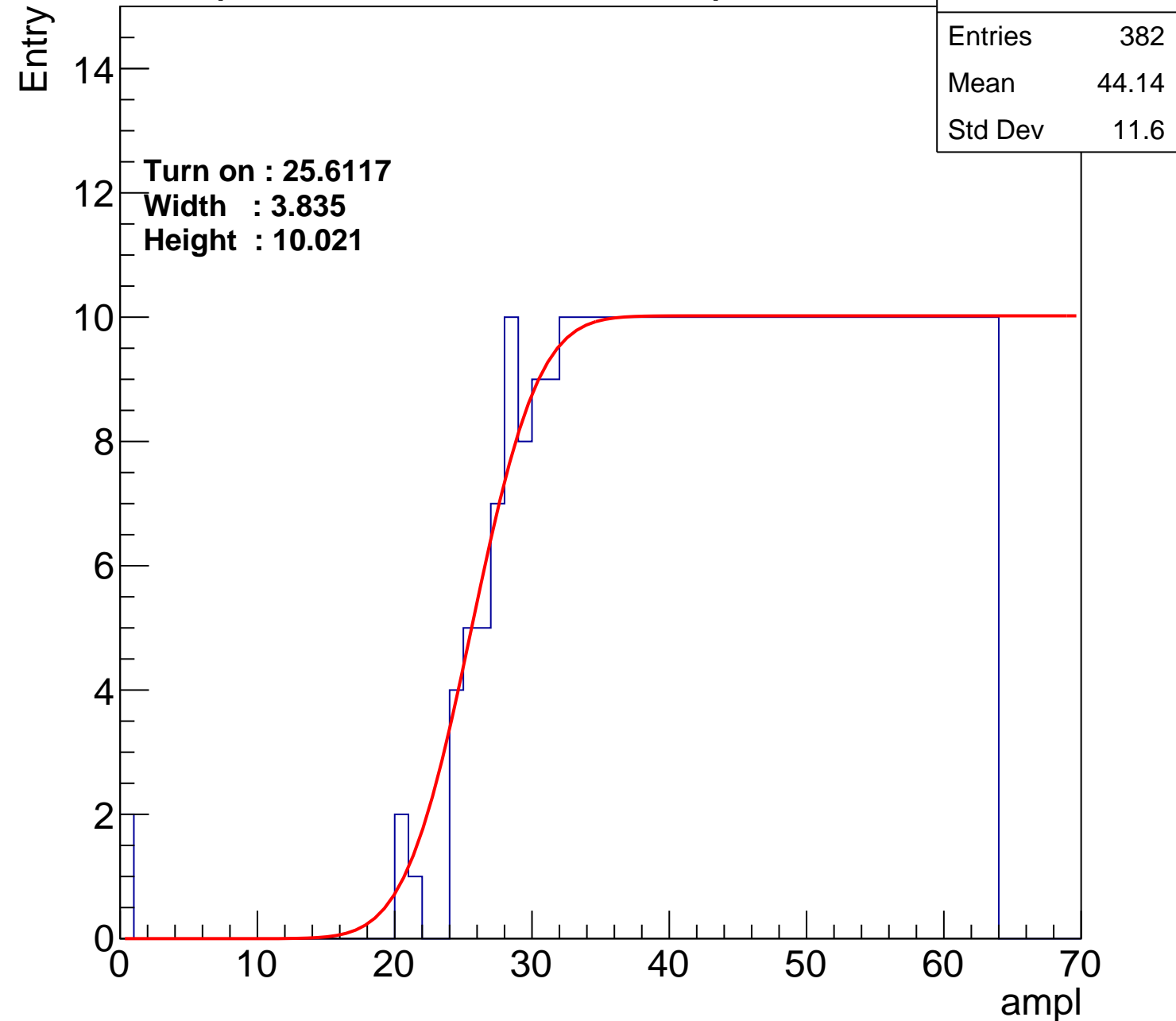
Width : 3.835

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.59
Std Dev	11.86

Turn on : 24.5968

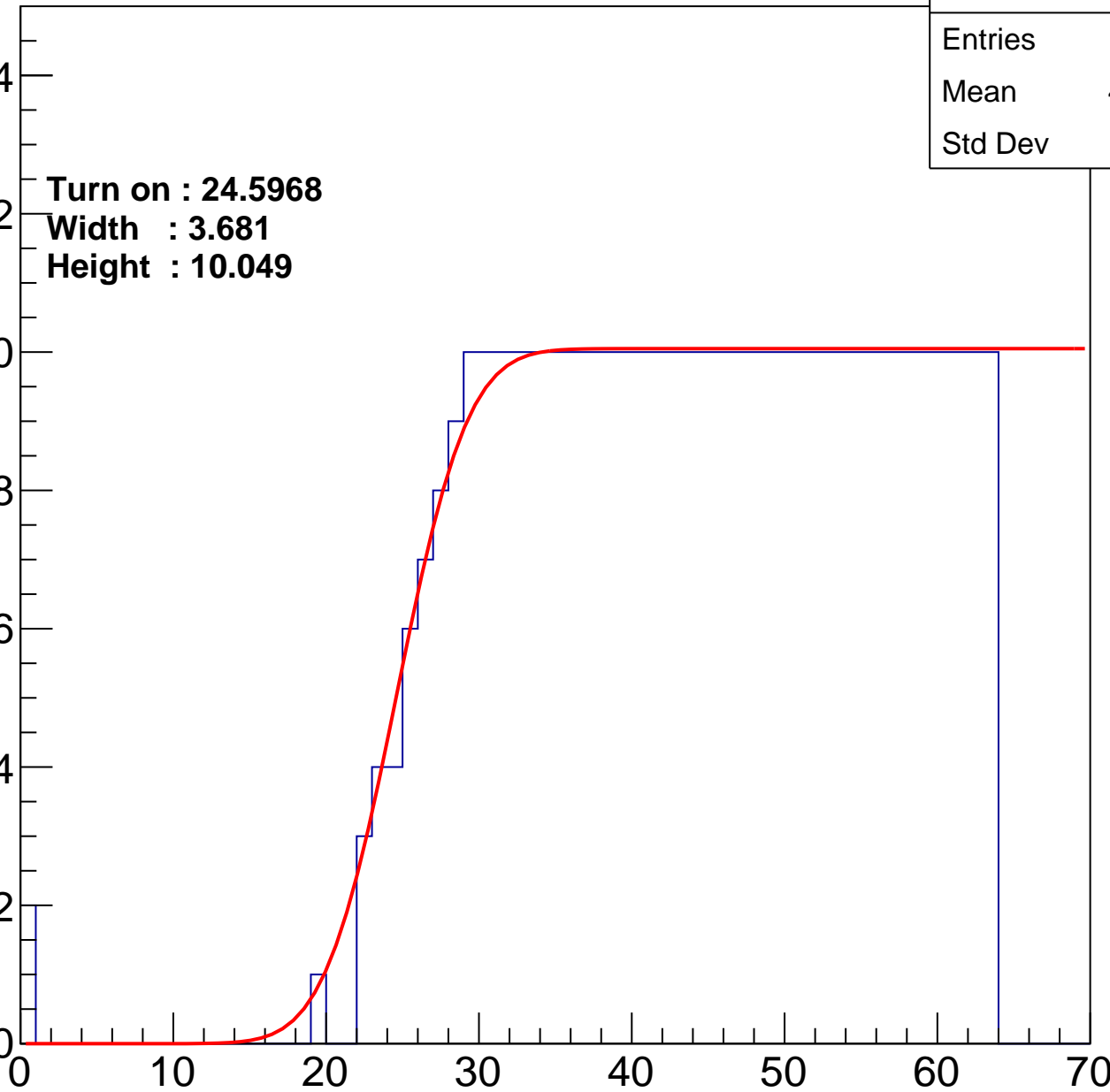
Width : 3.681

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch116

calib_packv5_042523_0143.root, FC#10, port B3

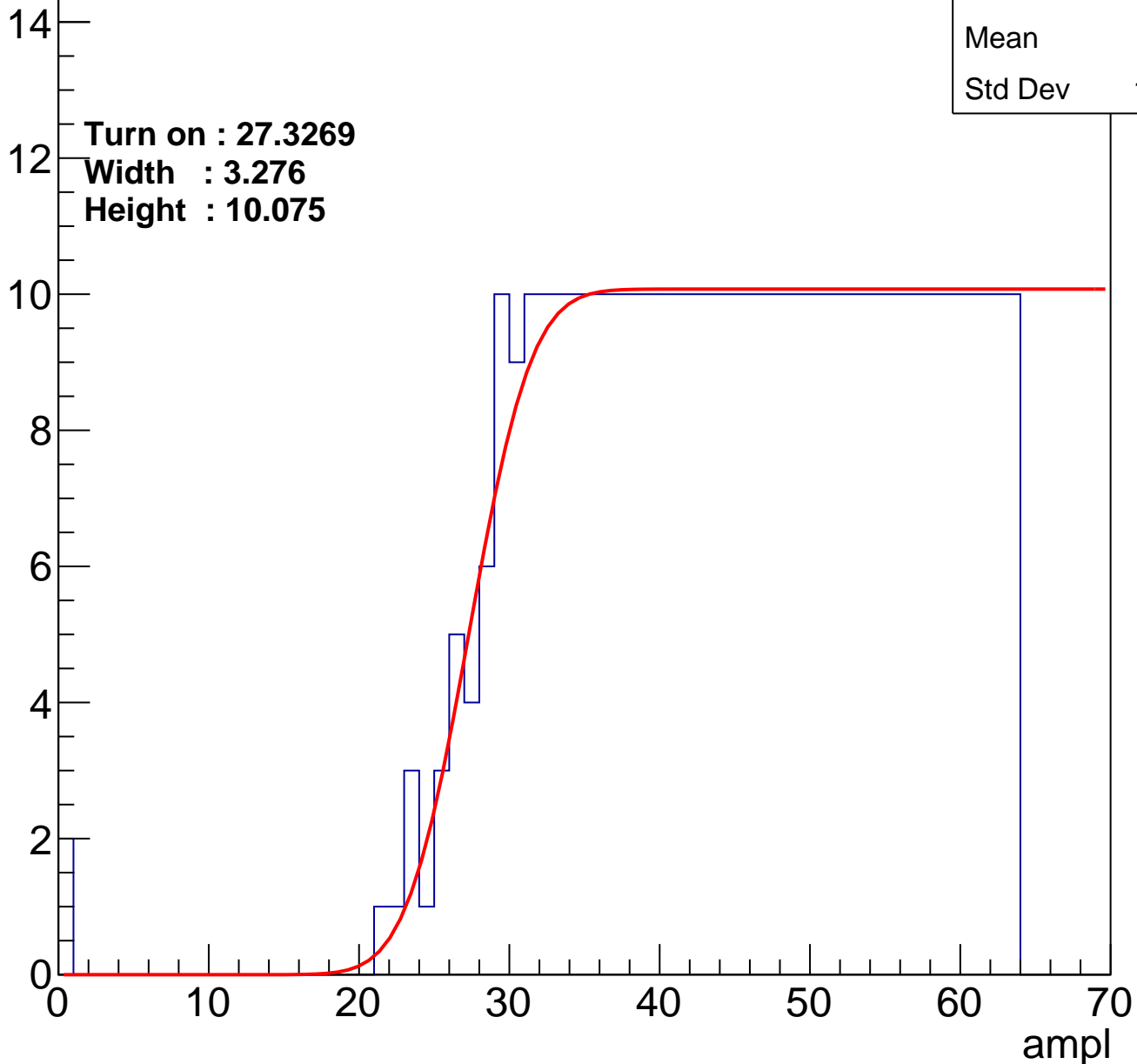
Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.3269

Width : 3.276

Height : 10.075

Entry



B1L002S, U10-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.24
Std Dev	11.75

Turn on : 26.7135

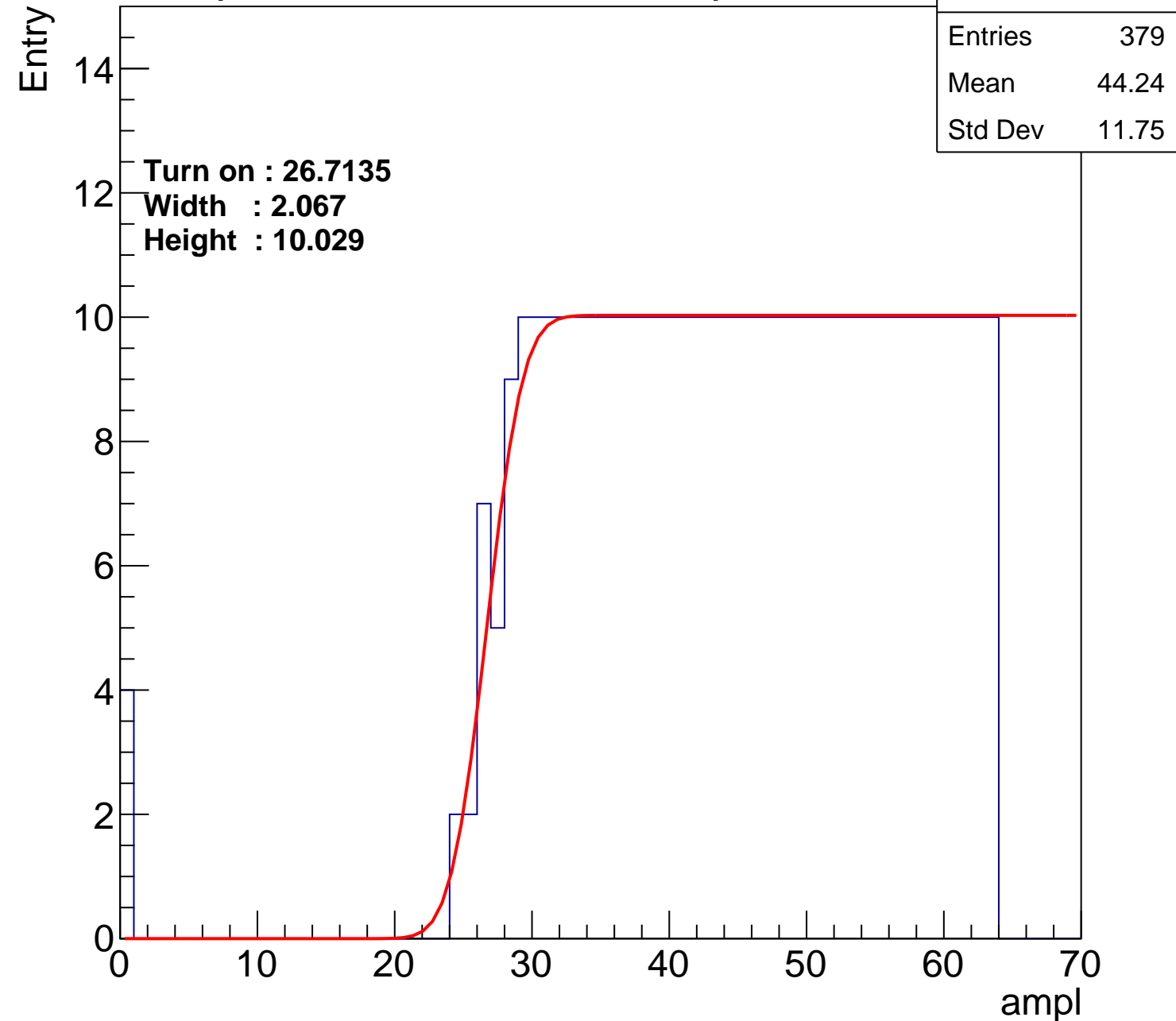
Width : 2.067

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.75

Turn on : 25.7808

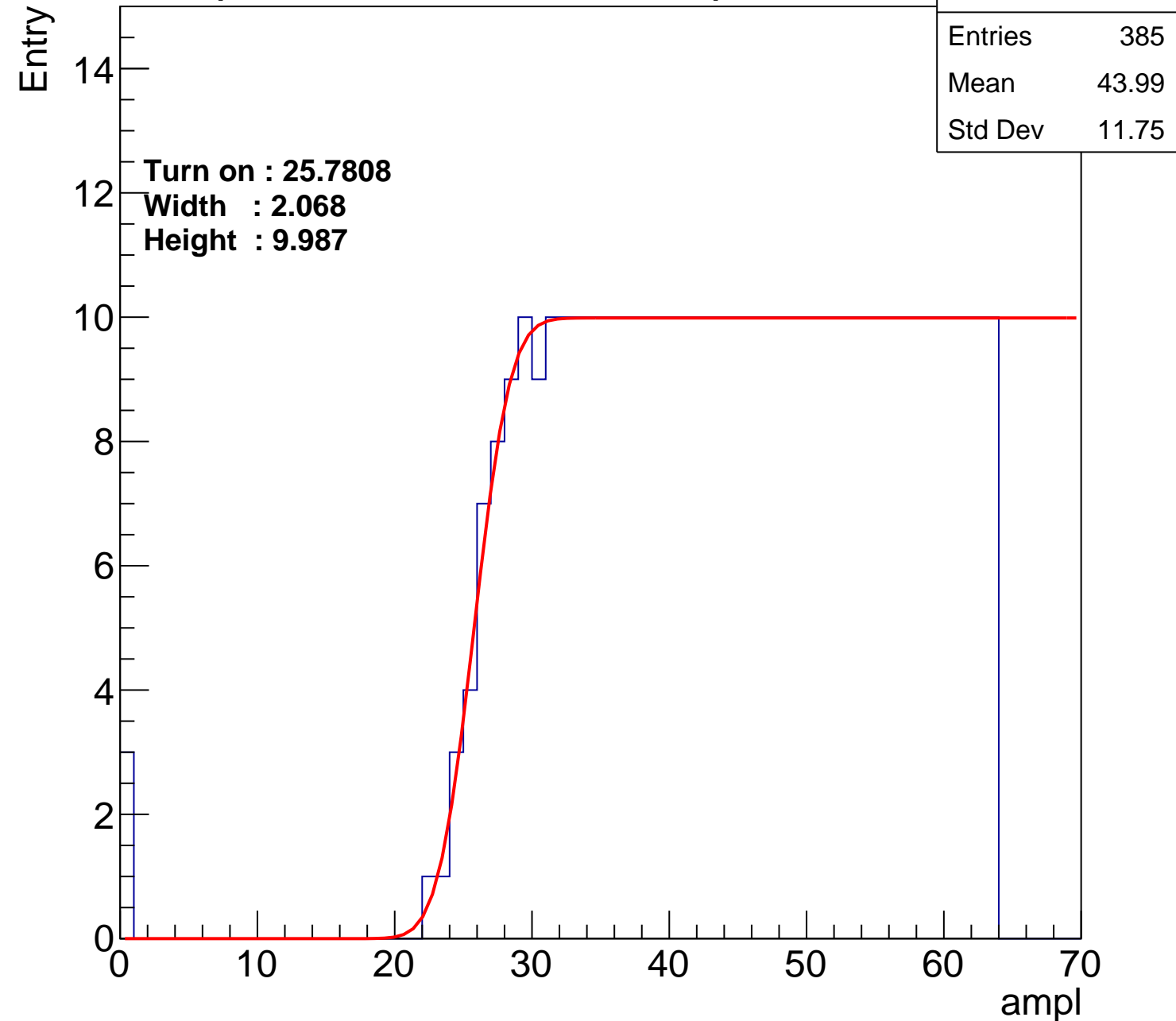
Width : 2.068

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.64
Std Dev	11.62

Turn on : 27.4720

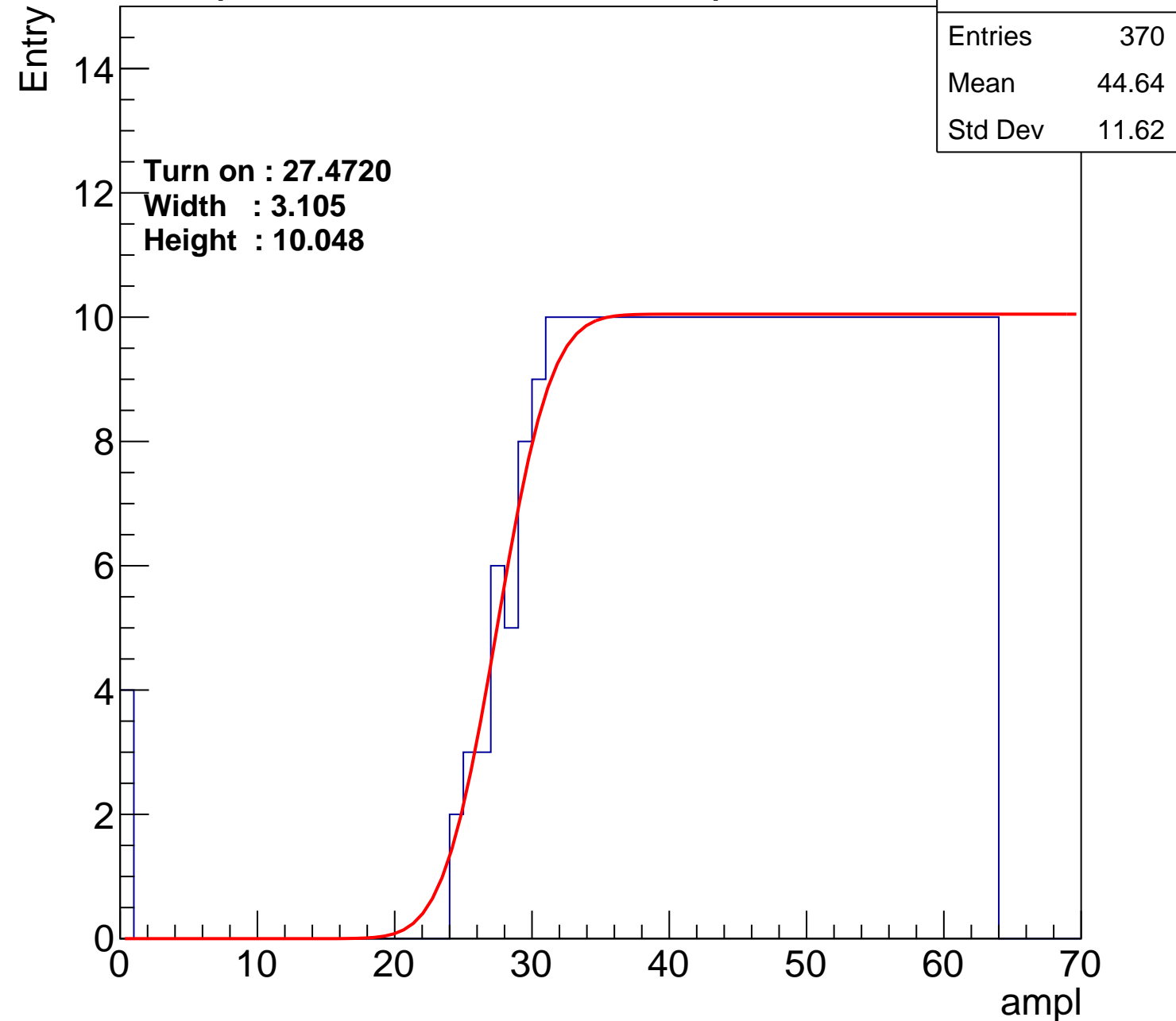
Width : 3.105

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.85
Std Dev	12.11

Turn on : 26.0635

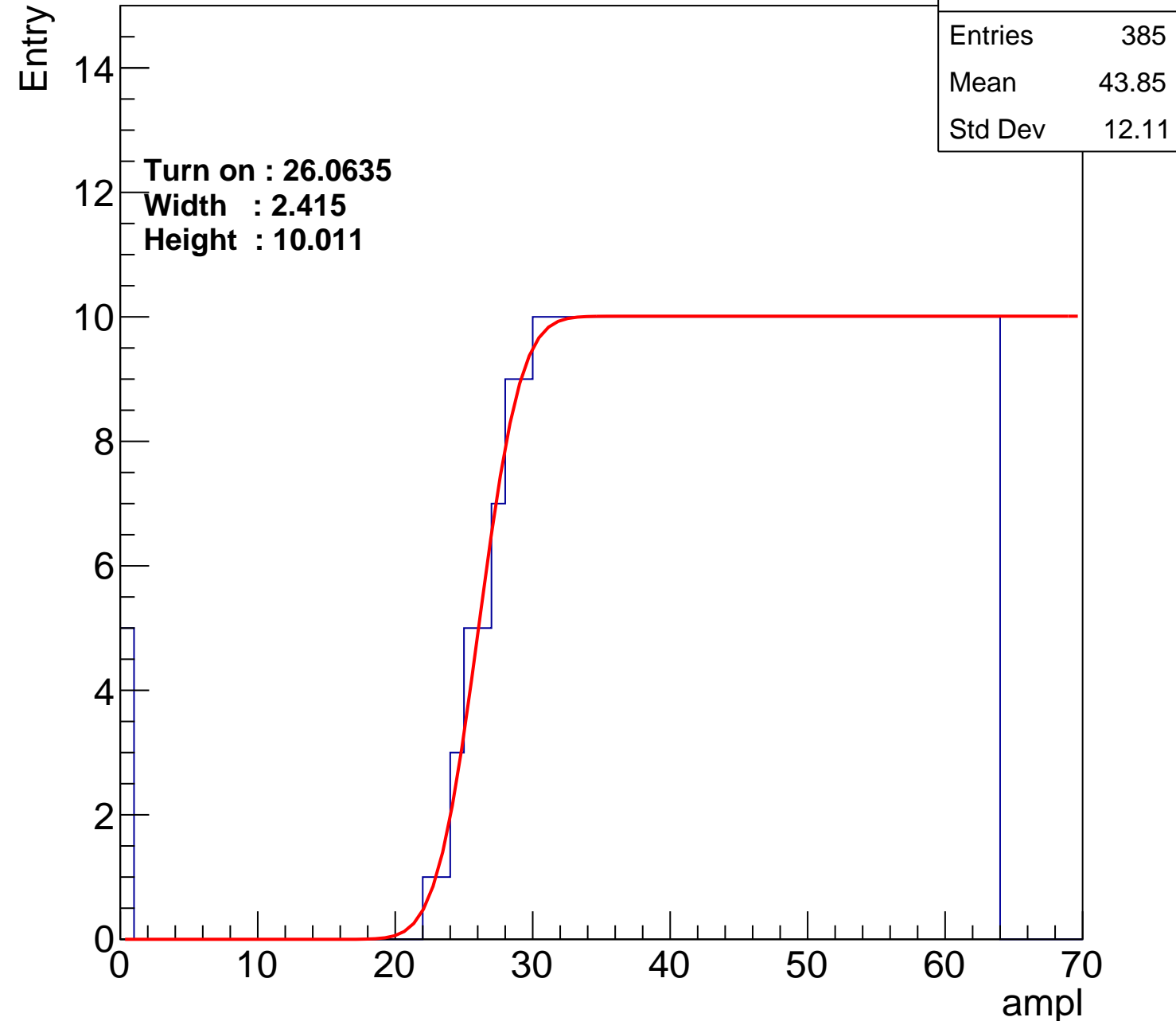
Width : 2.415

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.35
Std Dev	11.29

Turn on : 25.8634

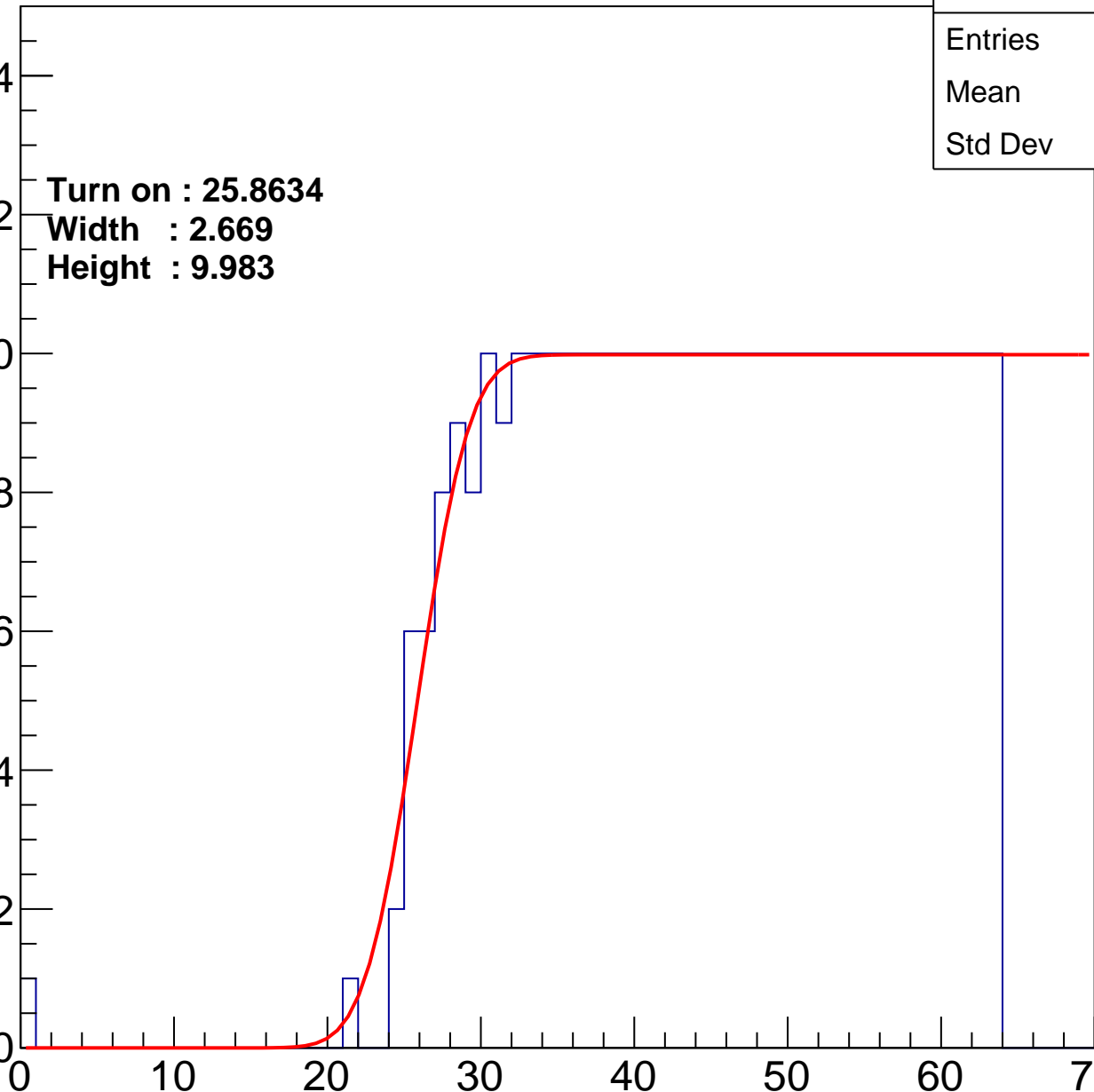
Width : 2.669

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch122

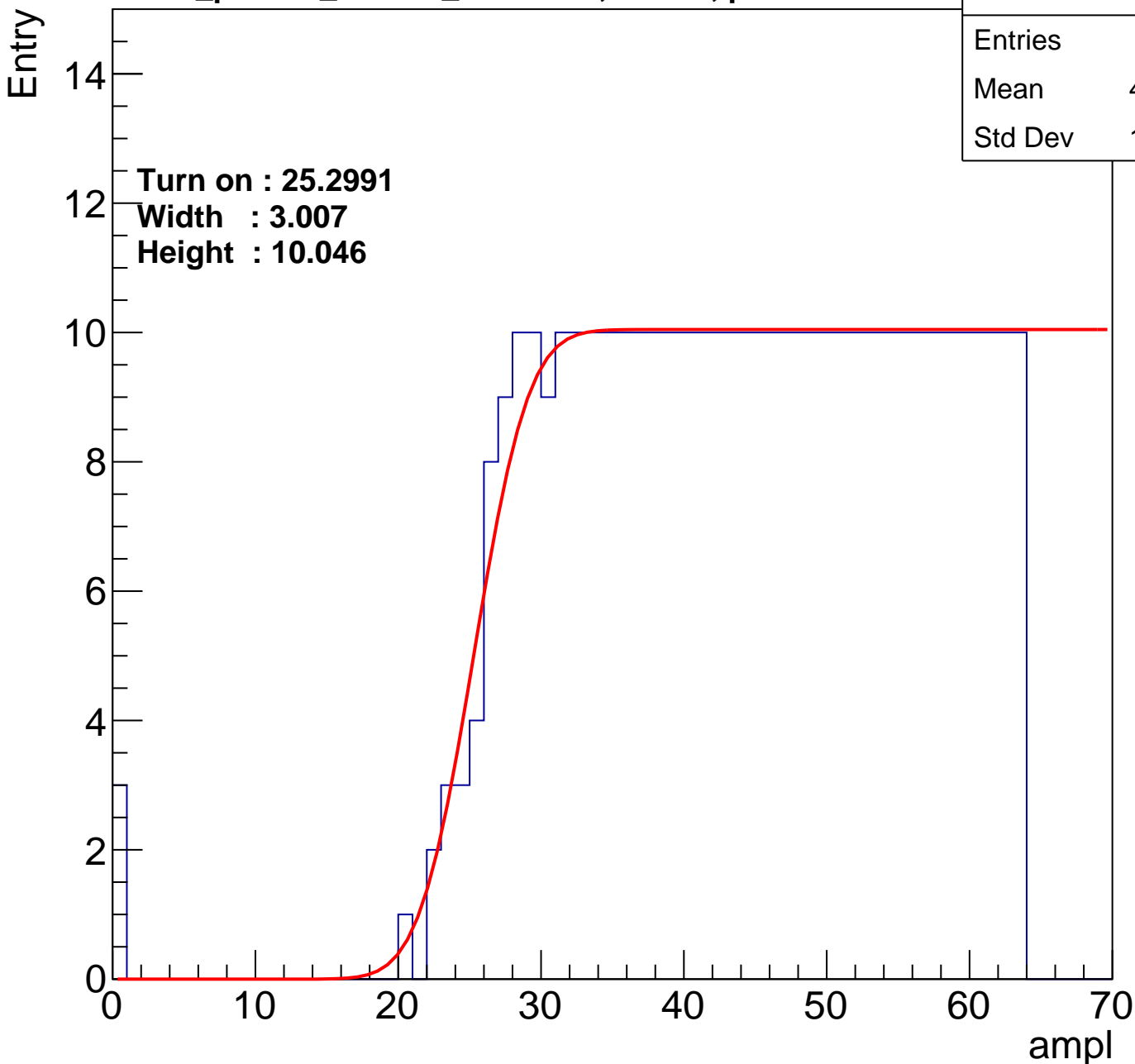
calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.64
Std Dev	11.94

Turn on : 25.2991

Width : 3.007

Height : 10.046



B1L002S, U10-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.0468

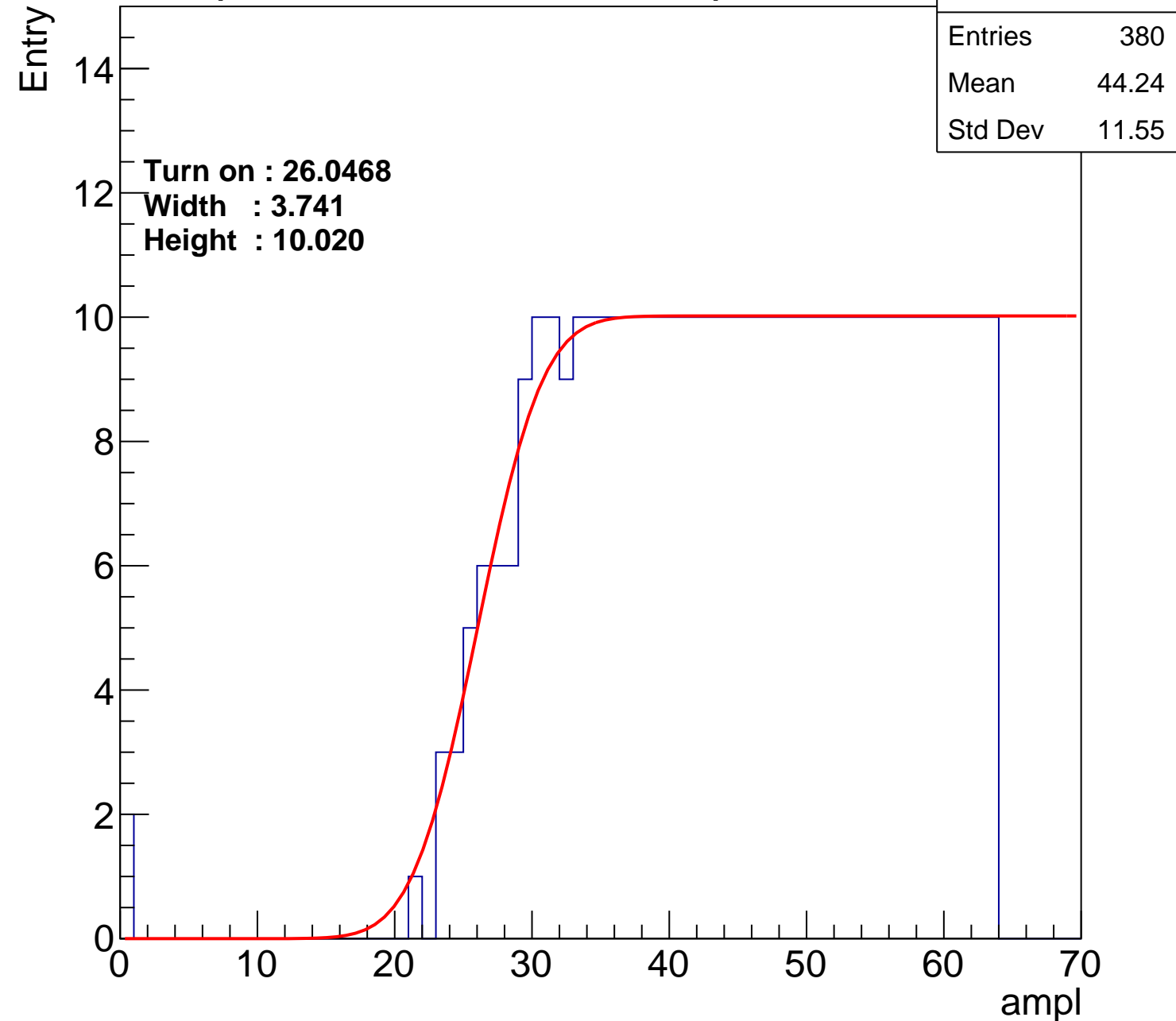
Width : 3.741

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.33
Std Dev	11.7

Turn on : 26.7977

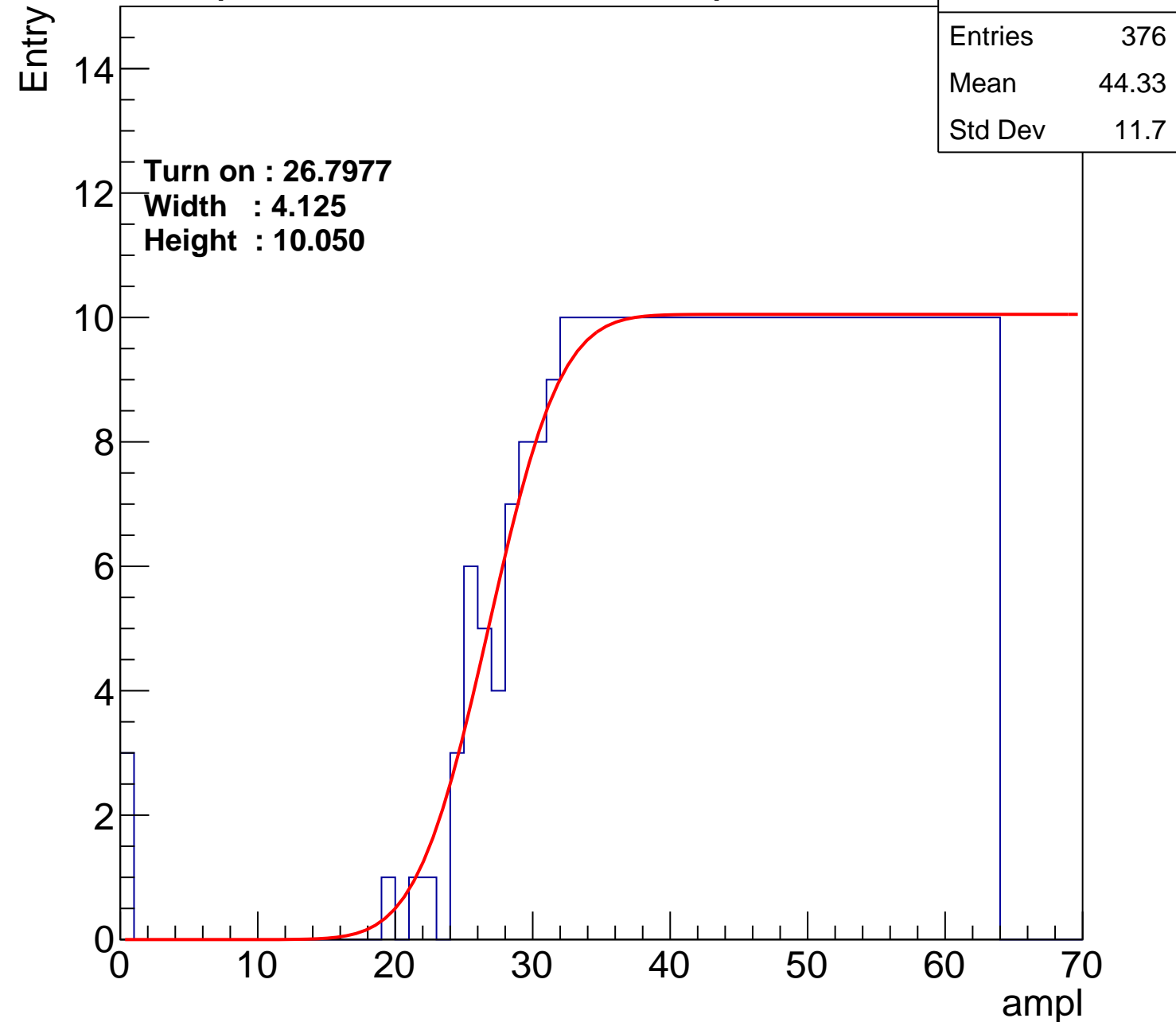
Width : 4.125

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.42
Std Dev	11.97

Turn on : 25.2676

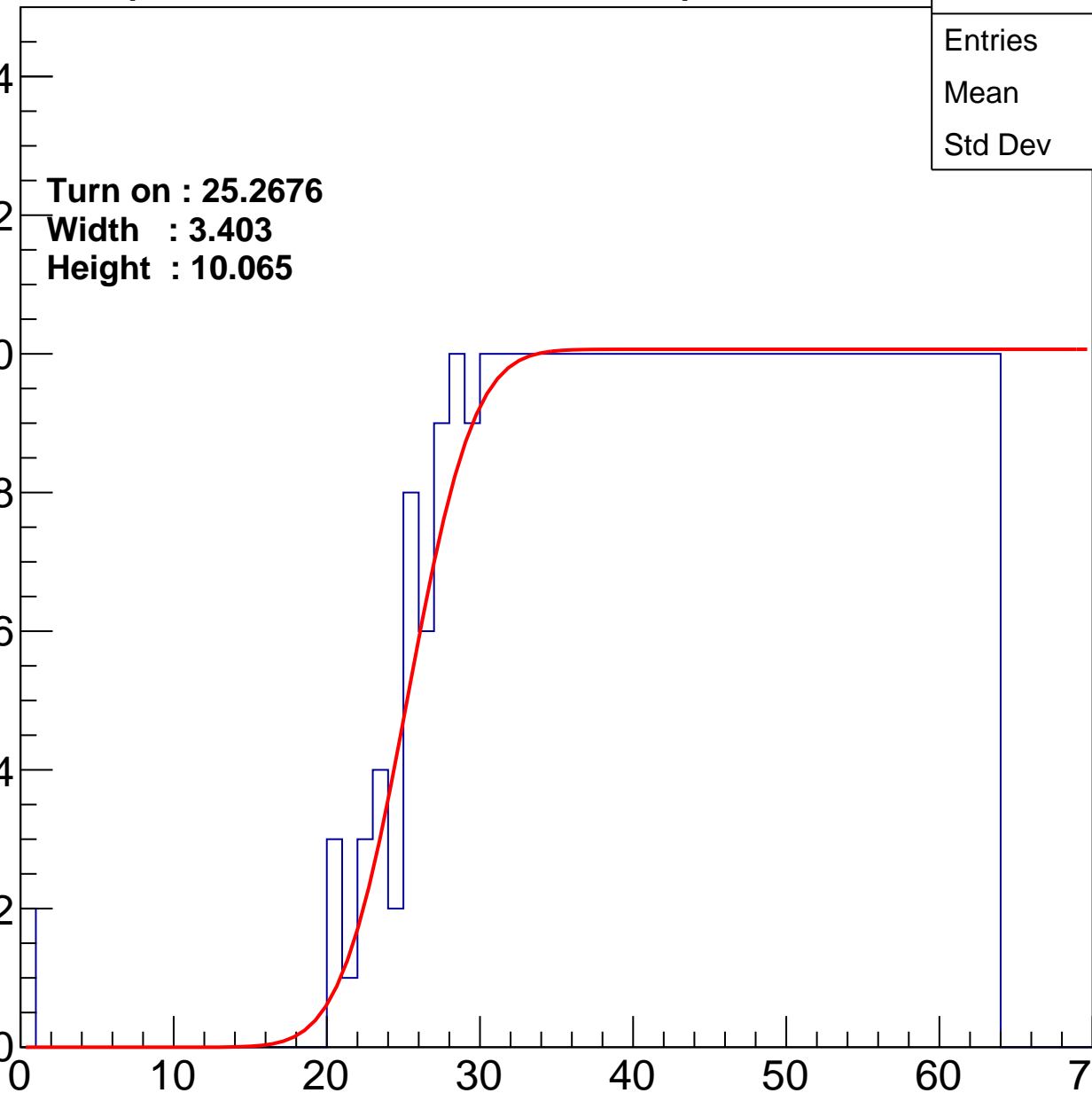
Width : 3.403

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.79

Turn on : 25.4139

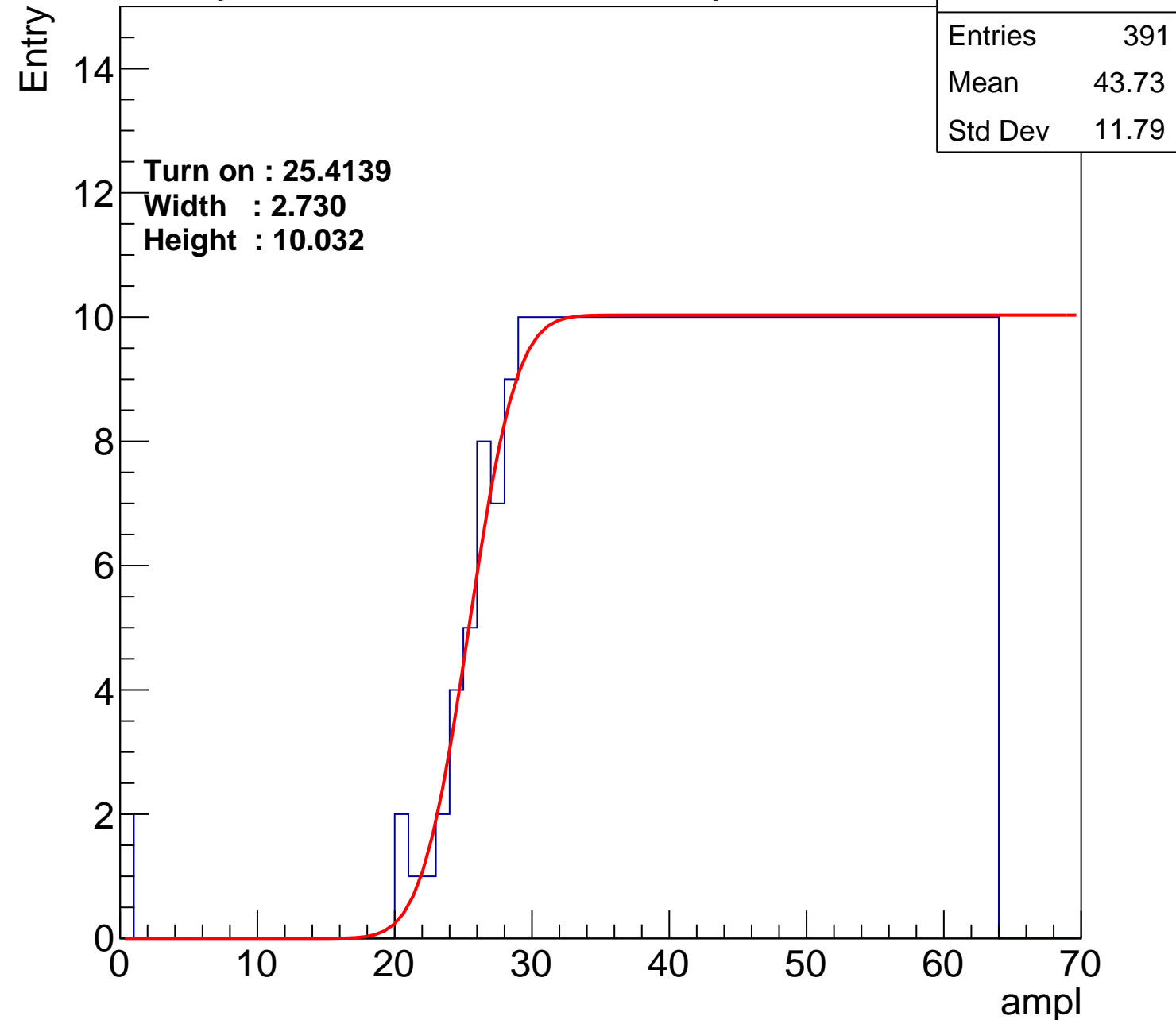
Width : 2.730

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.92
Std Dev	11.53

Turn on : 25.8177

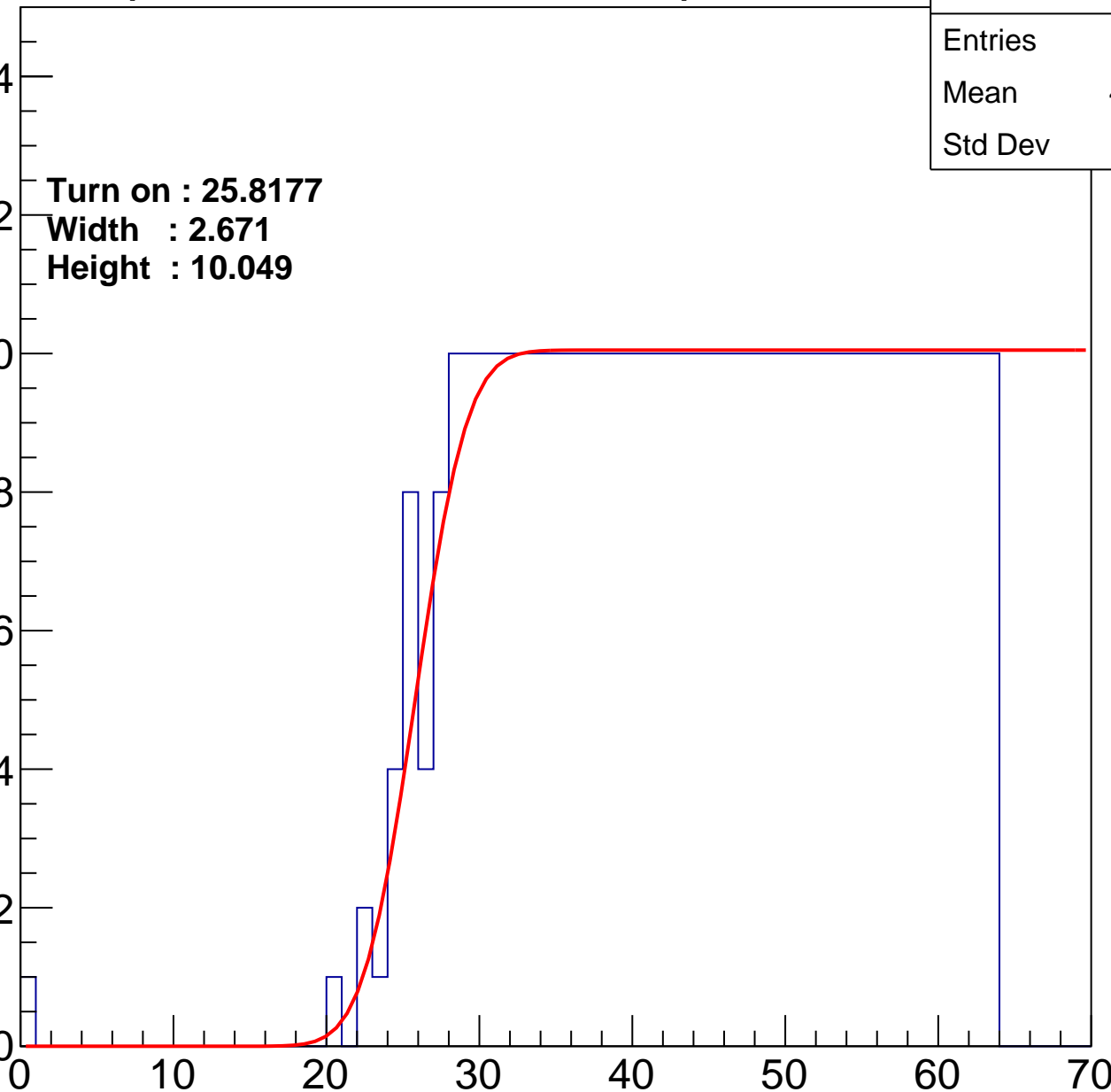
Width : 2.671

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U10-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.92
Std Dev	11.53

Turn on : 25.8177

Width : 2.671

Height : 10.049

Entry

