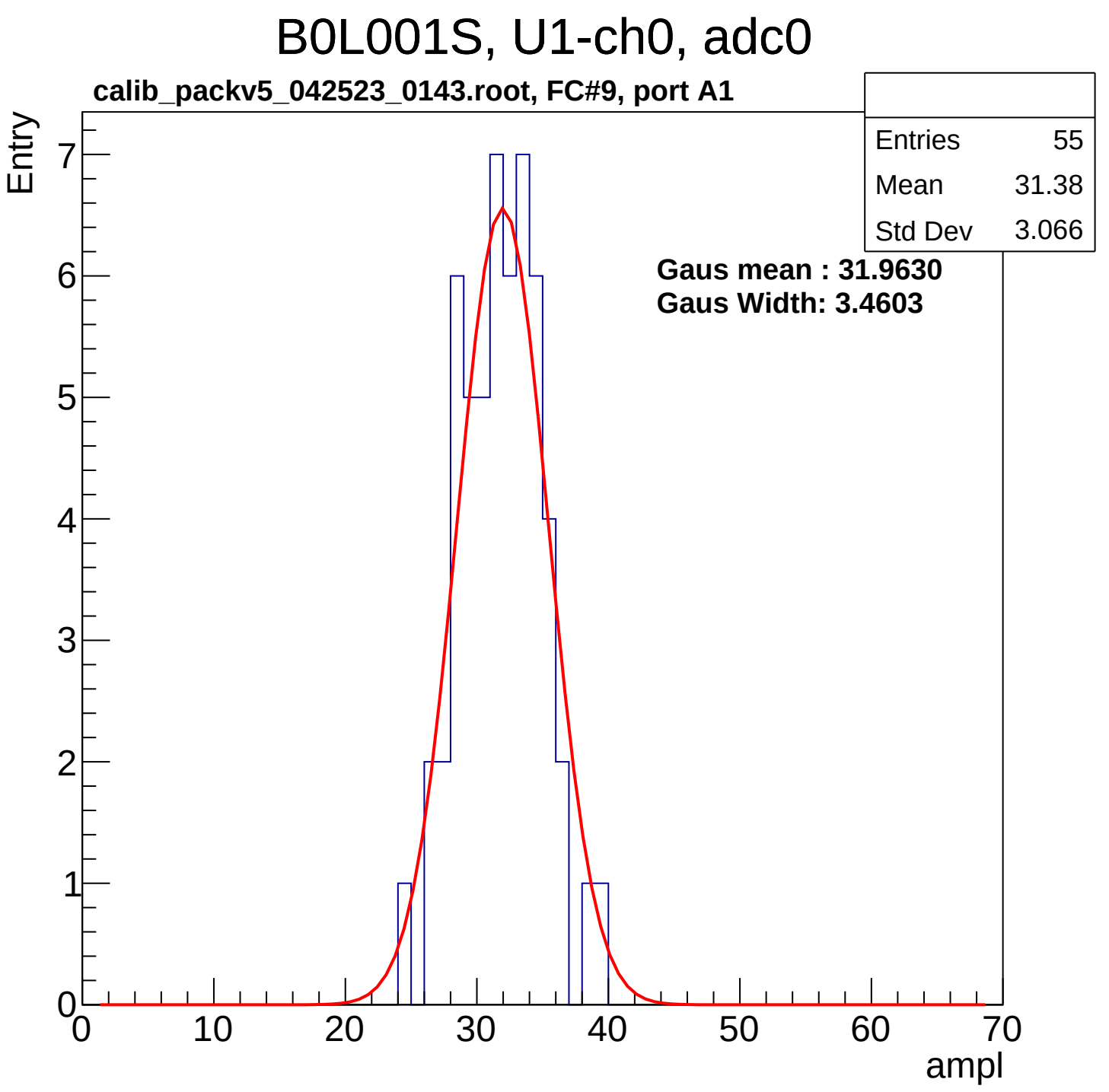
B0L001S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 31.38
Std Dev 3.066
Gaus mean : 31.9630
Gaus Width: 3.4603
Entry
ampl
0
1
2
3
4
5
6
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0
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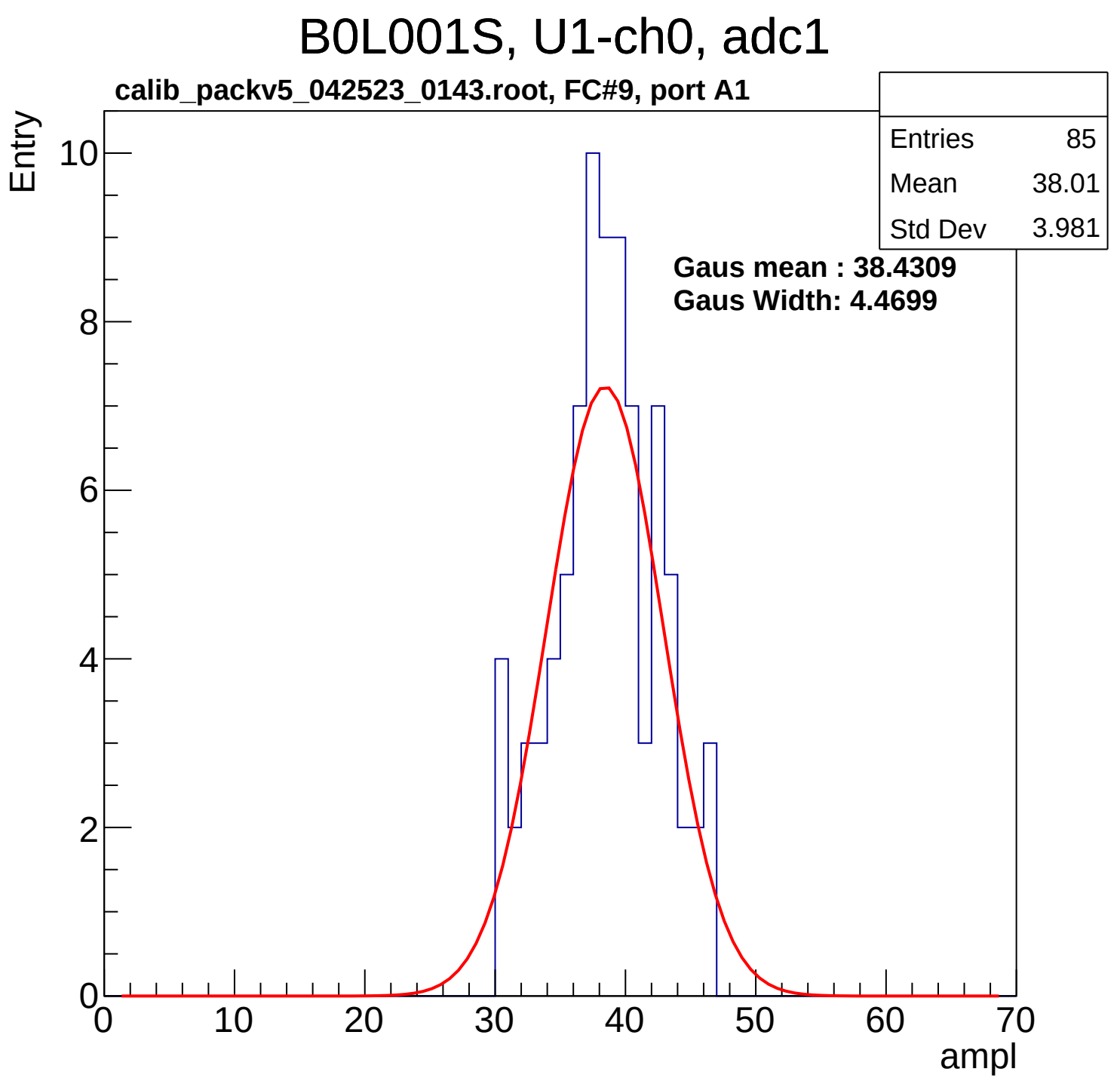
B0L001S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 38.01
Std Dev 3.981
Gaus mean : 38.4309
Gaus Width: 4.4699
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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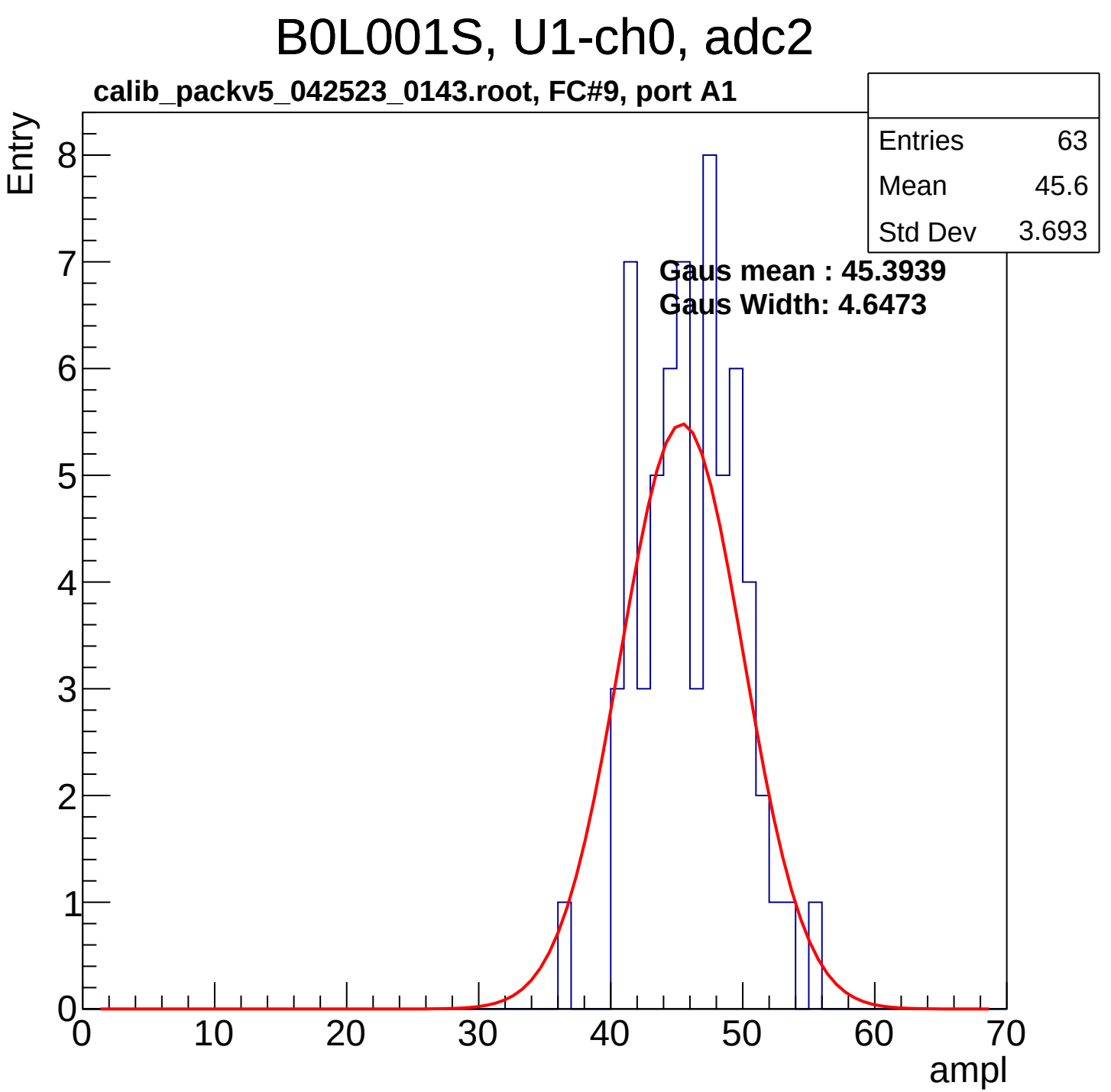
B0L001S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 45.6
Std Dev 3.693
Gaus mean : 45.3939
Gaus Width: 4.6473
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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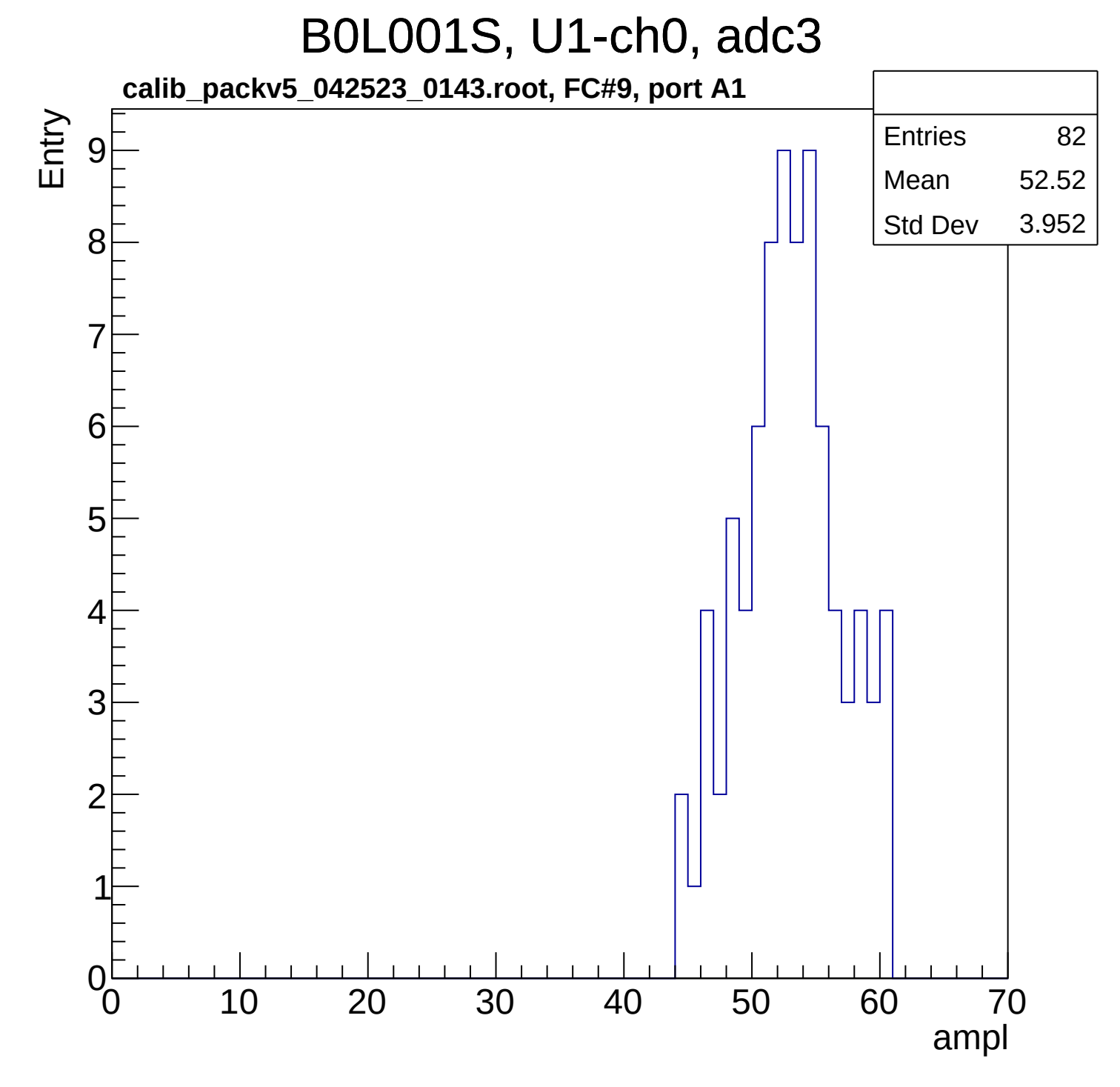
B0L001S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 52.52
Std Dev 3.952
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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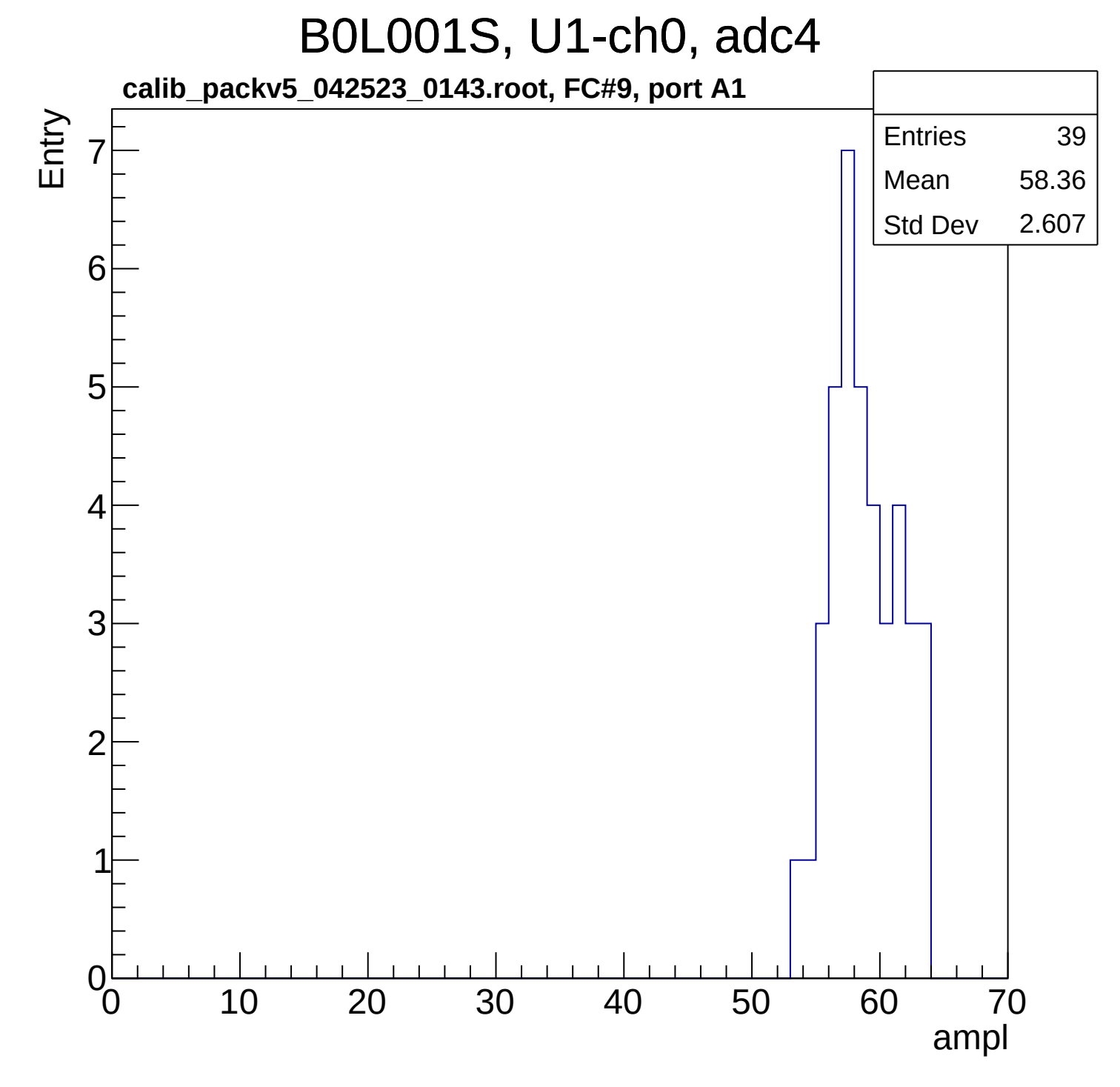
B0L001S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.36
Std Dev 2.607
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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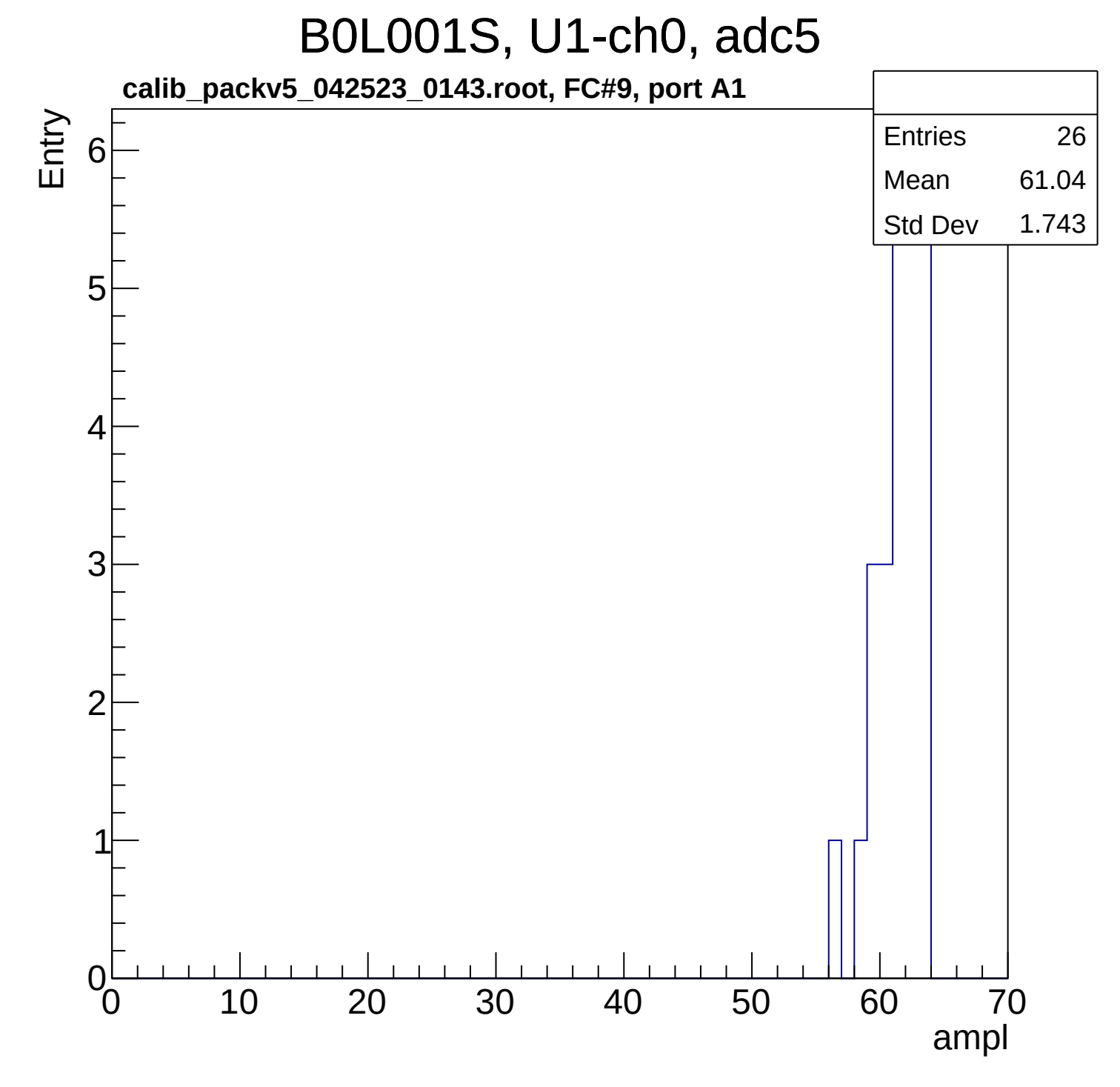
B0L001S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 61.04
Std Dev 1.743
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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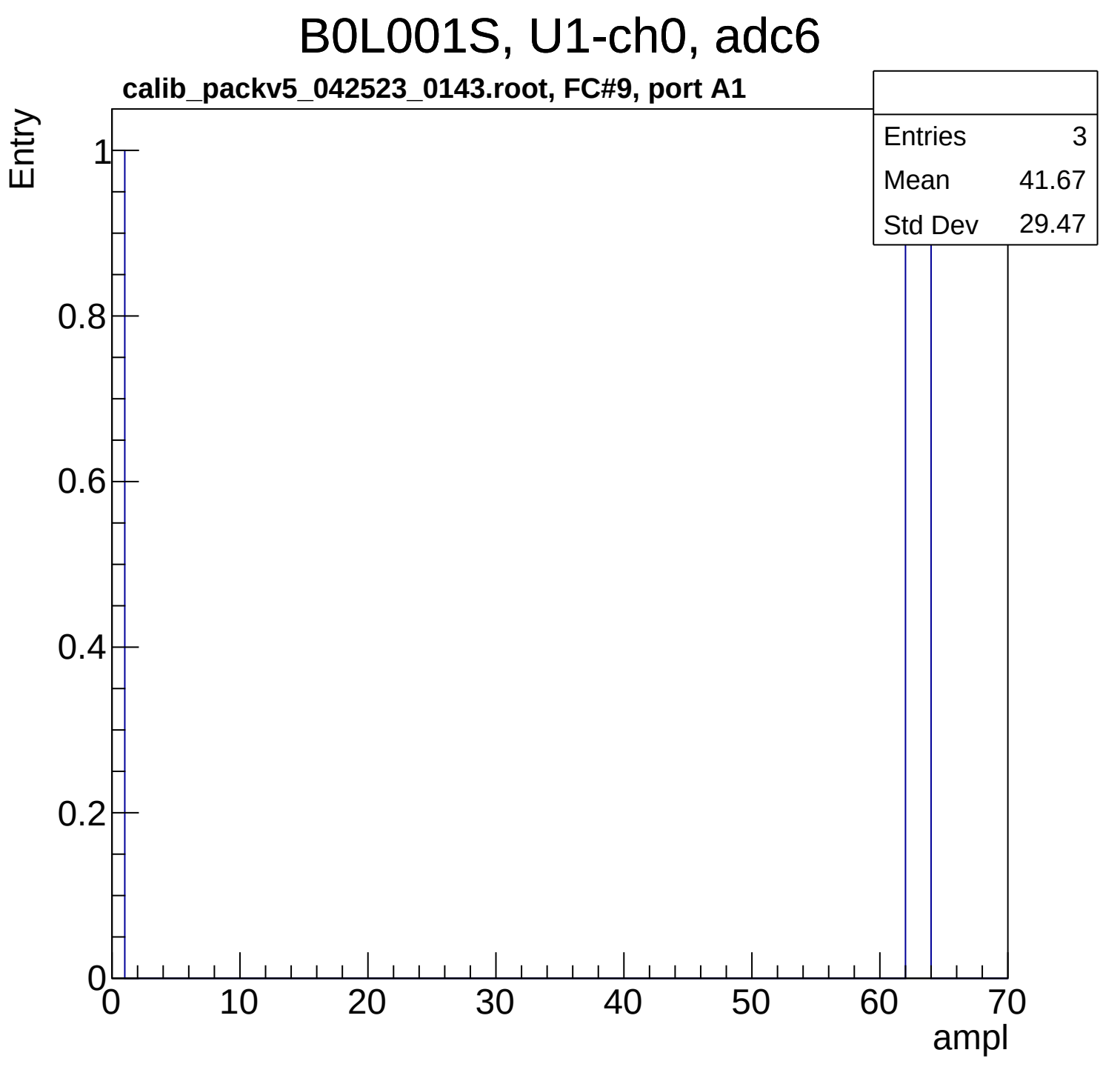
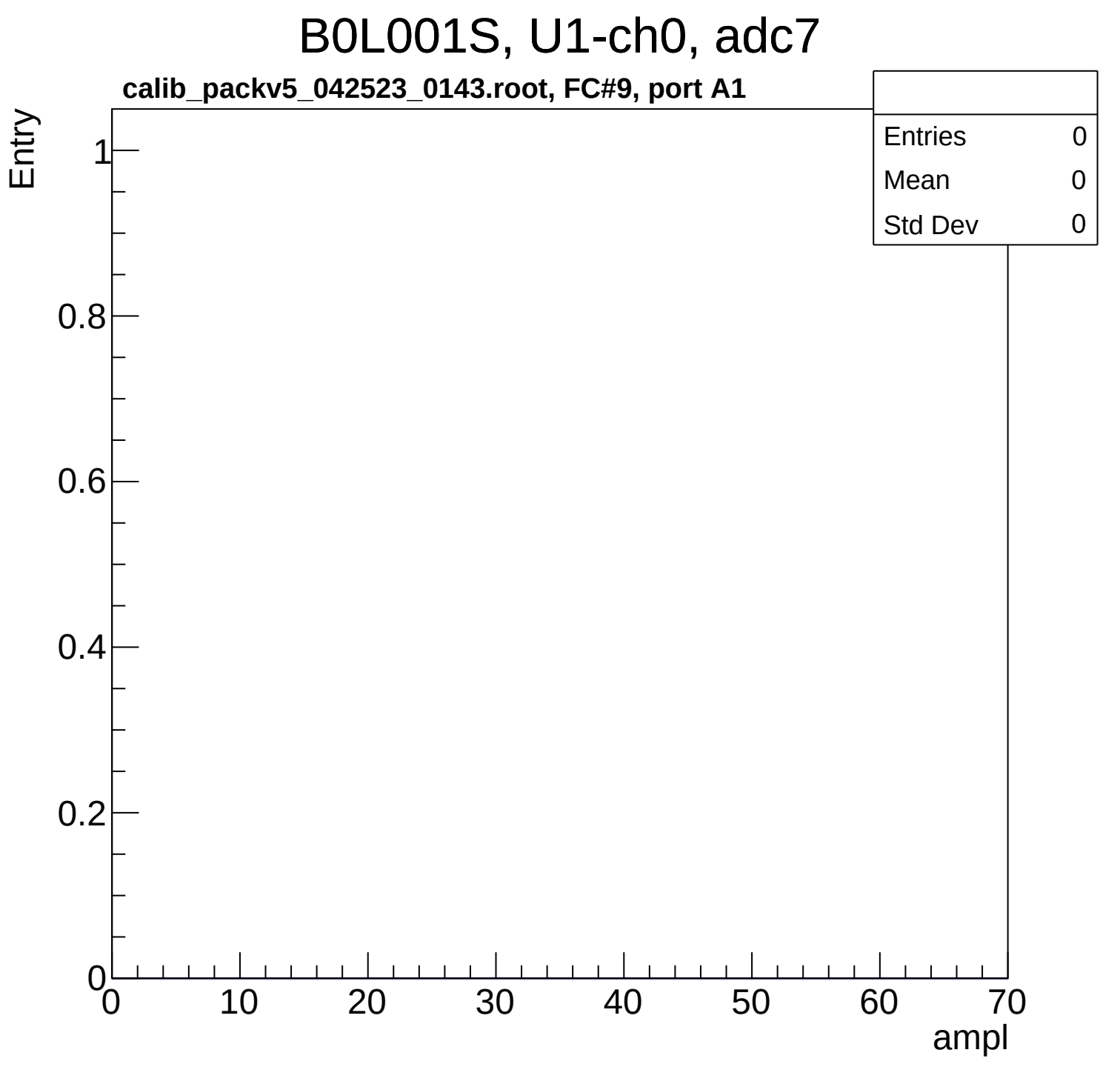

B0L001S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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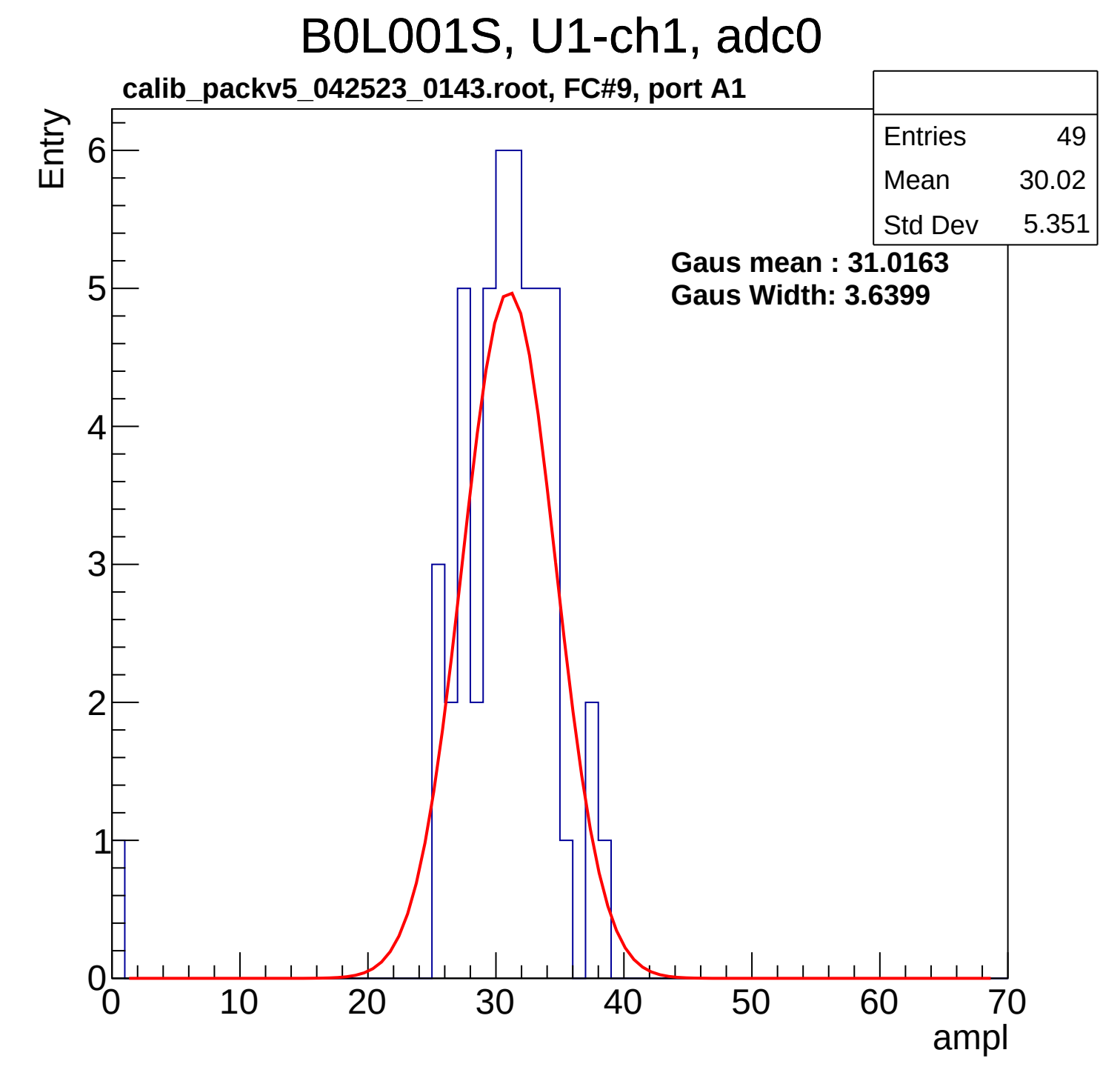
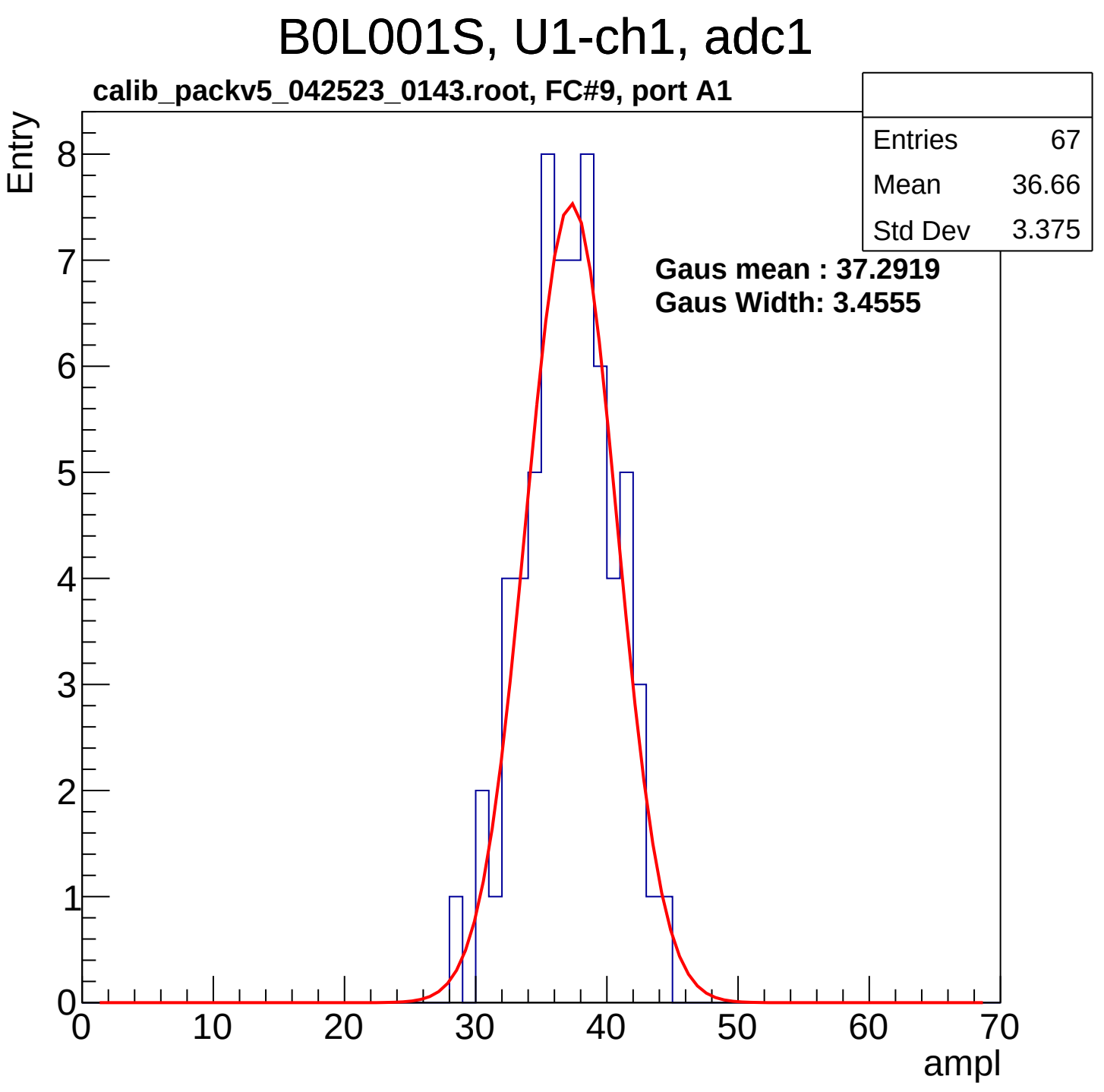
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 30.02
Std Dev 5.351
Gaus mean : 31.0163
Gaus Width: 3.6399
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.66
Std Dev 3.375
Gaus mean : 37.2919
Gaus Width: 3.4555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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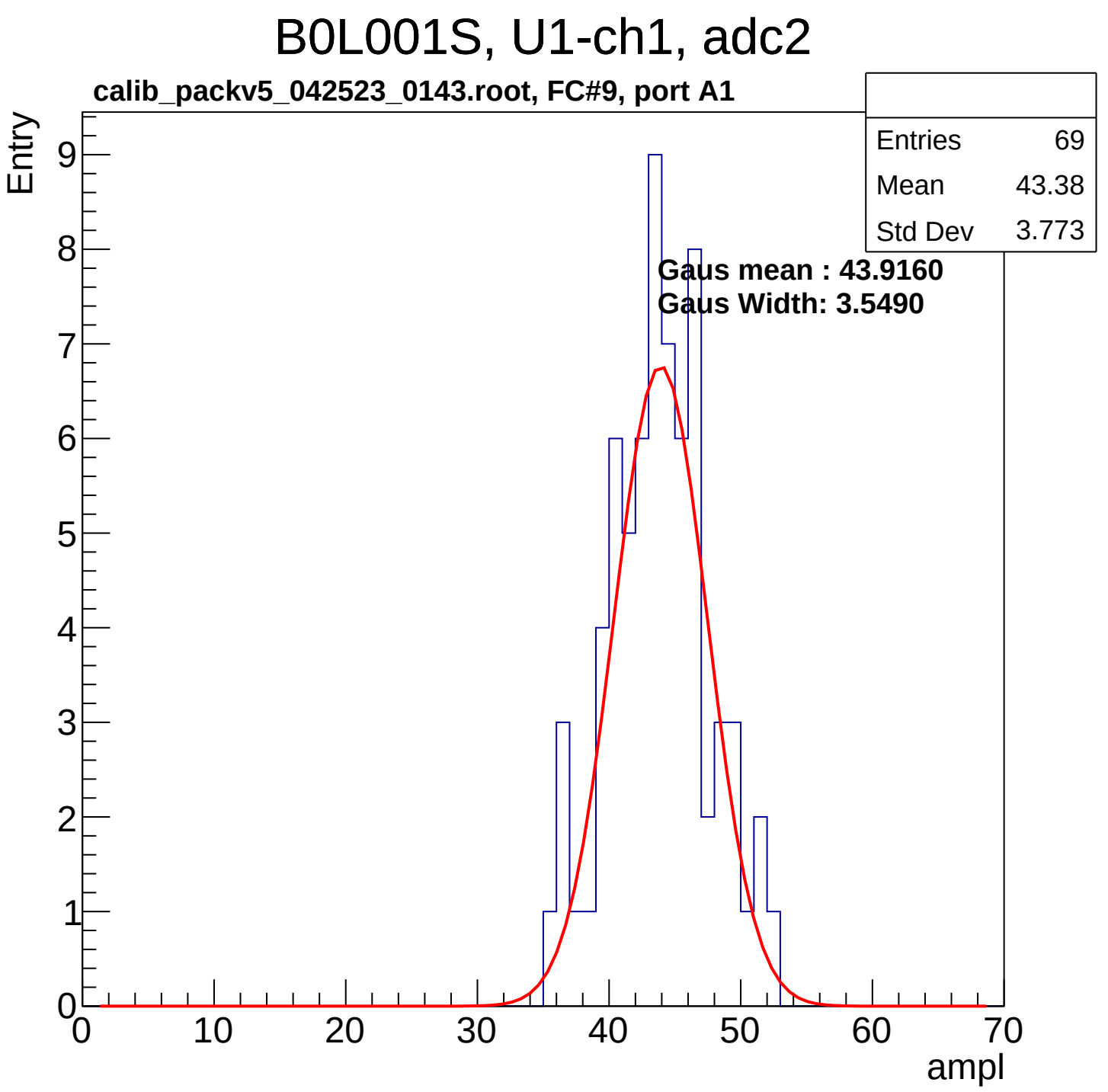
B0L001S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.38
Std Dev 3.773
Gaus mean : 43.9160
Gaus Width: 3.5490
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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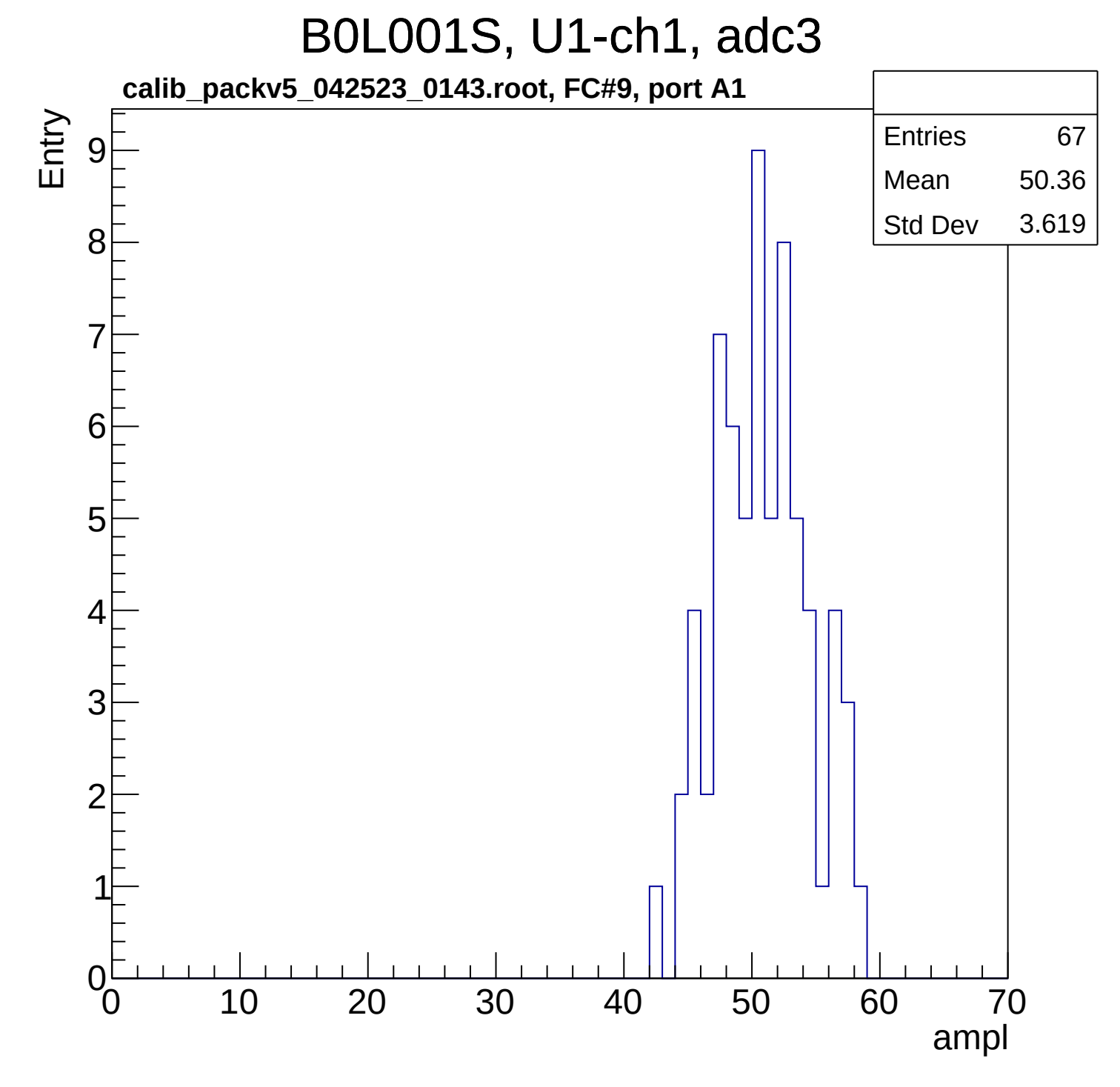
B0L001S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.36
Std Dev 3.619
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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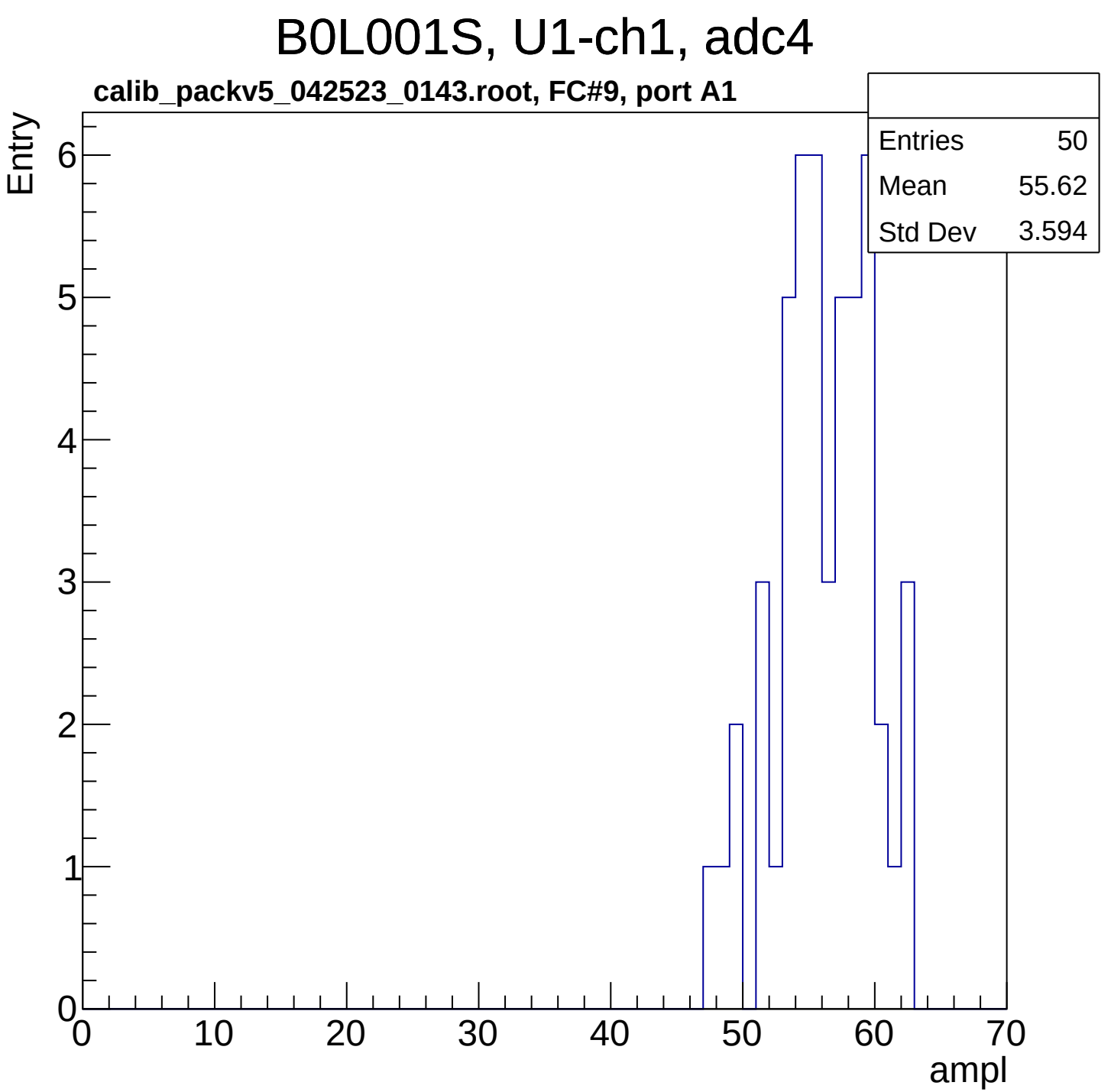
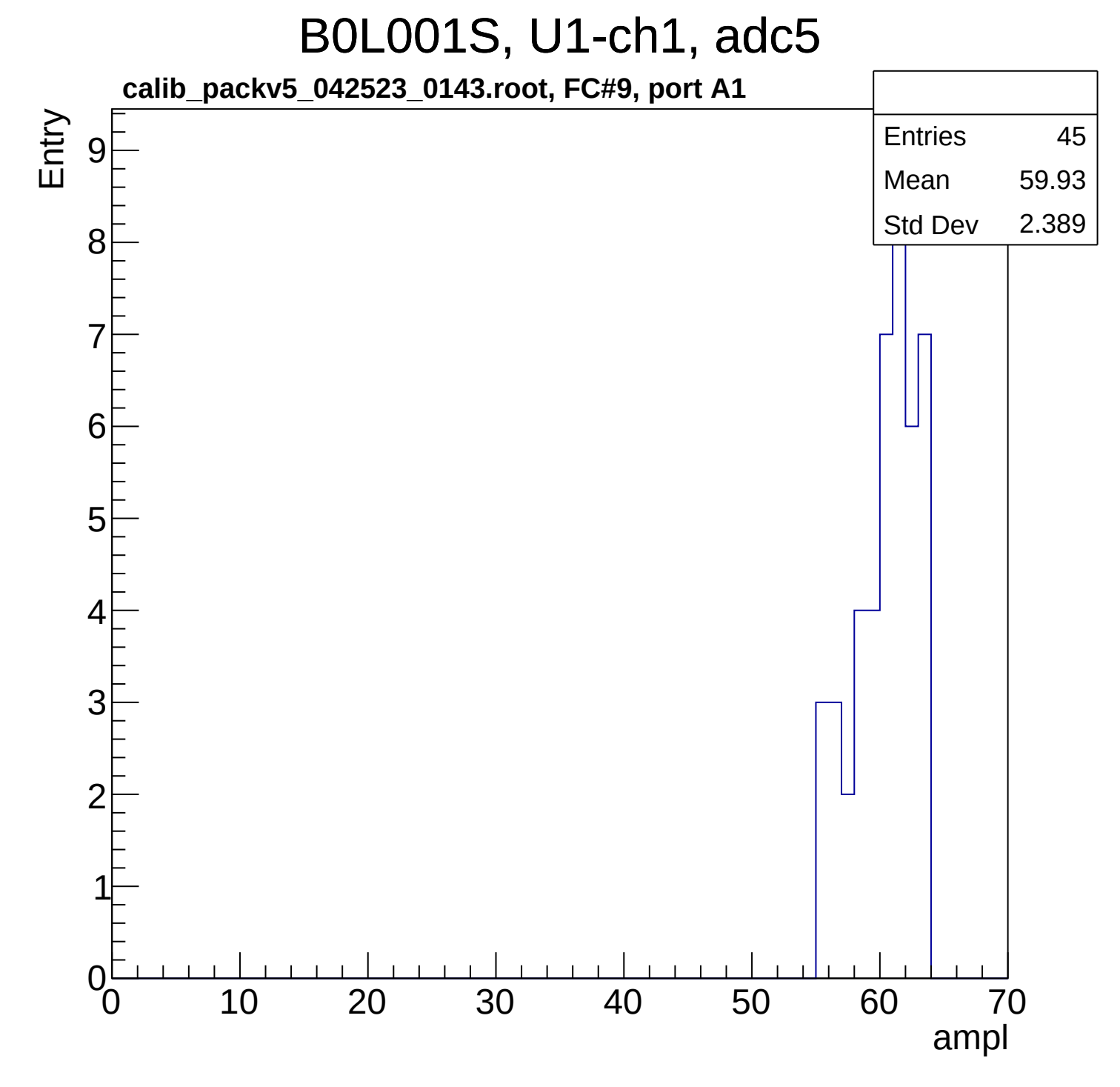

B0L001S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 55.62
Std Dev 3.594
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.93
Std Dev 2.389
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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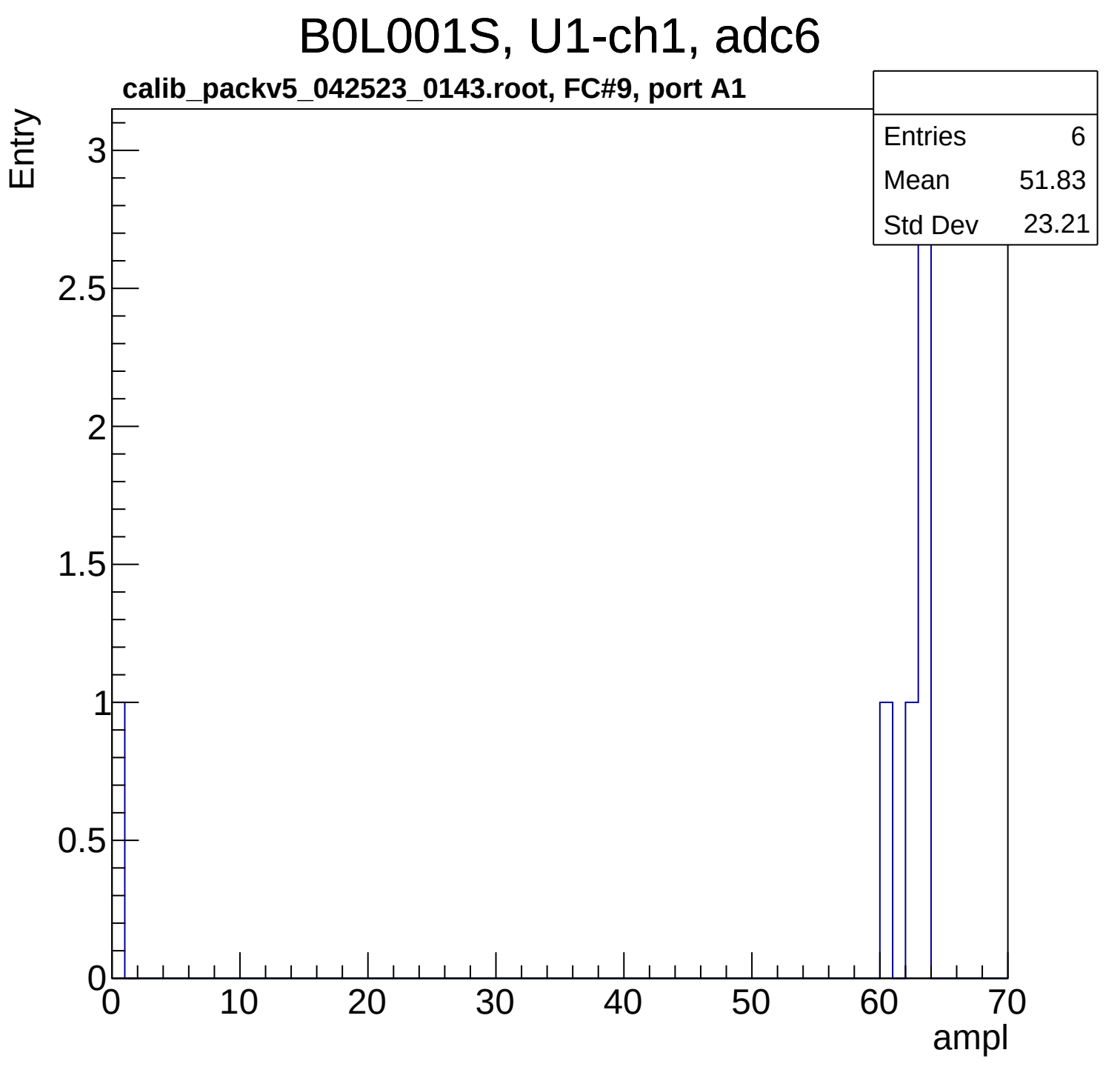
B0L001S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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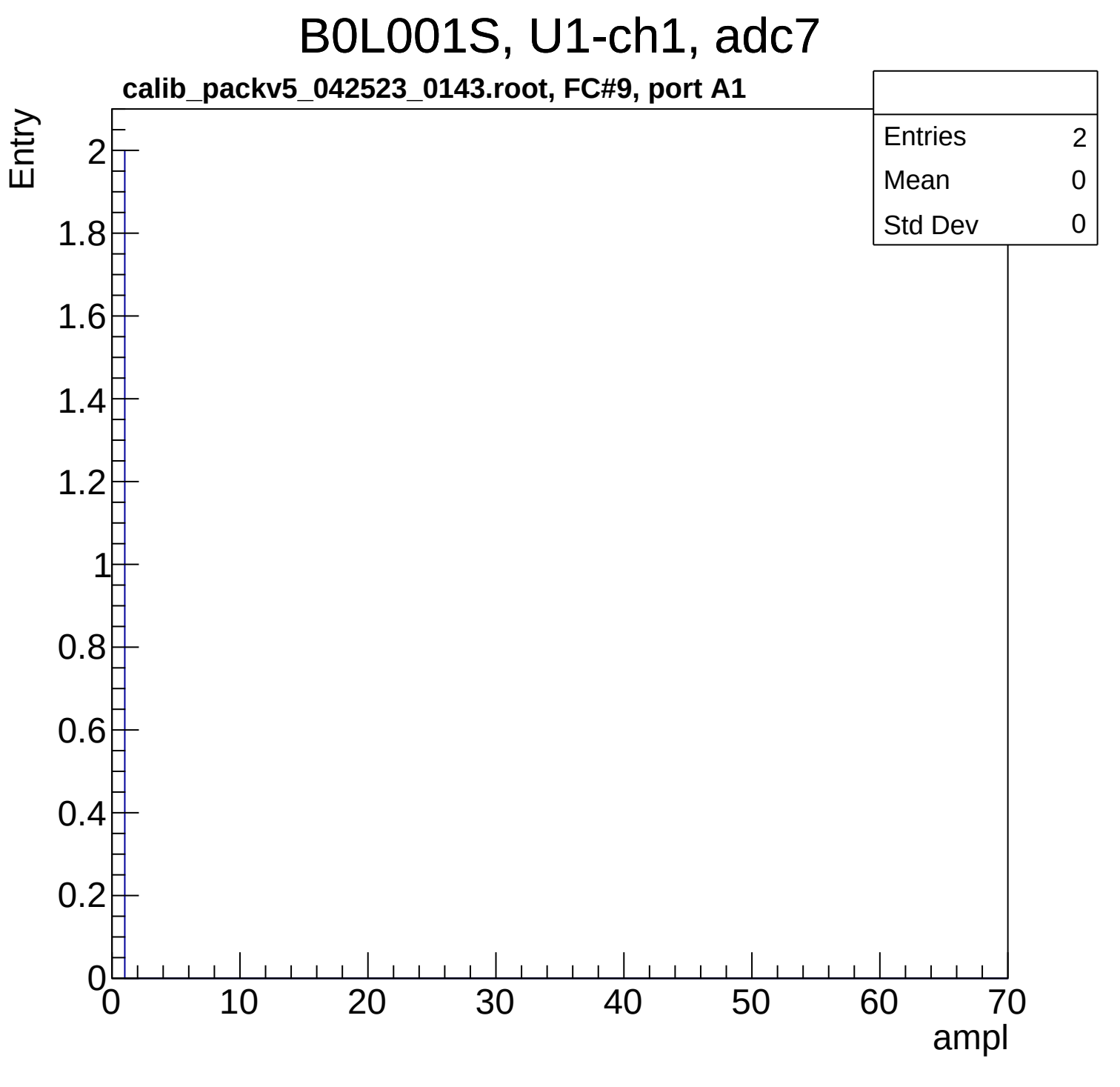
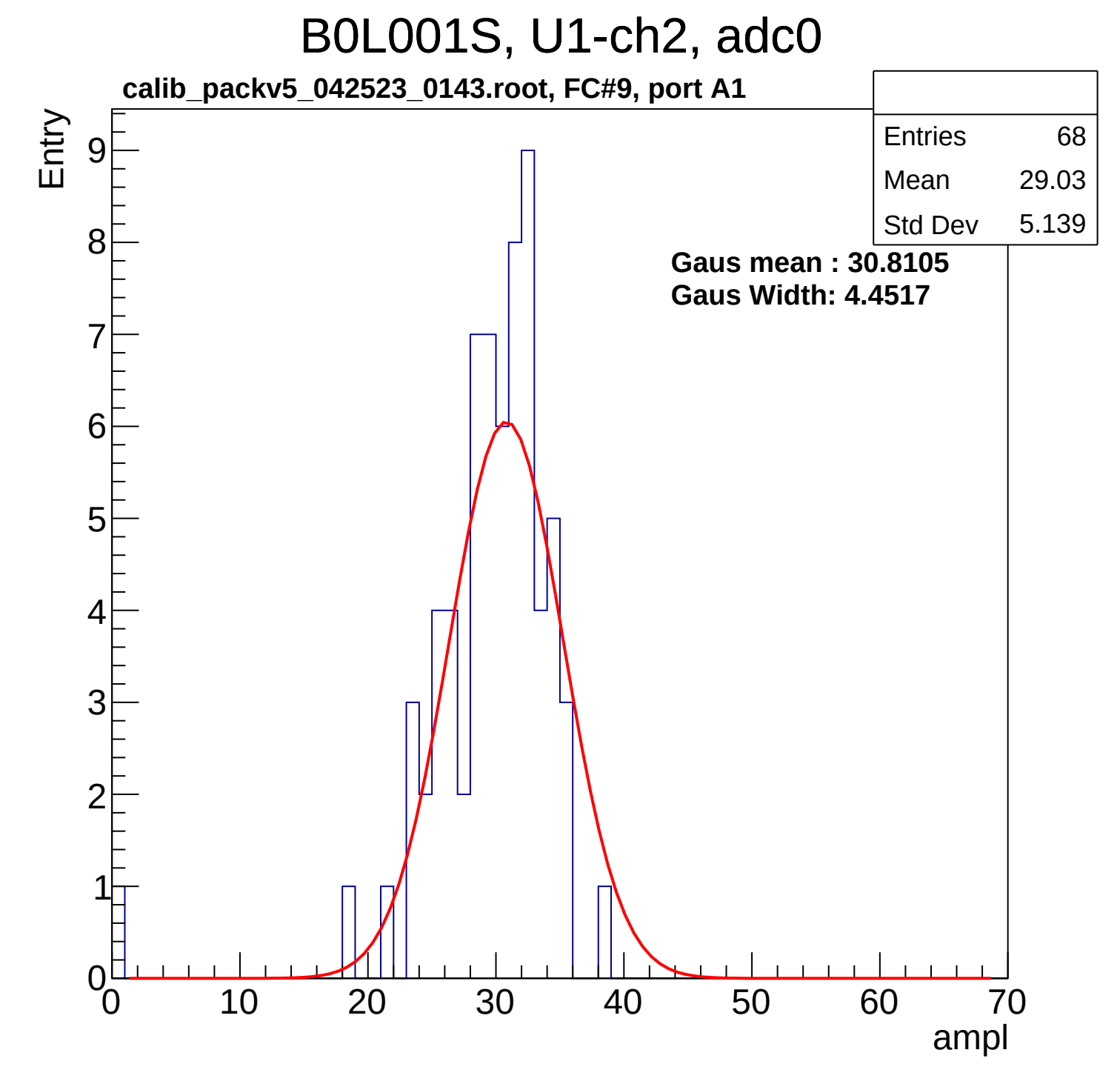

B0L001S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 29.03
Std Dev 5.139
Gaus mean : 30.8105
Gaus Width: 4.4517
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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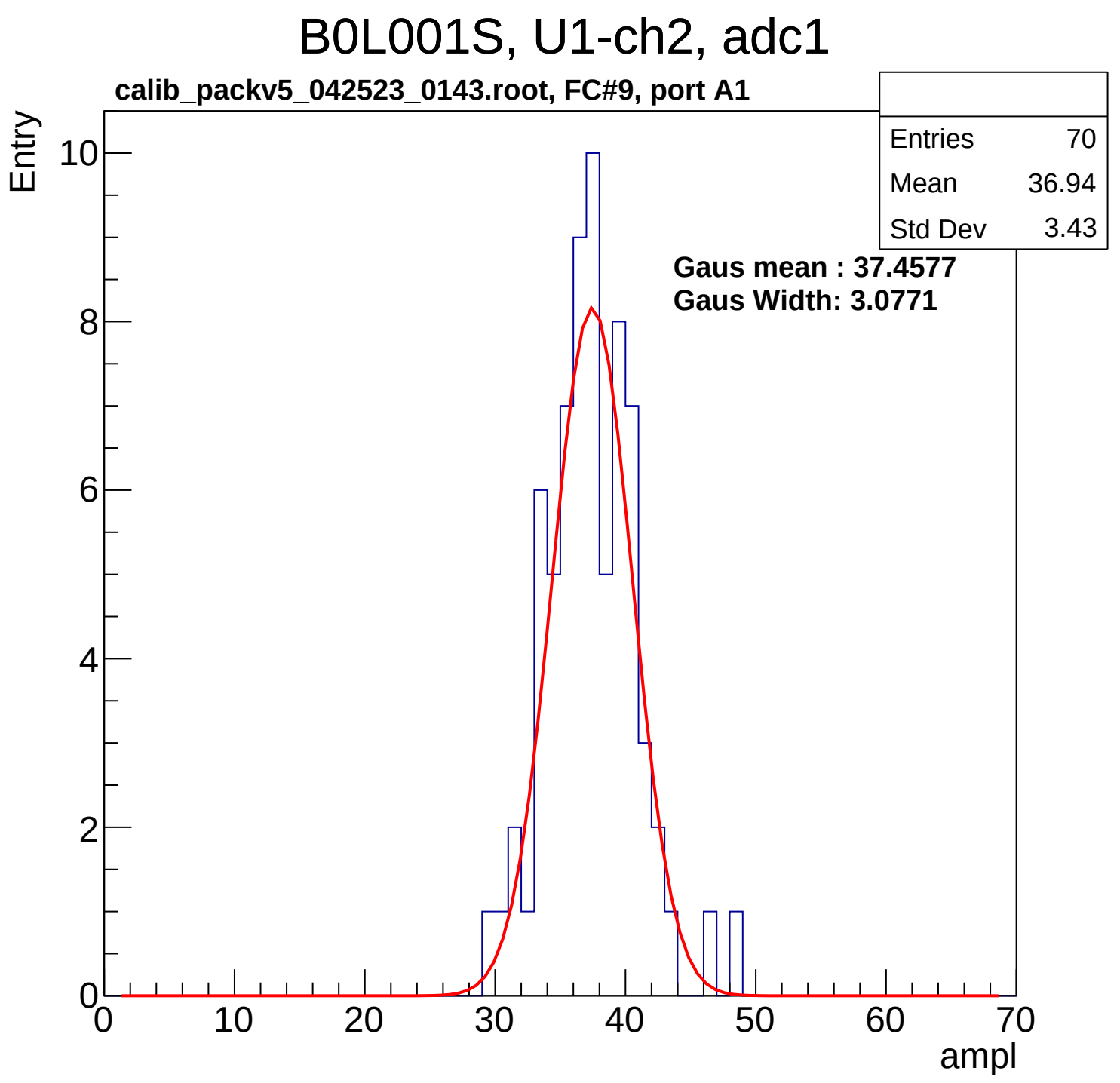
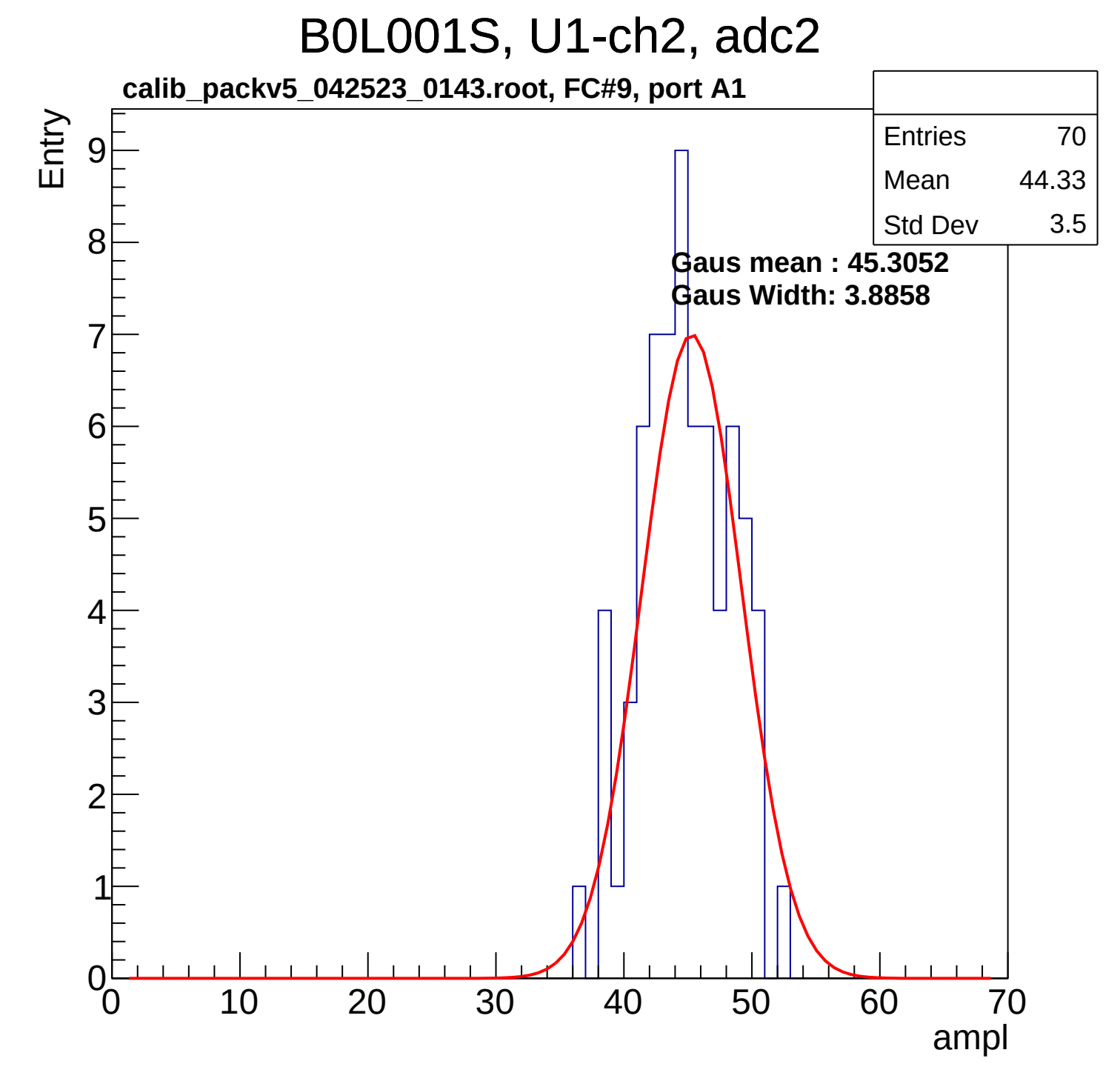
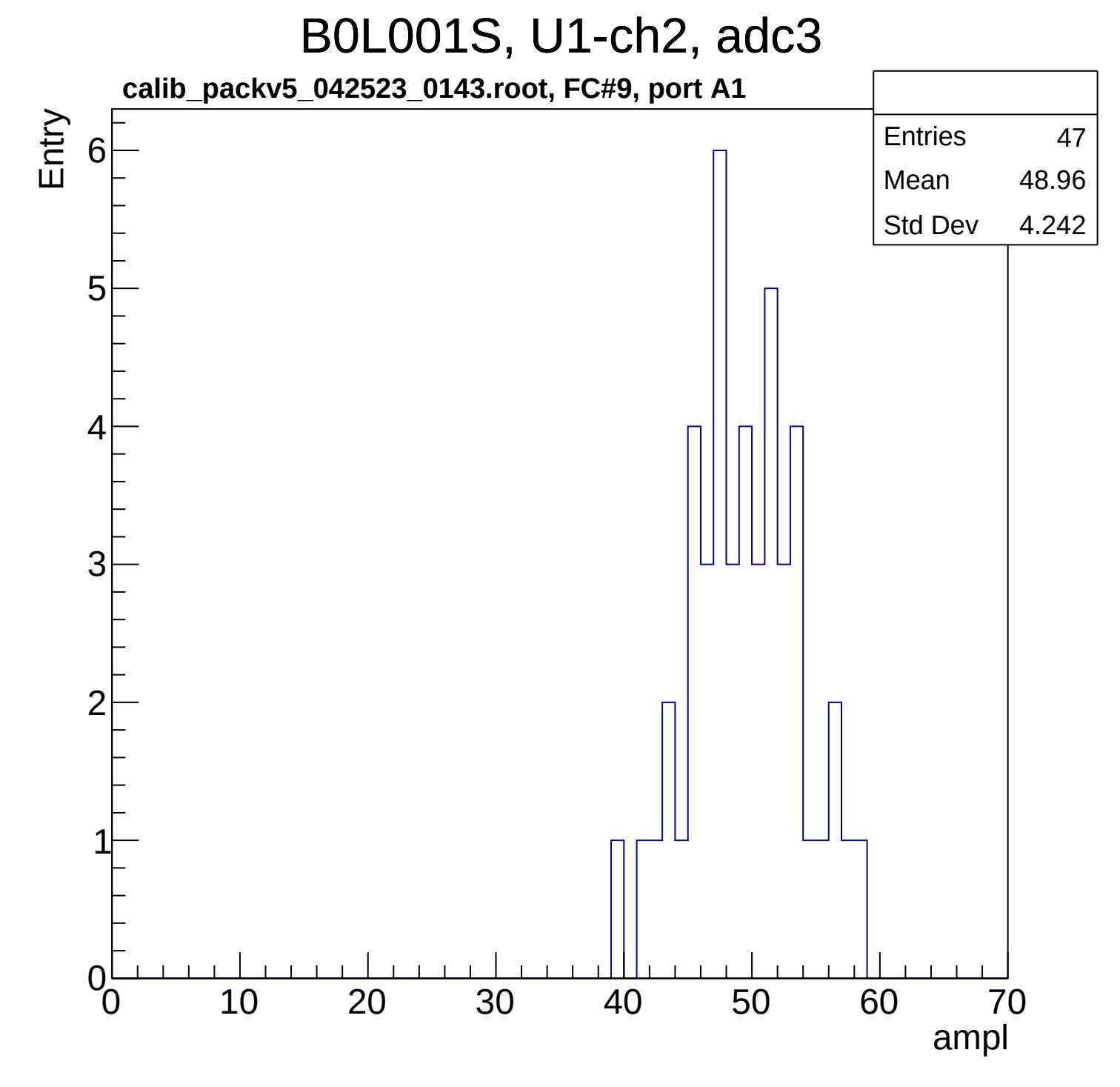
60
70


B0L001S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.94
Std Dev 3.43
Gaus mean : 37.4577
Gaus Width: 3.0771
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.33
Std Dev 3.5
Gaus mean : 45.3052
Gaus Width: 3.8858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 48.96
Std Dev 4.242
Entry
0
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2
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4
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6
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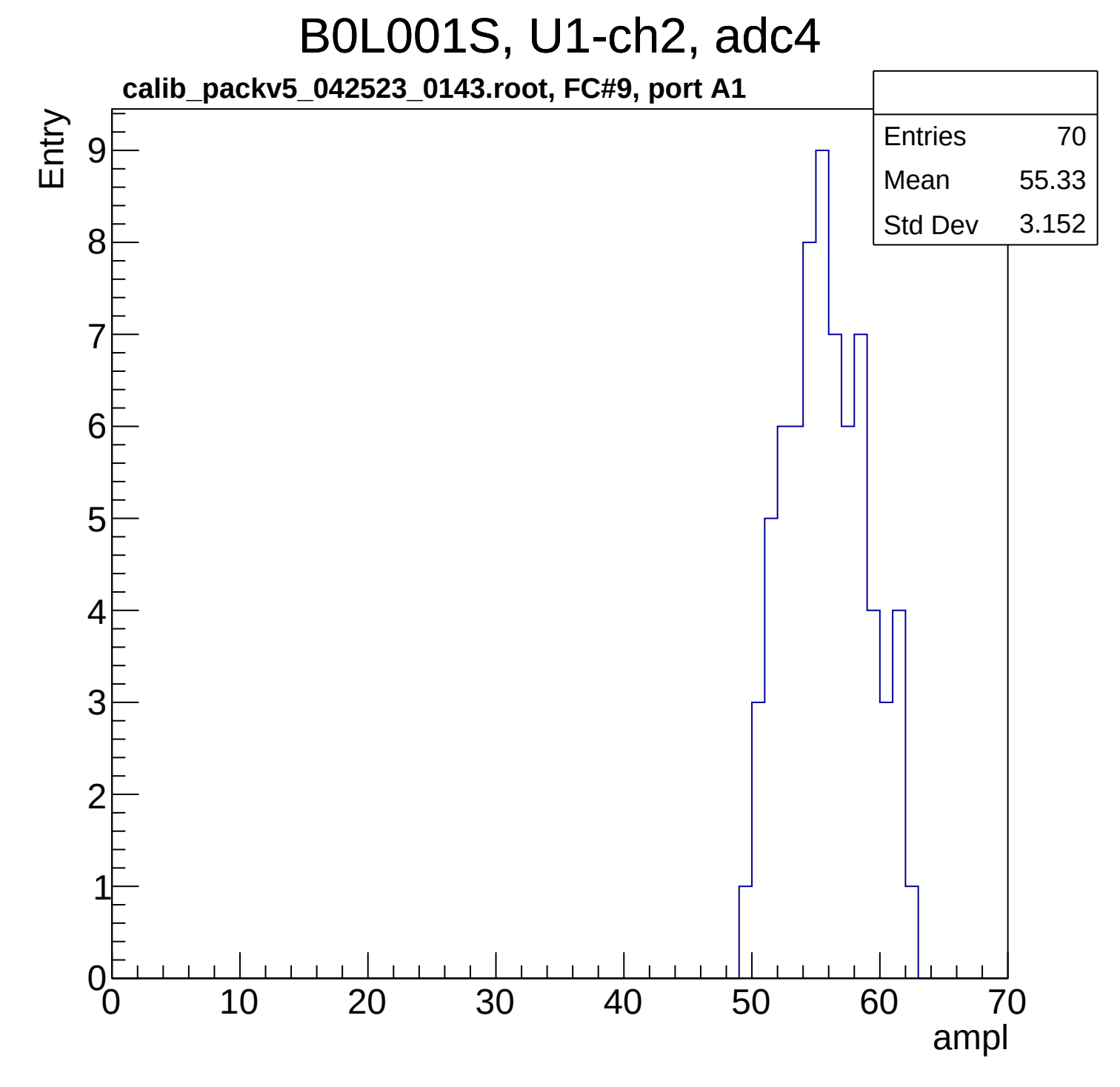
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 55.33
Std Dev 3.152
Entry
ampl
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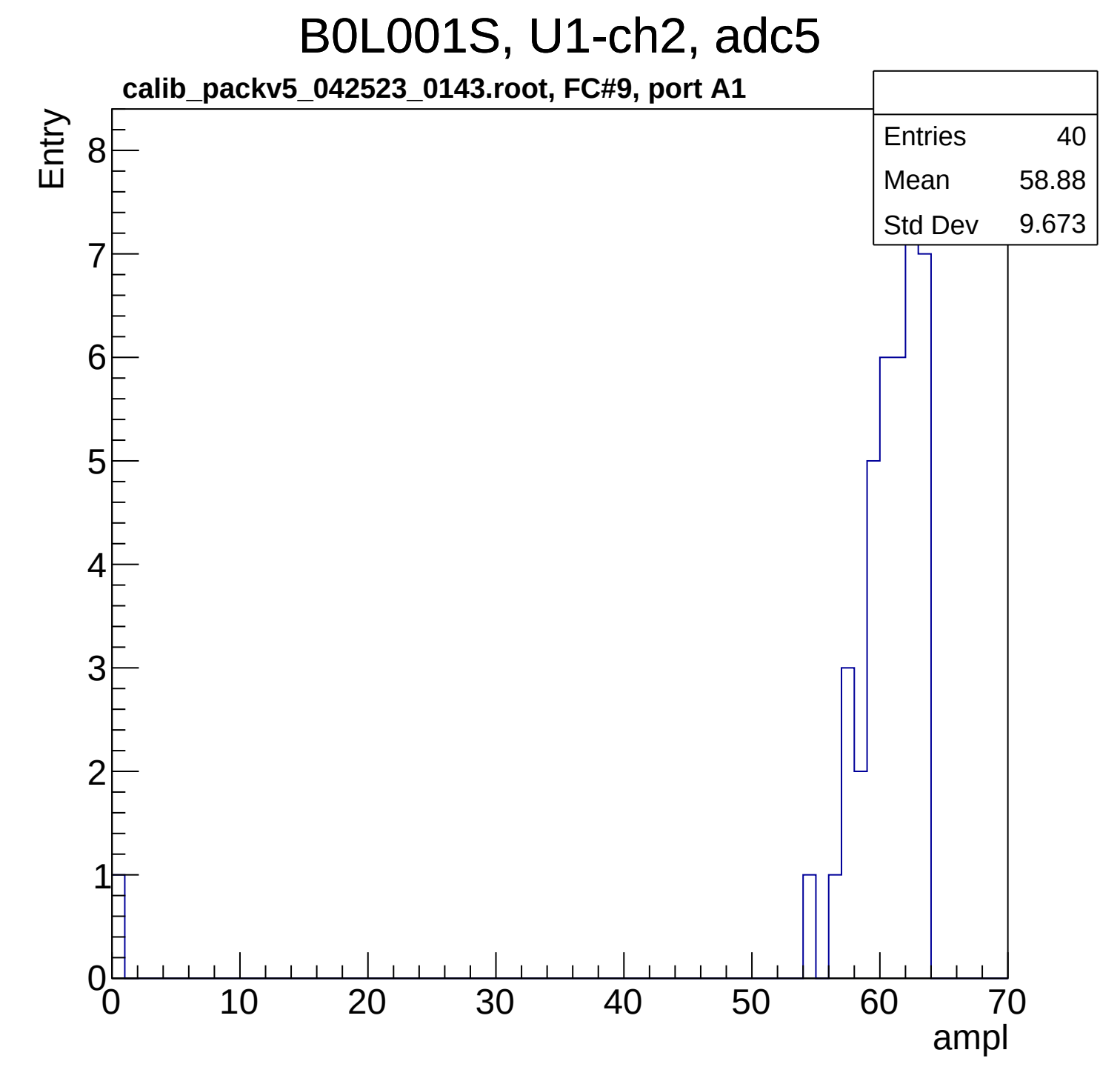
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.88
Std Dev 9.673
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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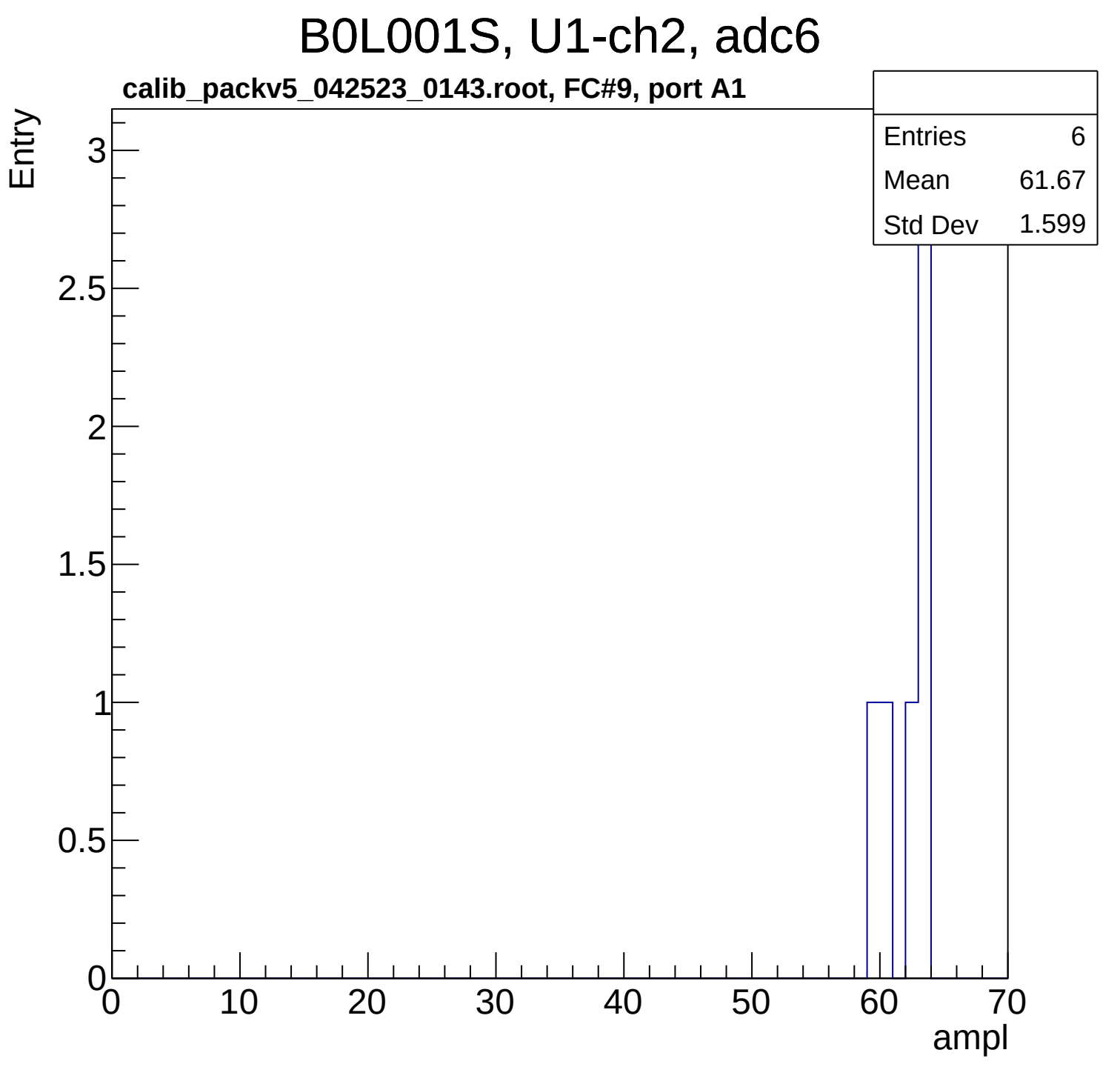
B0L001S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.67
Std Dev 1.599
Entry
ampl
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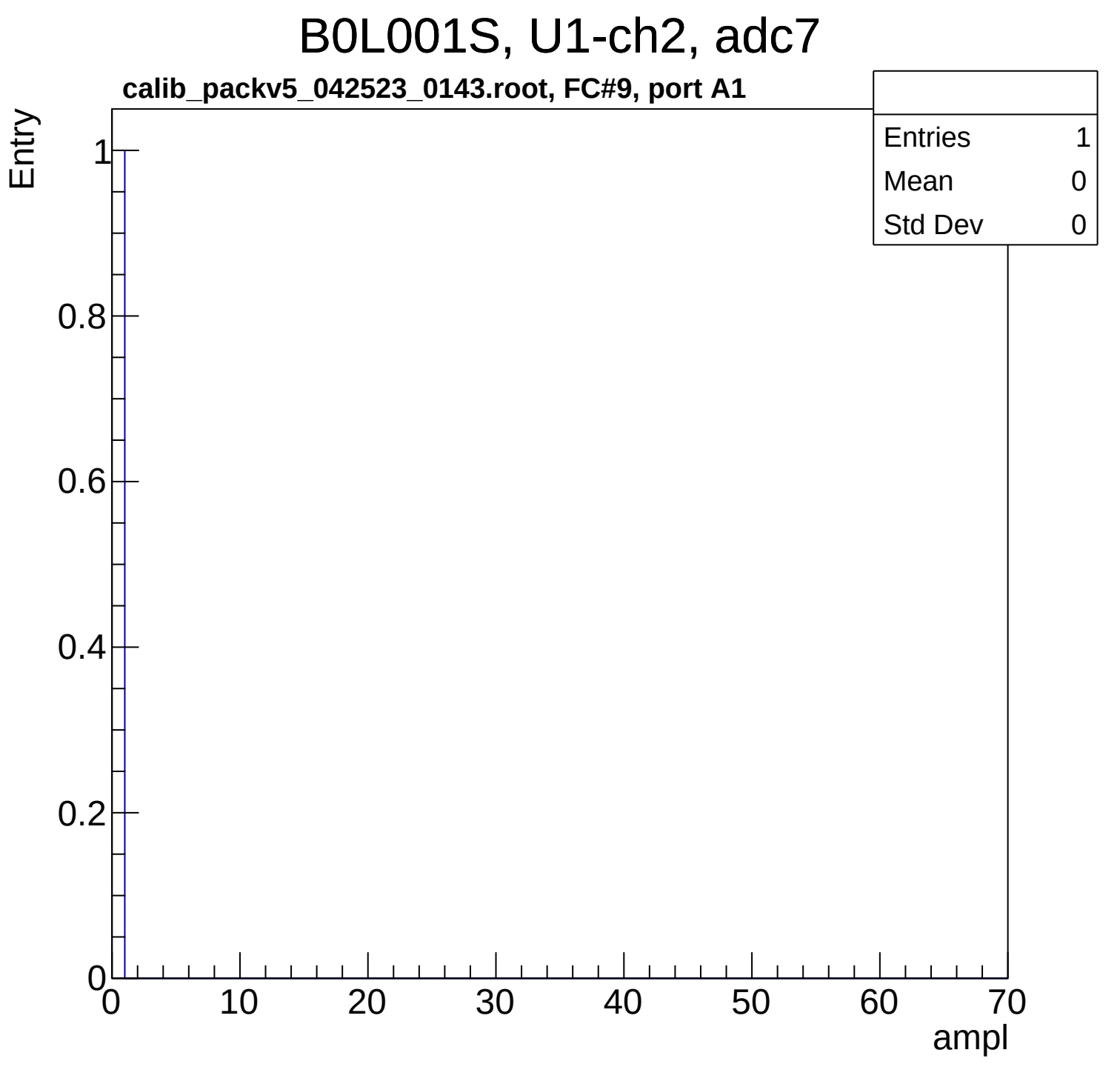
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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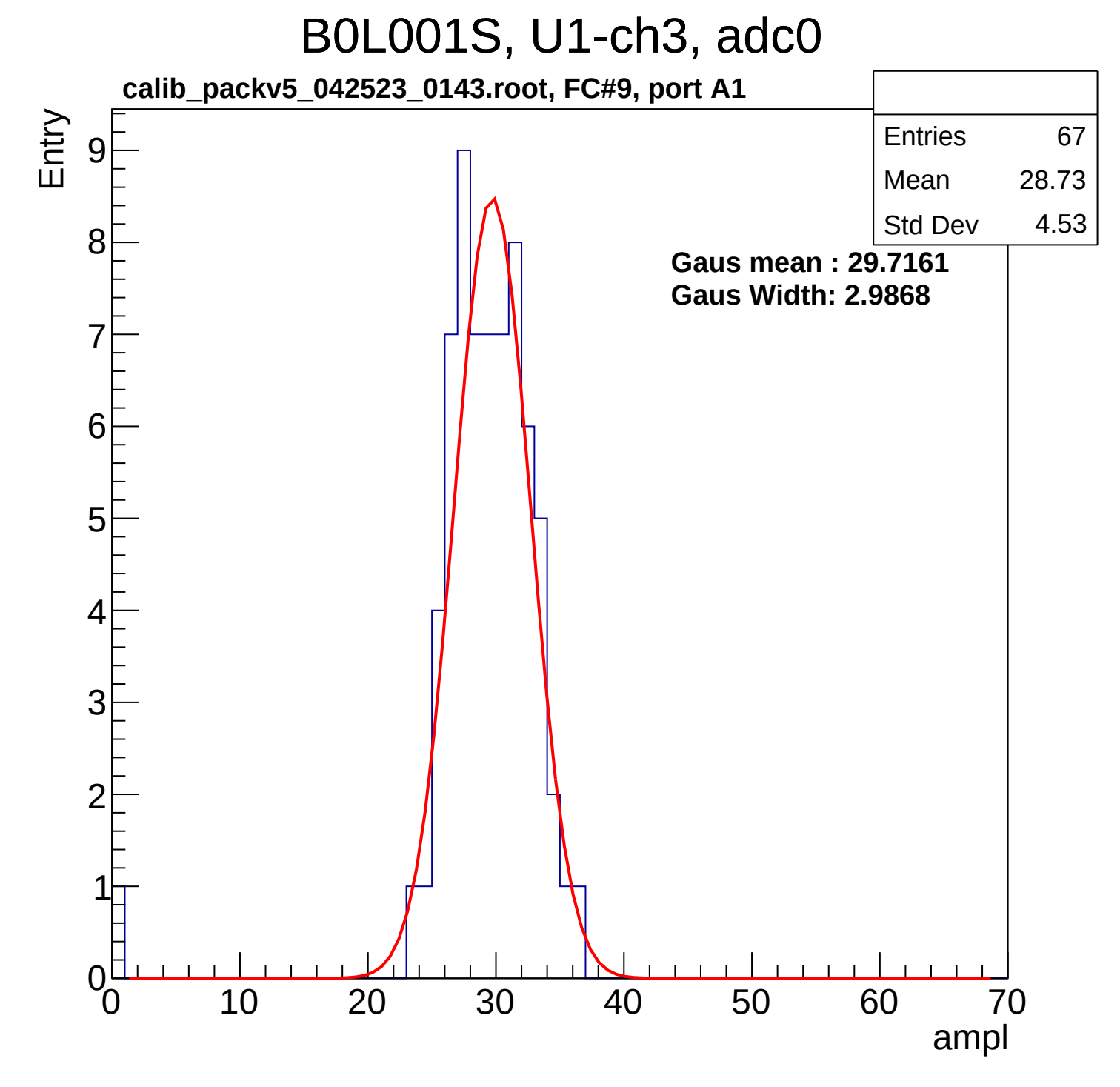
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 28.73
Std Dev 4.53
Gaus mean : 29.7161
Gaus Width: 2.9868
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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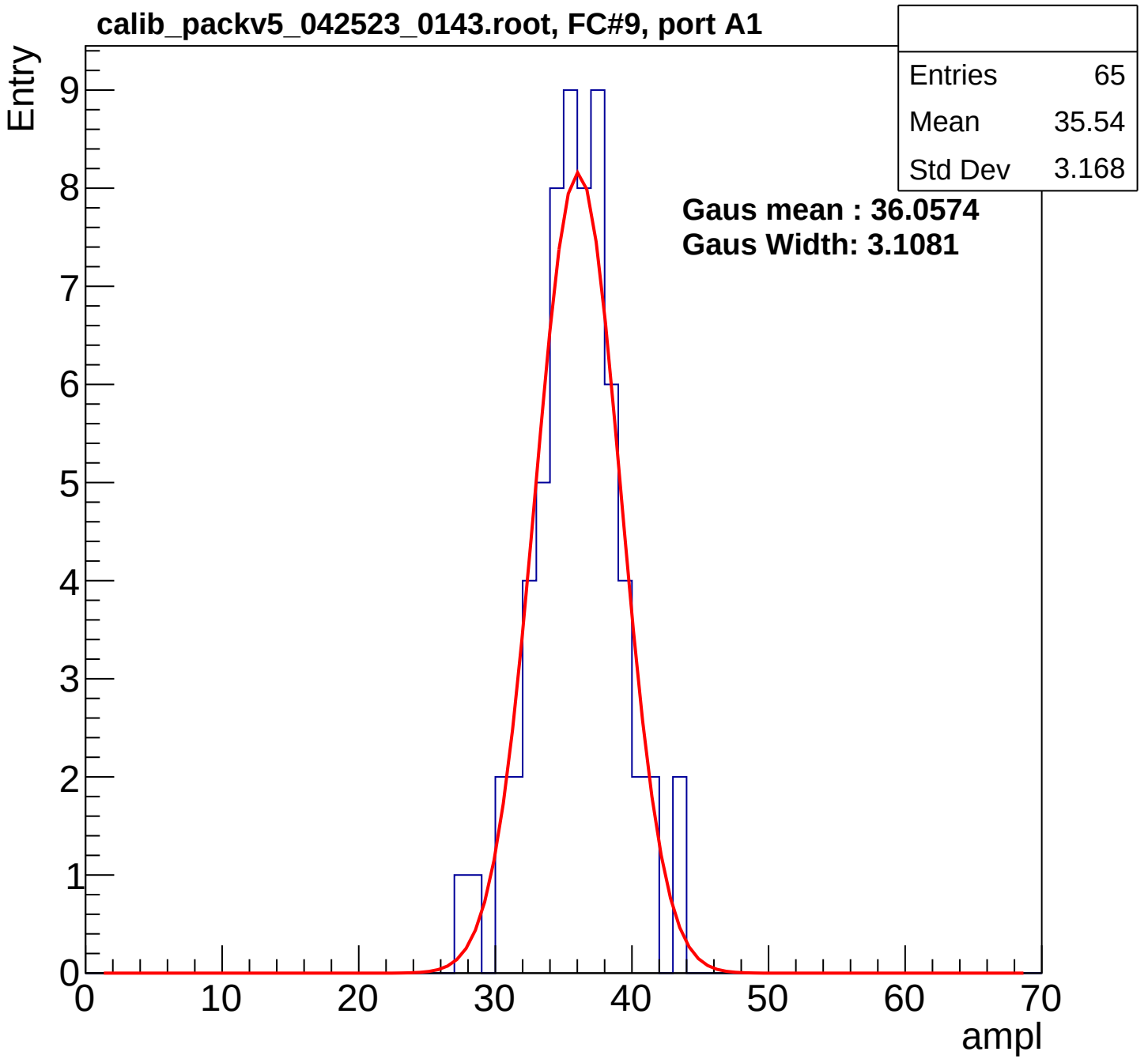
B0L001S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 35.54
Std Dev 3.168
Gaus mean : 36.0574
Gaus Width: 3.1081
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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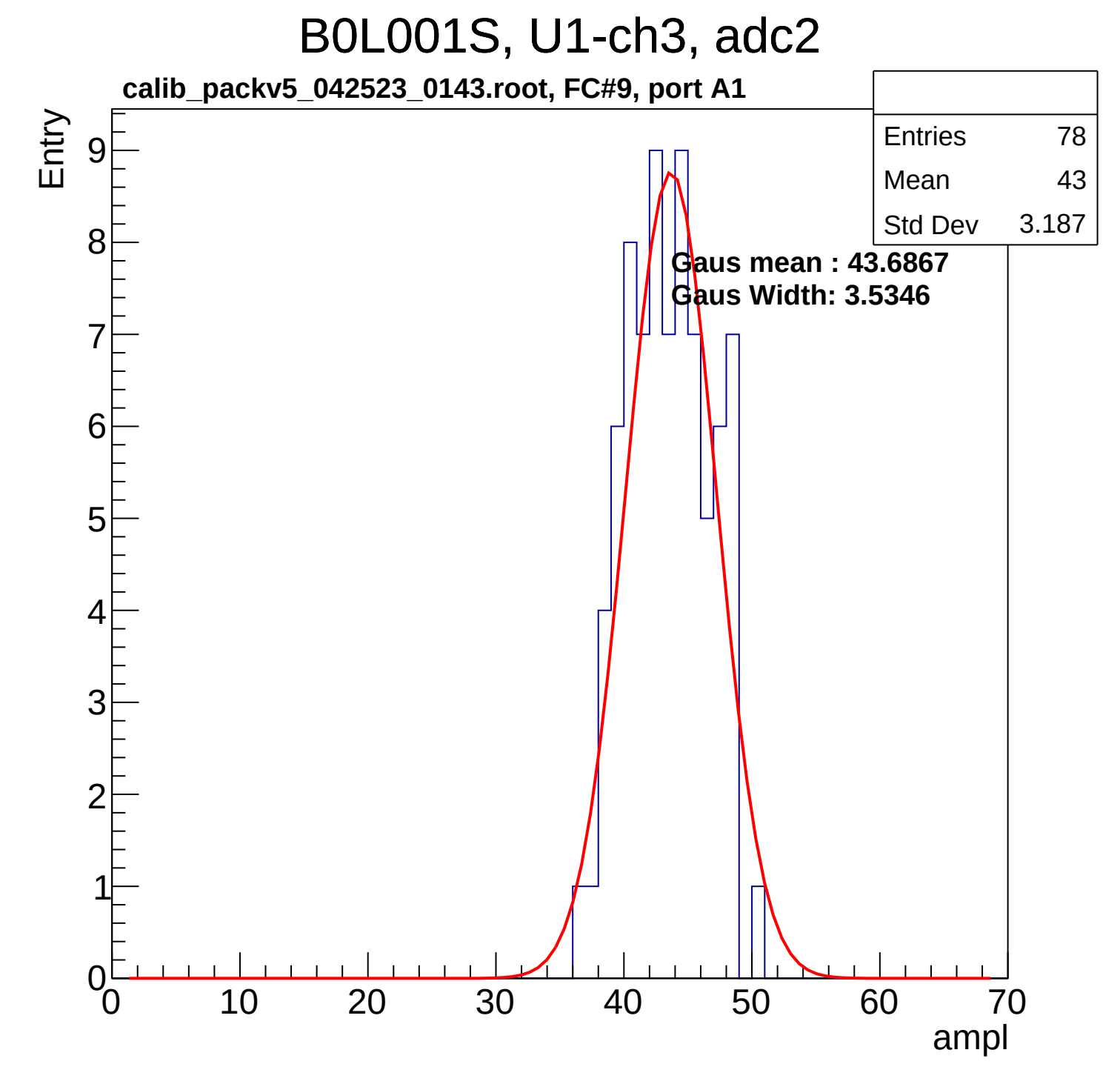
B0L001S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 43
Std Dev 3.187
Gaus mean : 43.6867
Gaus Width: 3.5346
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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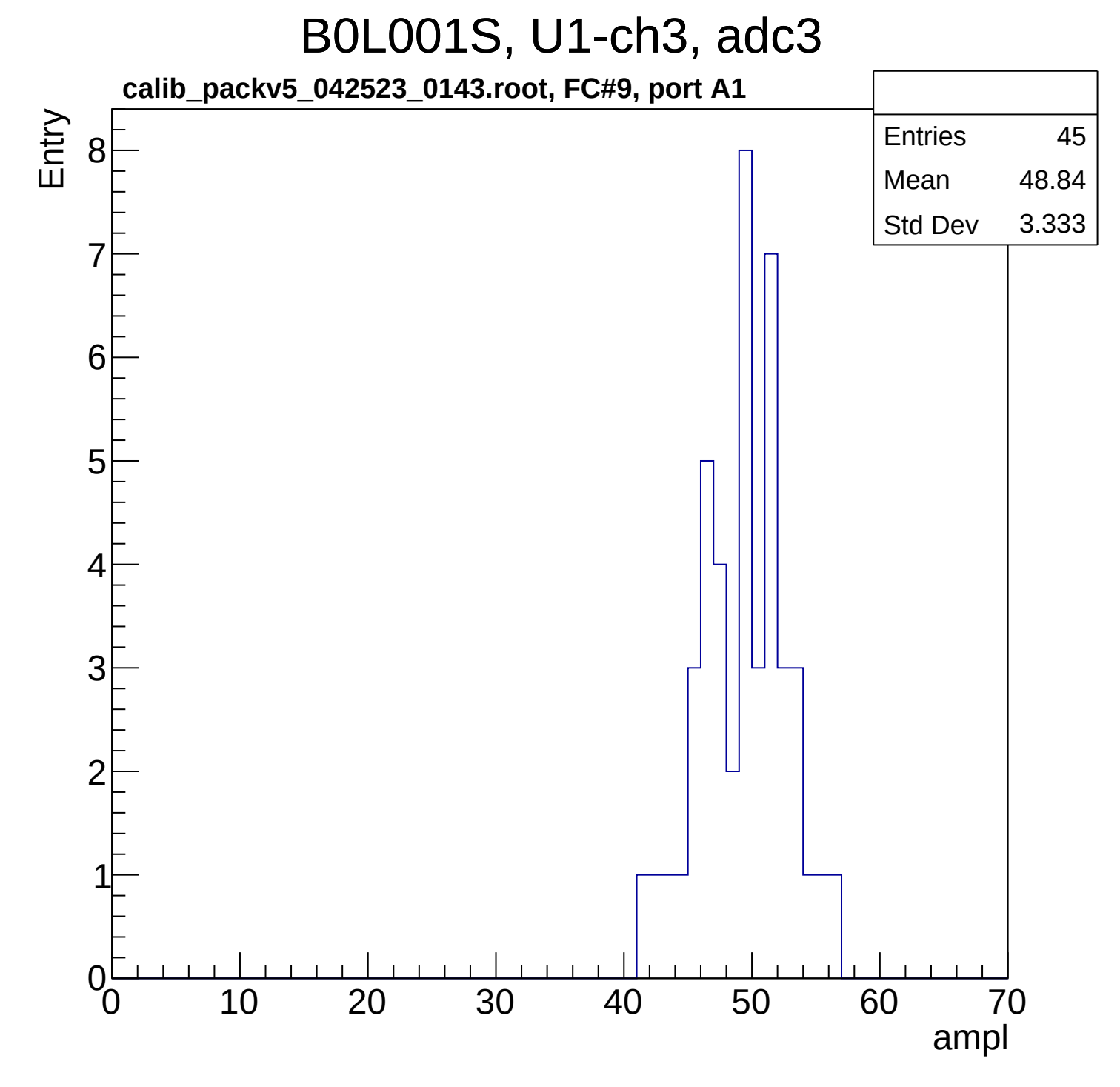
B0L001S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 48.84
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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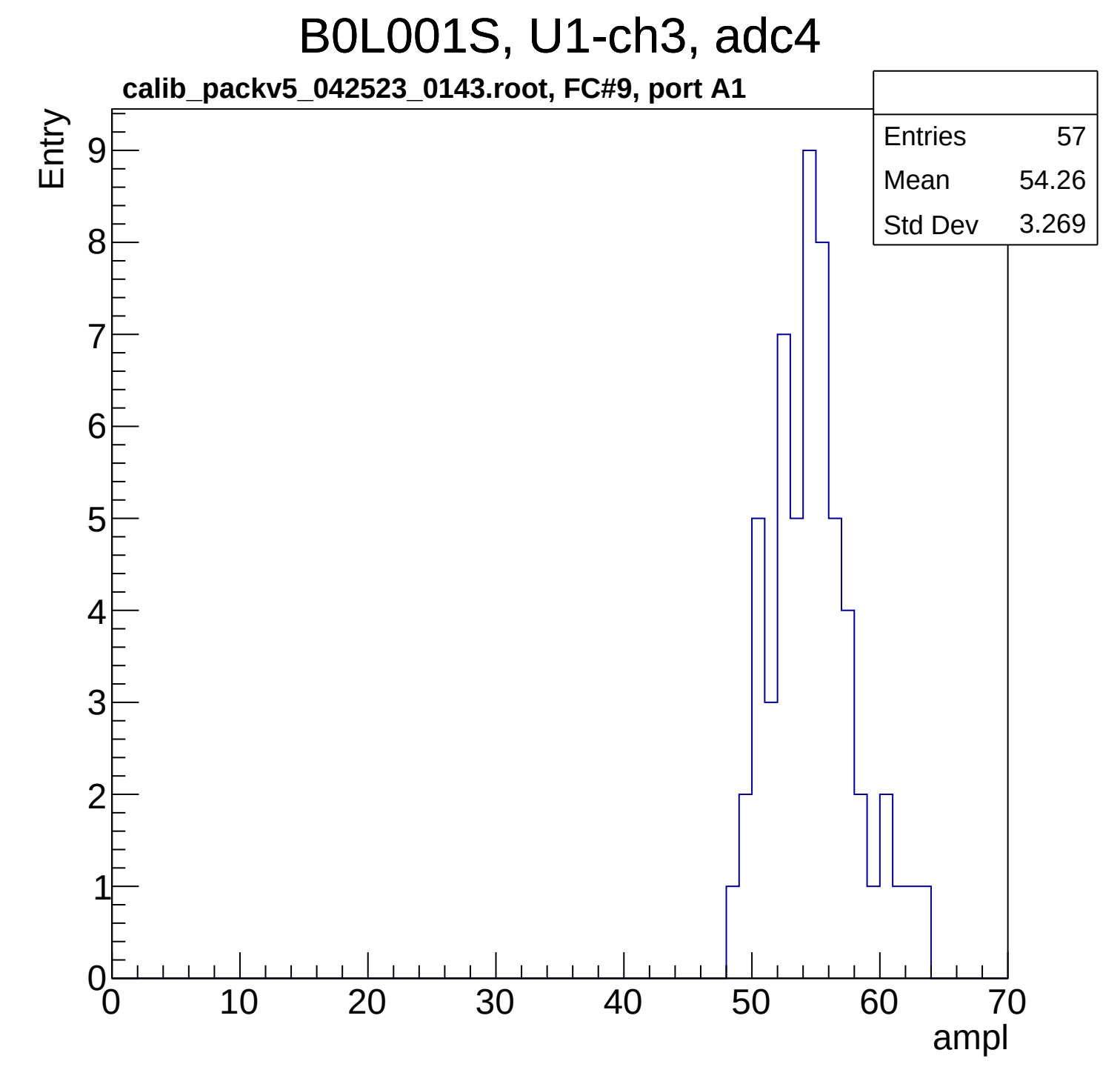
B0L001S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 54.26
Std Dev 3.269
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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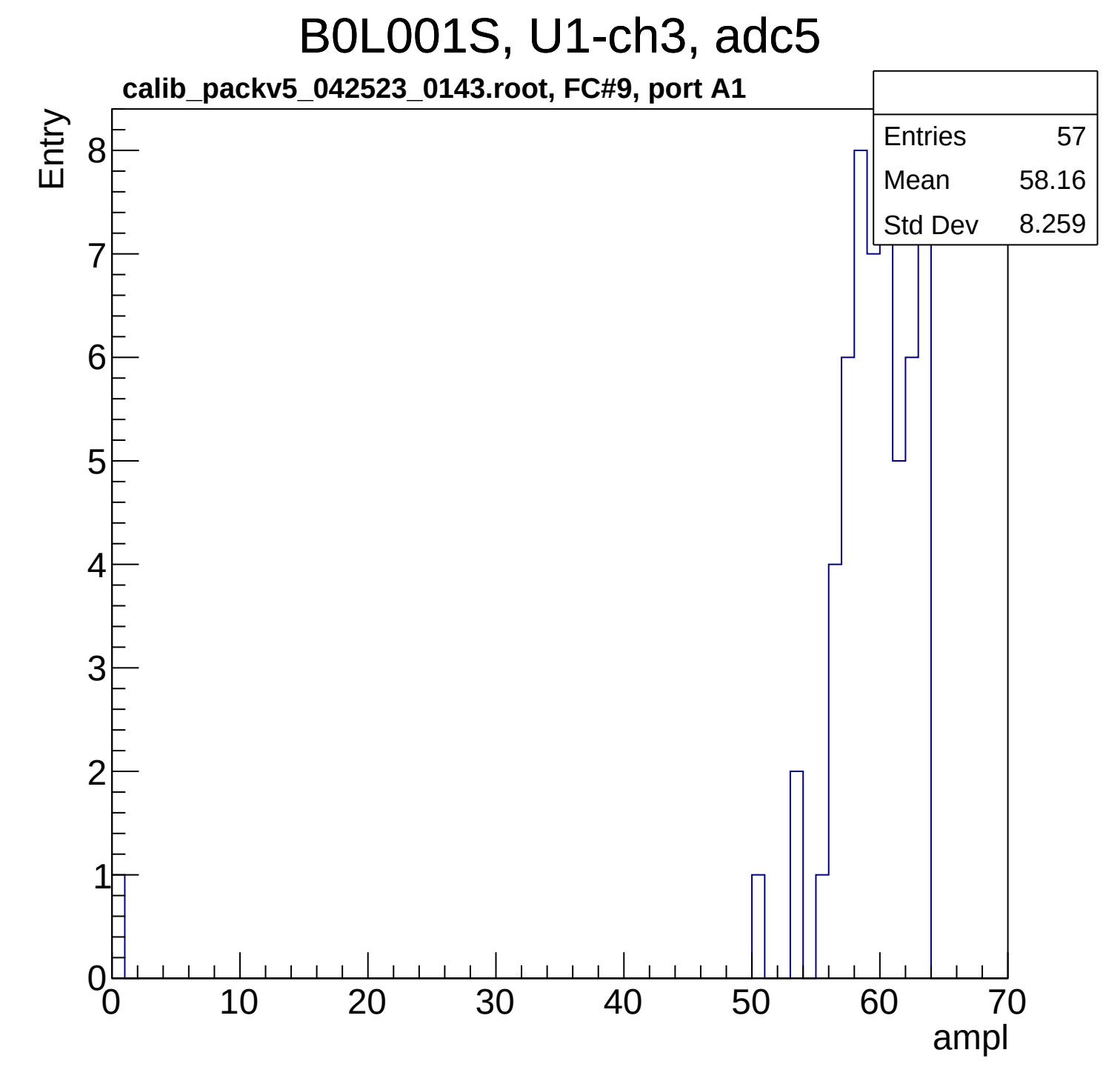
60
70


B0L001S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 58.16
Std Dev 8.259
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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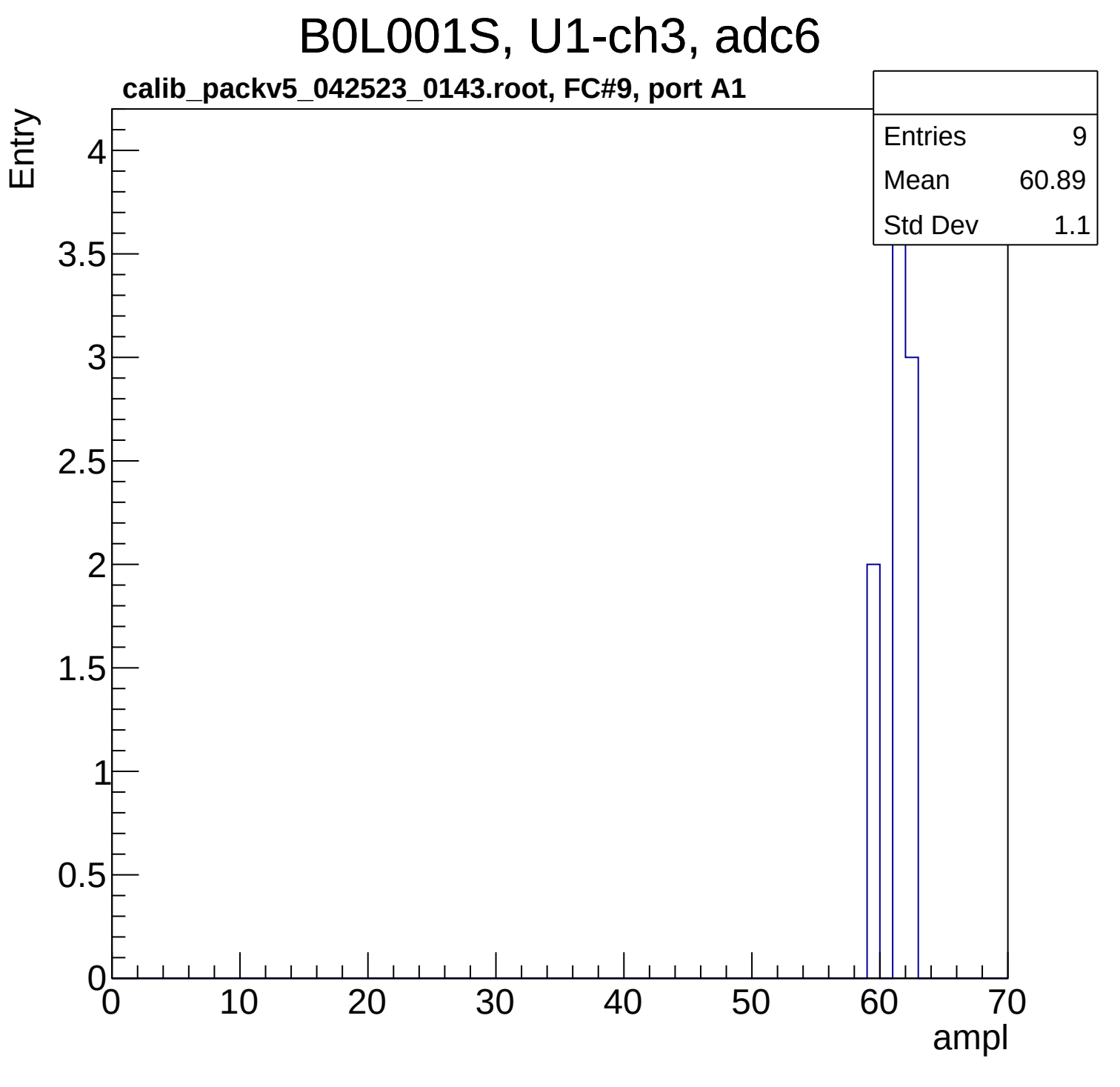
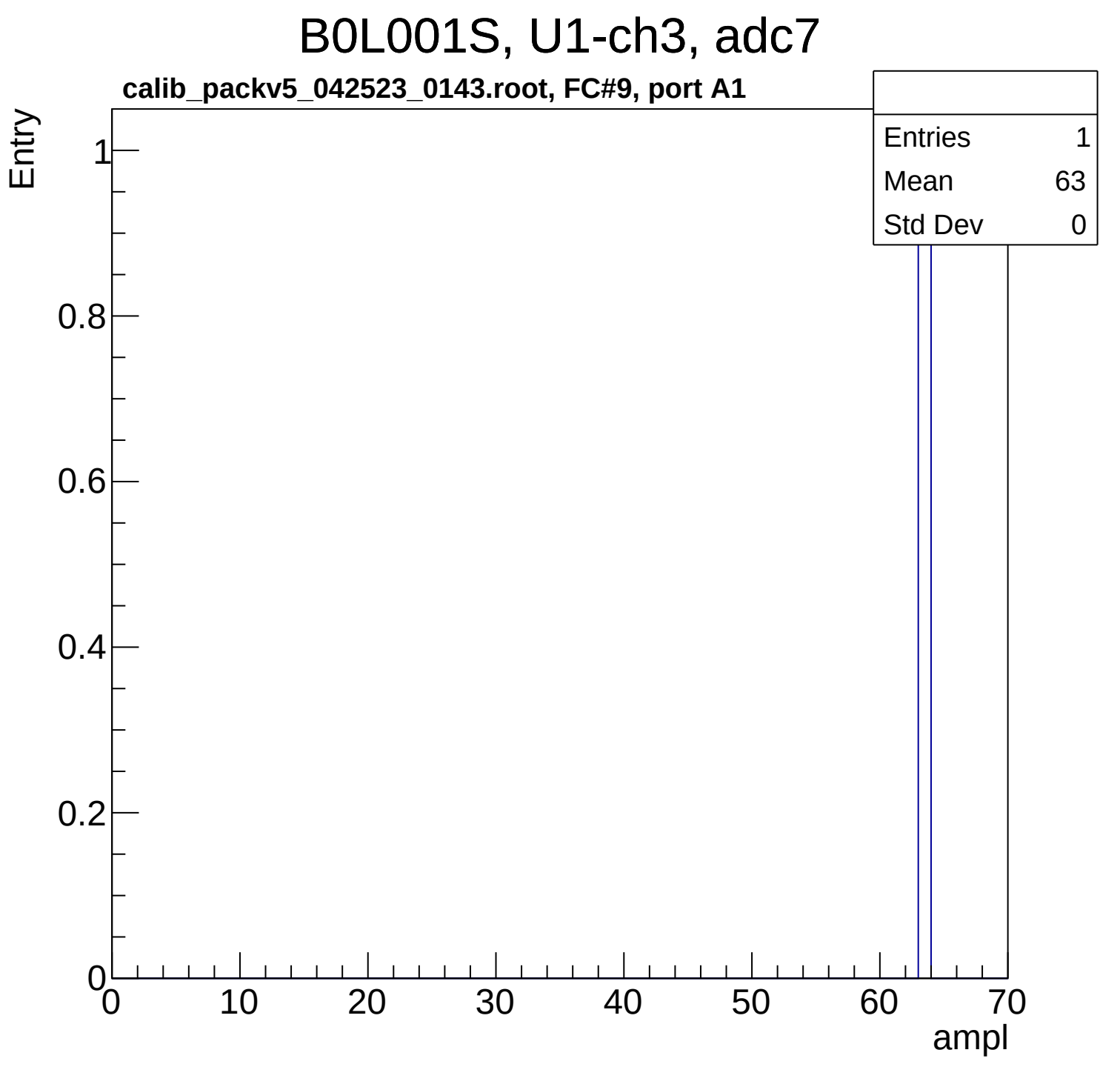
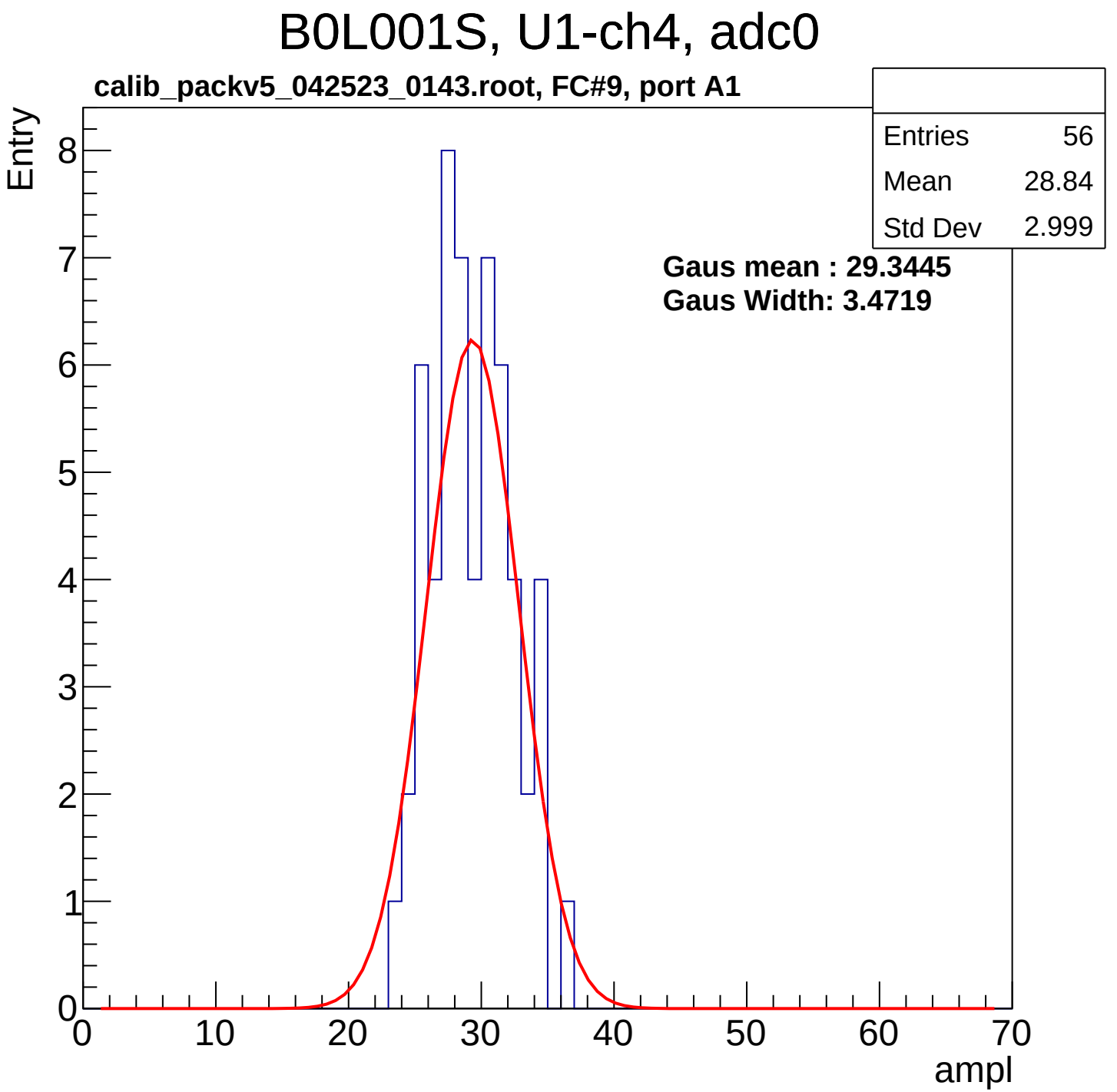
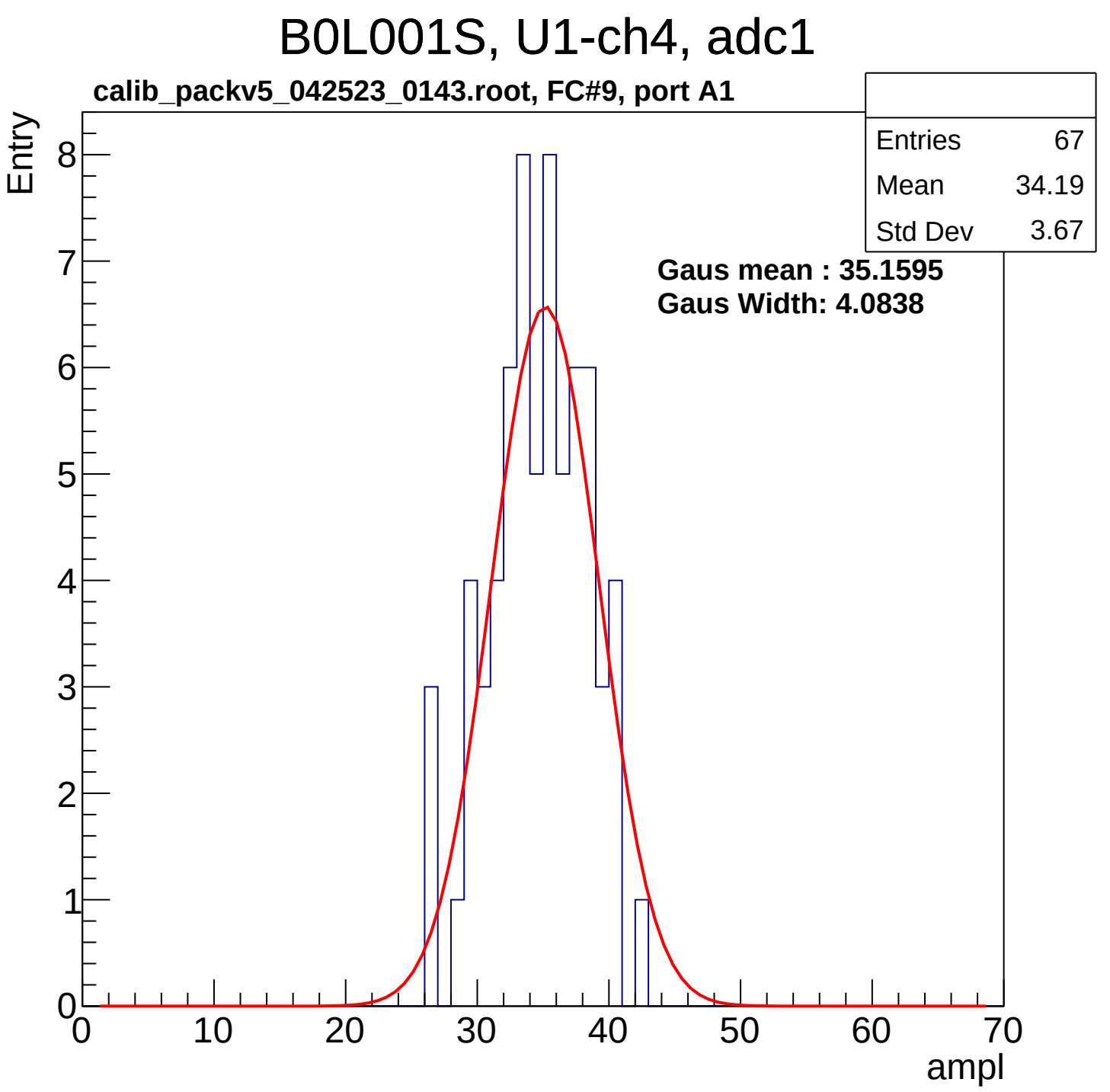
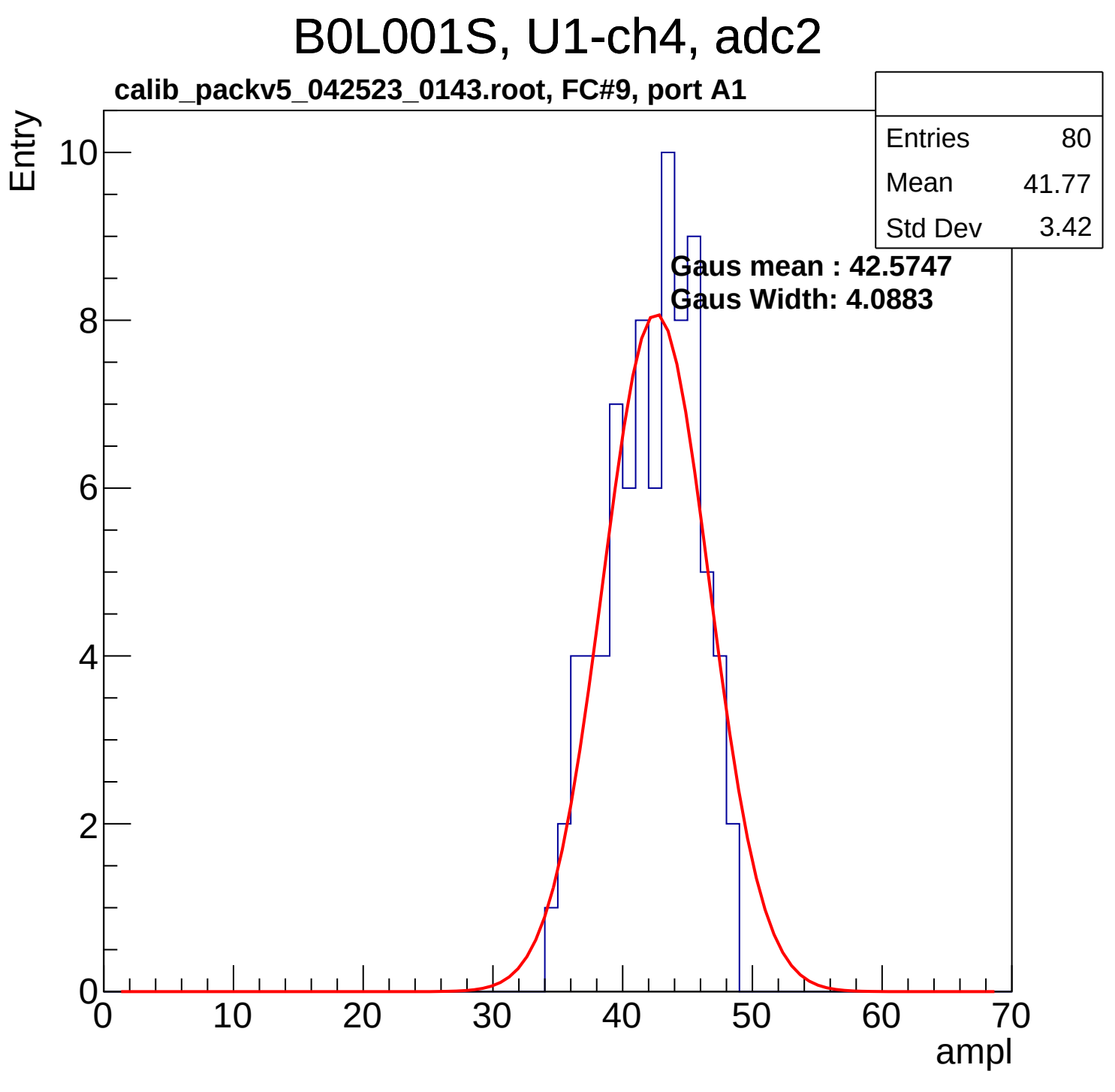
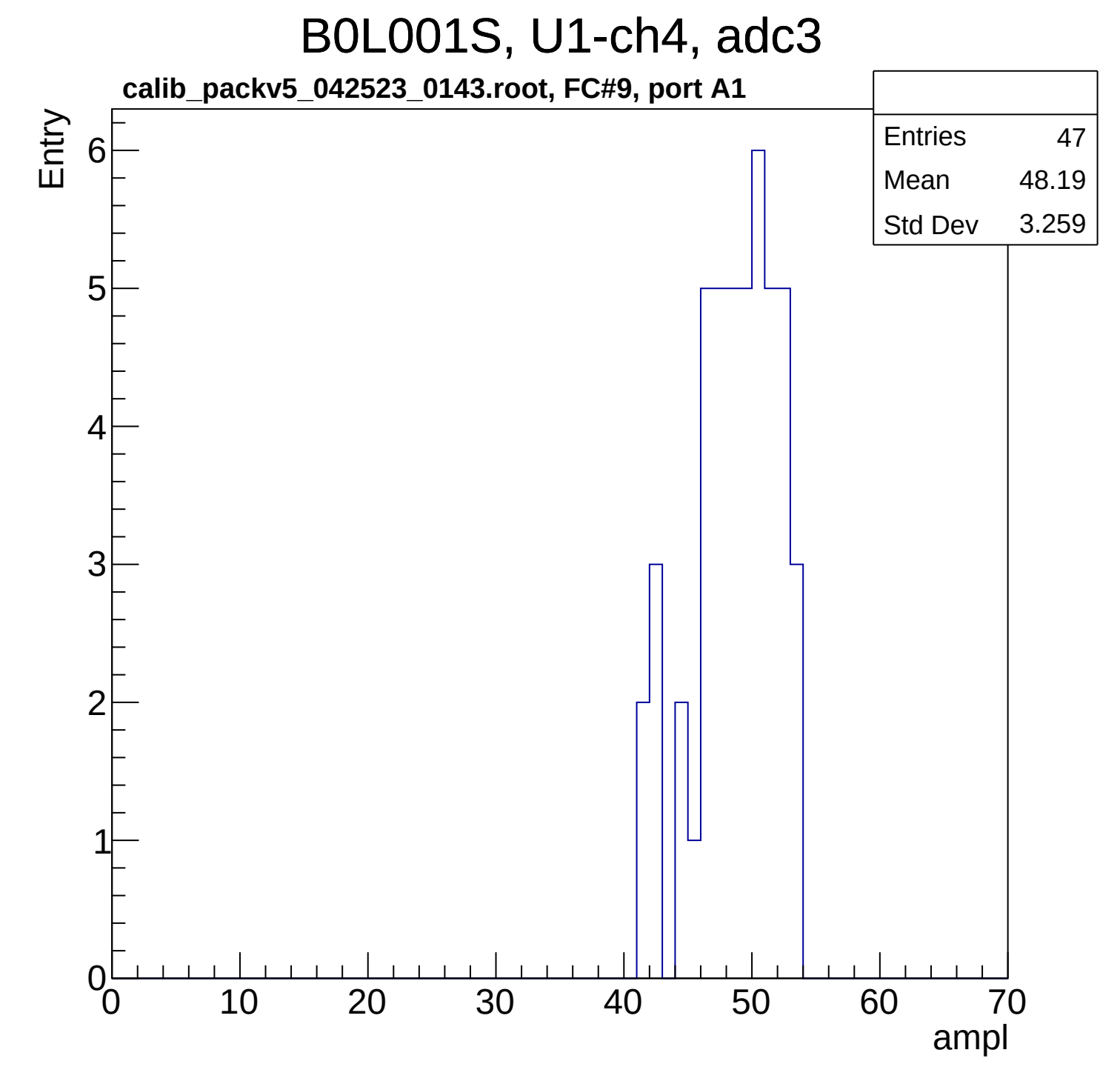

B0L001S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 60.89
Std Dev 1.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

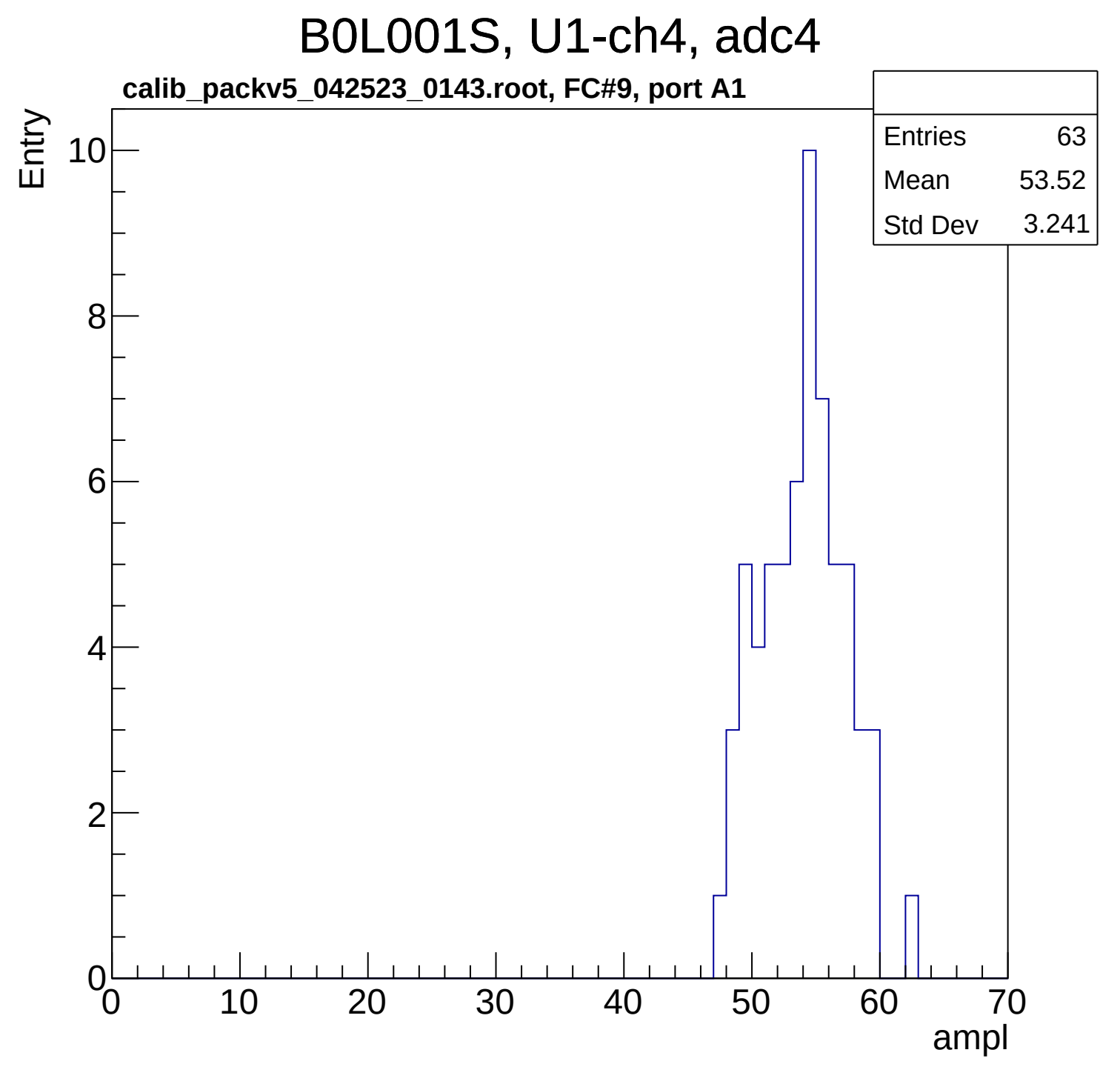

B0L001S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

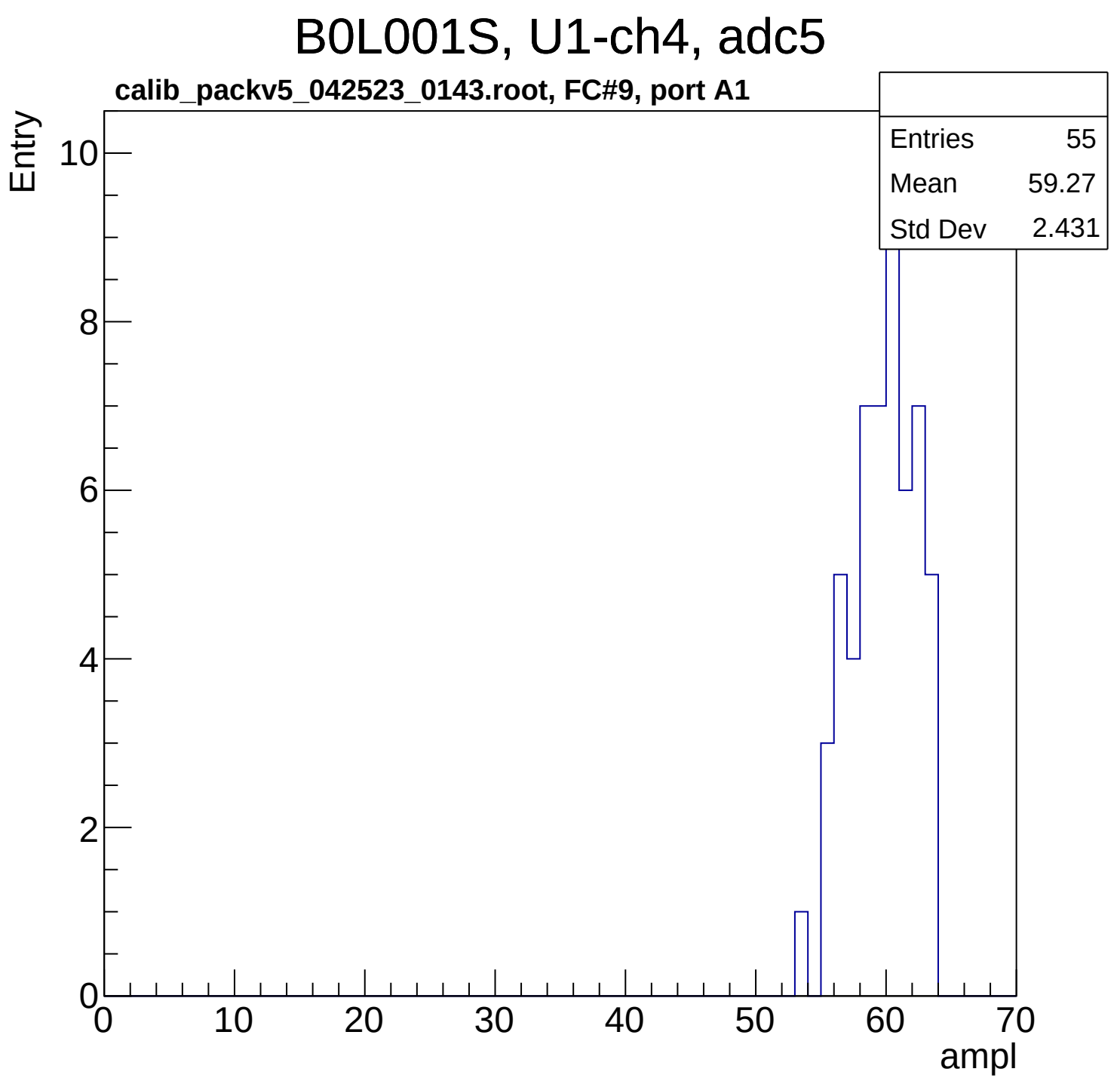

B0L001S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 28.84
Std Dev 2.999
Gaus mean : 29.3445
Gaus Width: 3.4719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

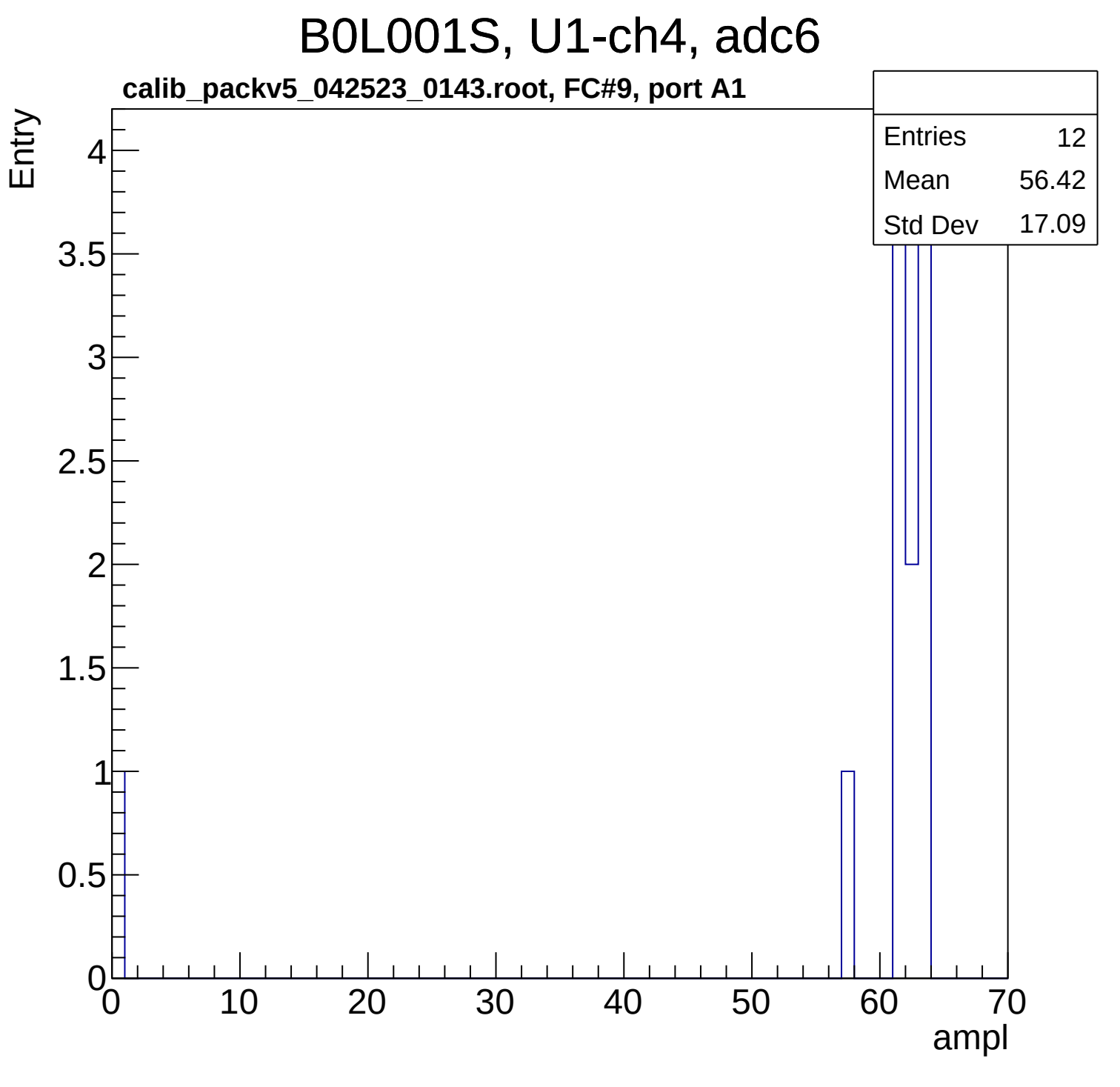

B0L001S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 34.19
Std Dev 3.67
Gaus mean : 35.1595
Gaus Width: 4.0838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

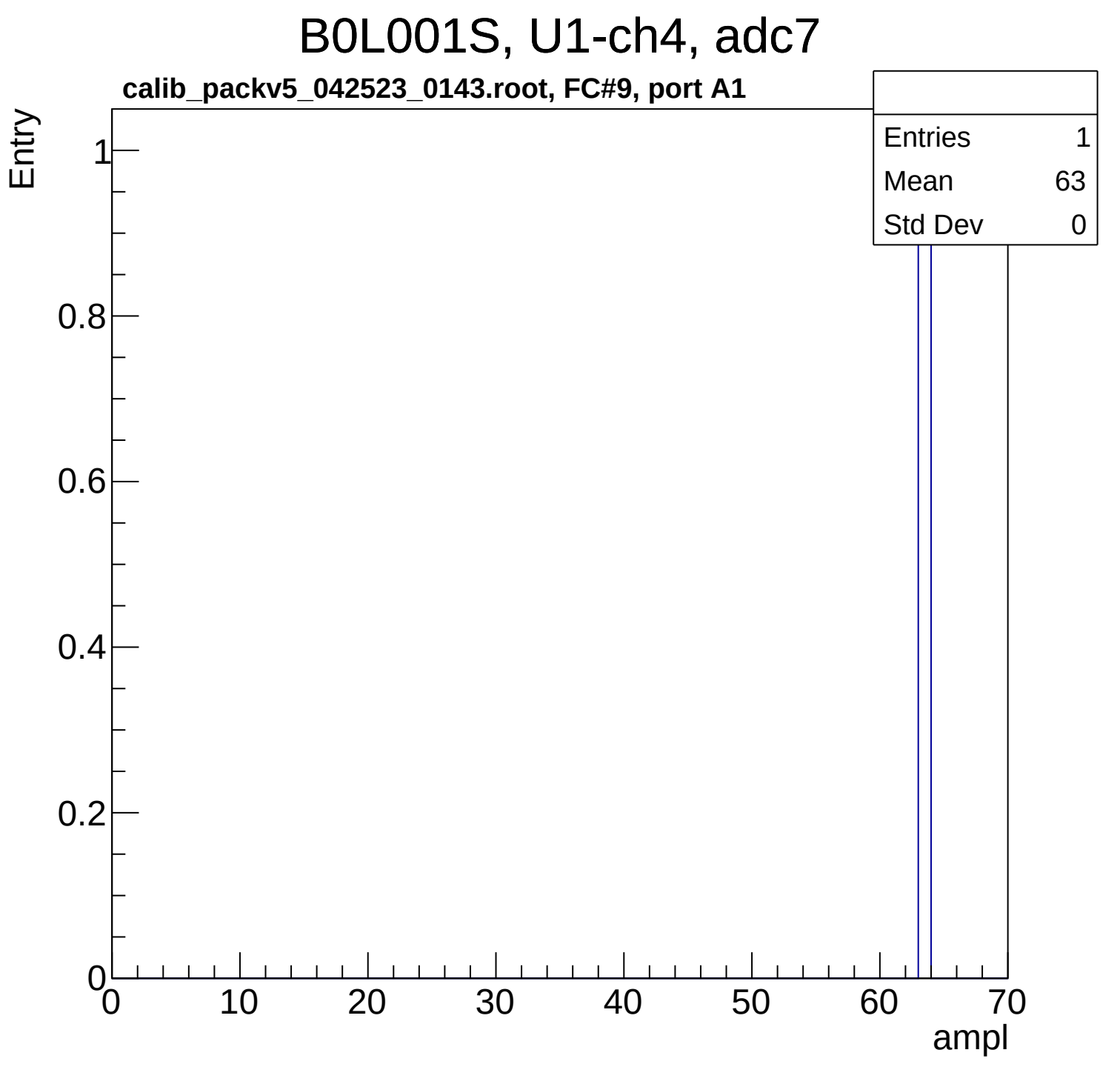

B0L001S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 41.77
Std Dev 3.42
Gaus mean : 42.5747
Gaus Width: 4.0883
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

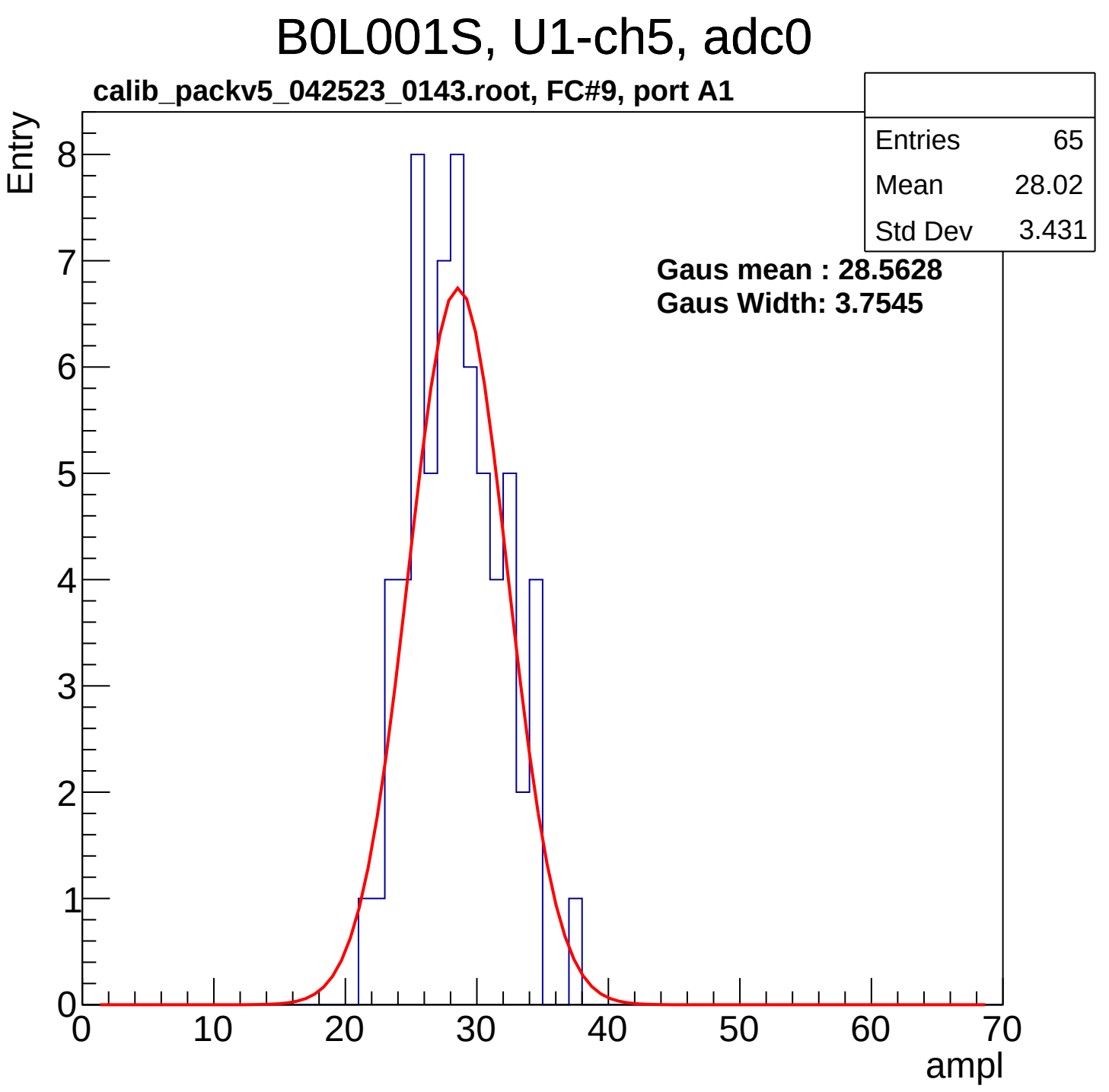

B0L001S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 48.19
Std Dev 3.259
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

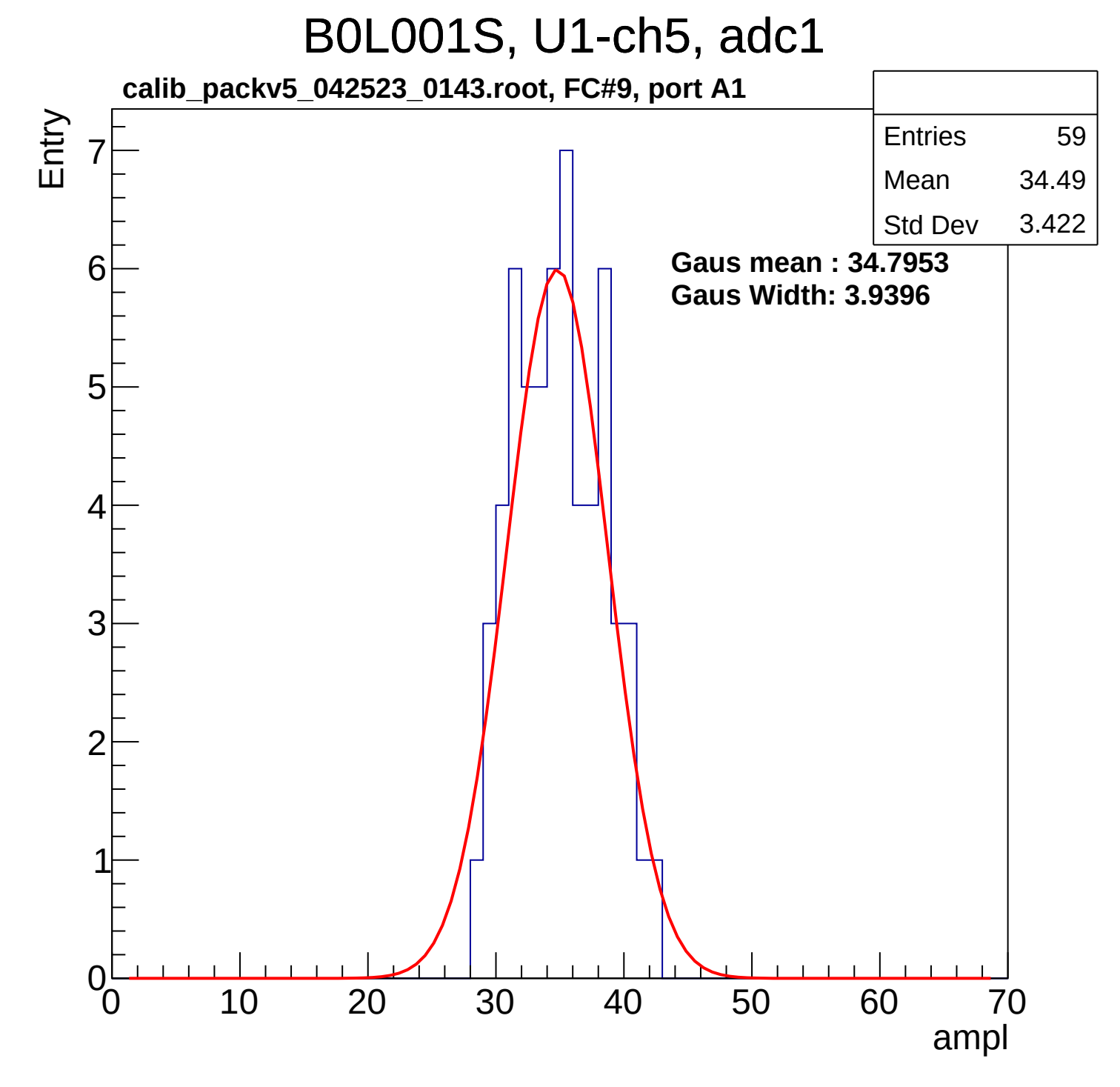

B0L001S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 53.52
Std Dev 3.241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

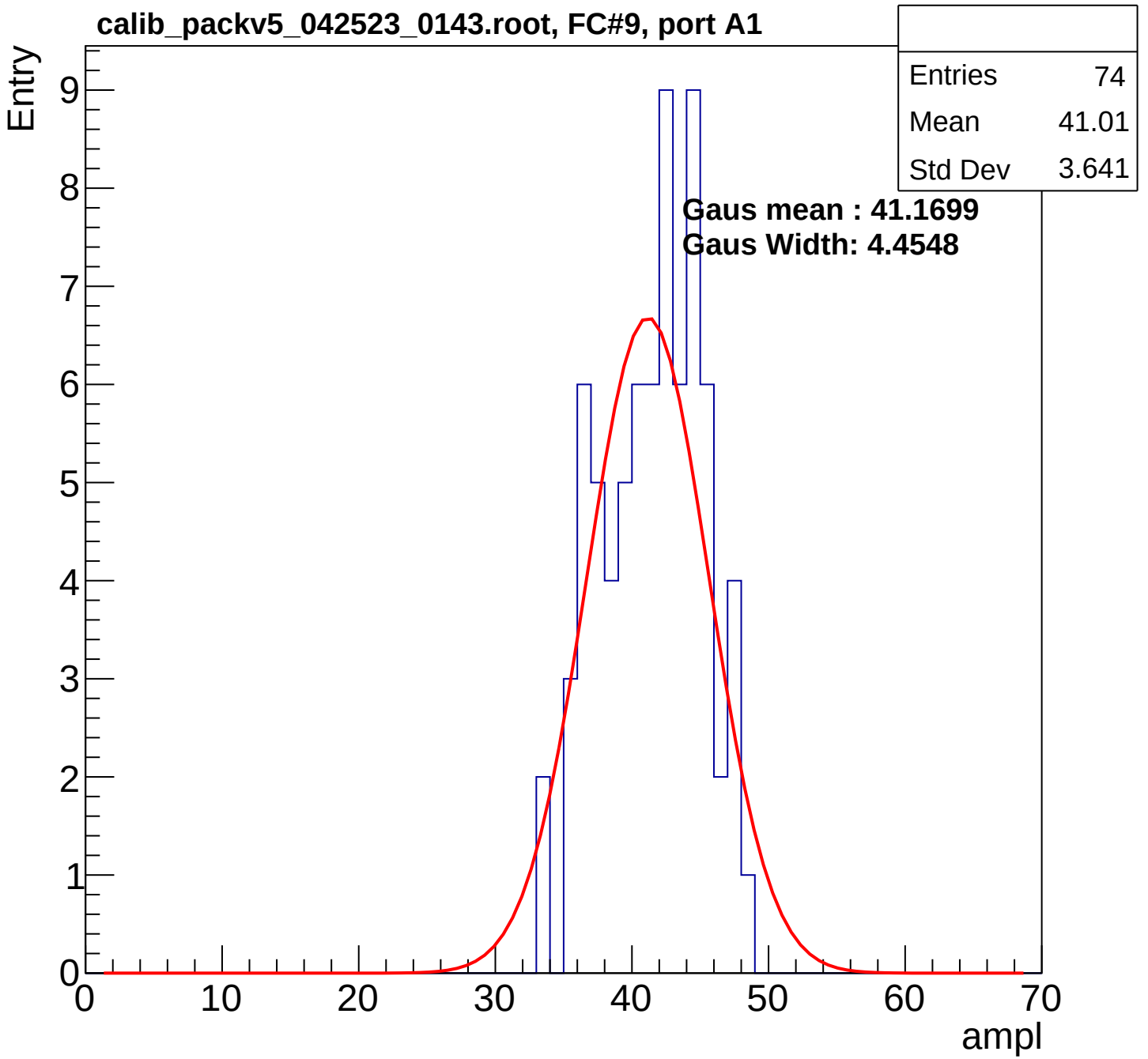

B0L001S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 59.27
Std Dev 2.431
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

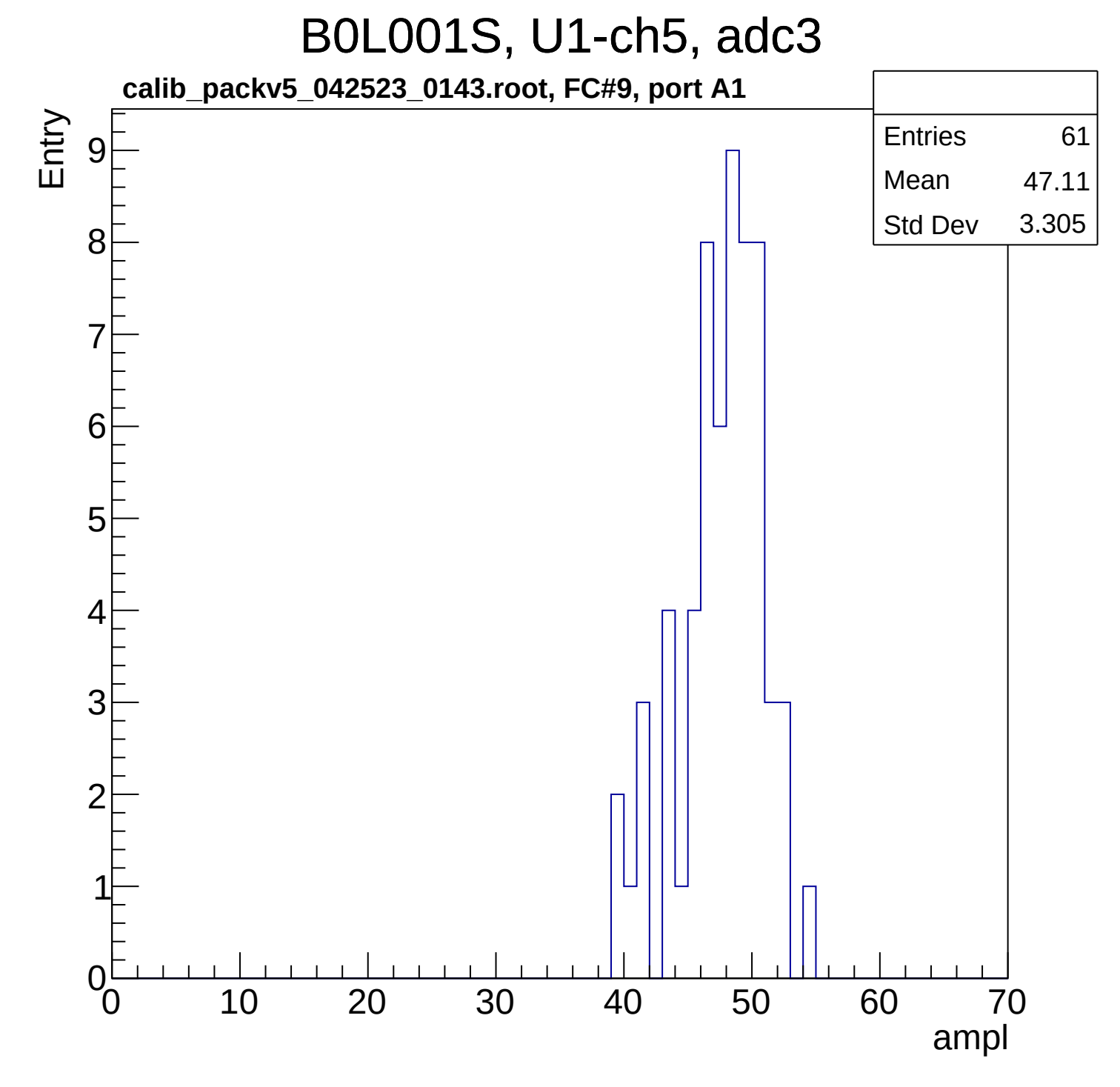

B0L001S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.42
Std Dev 17.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

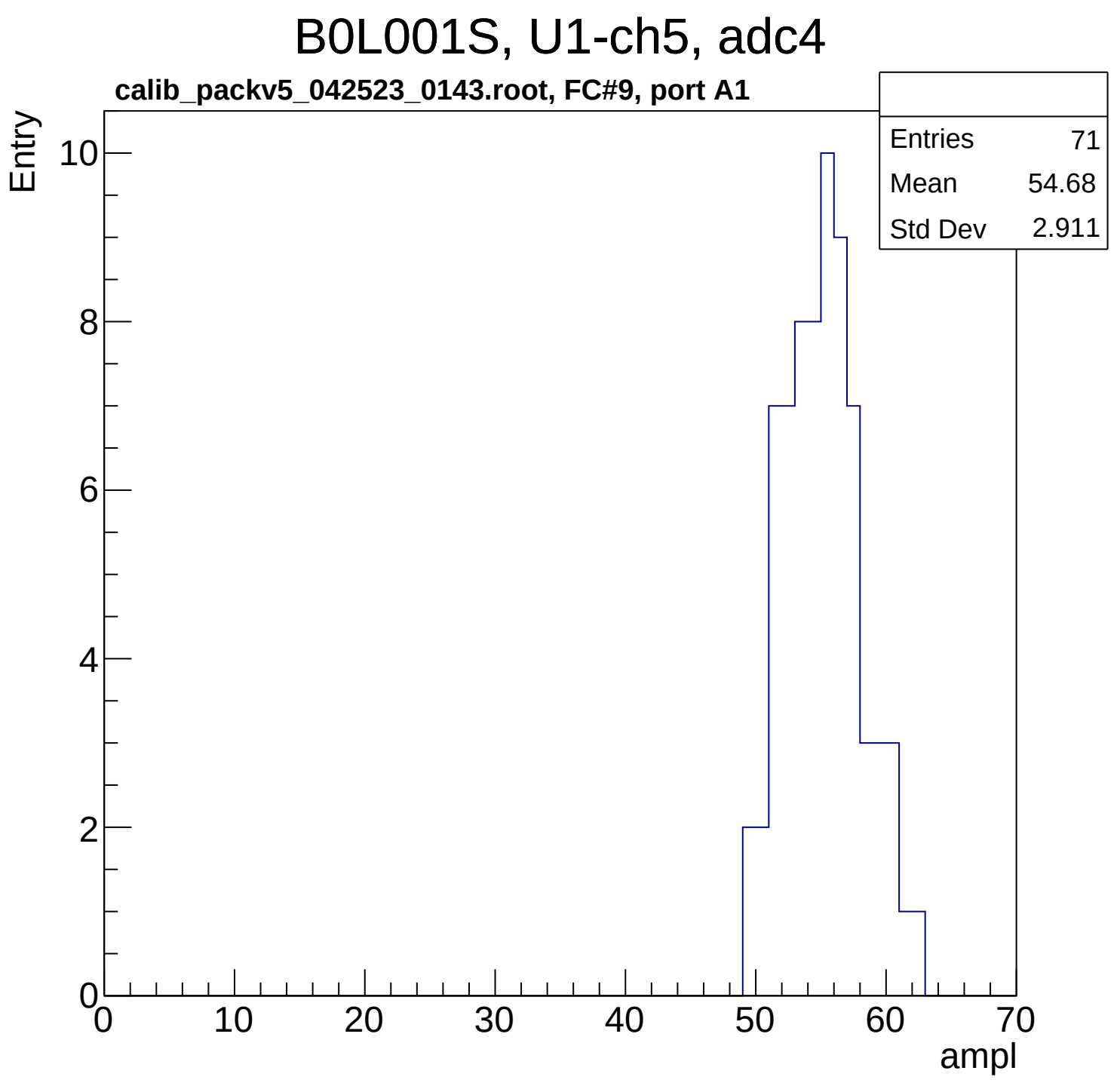

B0L001S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

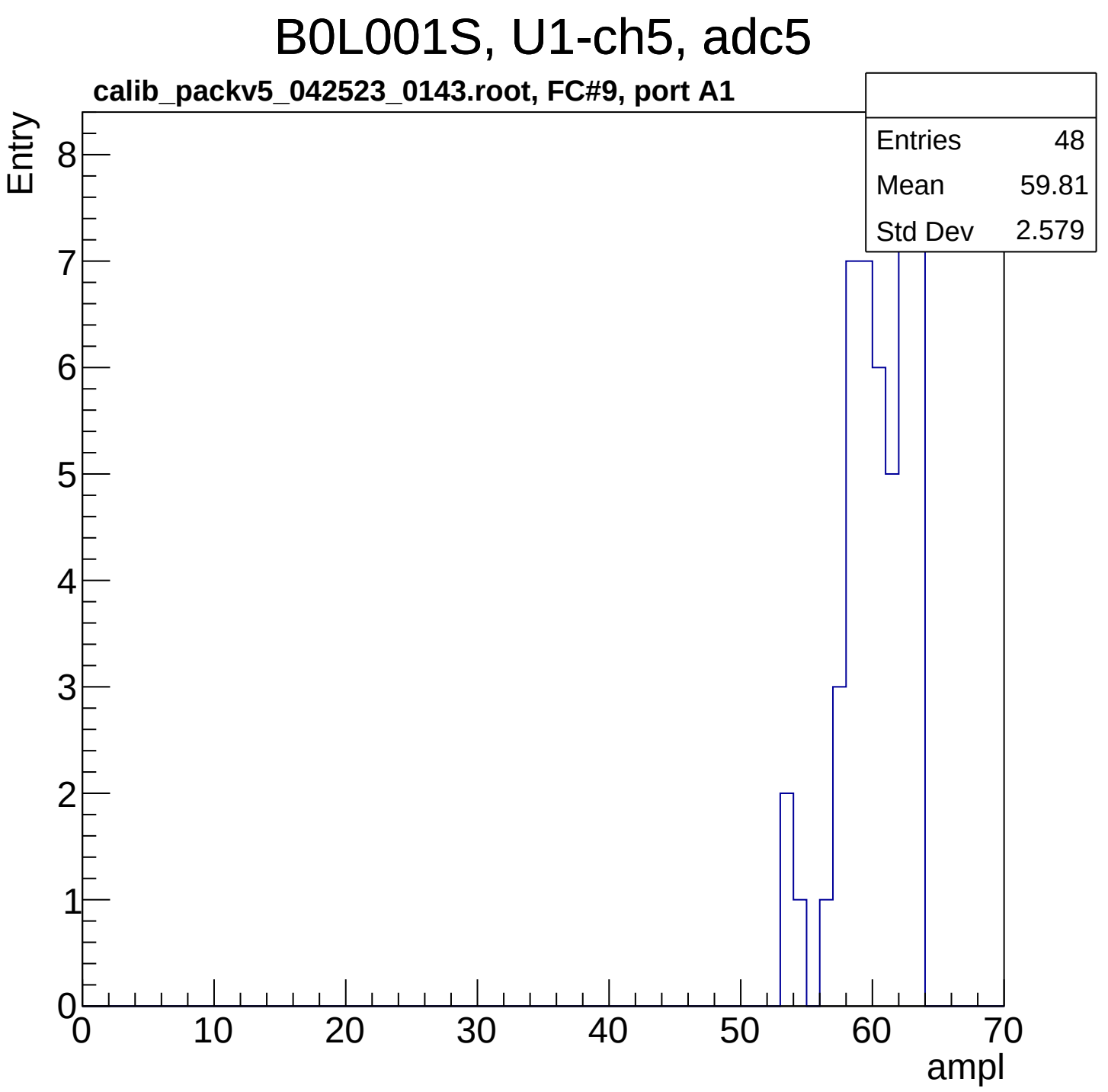

B0L001S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 28.02
Std Dev 3.431
Gaus mean : 28.5628
Gaus Width: 3.7545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

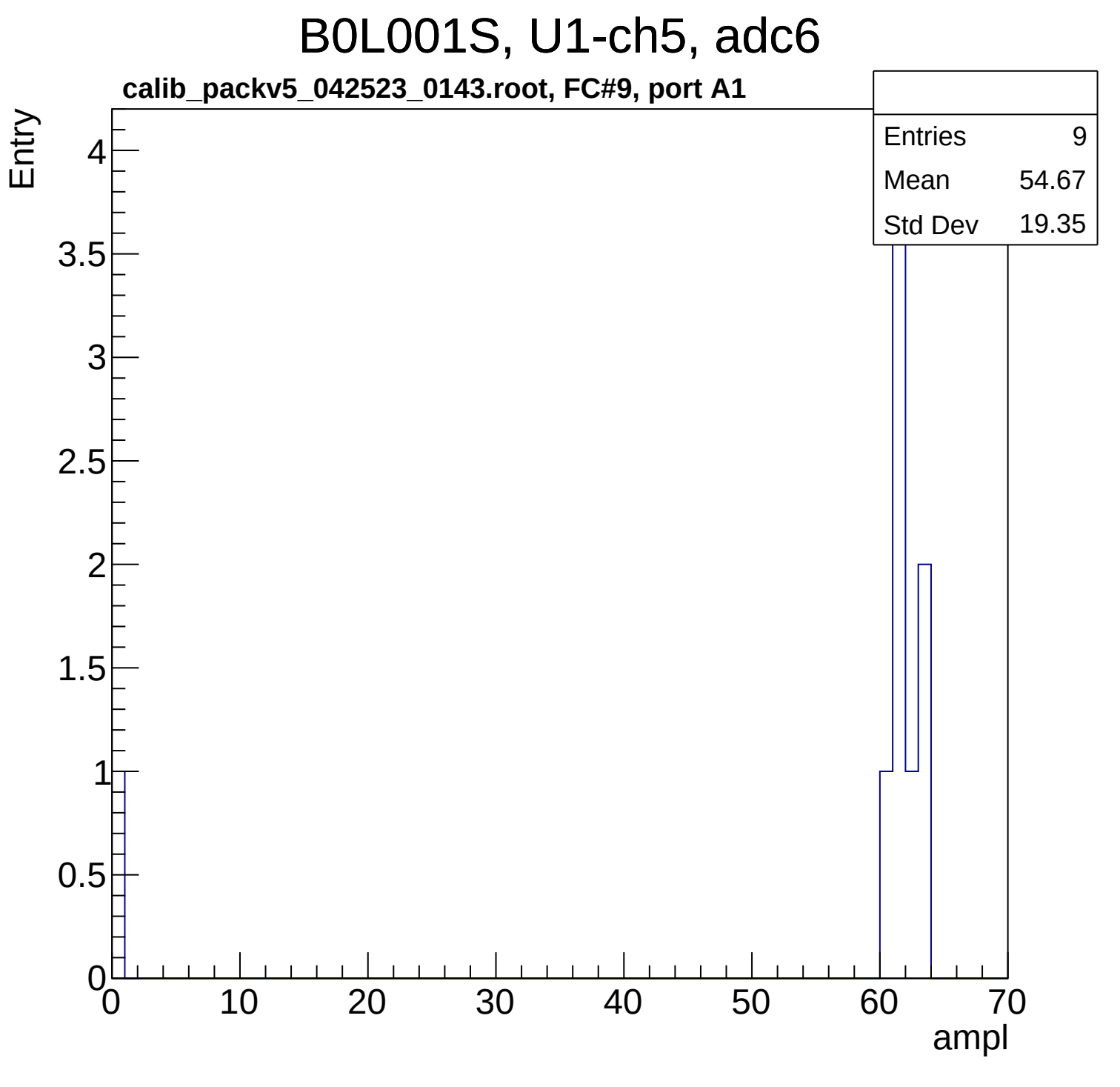

B0L001S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 34.49
Std Dev 3.422
Gaus mean : 34.7953
Gaus Width: 3.9396
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

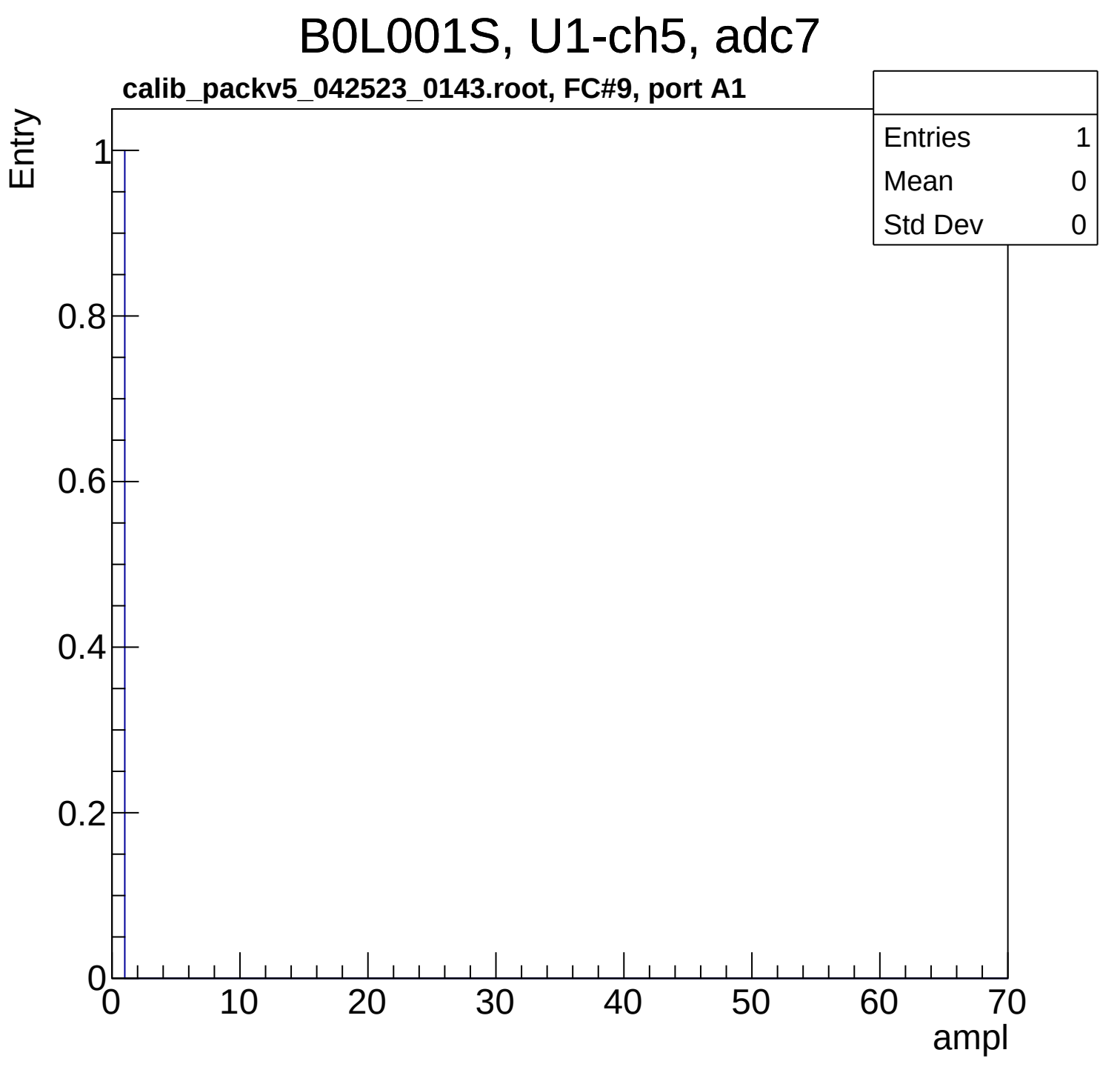

B0L001S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 41.01
Std Dev 3.641
Gaus mean : 41.1699
Gaus Width: 4.4548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

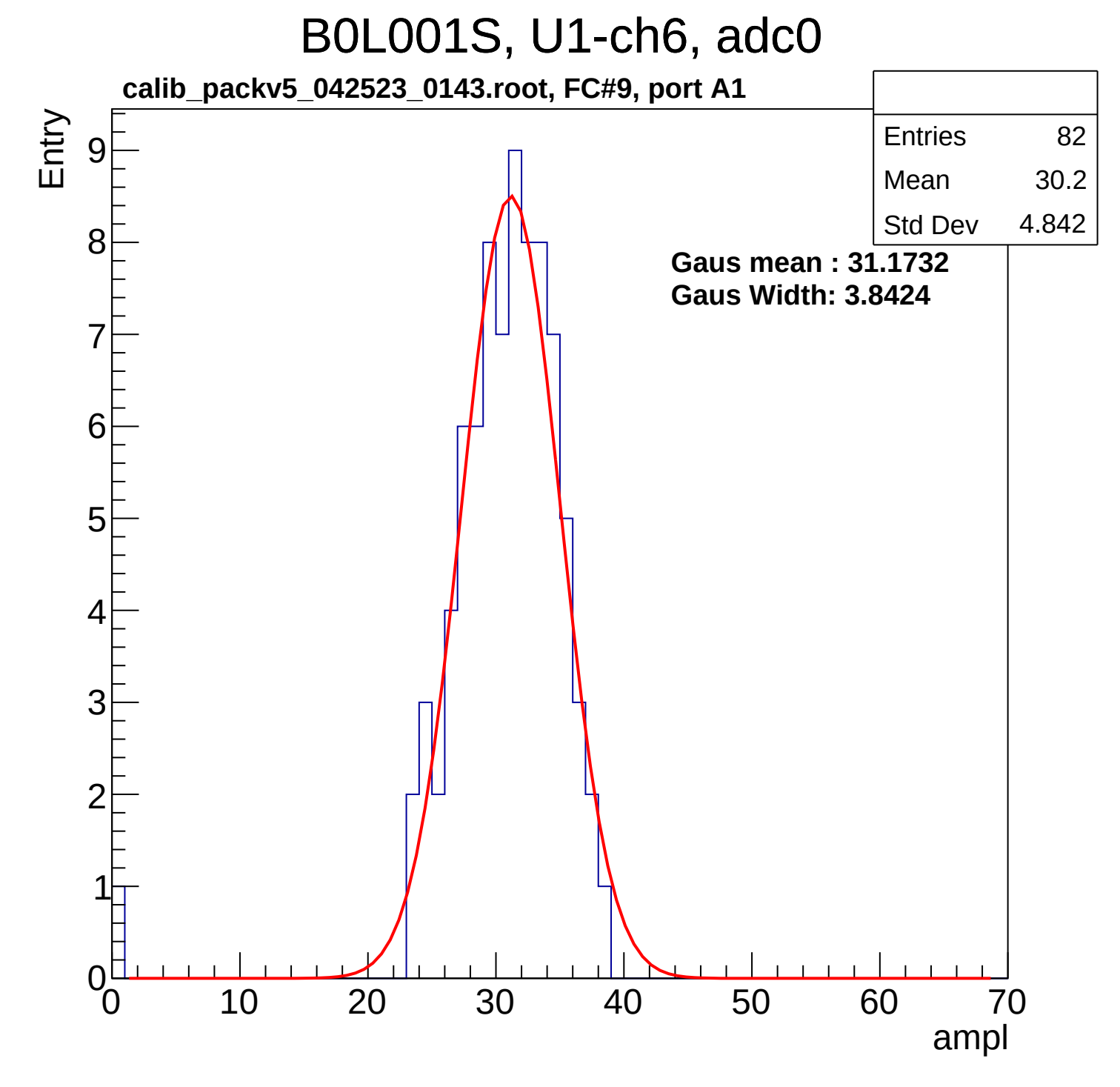

B0L001S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 47.11
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

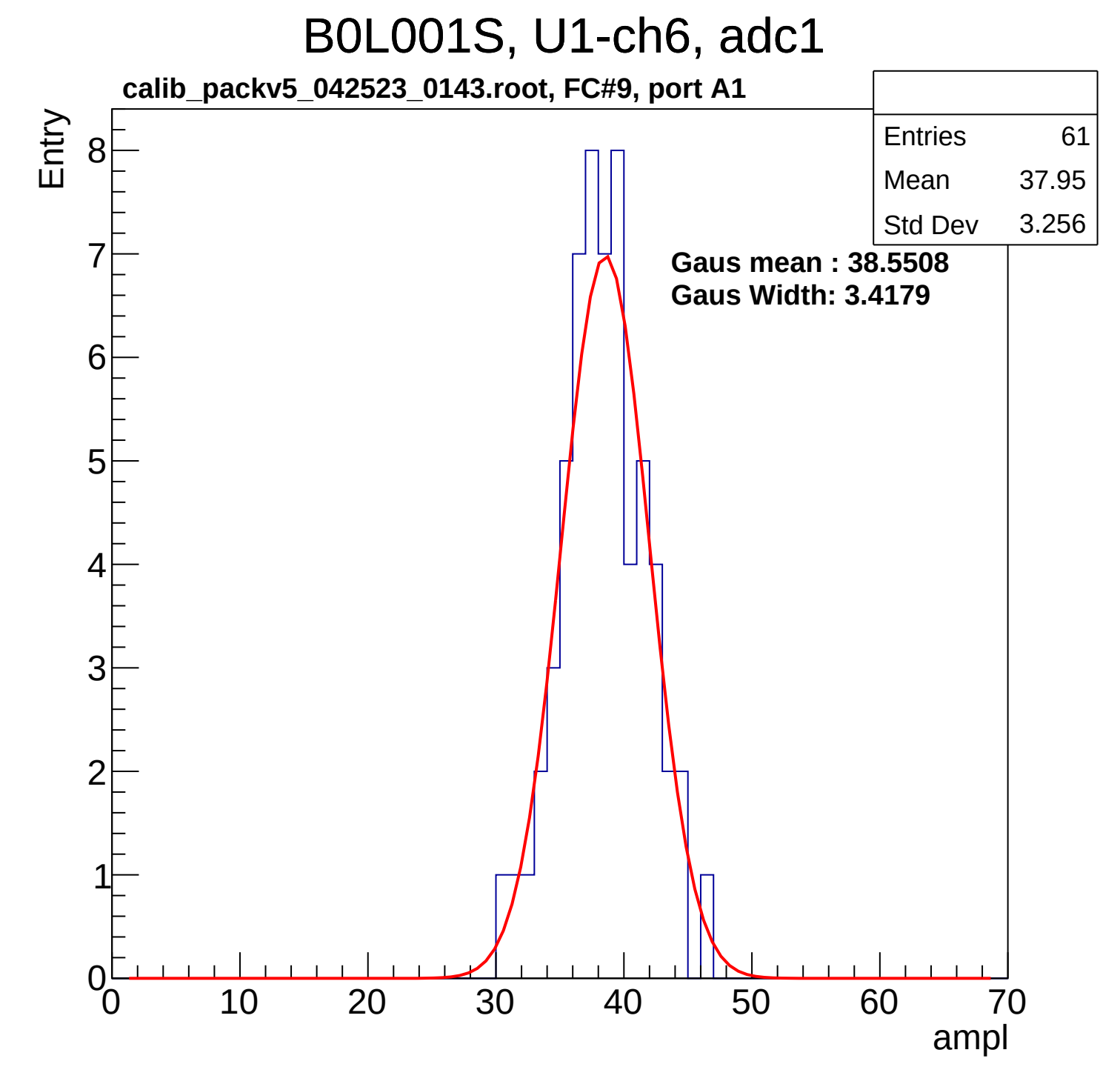

B0L001S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 54.68
Std Dev 2.911
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

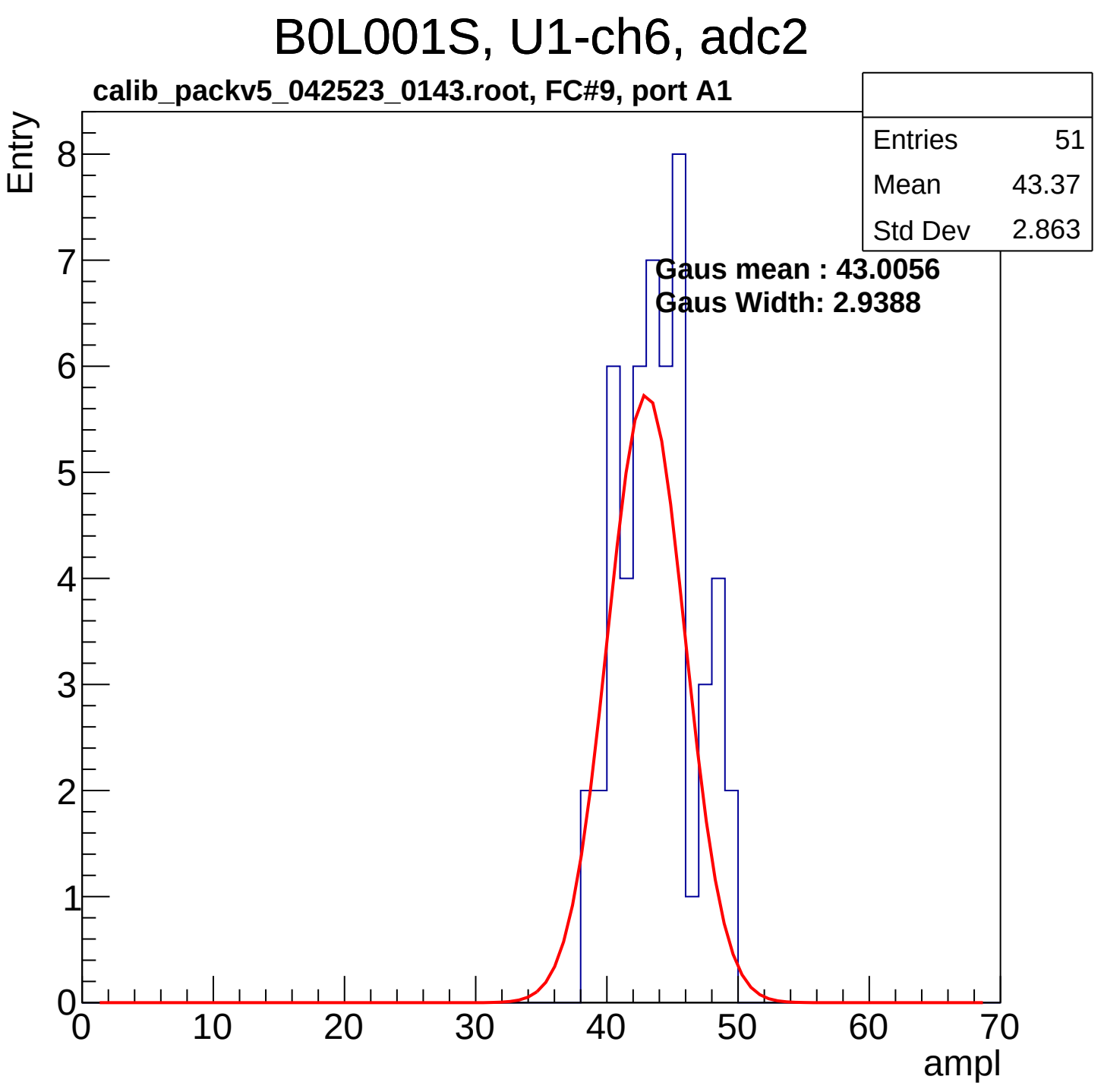

B0L001S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59.81
Std Dev 2.579
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

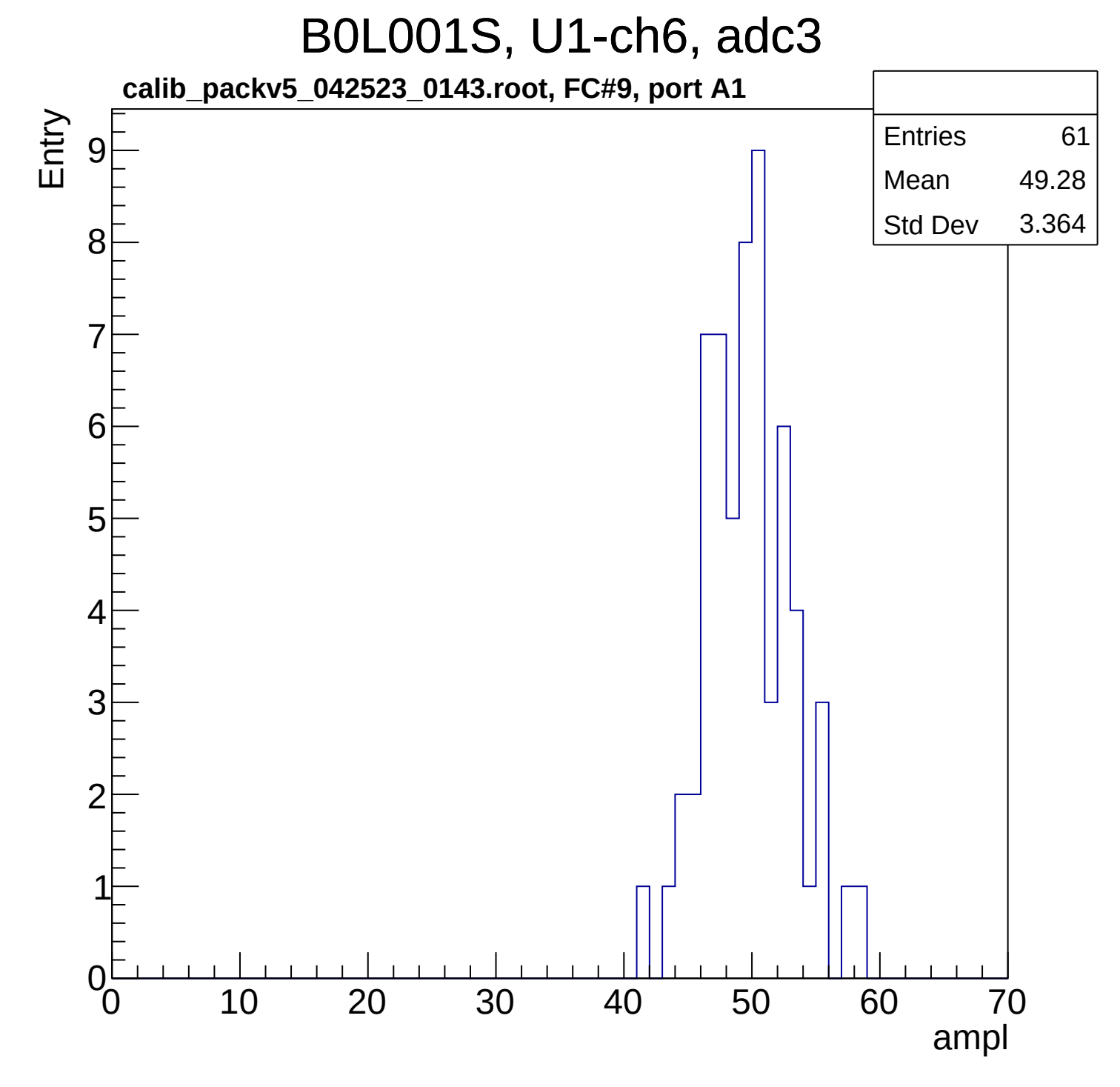

B0L001S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.67
Std Dev 19.35
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

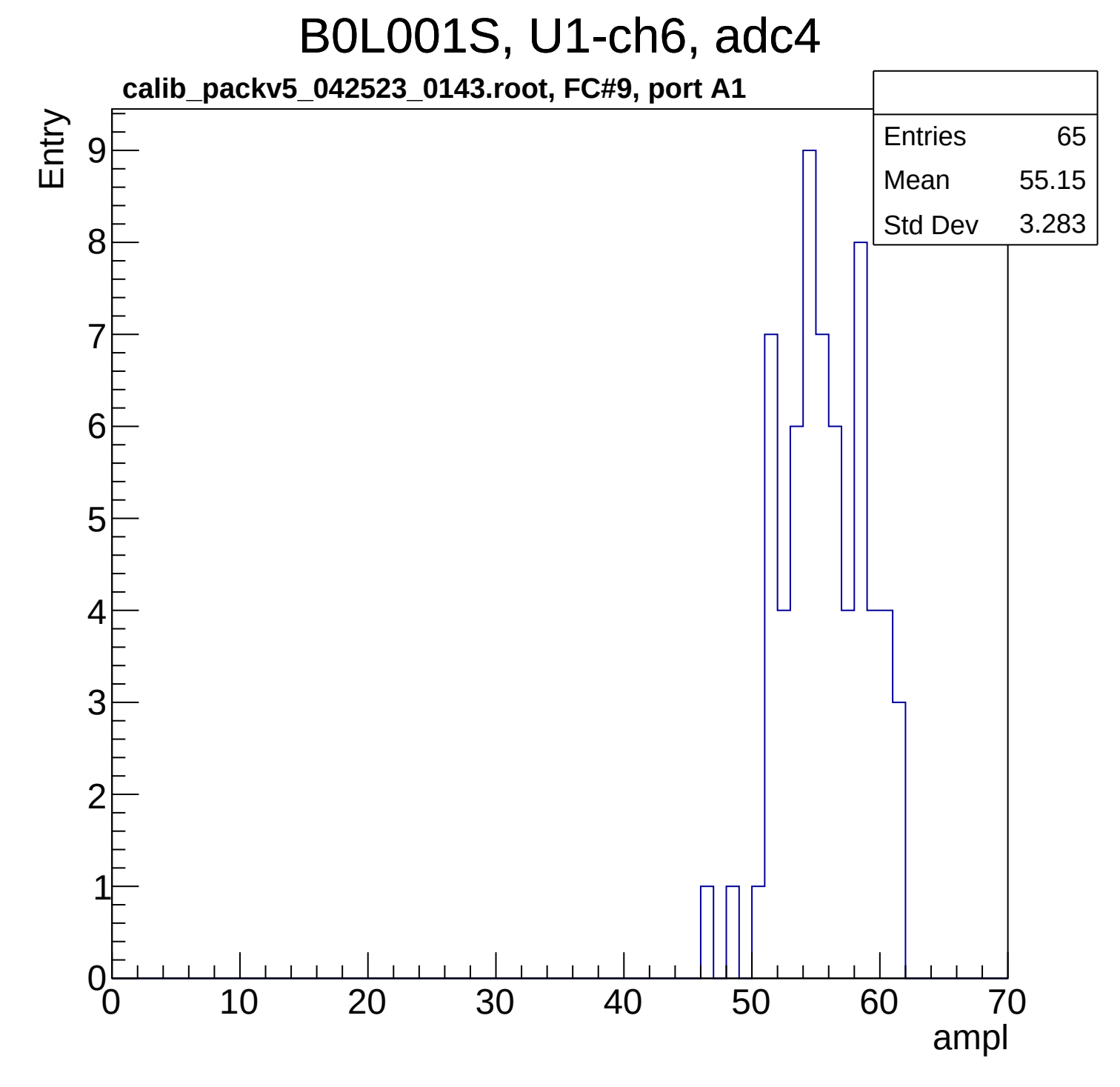

B0L001S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

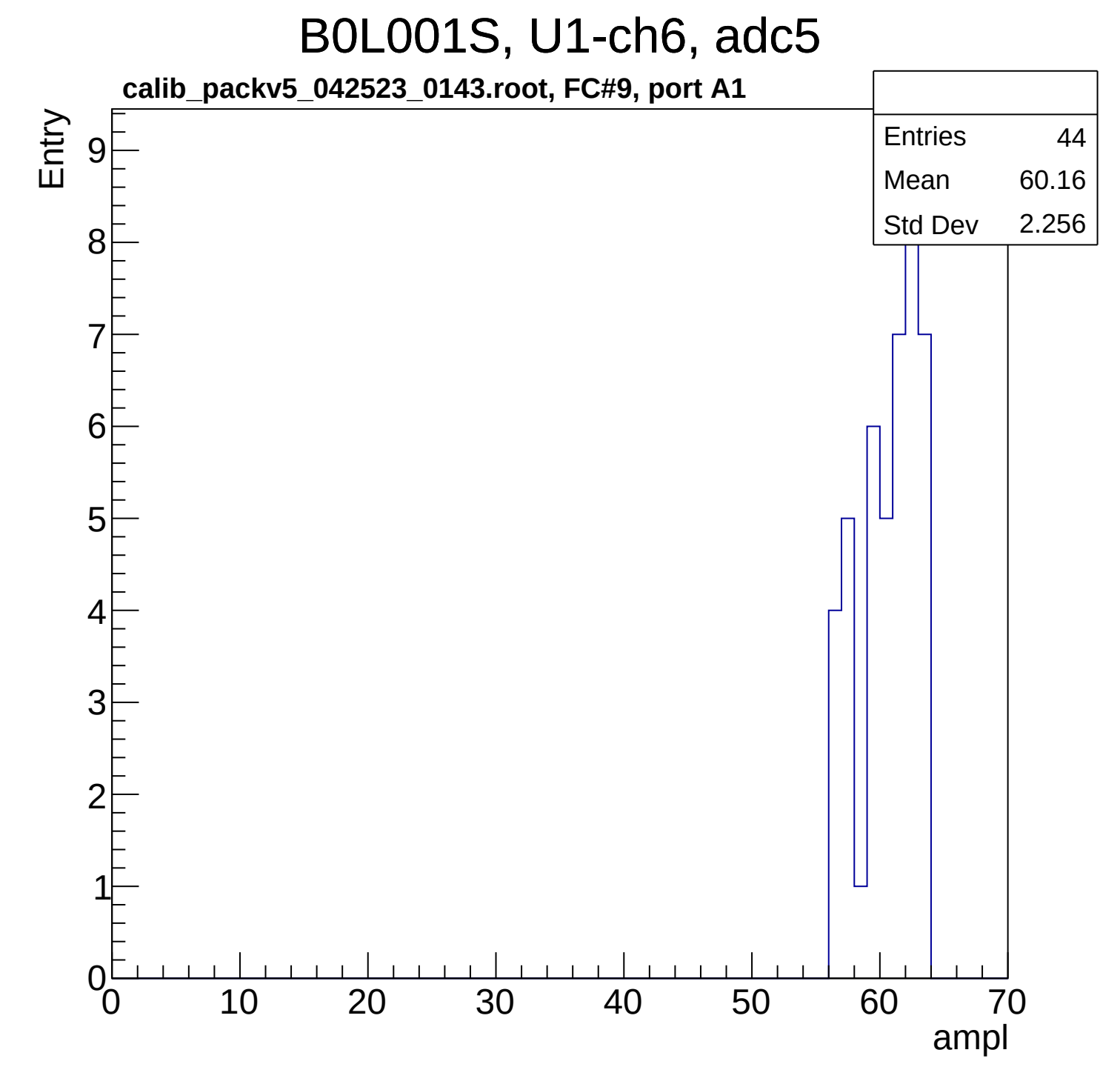

B0L001S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30.2
Std Dev 4.842
Gaus mean : 31.1732
Gaus Width: 3.8424
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

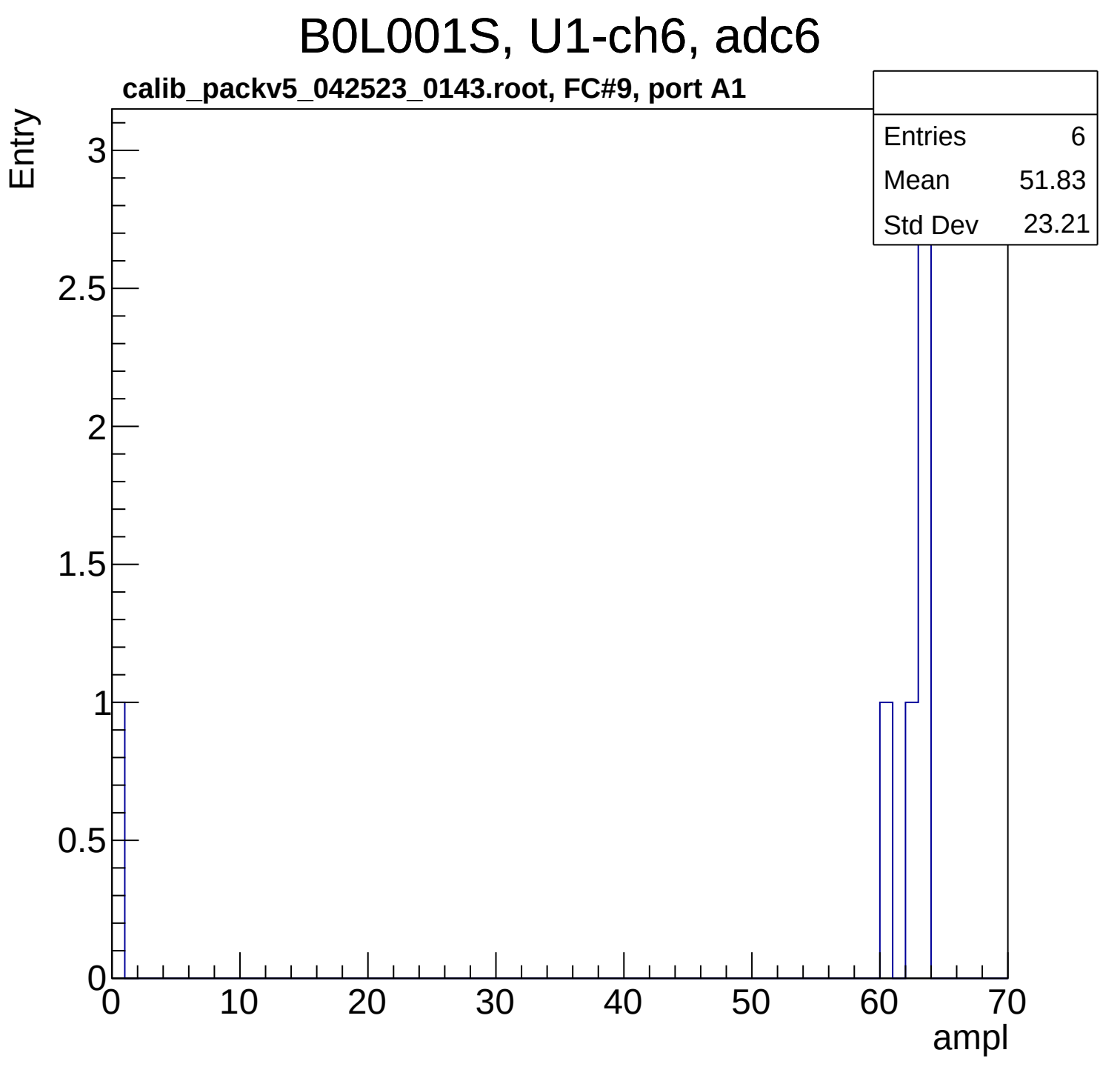

B0L001S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.95
Std Dev 3.256
Gaus mean : 38.5508
Gaus Width: 3.4179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.37
Std Dev 2.863
Gaus mean : 43.0056
Gaus Width: 2.9388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.28
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.15
Std Dev 3.283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 60.16
Std Dev 2.256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.83
Std Dev 23.21
Entry
ampl
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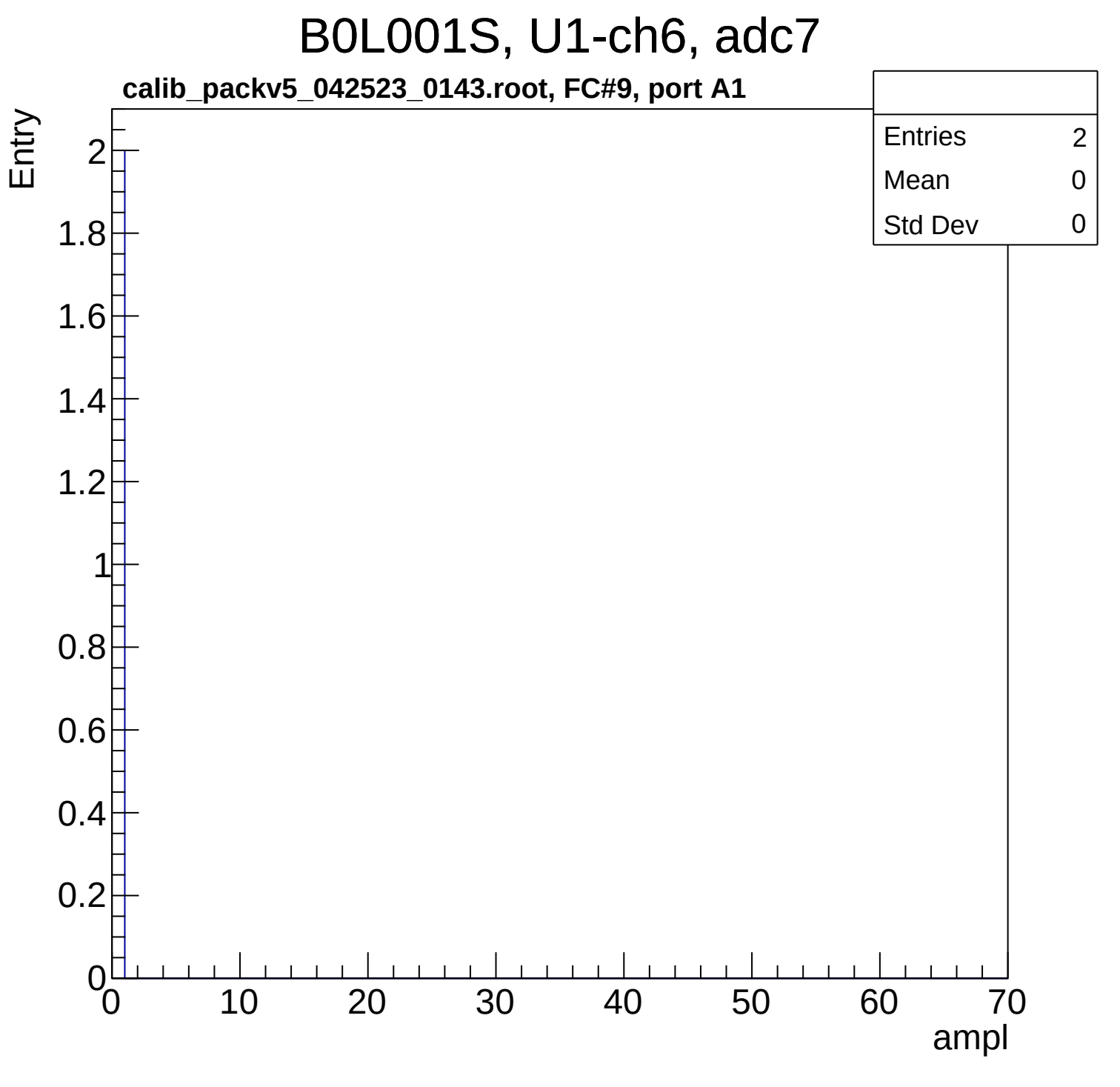
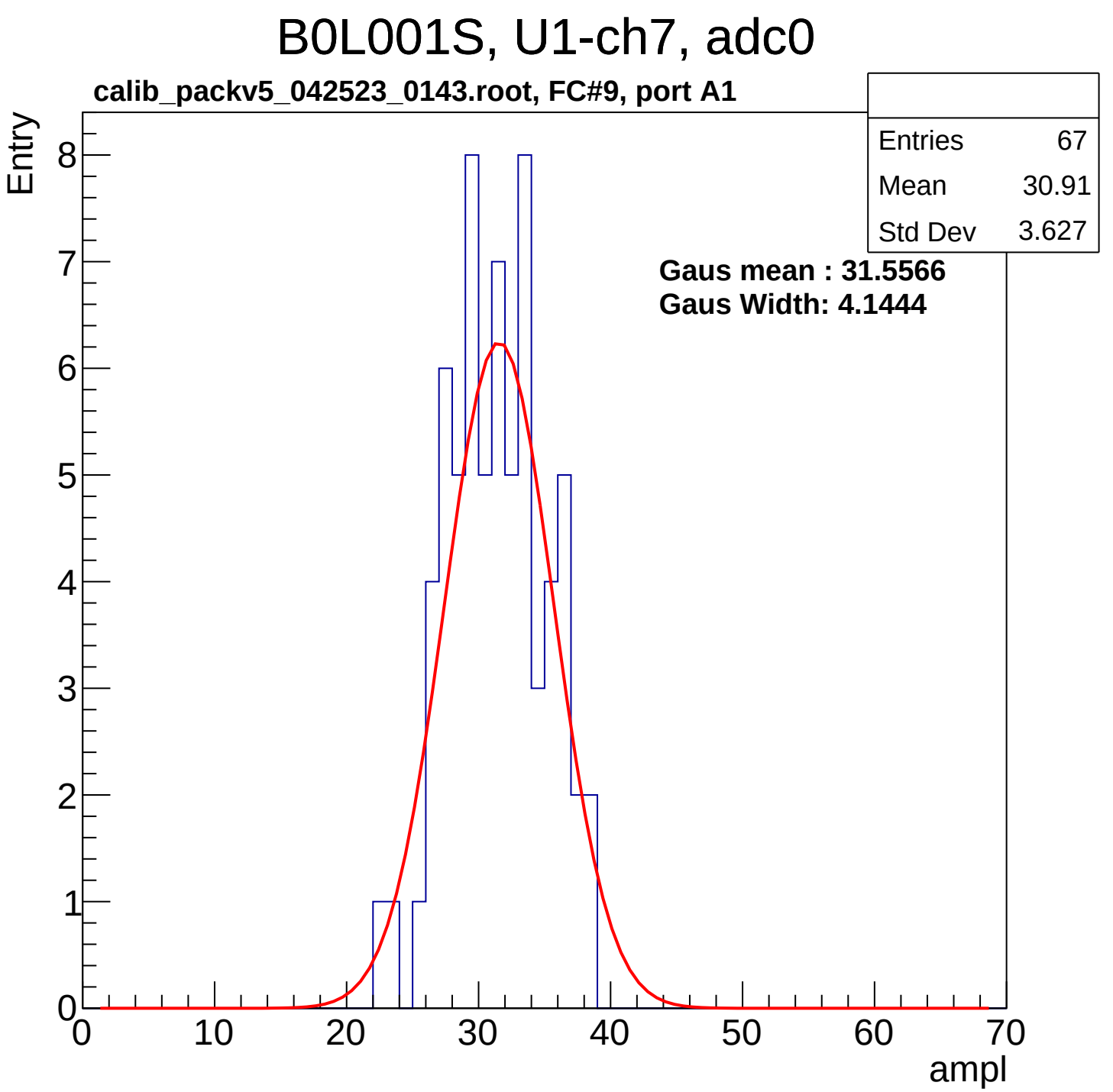
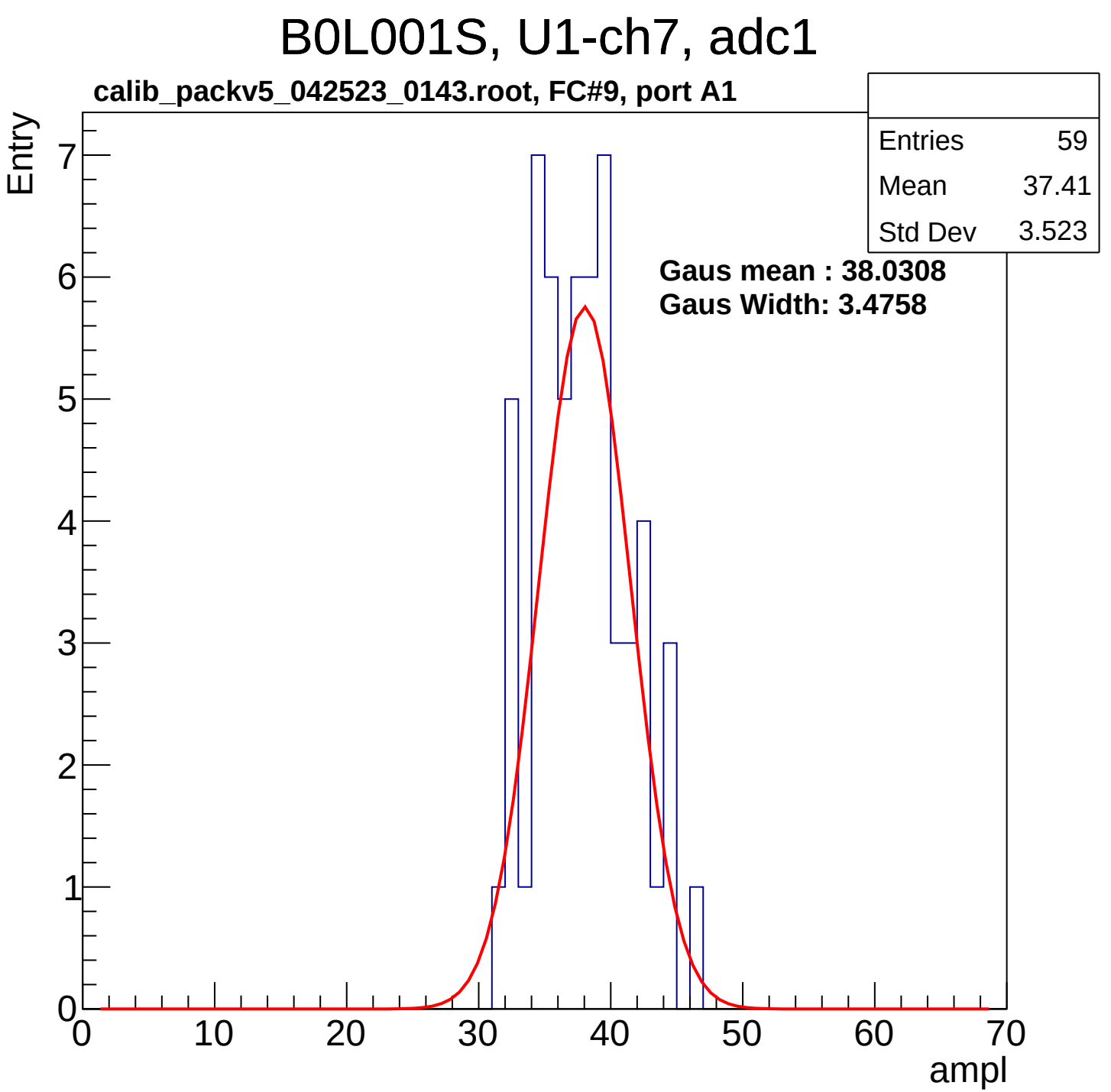
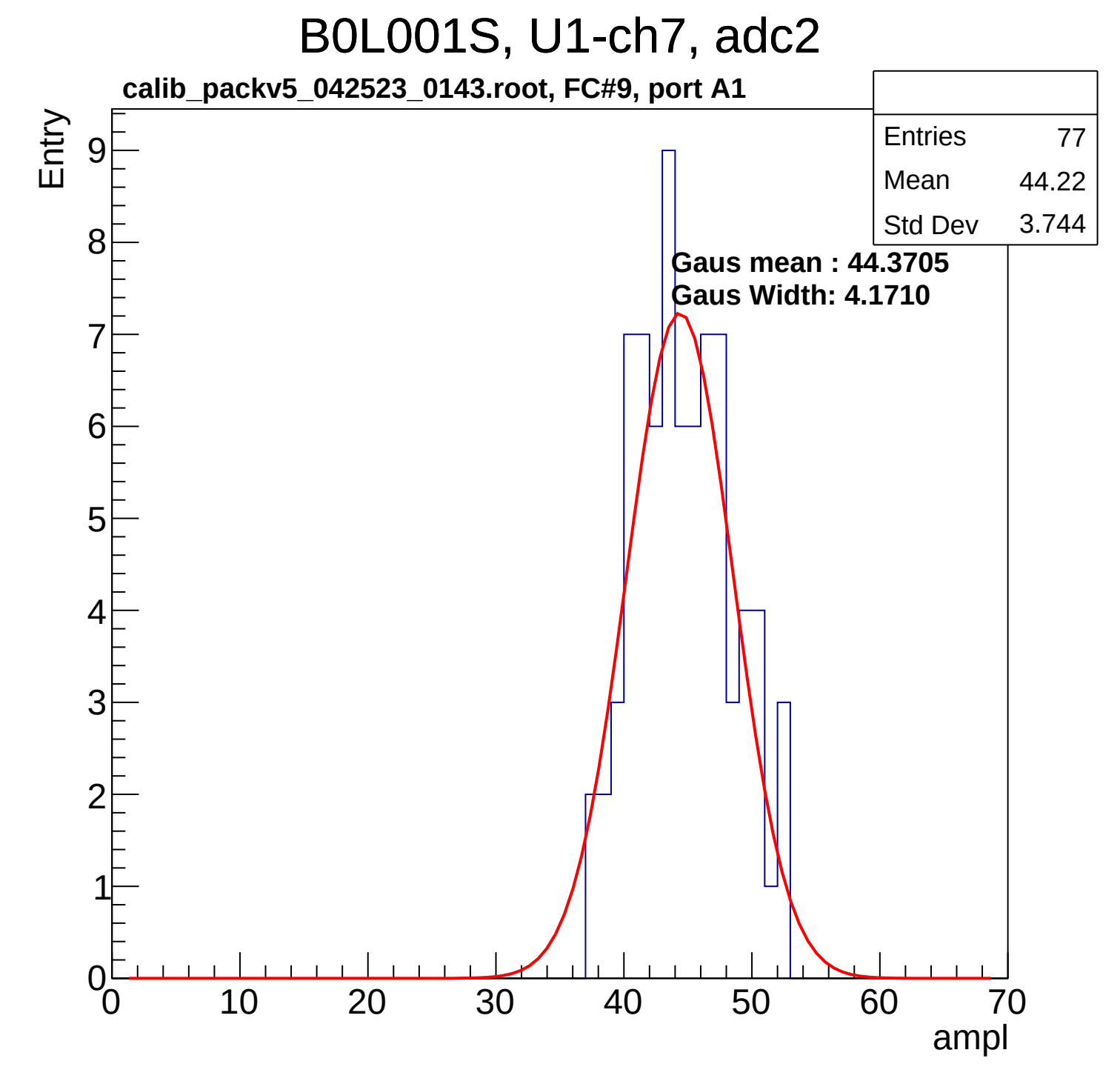
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 30.91
Std Dev 3.627
Gaus mean : 31.5566
Gaus Width: 4.1444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.41
Std Dev 3.523
Gaus mean : 38.0308
Gaus Width: 3.4758
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 44.22
Std Dev 3.744
Gaus mean : 44.3705
Gaus Width: 4.1710
Entry
ampl
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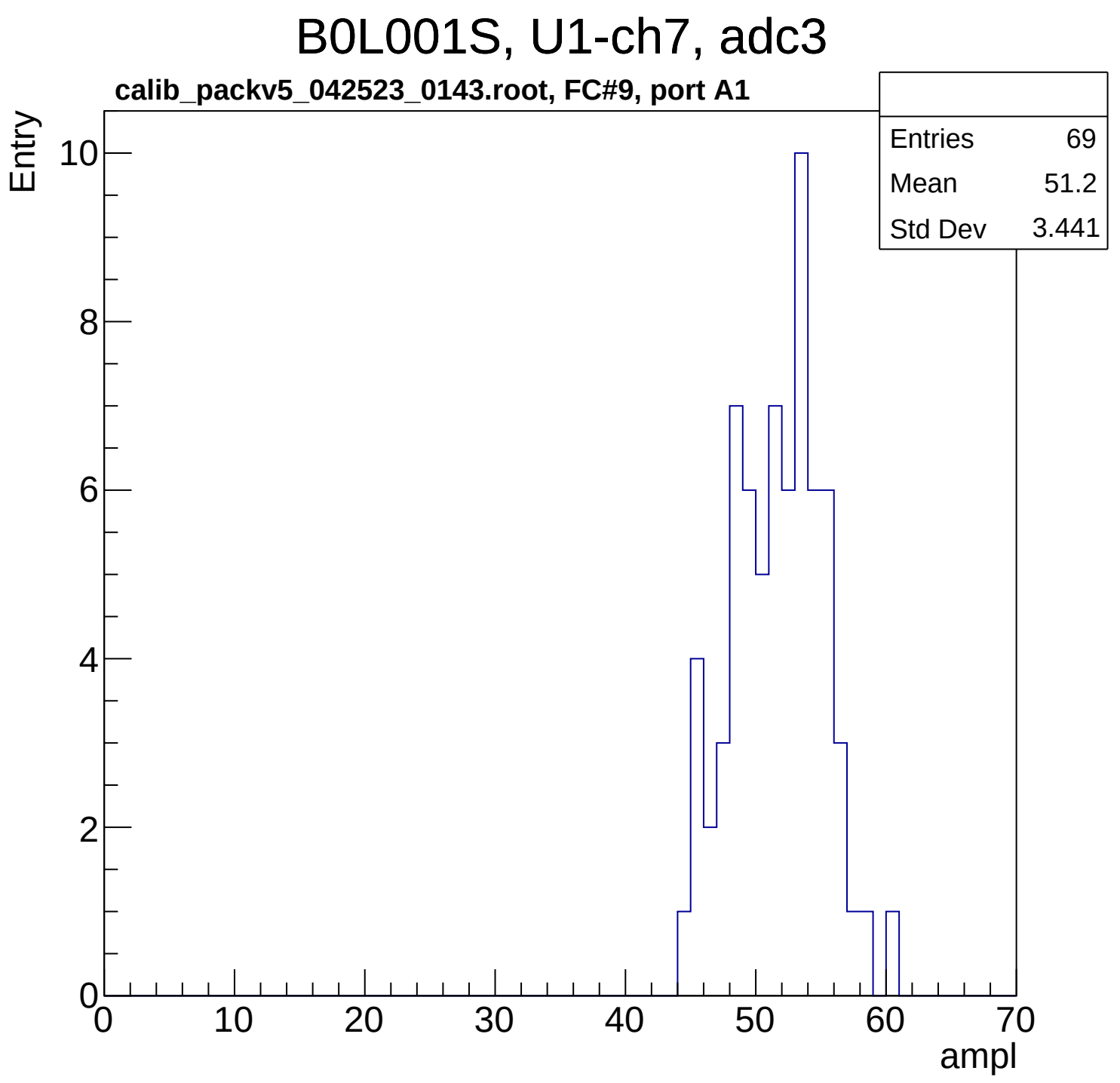
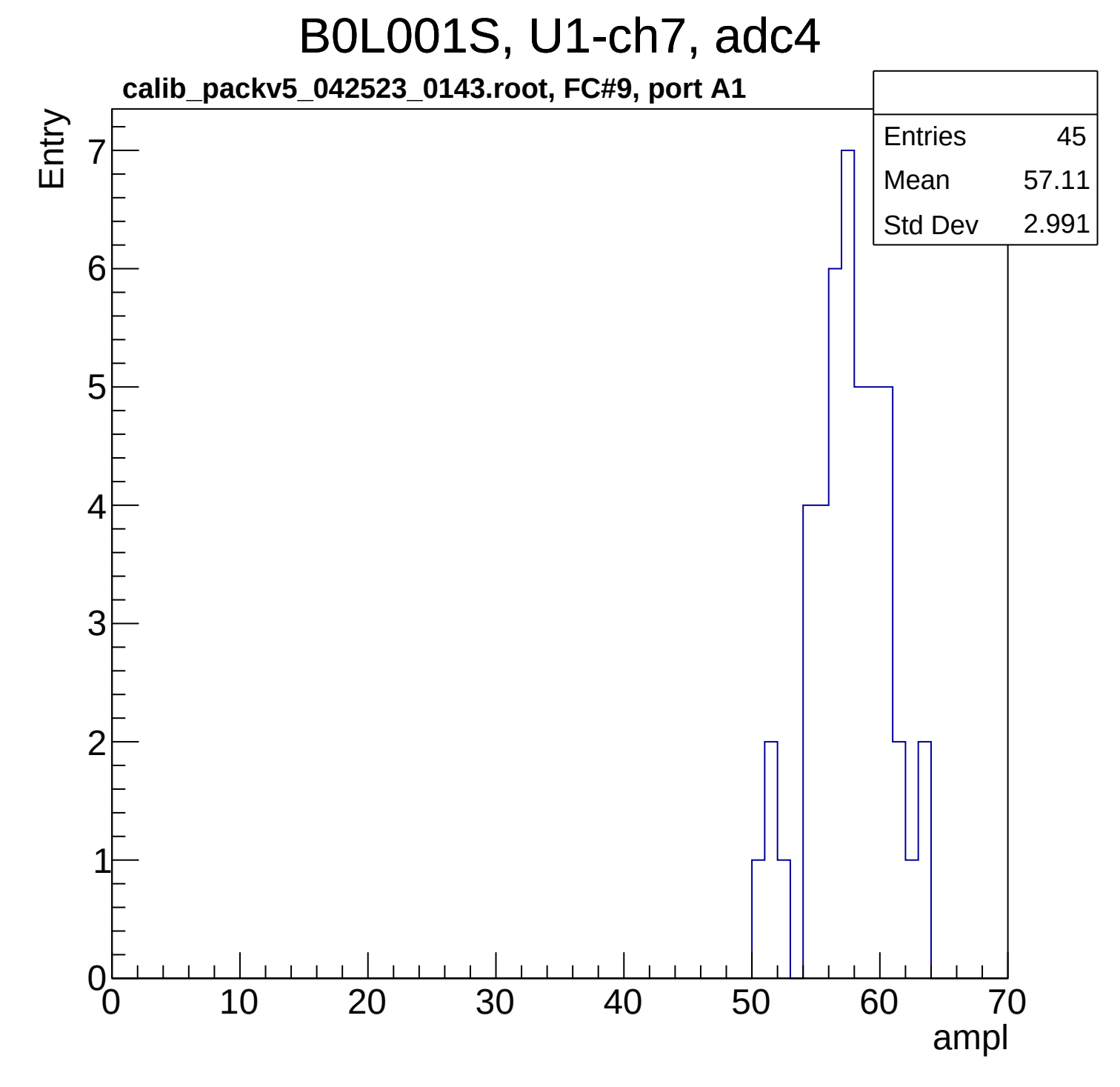
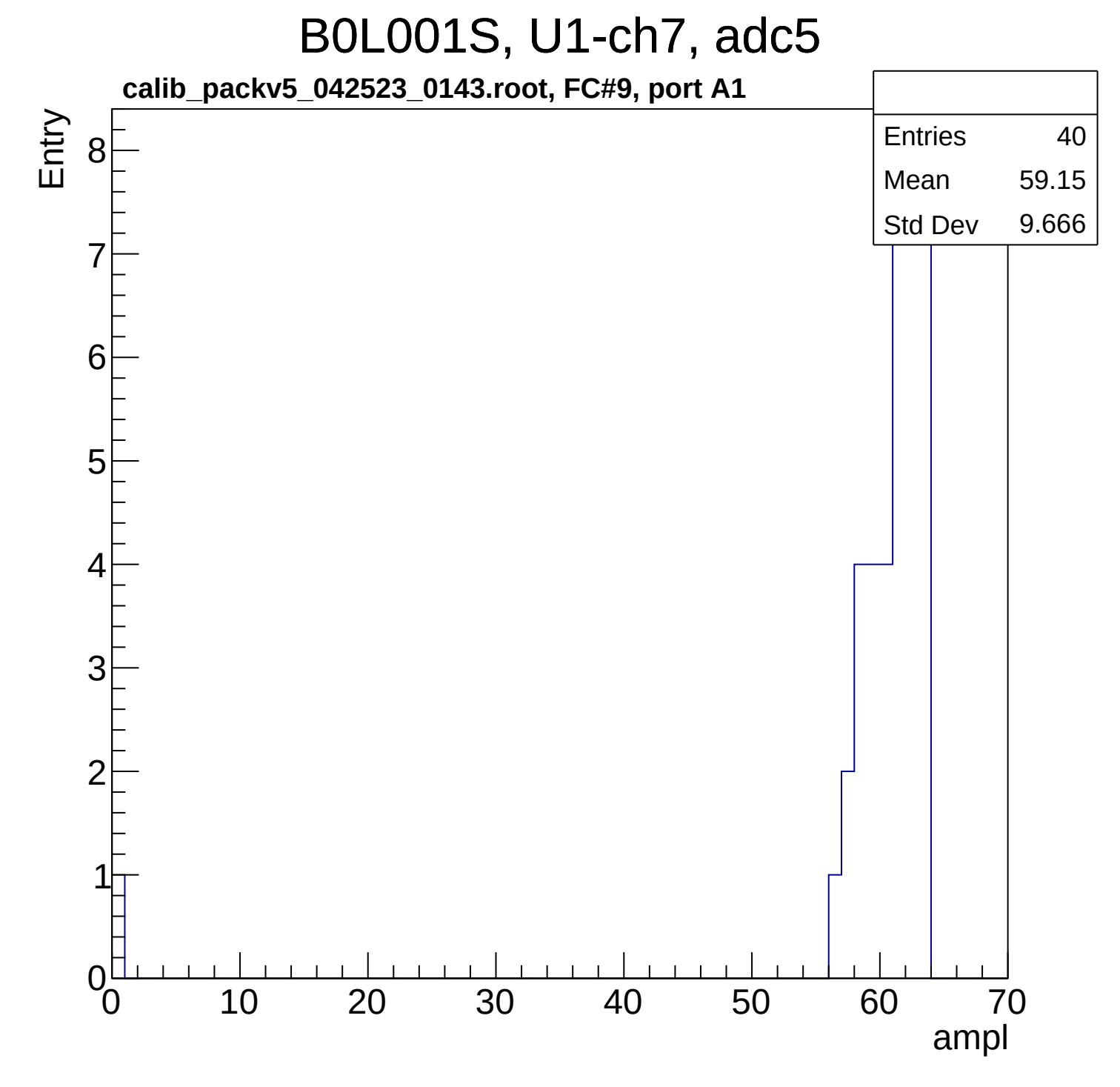
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 51.2
Std Dev 3.441
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 57.11
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.15
Std Dev 9.666
Entry
ampl
0
1
2
3
4
5
6
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60
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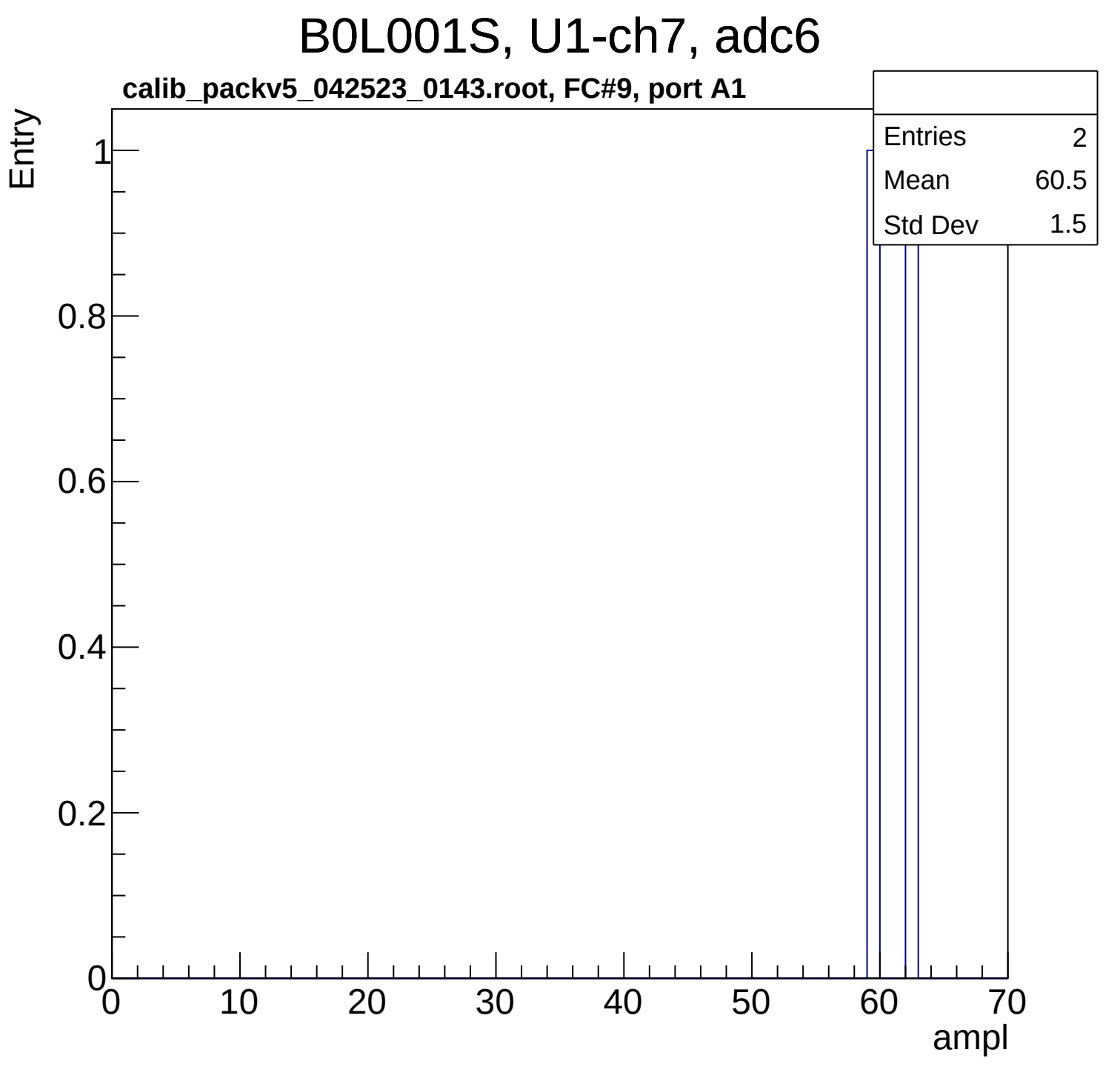
B0L001S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 60.5
Std Dev 1.5
Entry
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0.6
0.4
0.2
0
0
10
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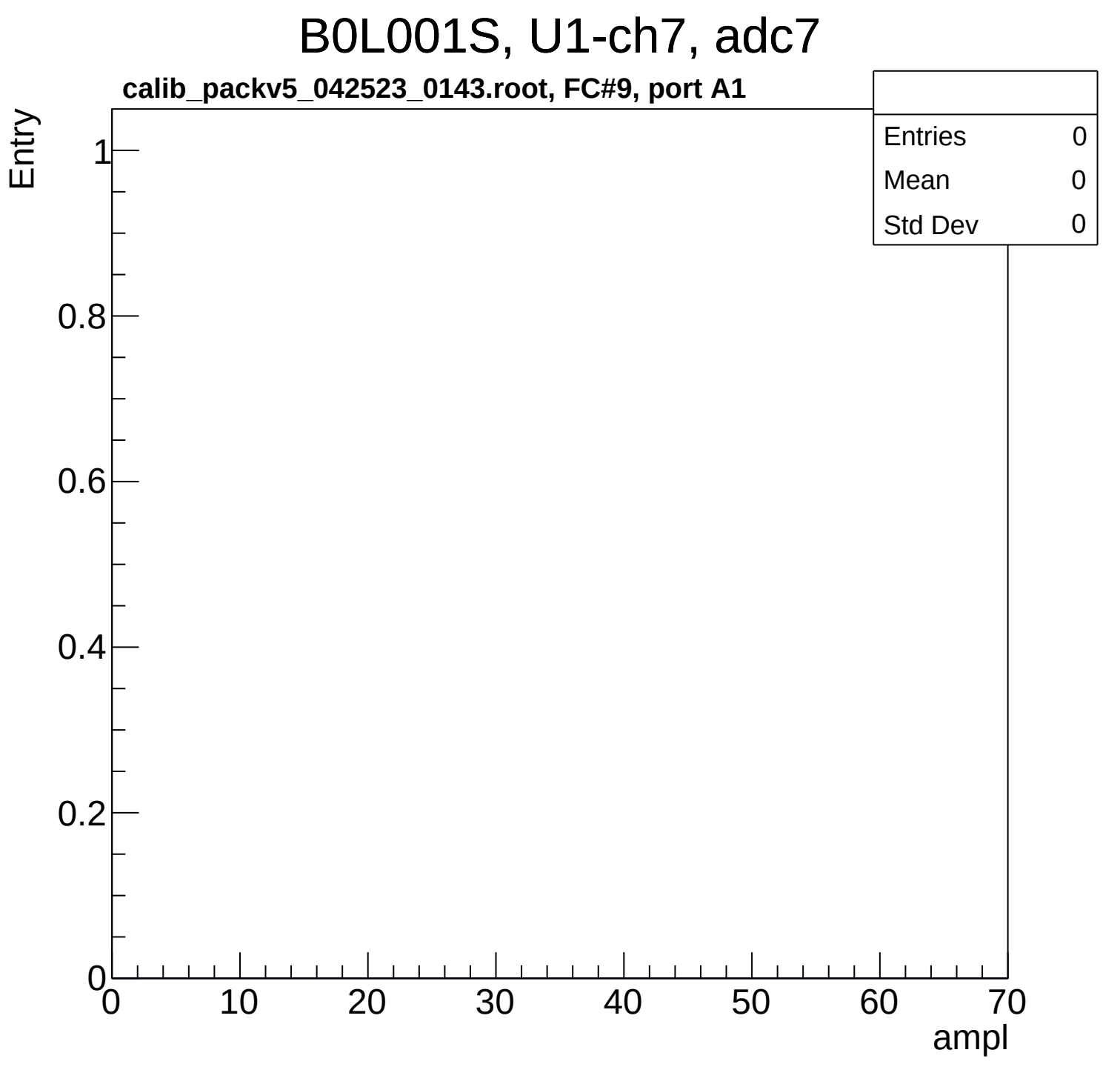
50
60
70
ampl


B0L001S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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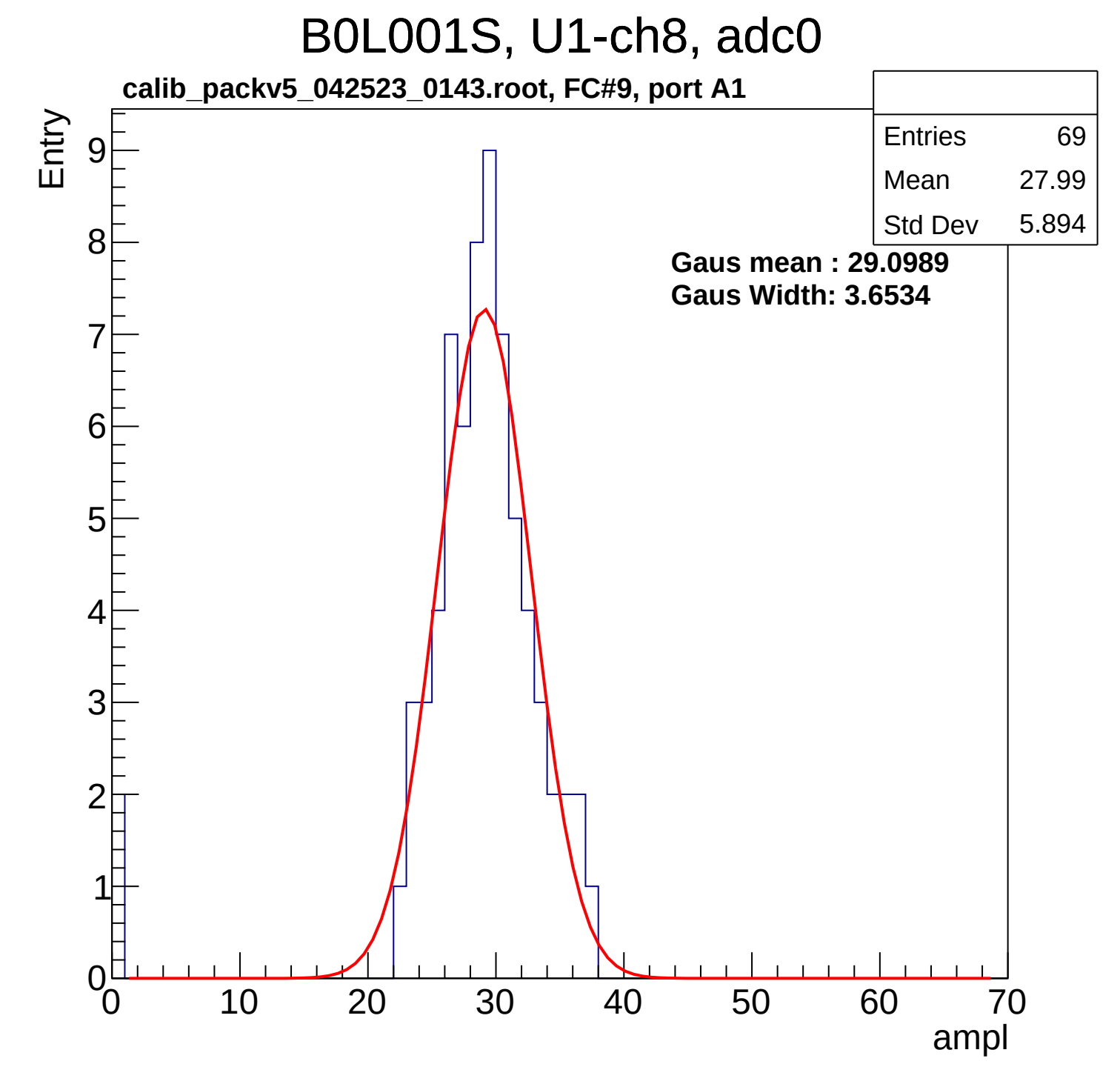
50
60
70
ampl


B0L001S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 27.99
Std Dev 5.894
Gaus mean : 29.0989
Gaus Width: 3.6534
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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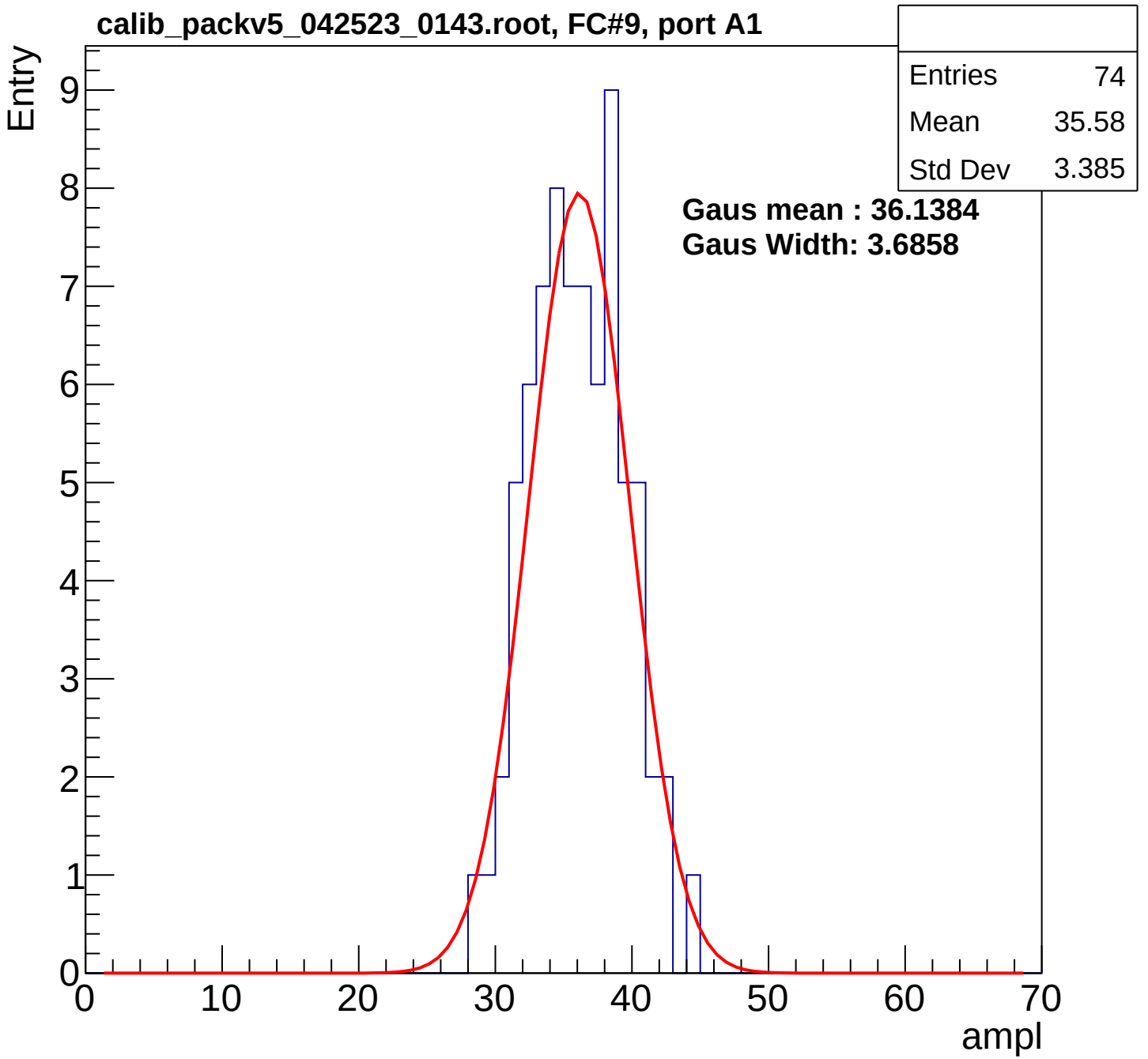
B0L001S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 35.58
Std Dev 3.385
Gaus mean : 36.1384
Gaus Width: 3.6858
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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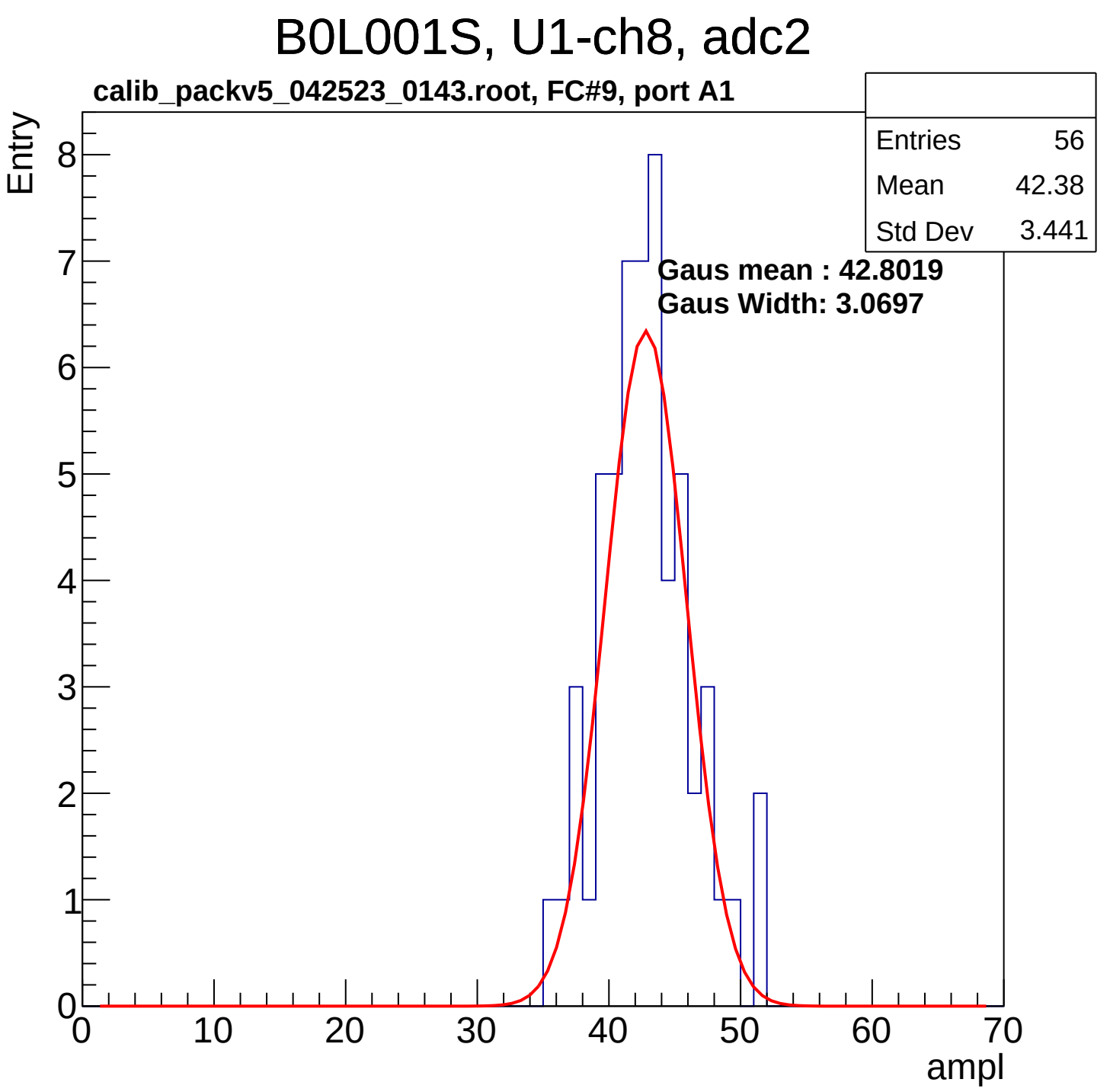
B0L001S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.38
Std Dev 3.441
Gaus mean : 42.8019
Gaus Width: 3.0697
Entry
ampl
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1
2
3
4
5
6
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0
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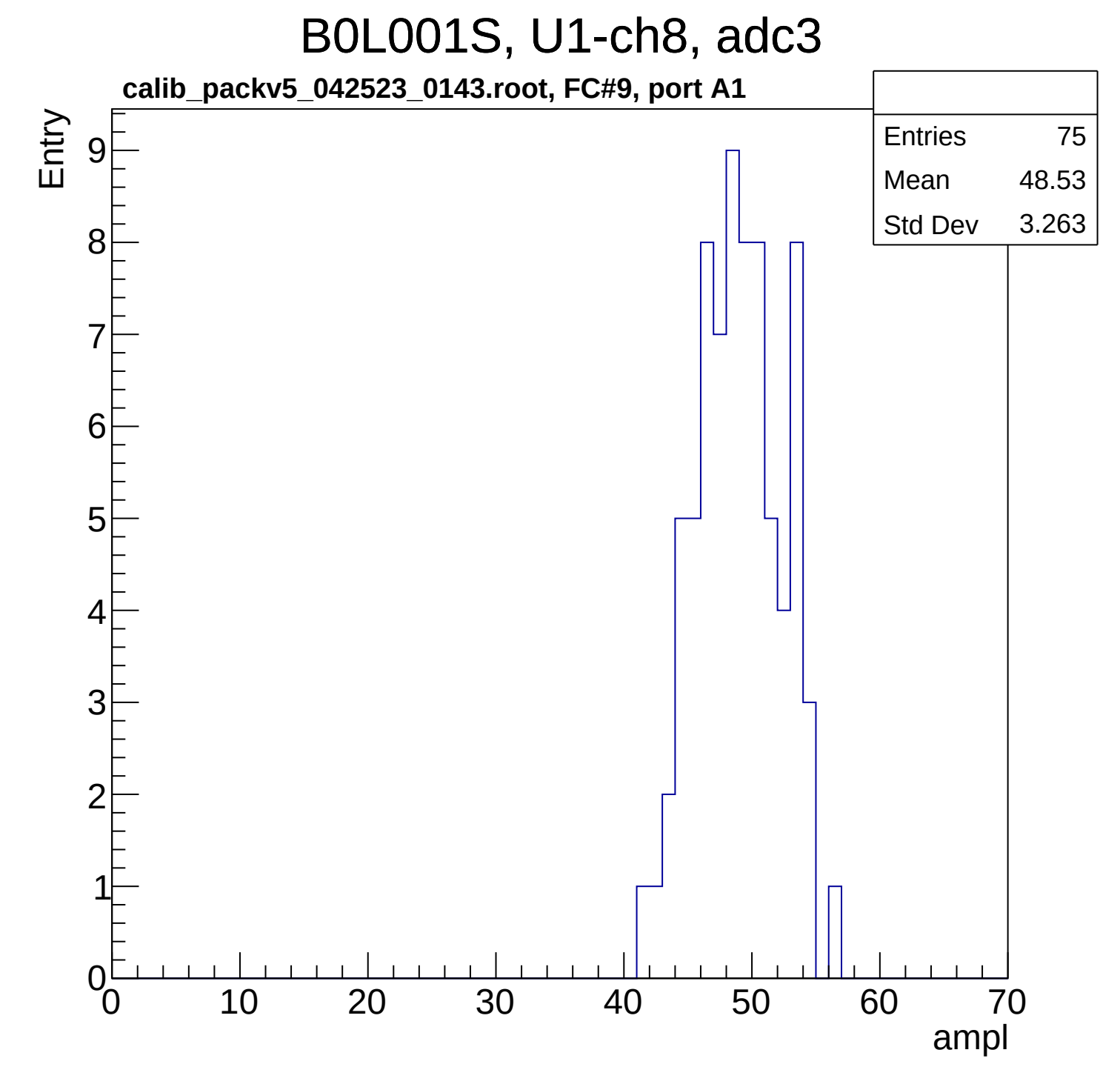
B0L001S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 48.53
Std Dev 3.263
Entry
ampl
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3
4
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0
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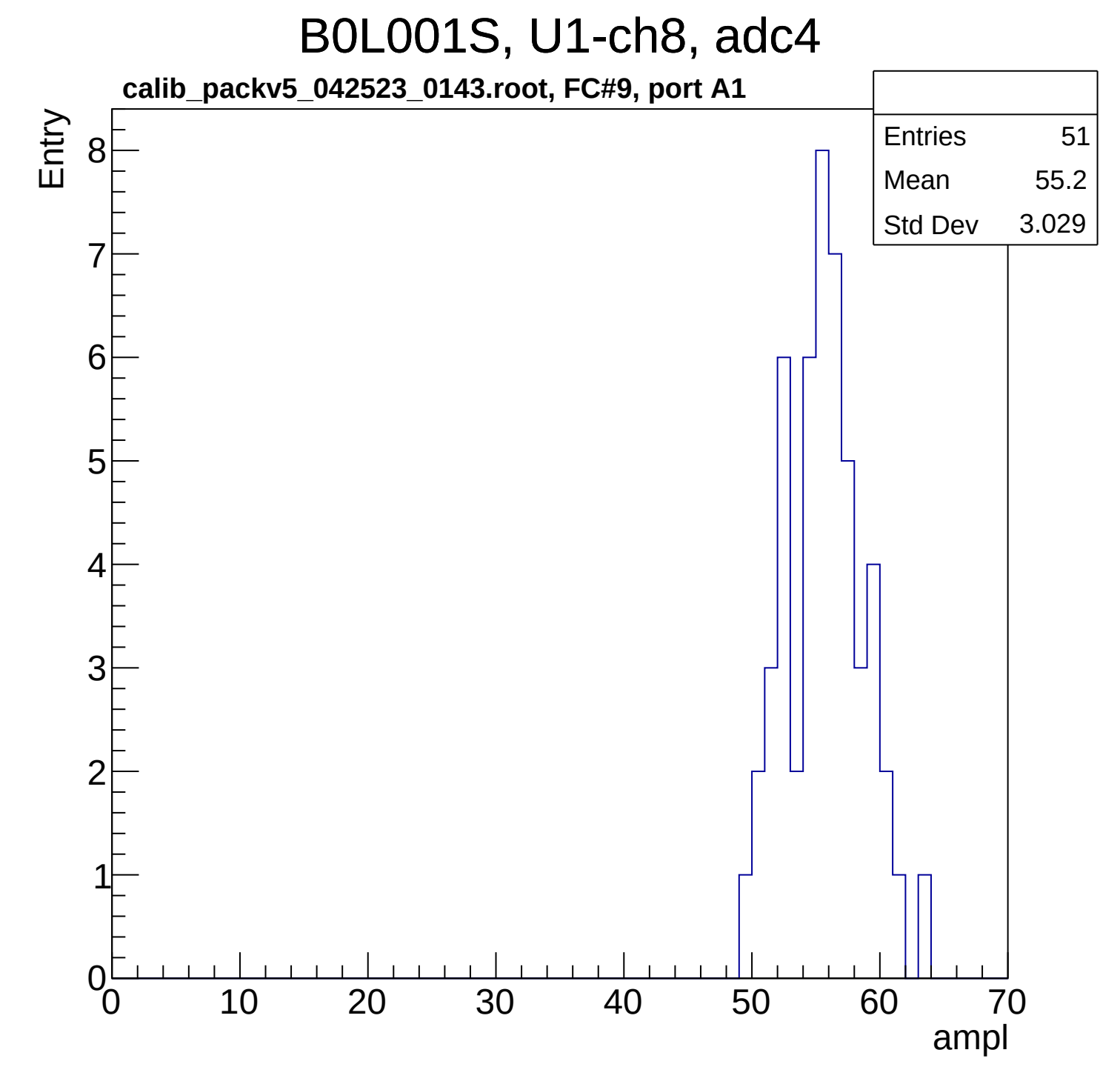
B0L001S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 55.2
Std Dev 3.029
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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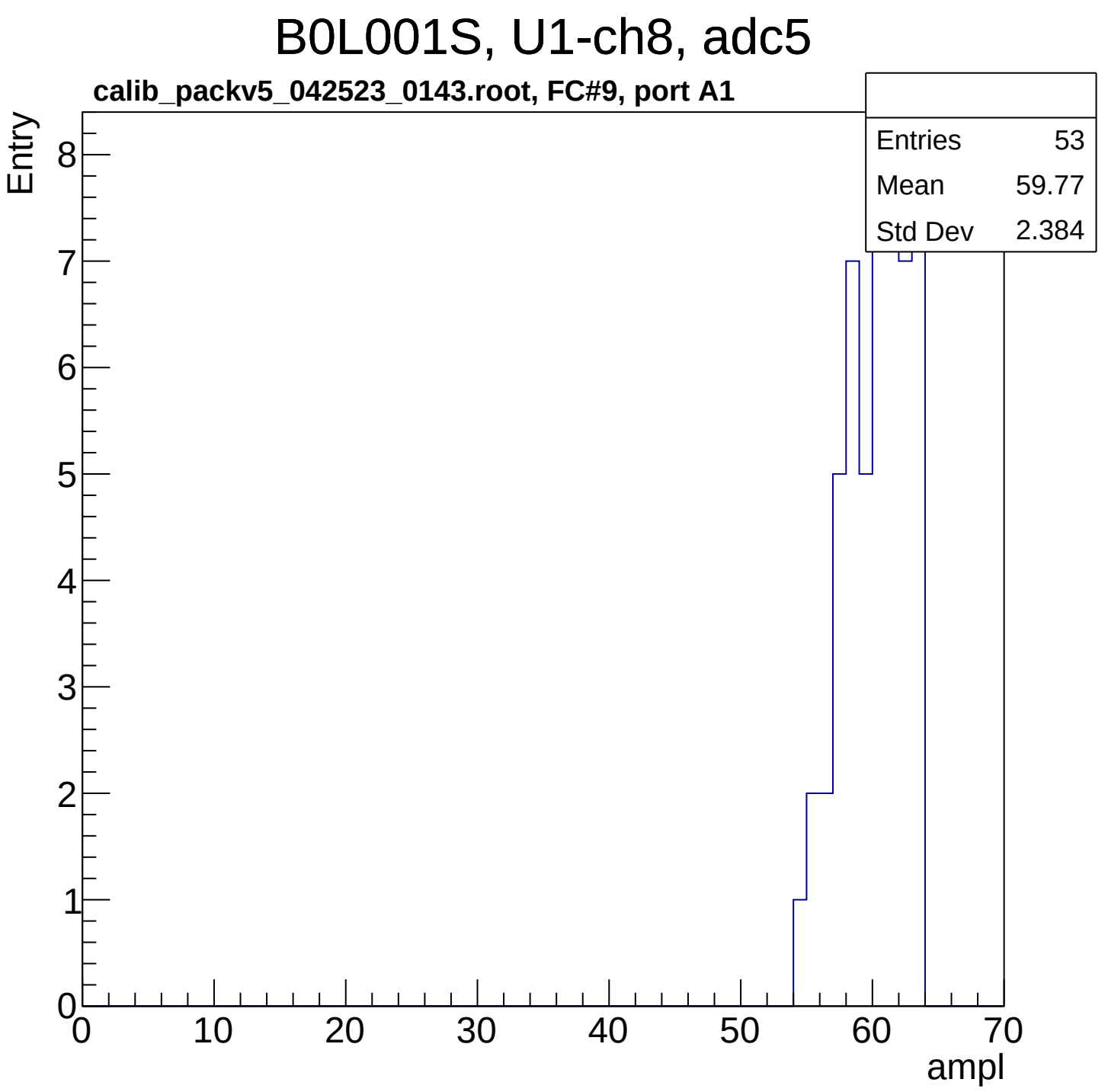
B0L001S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 59.77
Std Dev 2.384
Entry
ampl
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2
3
4
5
6
7
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0
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60
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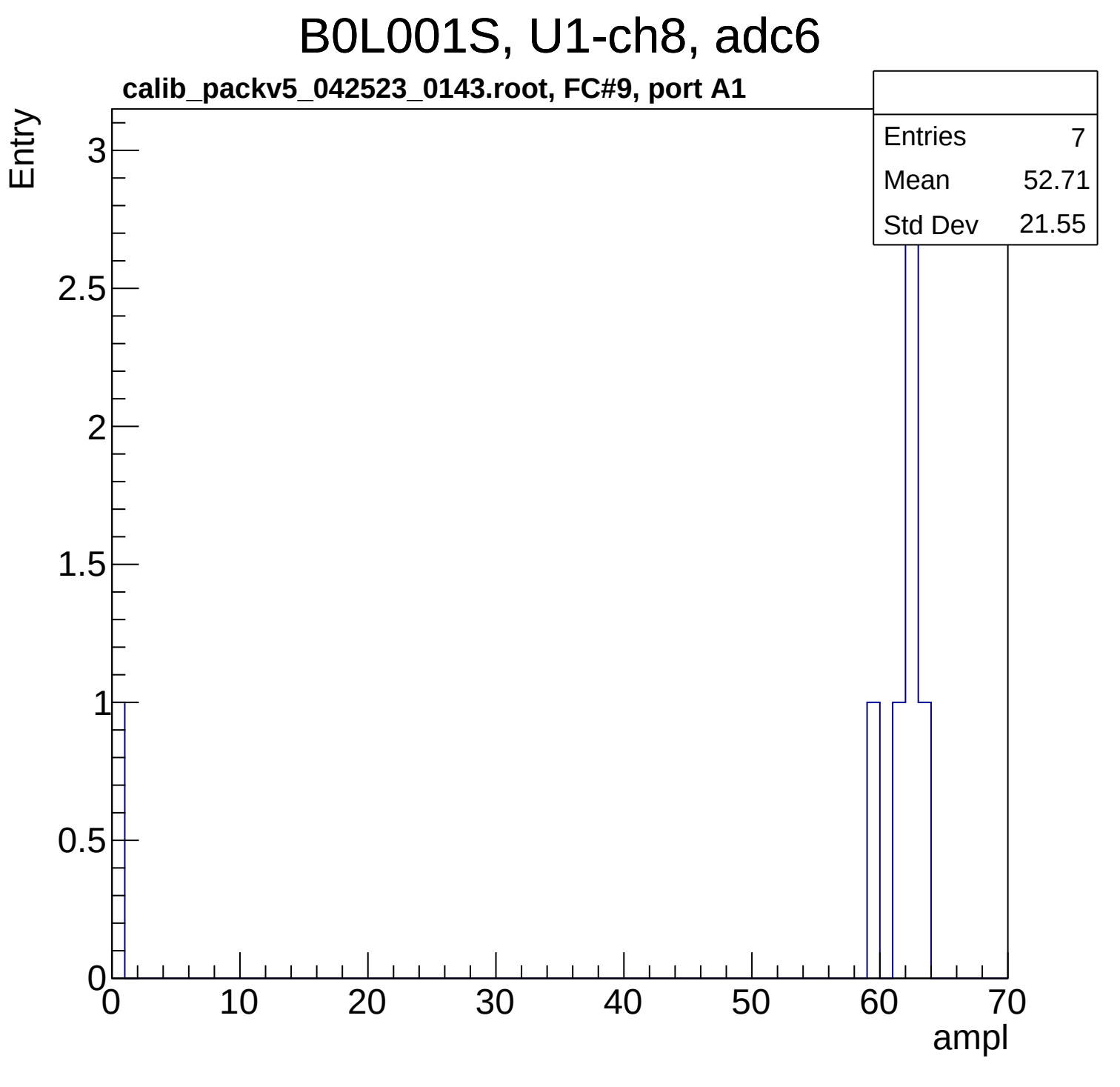
B0L001S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 52.71
Std Dev 21.55
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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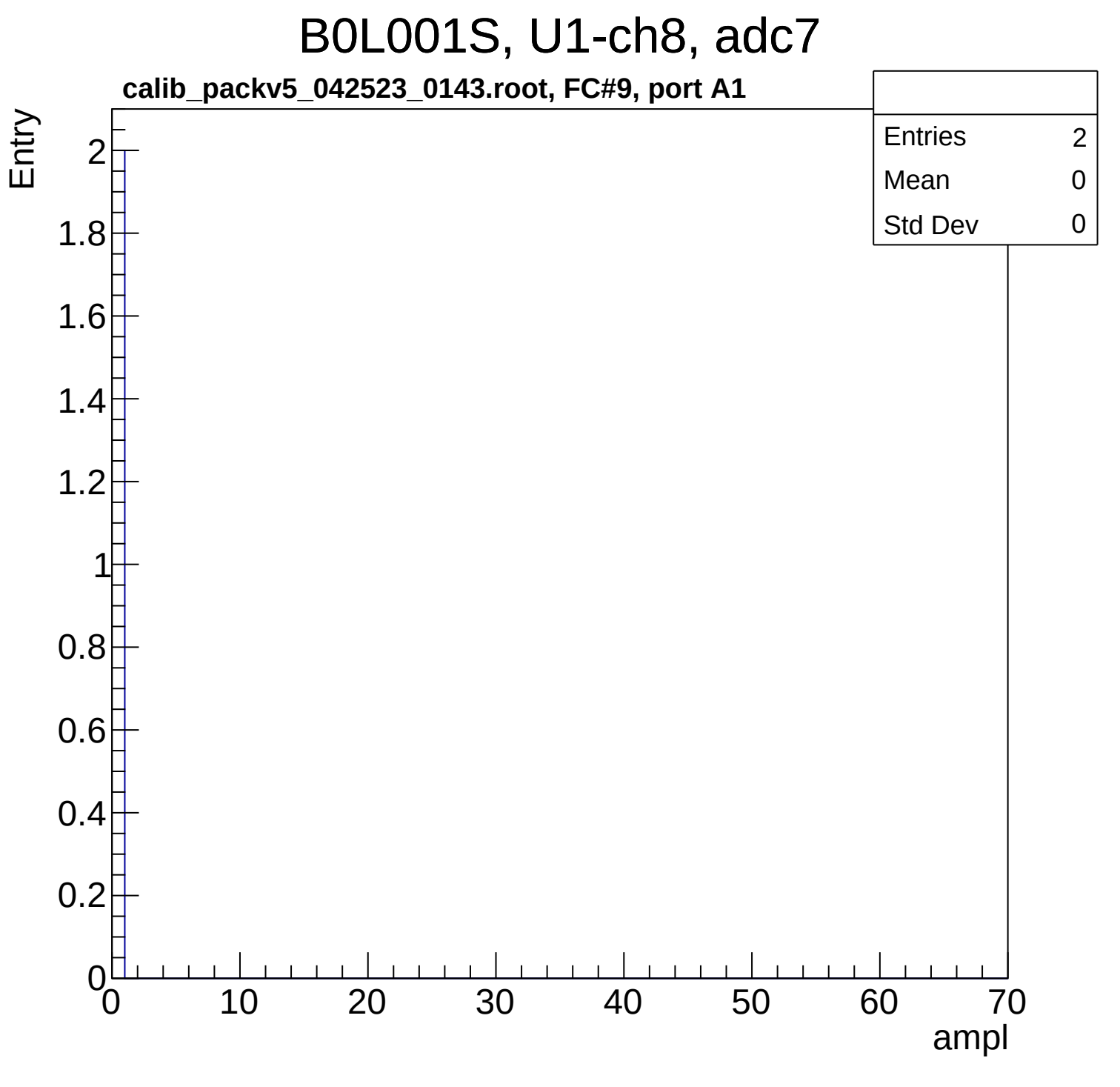
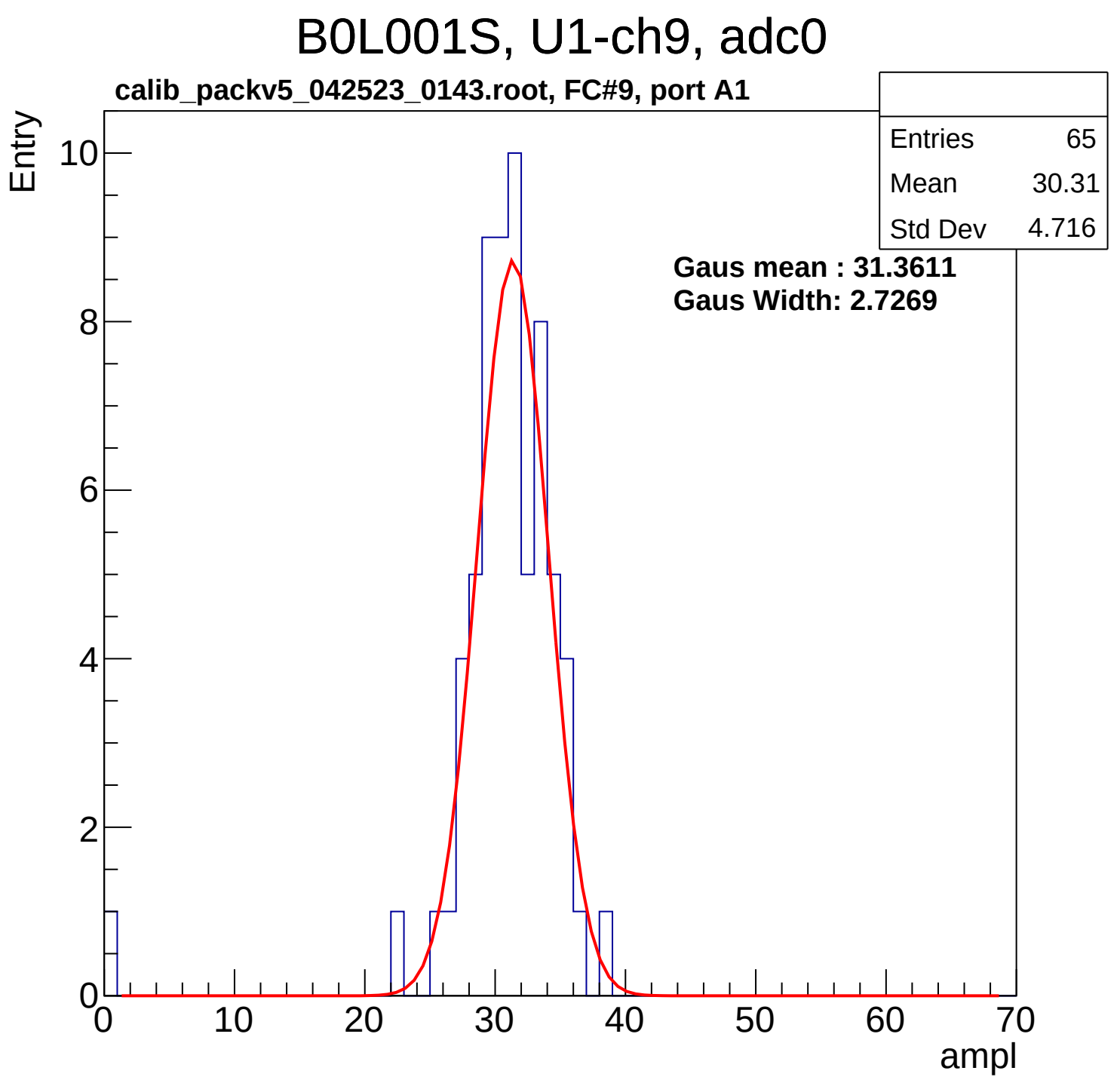
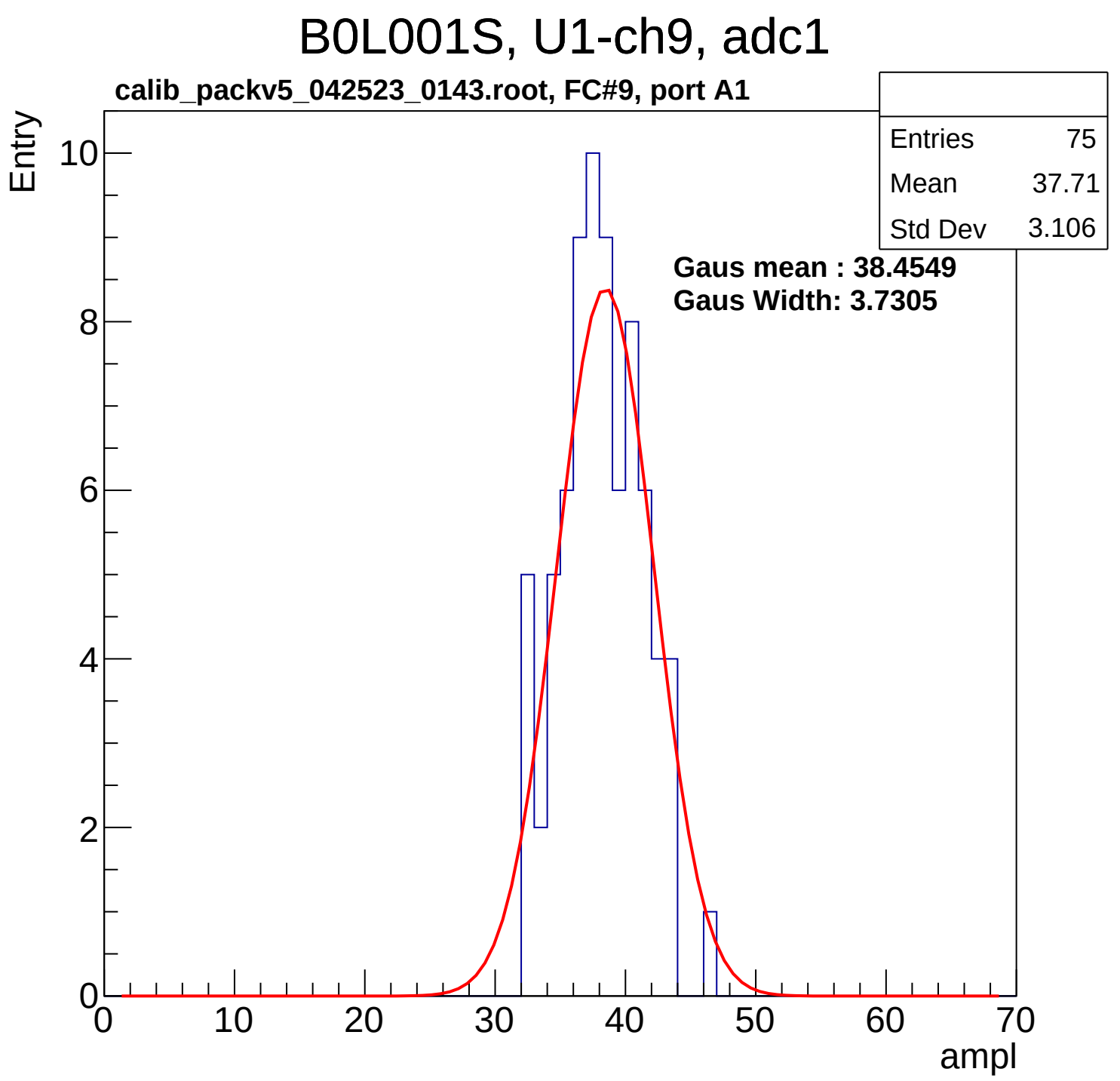
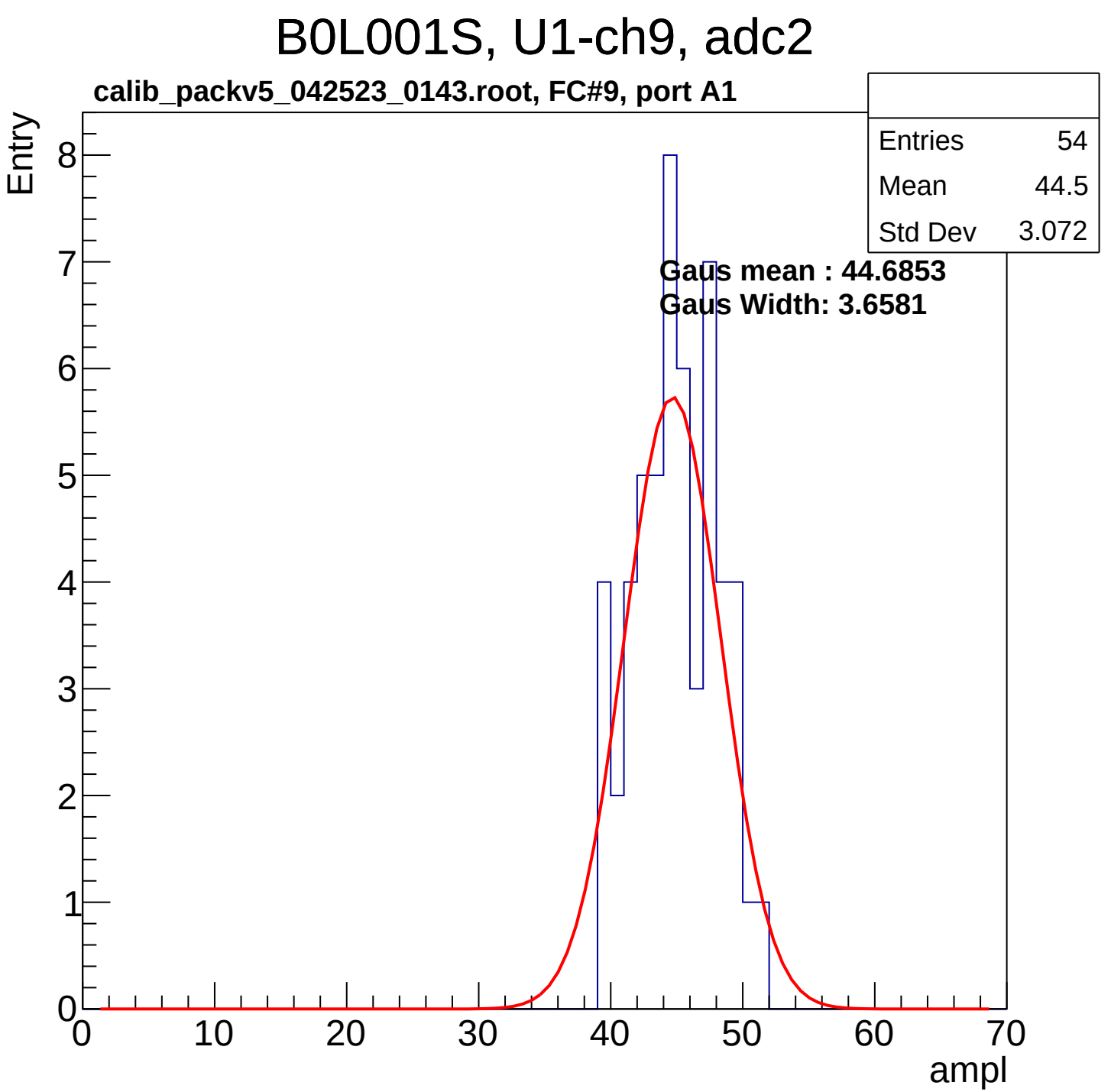

B0L001S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.31
Std Dev 4.716
Gaus mean : 31.3611
Gaus Width: 2.7269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.71
Std Dev 3.106
Gaus mean : 38.4549
Gaus Width: 3.7305
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.5
Std Dev 3.072
Gaus mean : 44.6853
Gaus Width: 3.6581
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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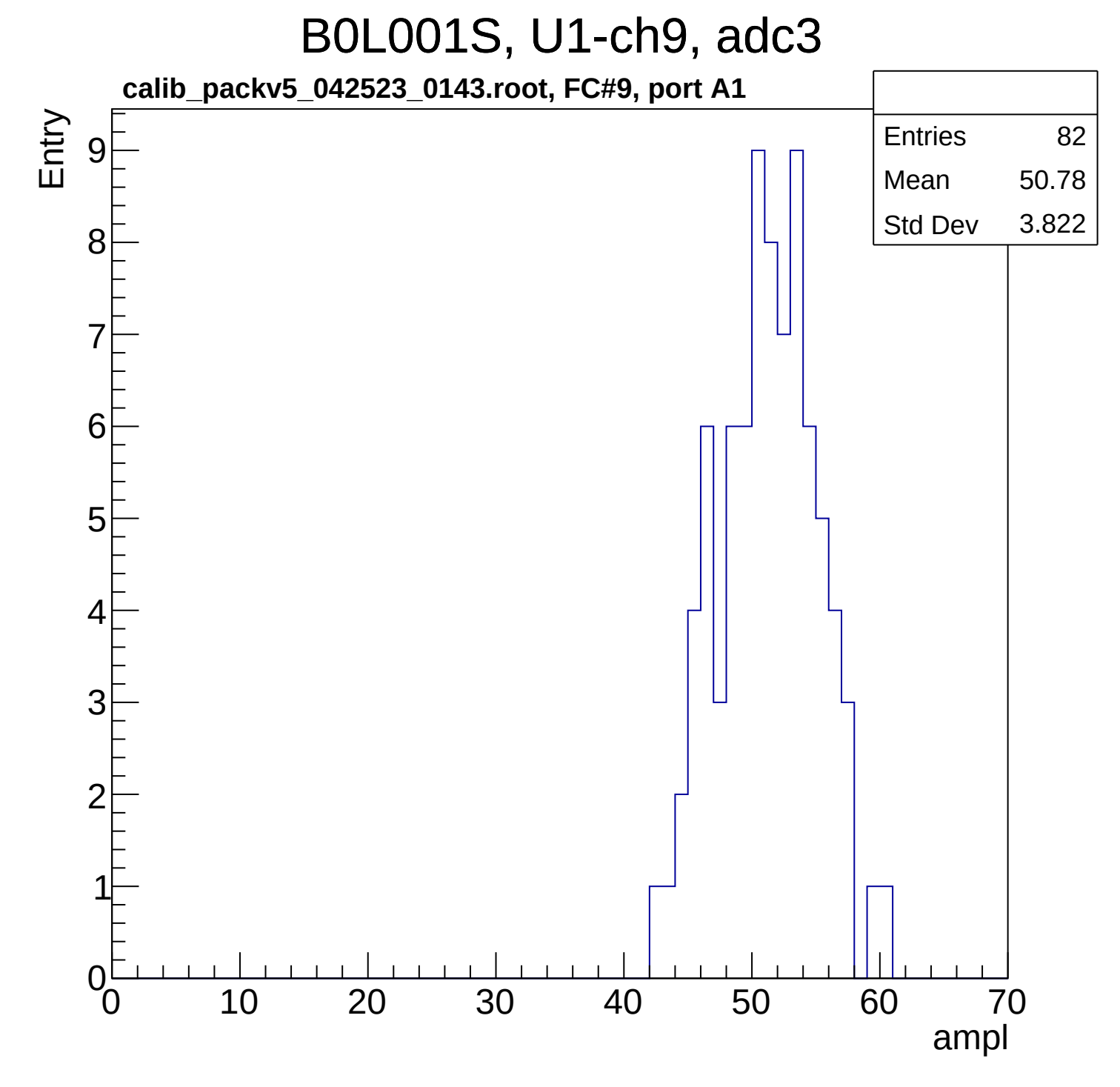
B0L001S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 50.78
Std Dev 3.822
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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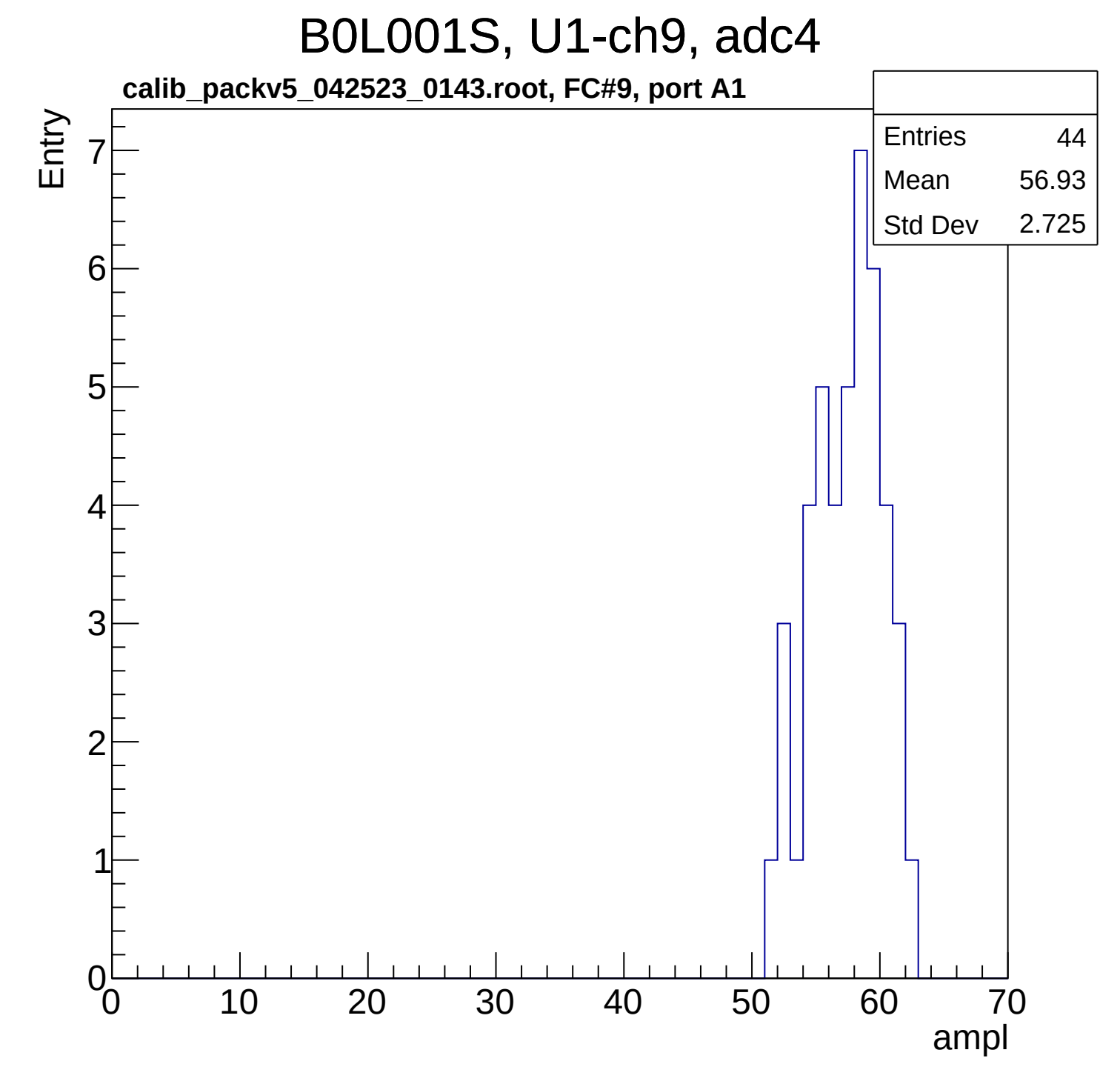
B0L001S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 56.93
Std Dev 2.725
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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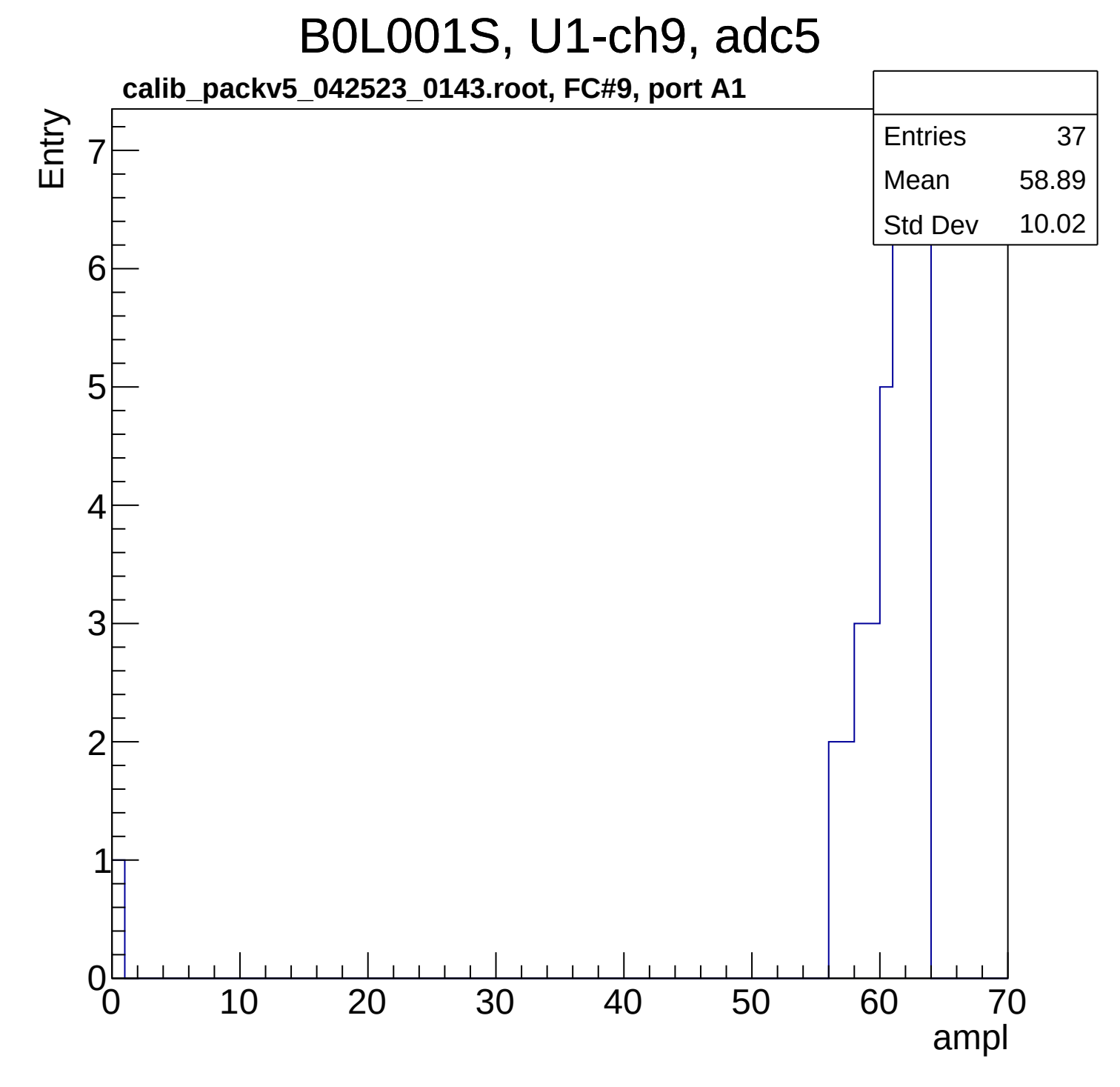
B0L001S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.89
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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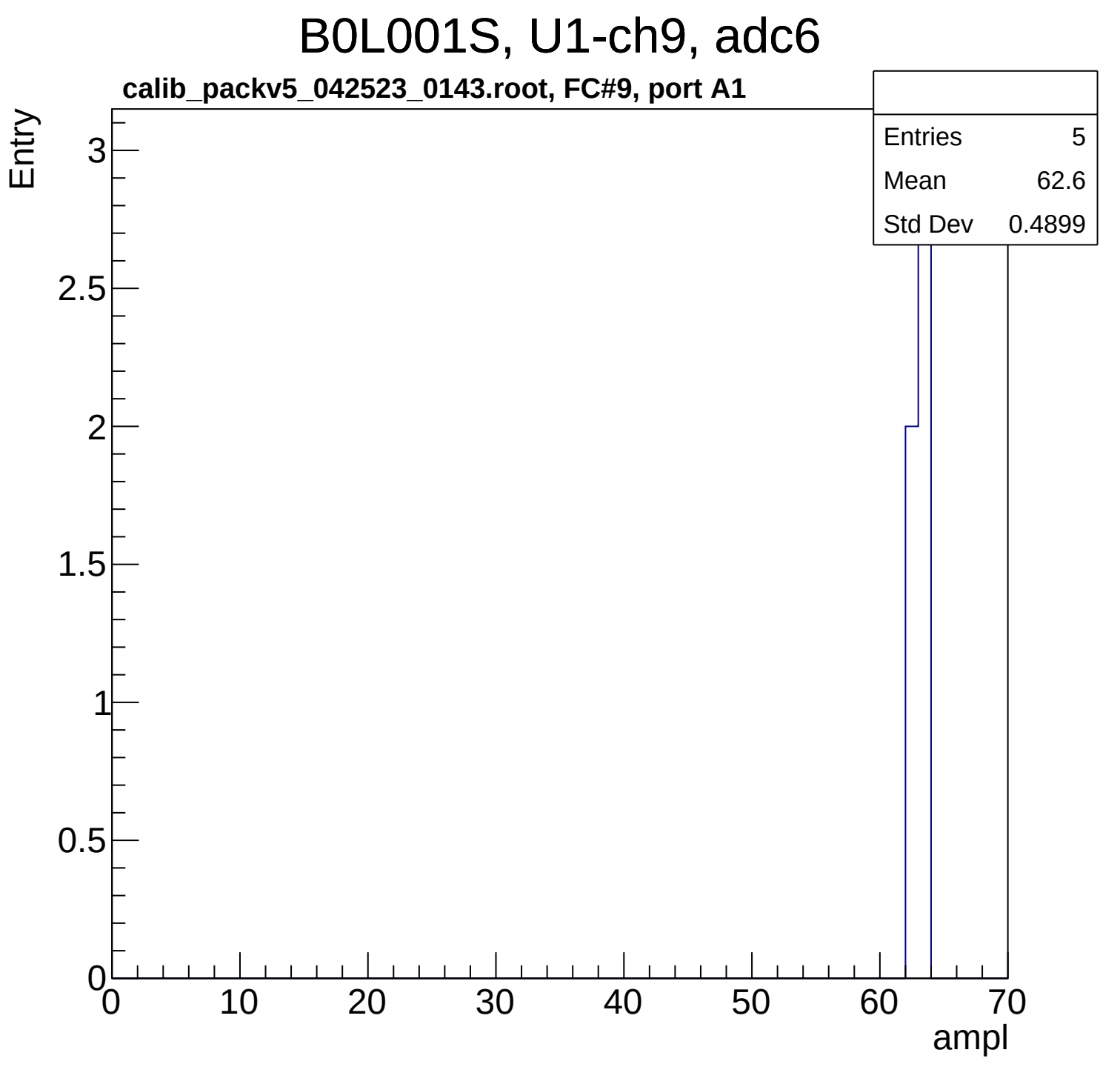
B0L001S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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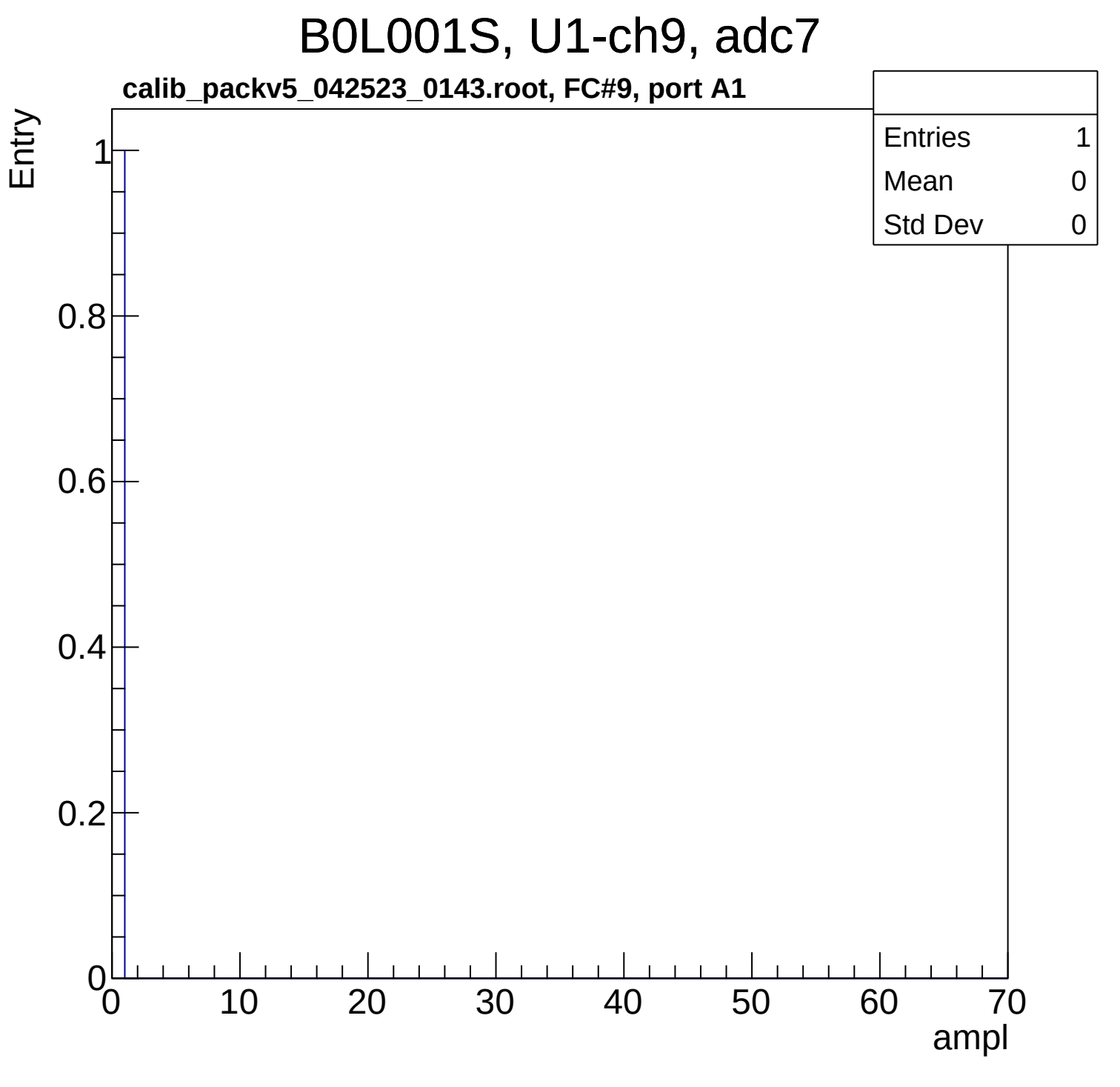
B0L001S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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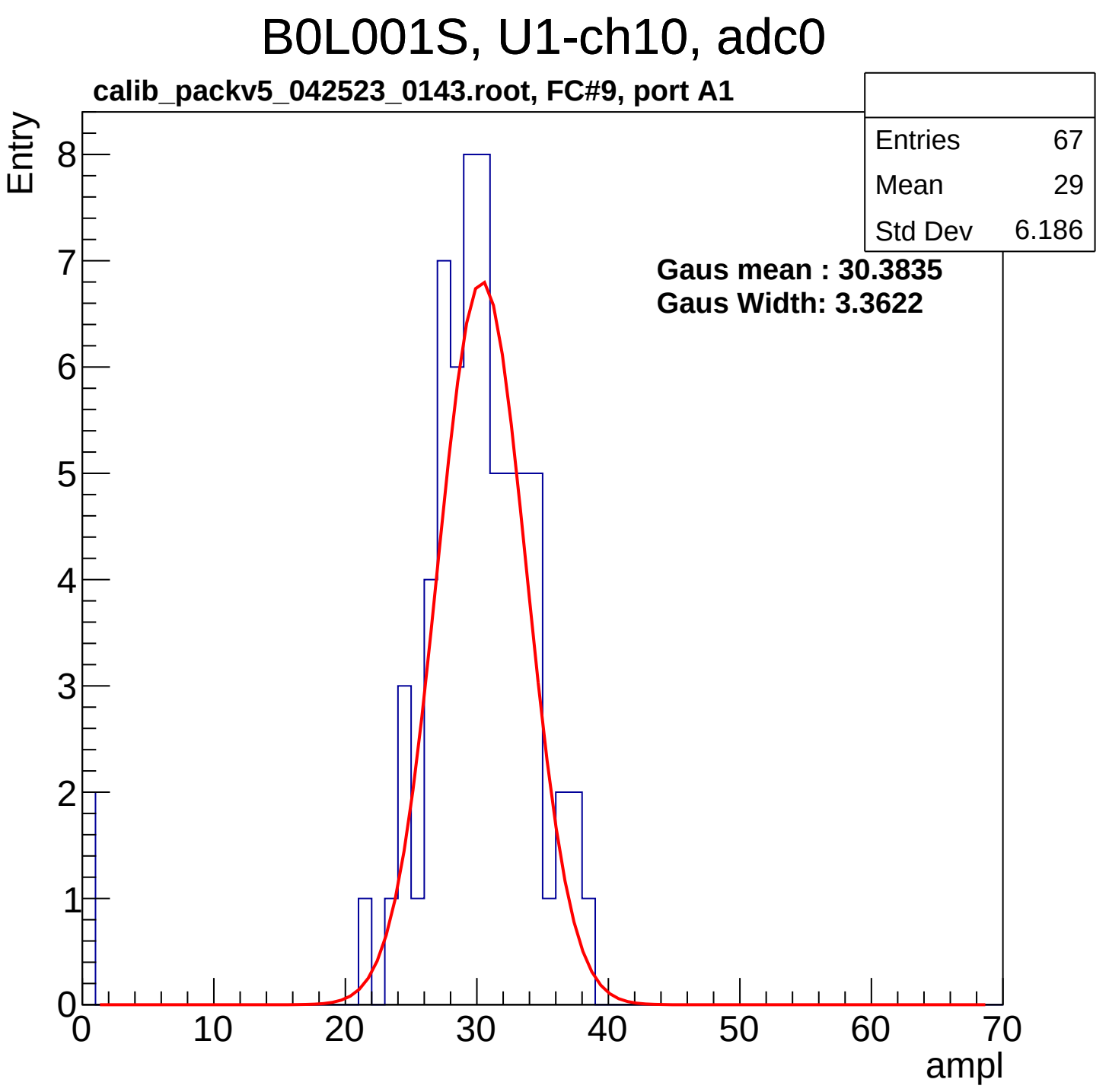
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29
Std Dev 6.186
Gaus mean : 30.3835
Gaus Width: 3.3622
Entry
ampl
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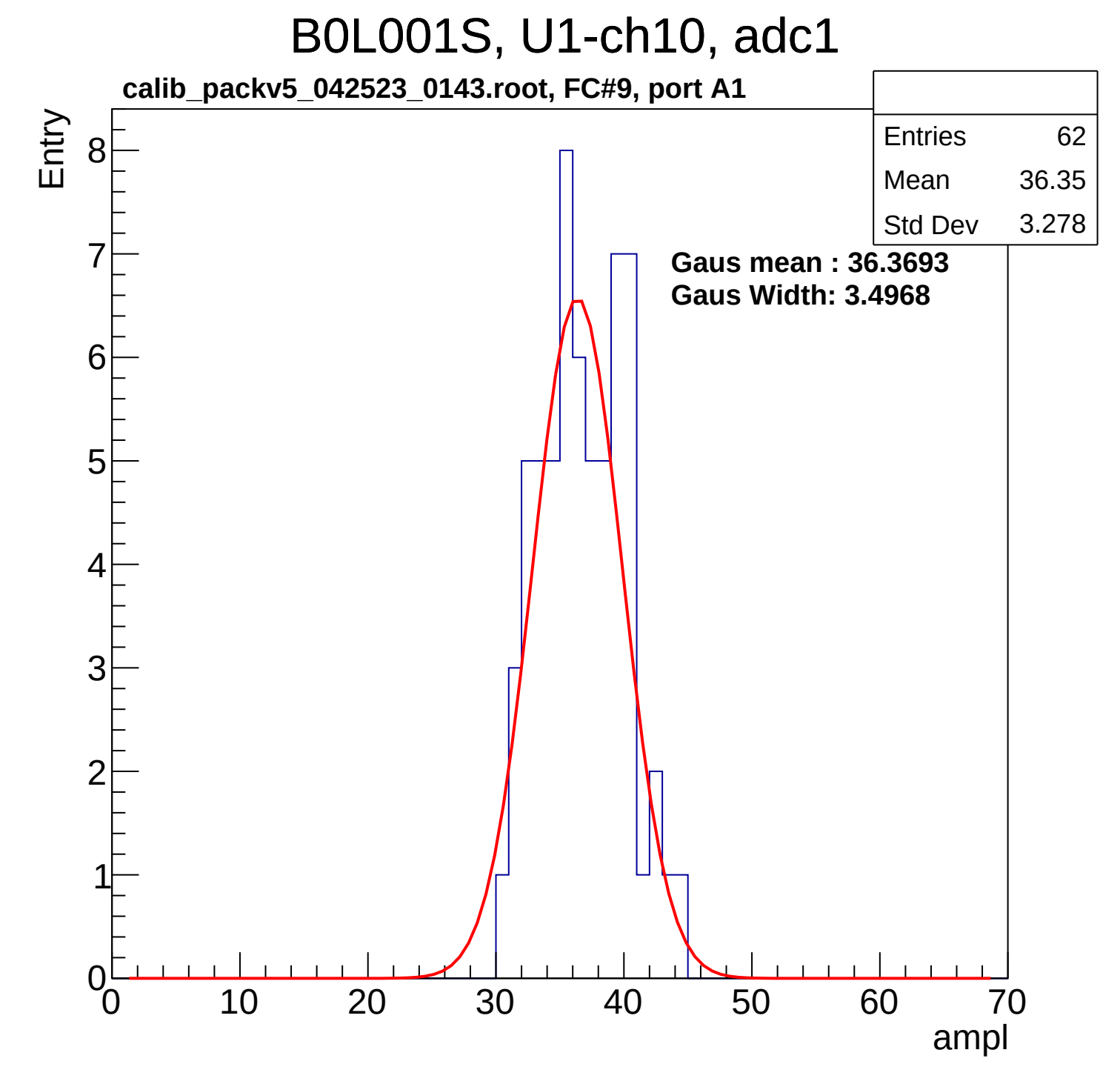
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.35
Std Dev 3.278
Gaus mean : 36.3693
Gaus Width: 3.4968
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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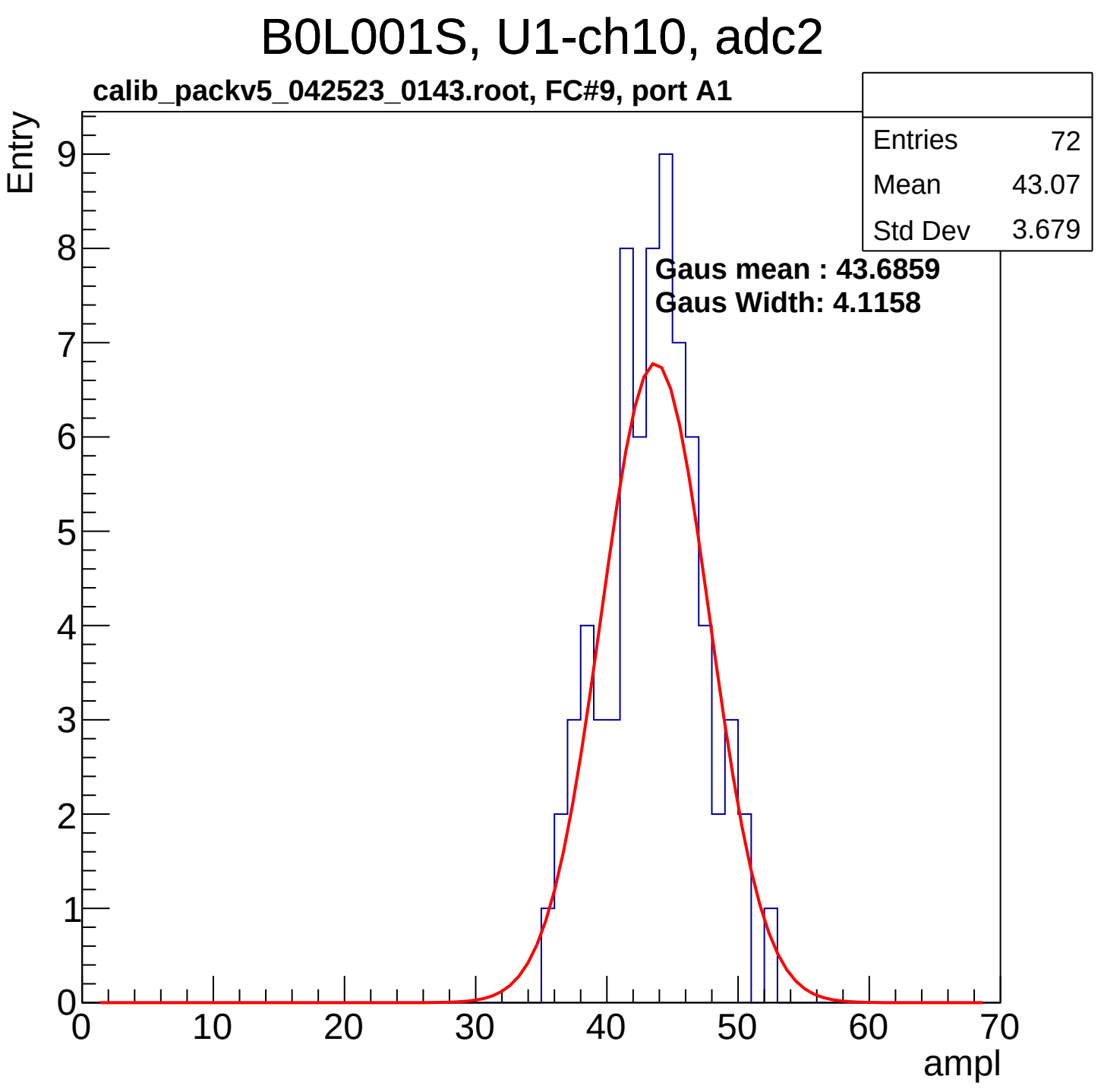
B0L001S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 43.07
Std Dev 3.679
Gaus mean : 43.6859
Gaus Width: 4.1158
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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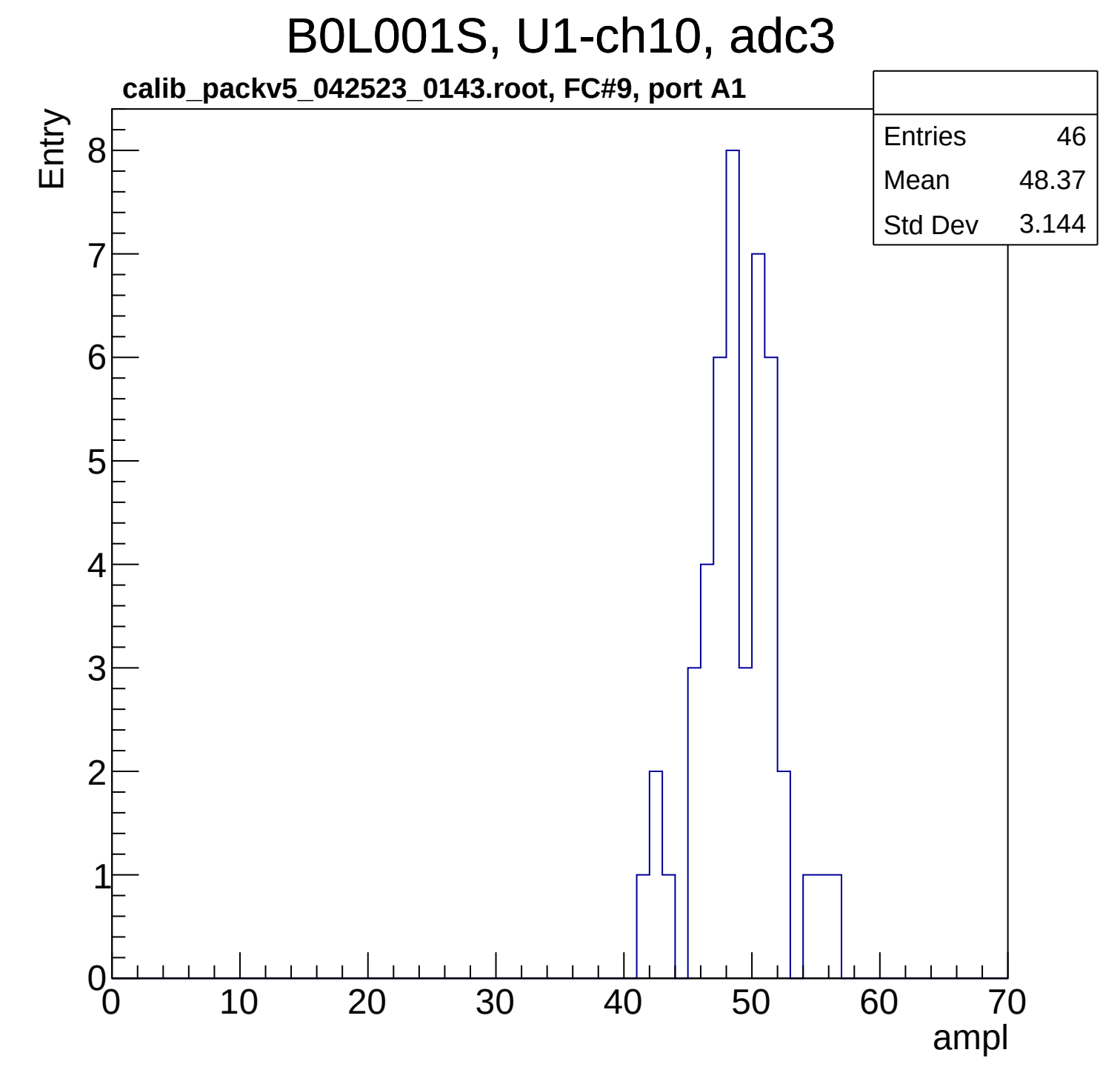
B0L001S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 48.37
Std Dev 3.144
Entry
ampl
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2
3
4
5
6
7
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0
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60
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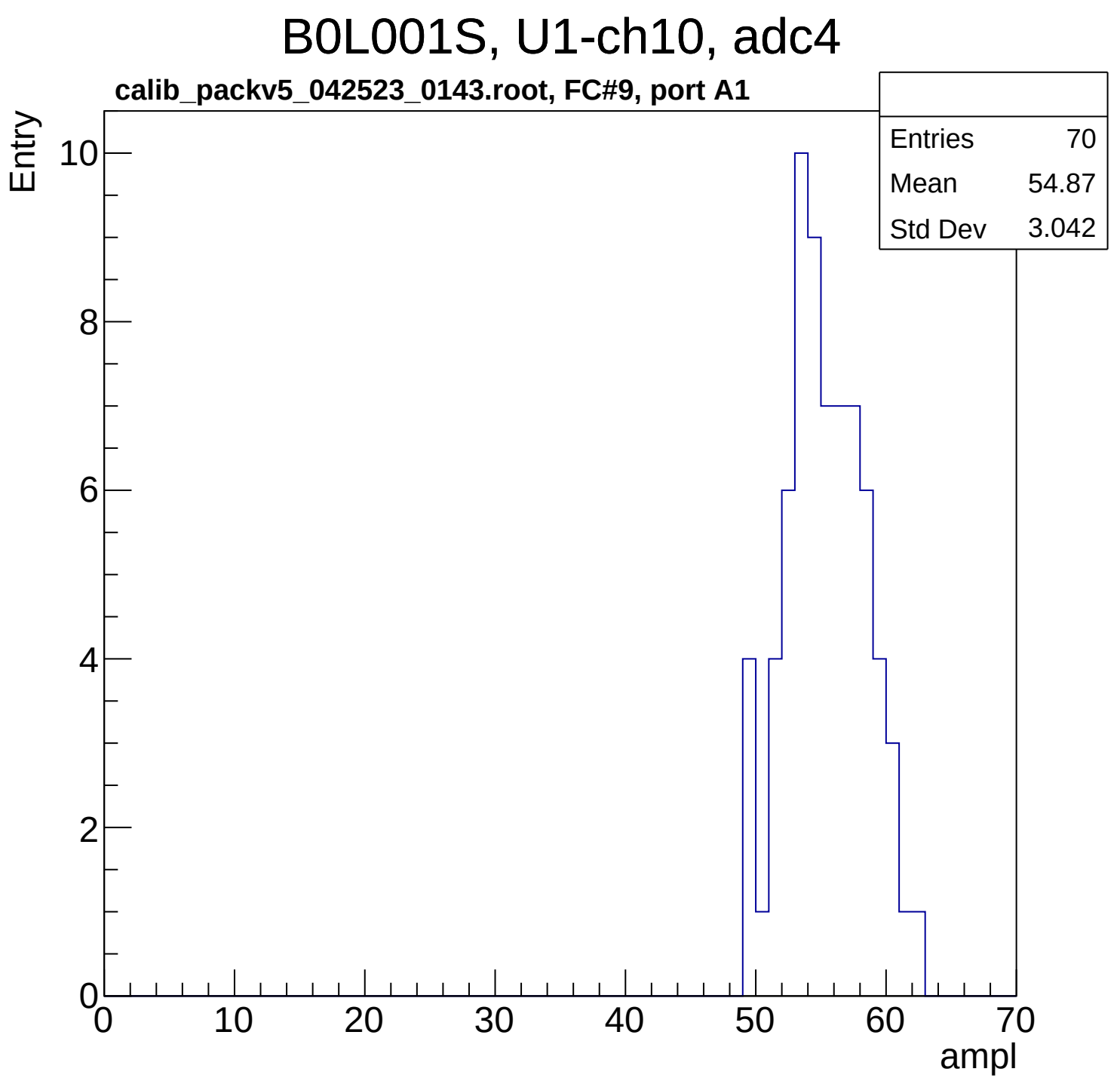
B0L001S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.87
Std Dev 3.042
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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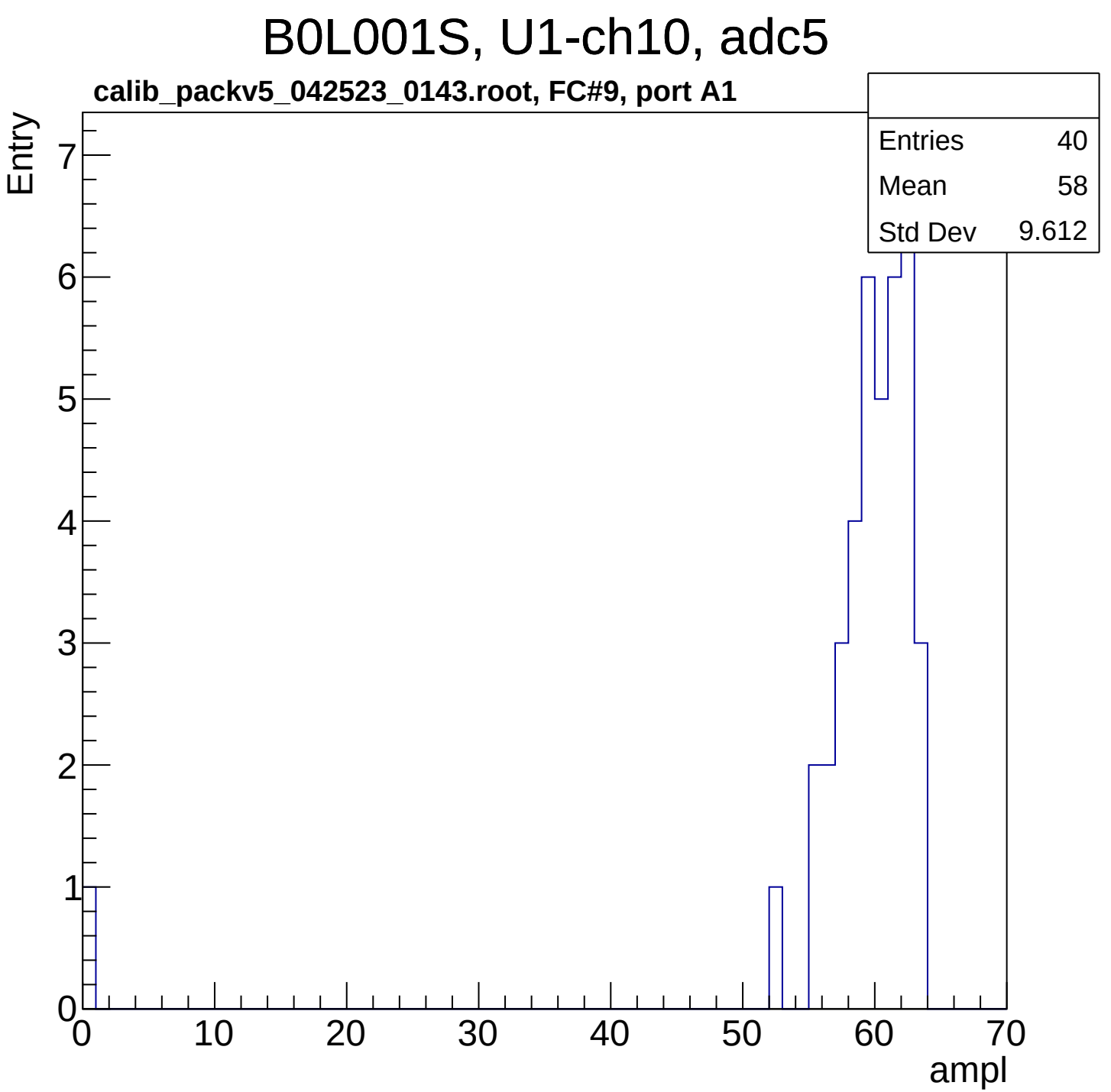
B0L001S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58
Std Dev 9.612
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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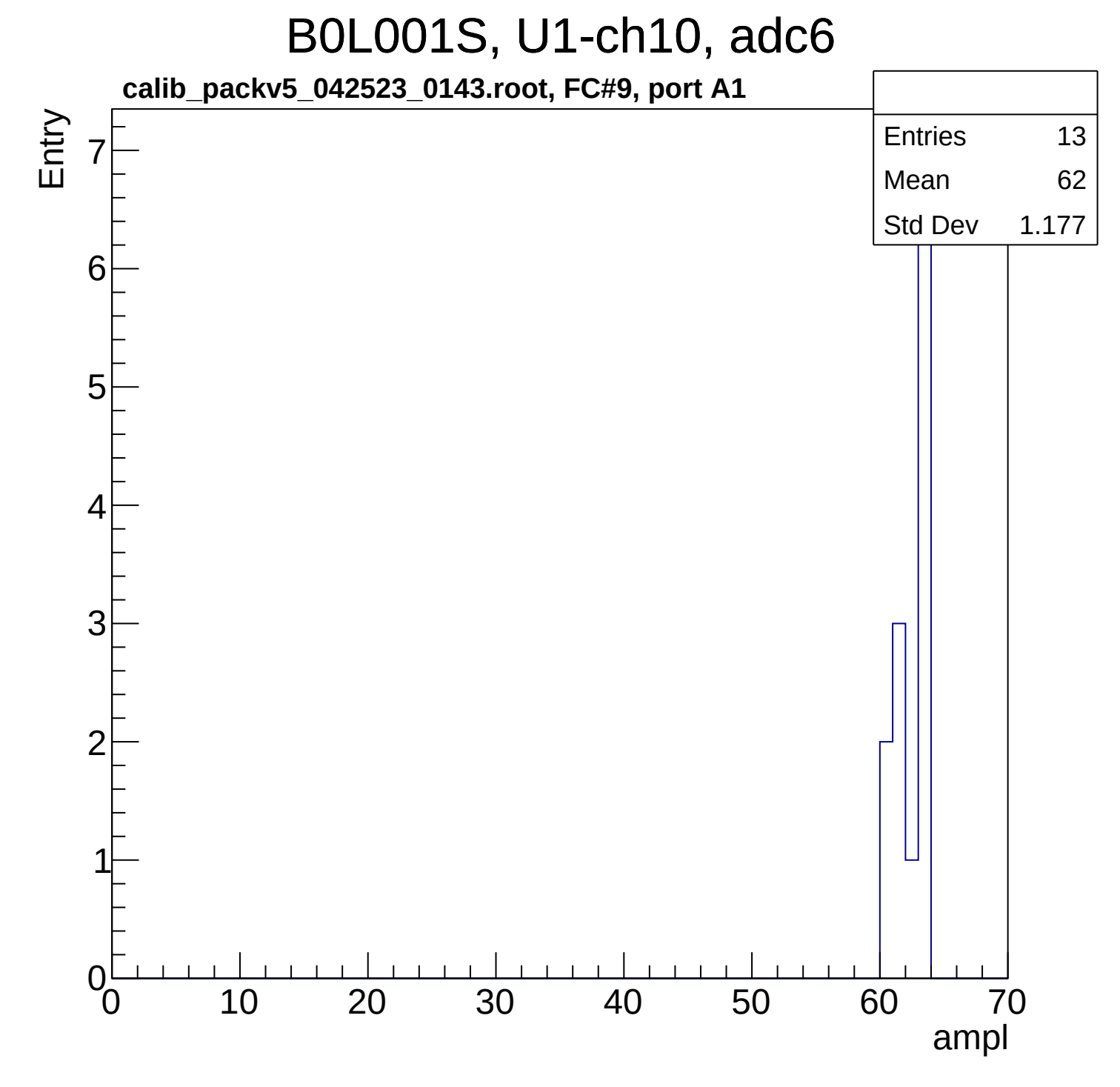
40
50
60
70


B0L001S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 62
Std Dev 1.177
Entry
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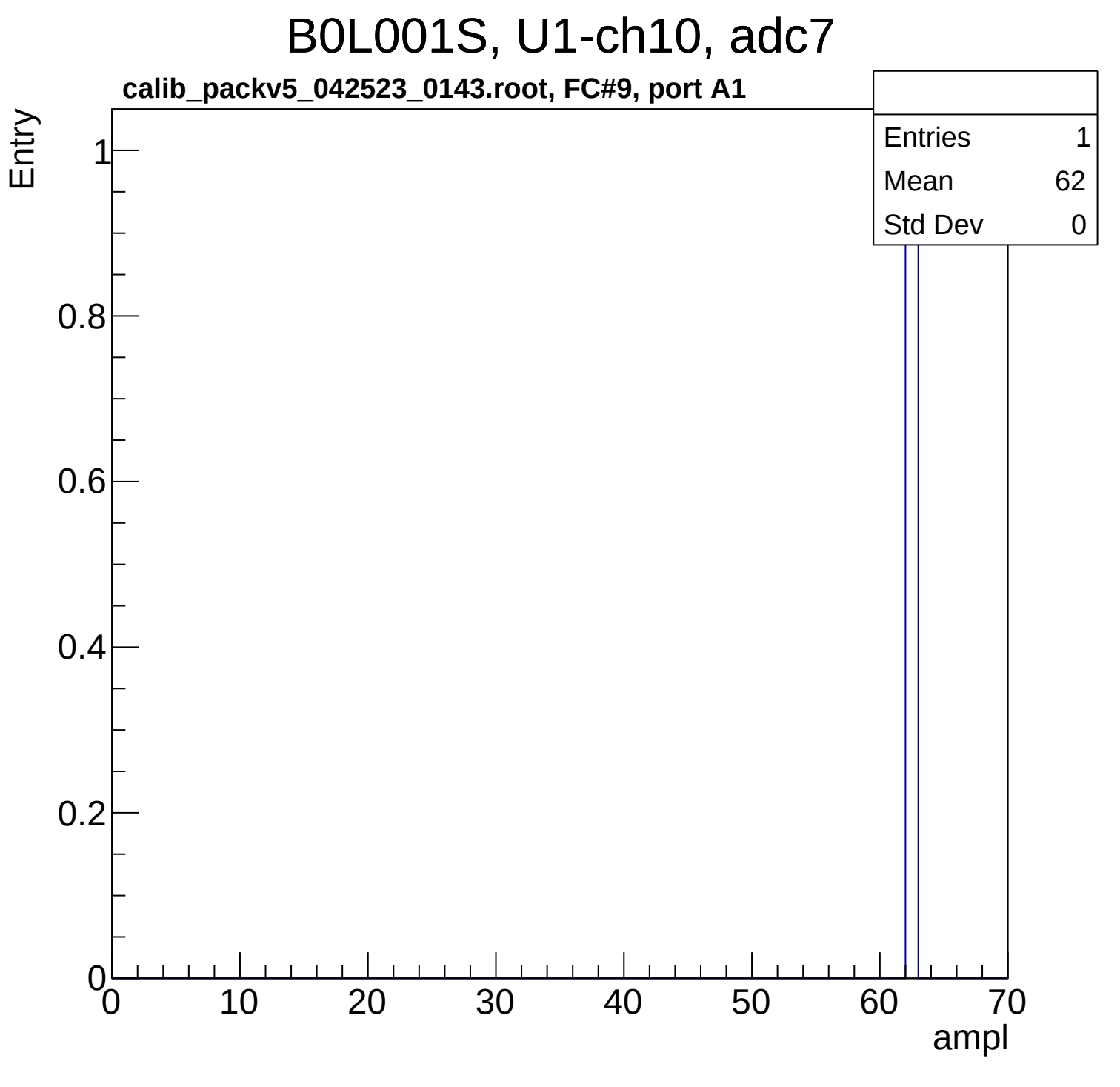
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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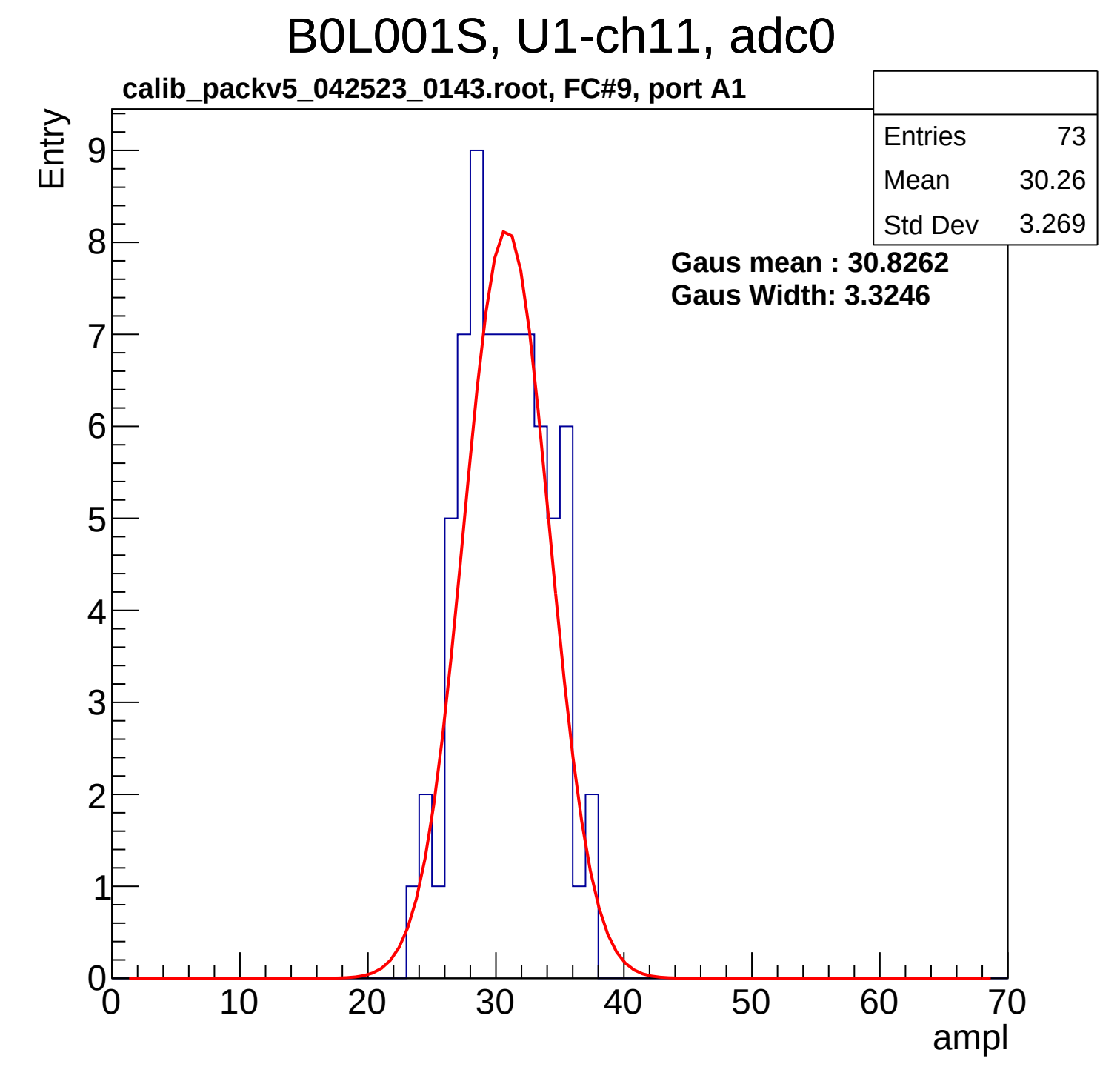
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.26
Std Dev 3.269
Gaus mean : 30.8262
Gaus Width: 3.3246
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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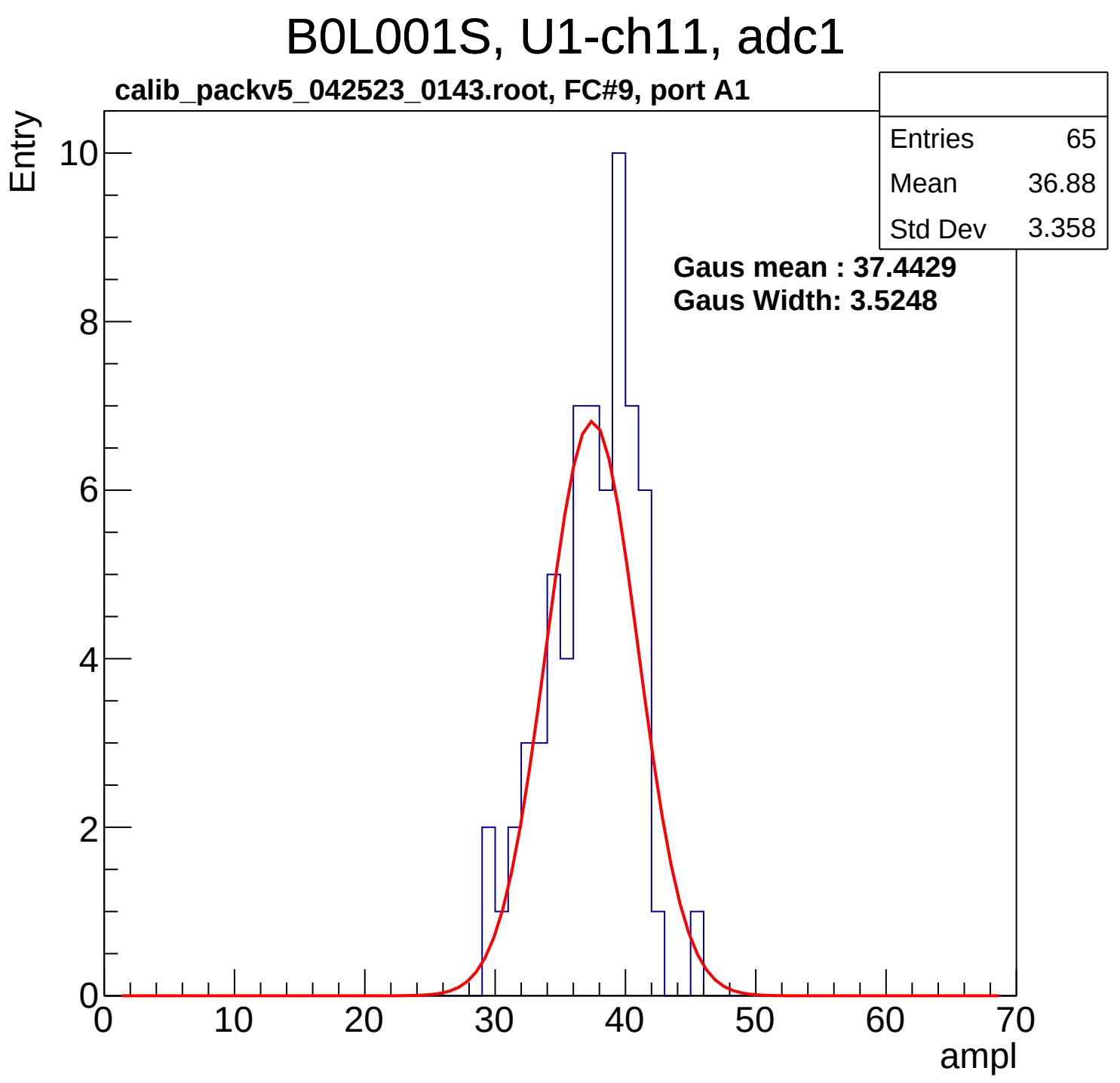
B0L001S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.88
Std Dev 3.358
Gaus mean : 37.4429
Gaus Width: 3.5248
Entry
ampl
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2
4
6
8
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0
10
20
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40
50
60
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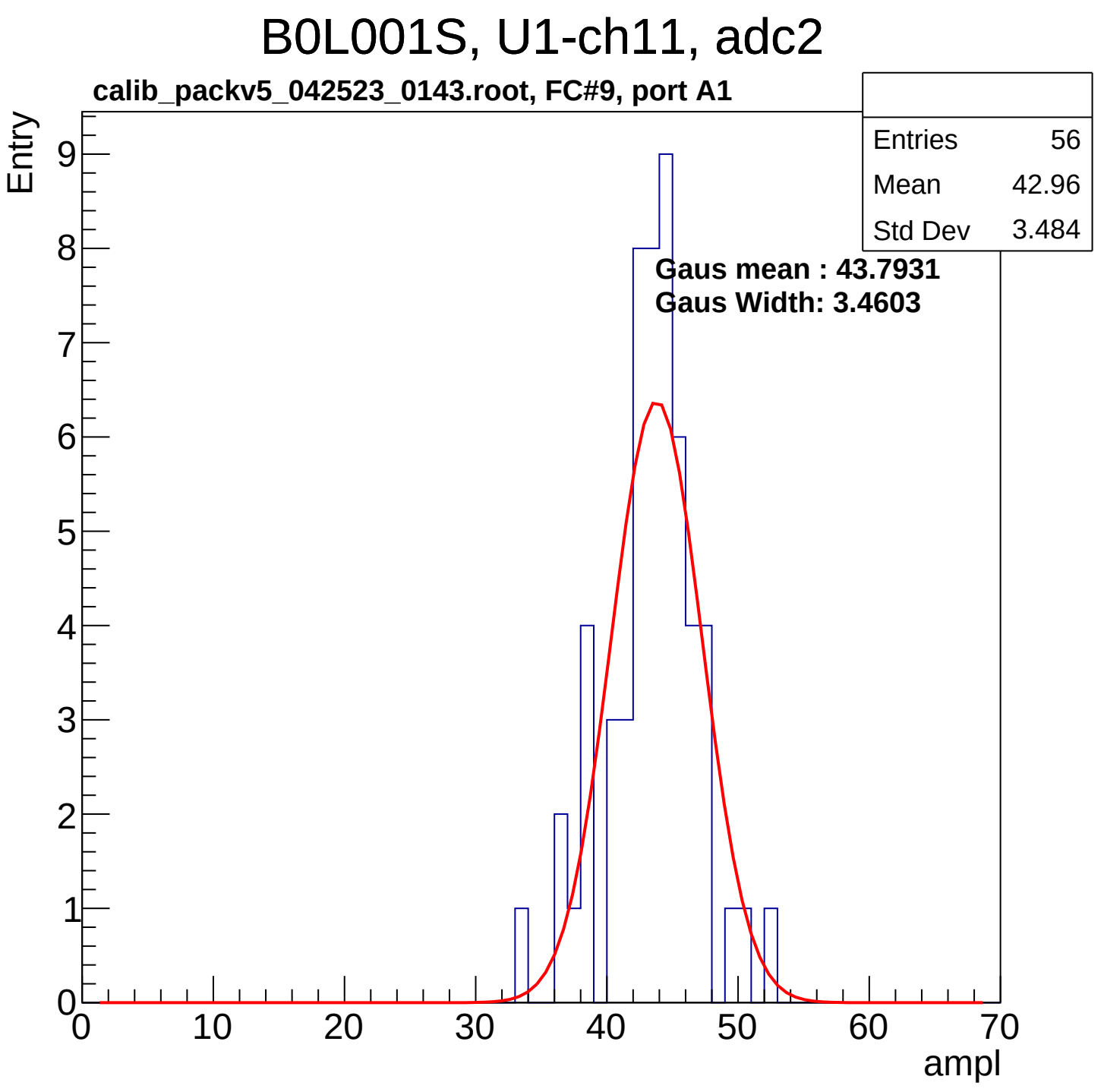
B0L001S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 42.96
Std Dev 3.484
Gaus mean : 43.7931
Gaus Width: 3.4603
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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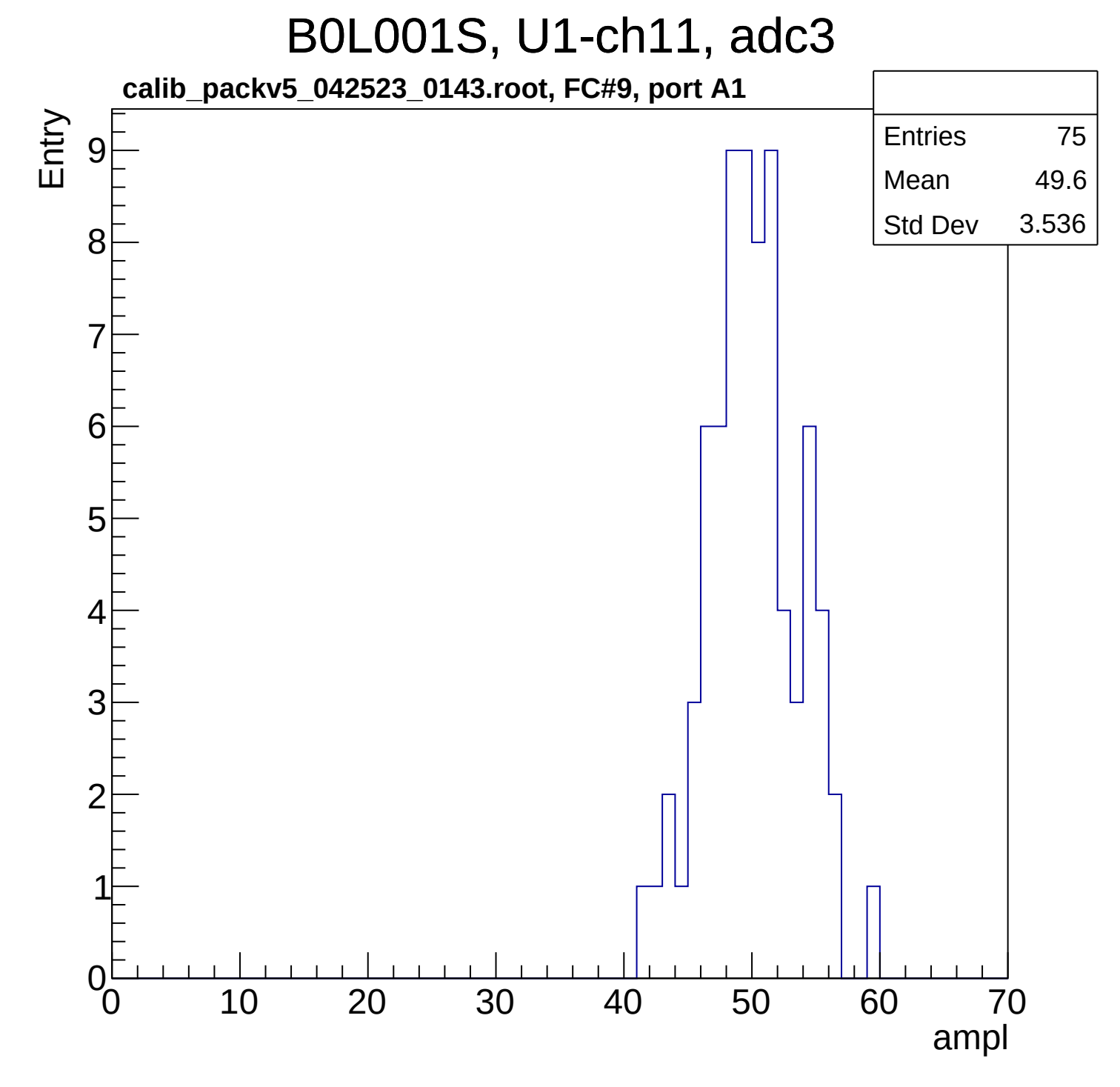
B0L001S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.6
Std Dev 3.536
Entry
ampl
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4
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0
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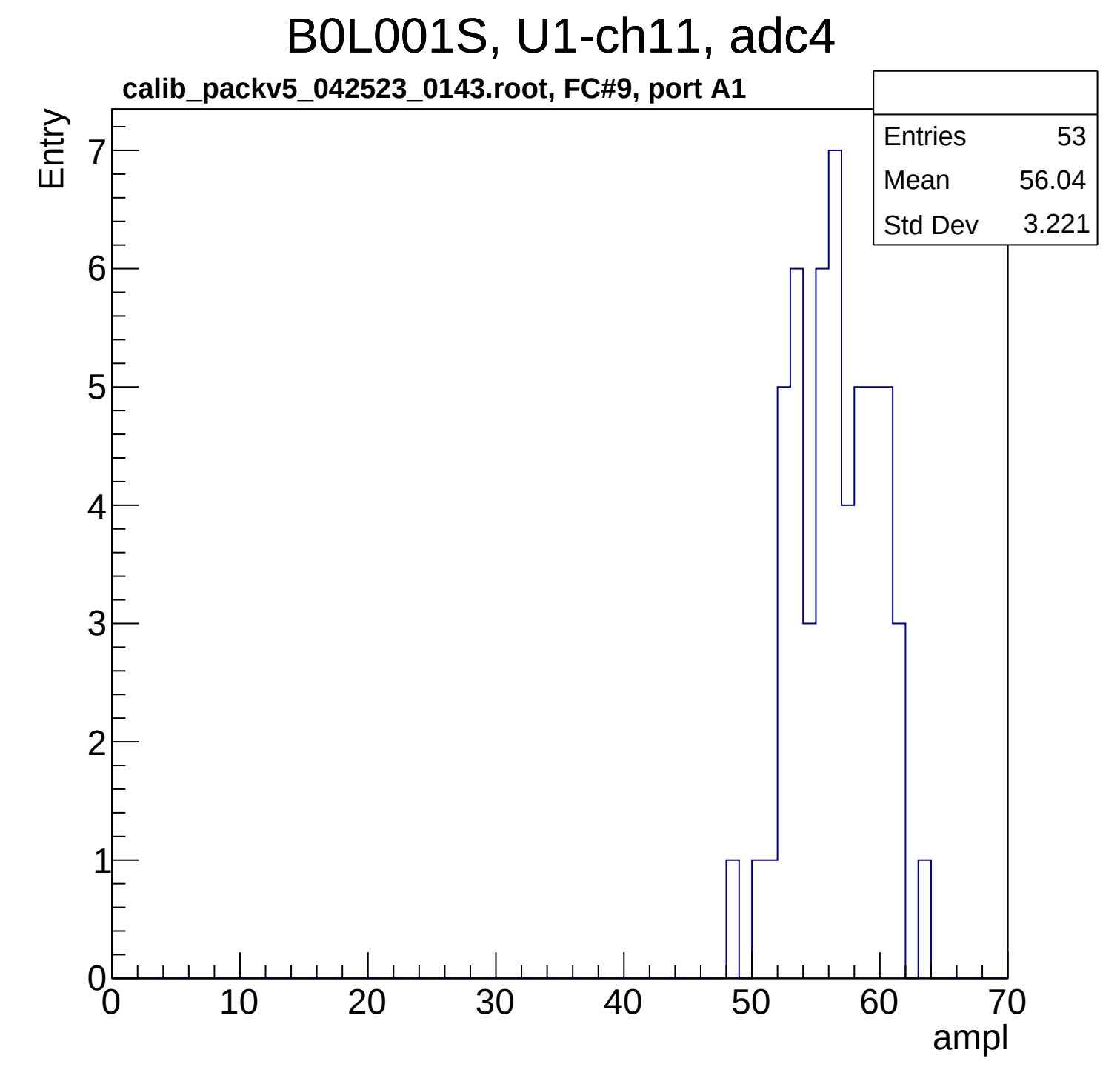
B0L001S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.04
Std Dev 3.221
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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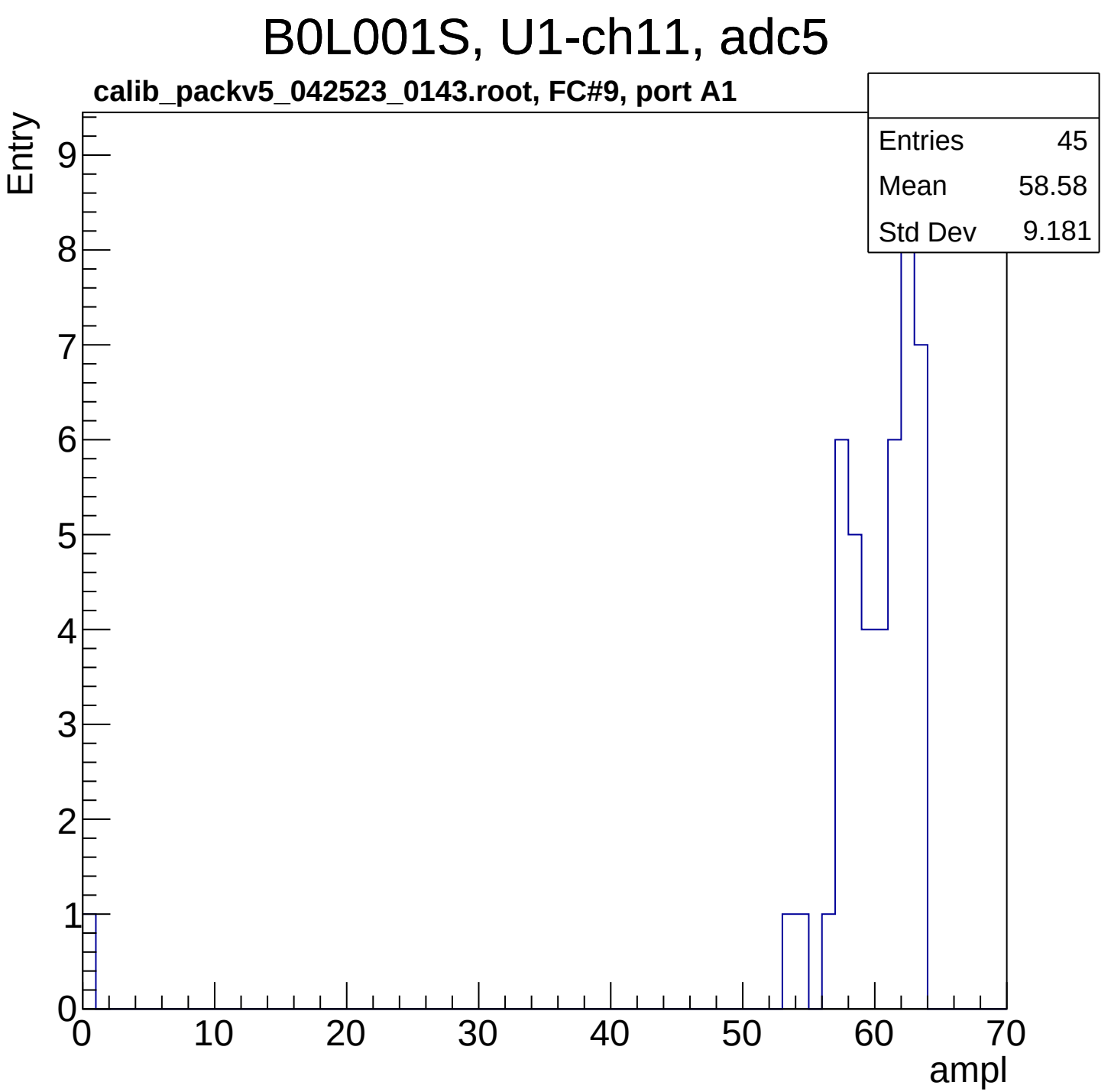
40
50
60
70


B0L001S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.58
Std Dev 9.181
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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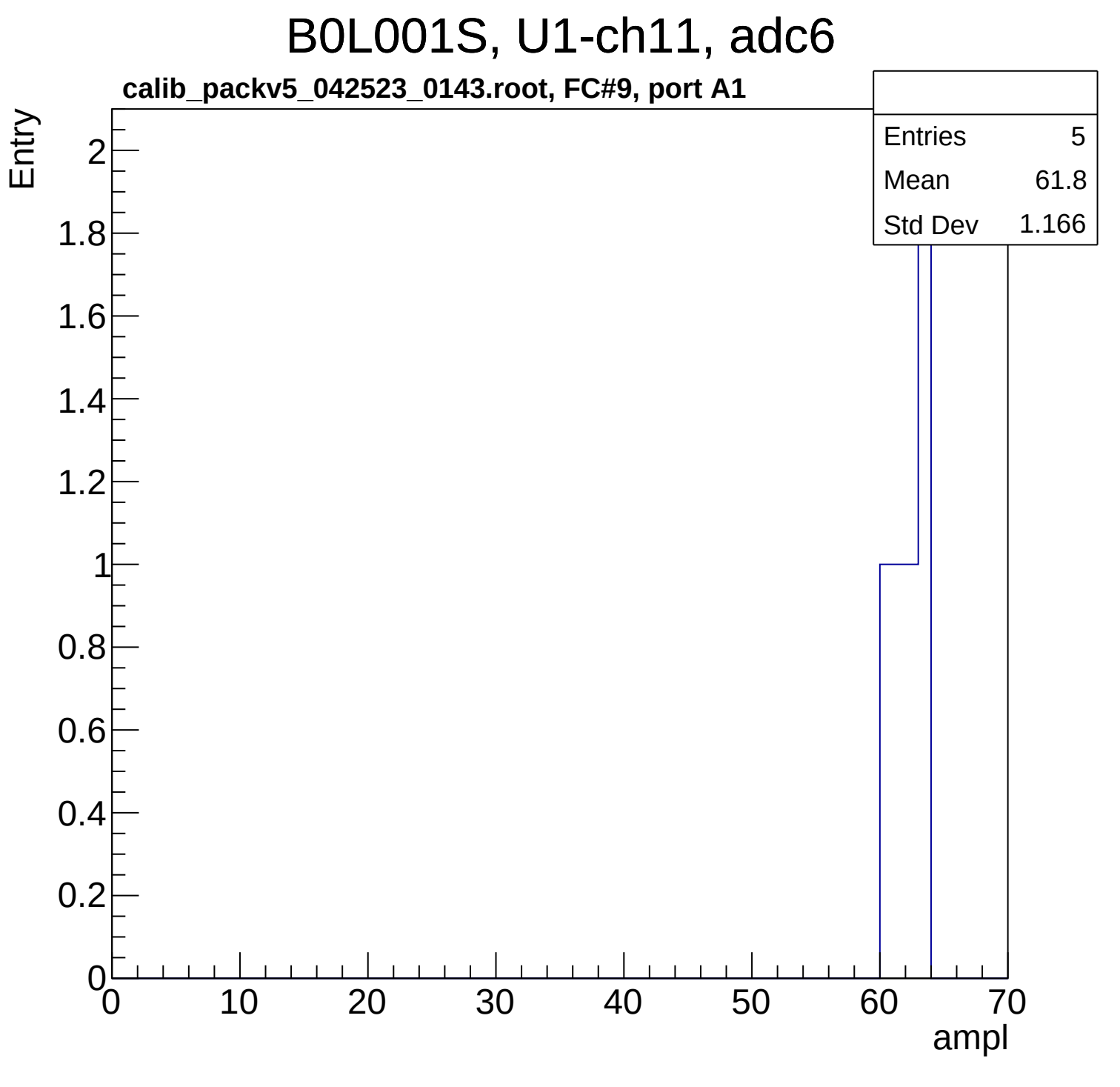
B0L001S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 1.166
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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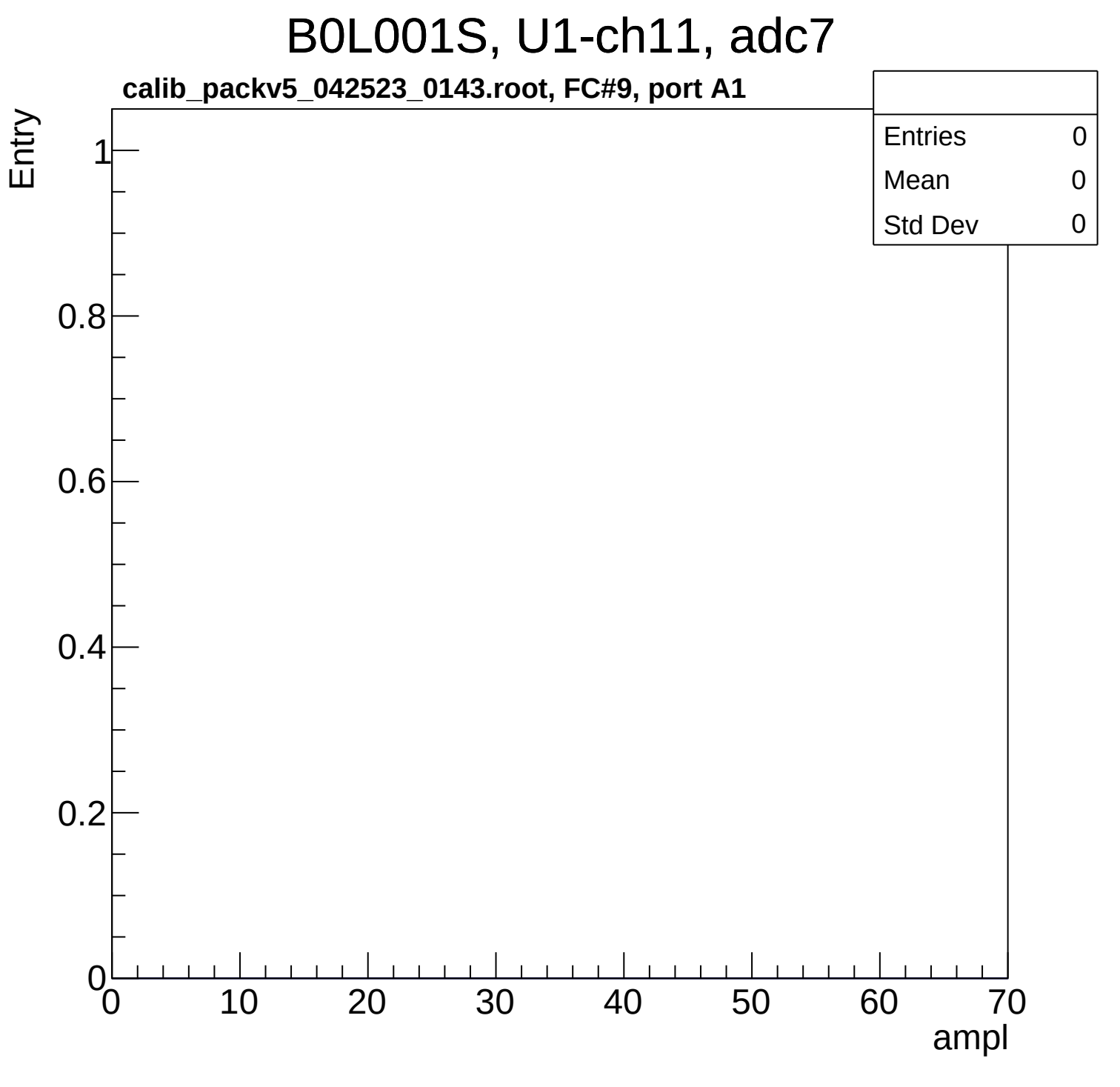
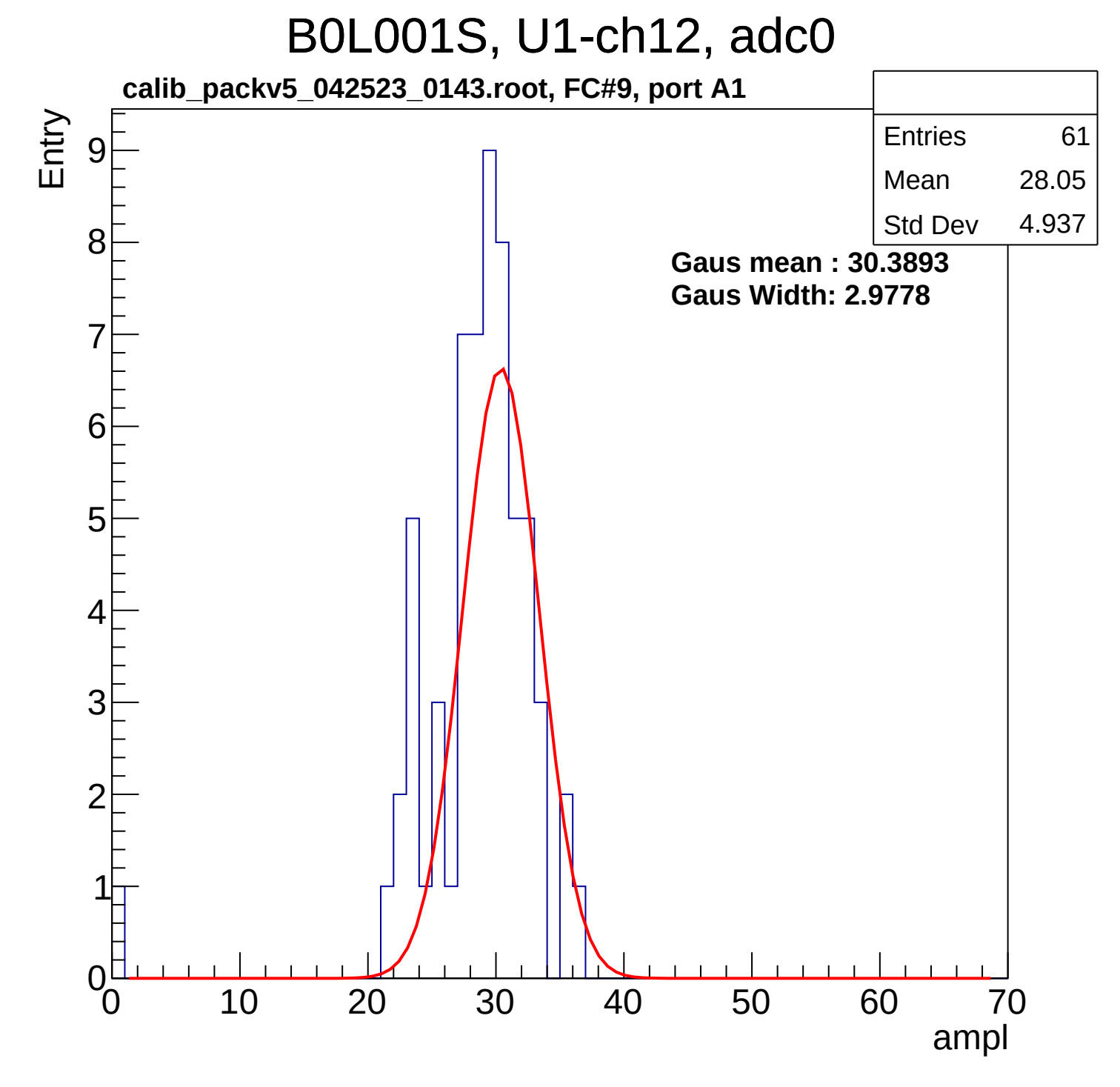

B0L001S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 28.05
Std Dev 4.937
Gaus mean : 30.3893
Gaus Width: 2.9778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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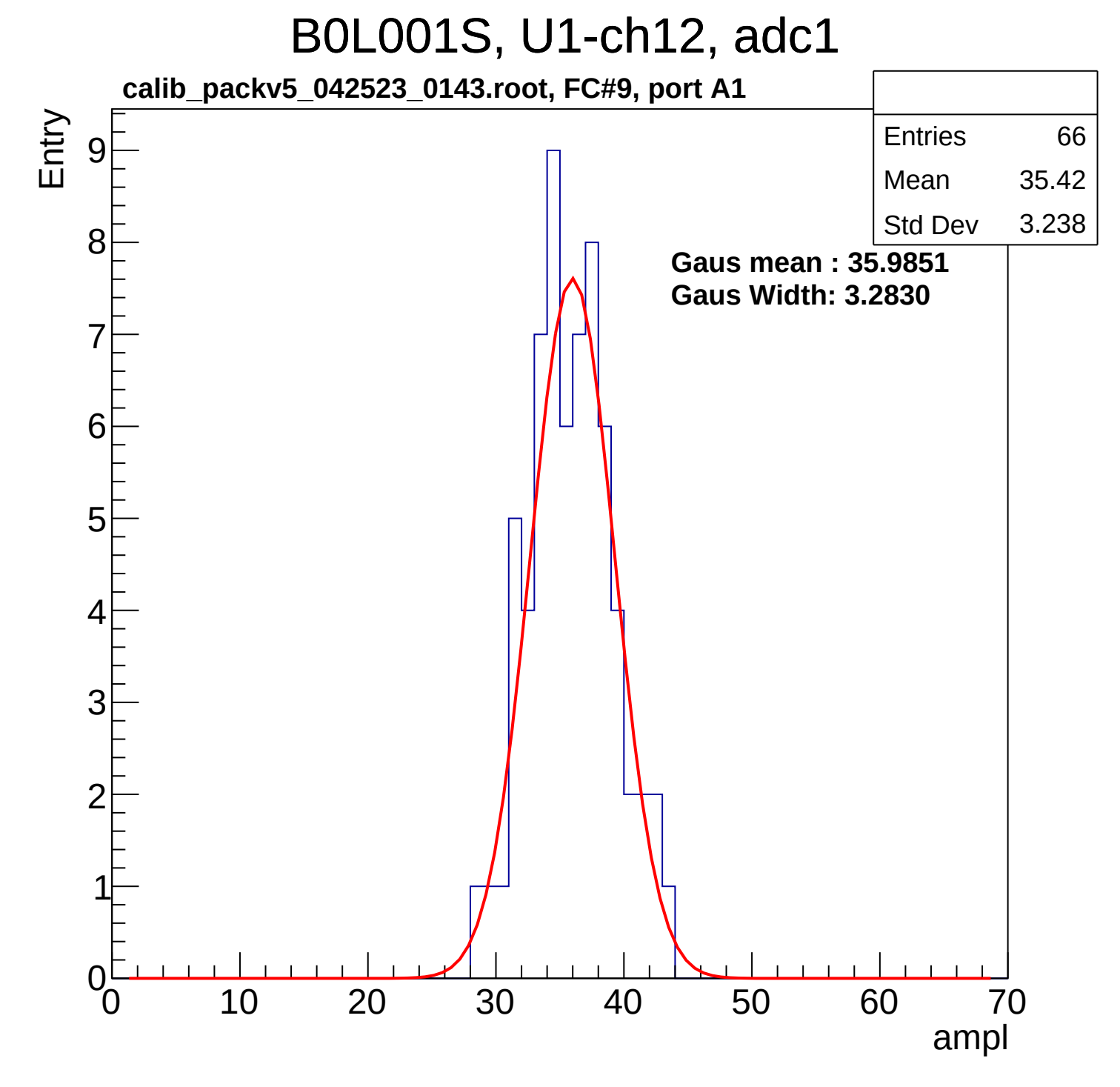
B0L001S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 35.42
Std Dev 3.238
Gaus mean : 35.9851
Gaus Width: 3.2830
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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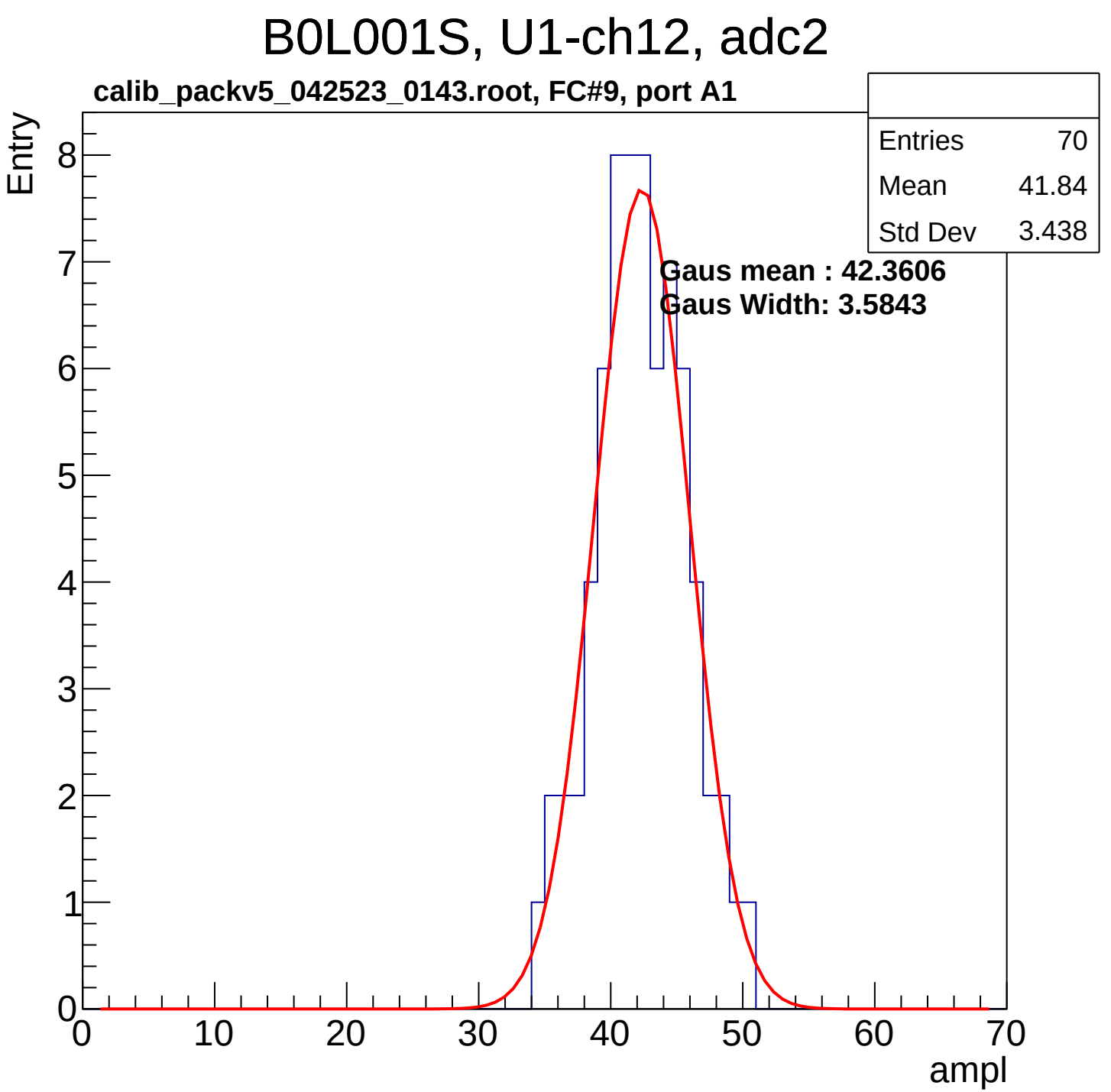
B0L001S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 41.84
Std Dev 3.438
Gaus mean : 42.3606
Gaus Width: 3.5843
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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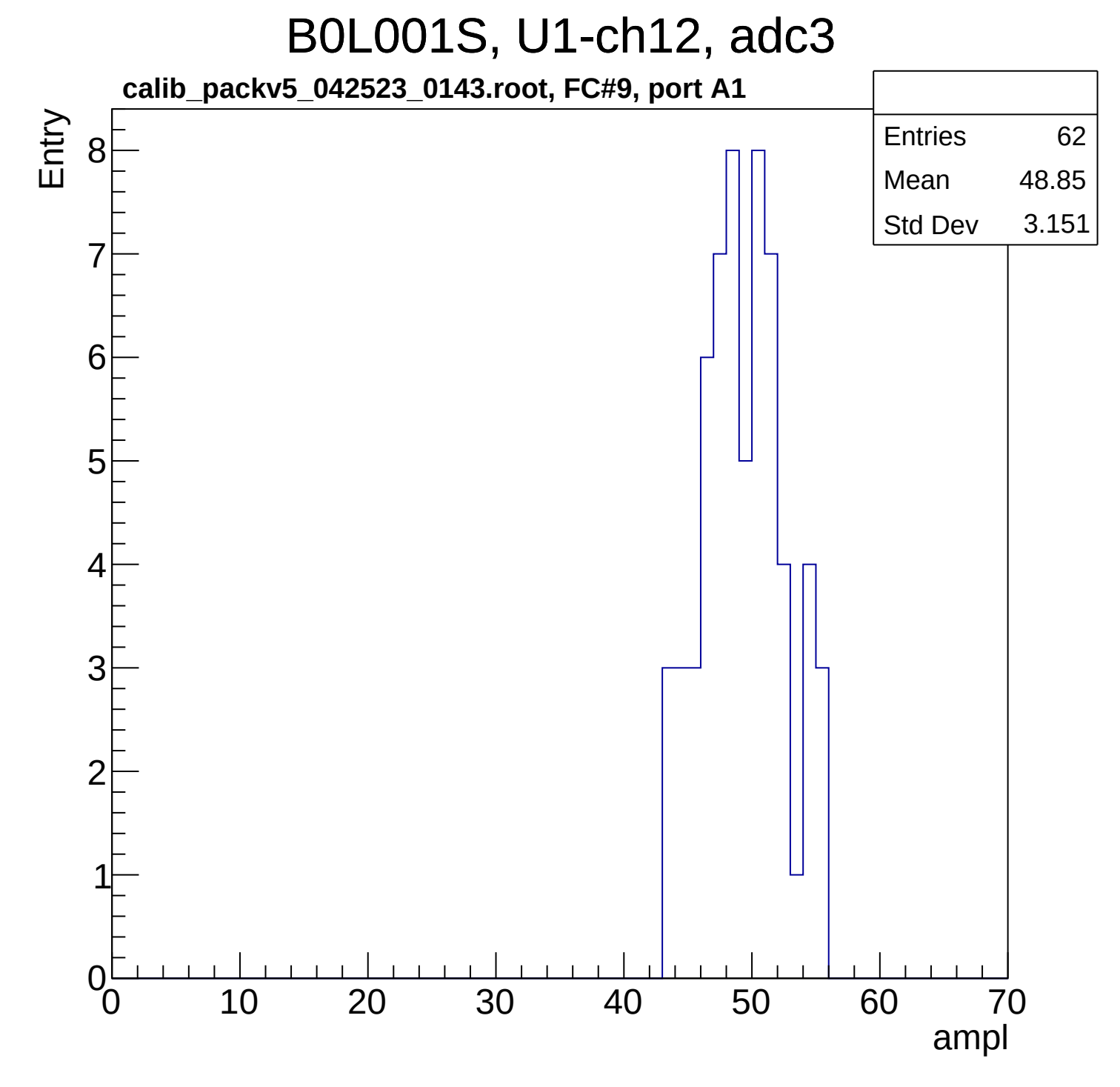
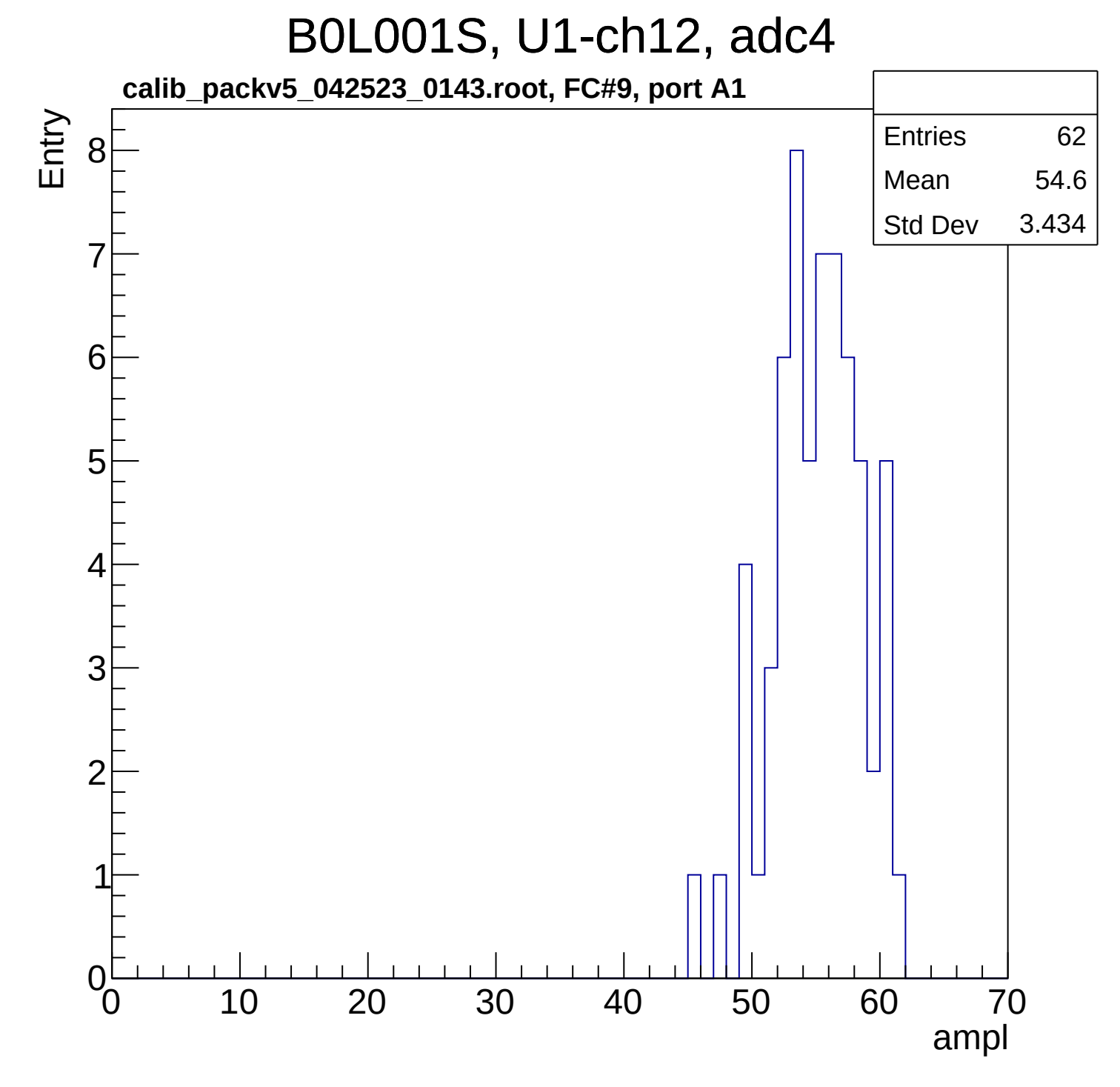
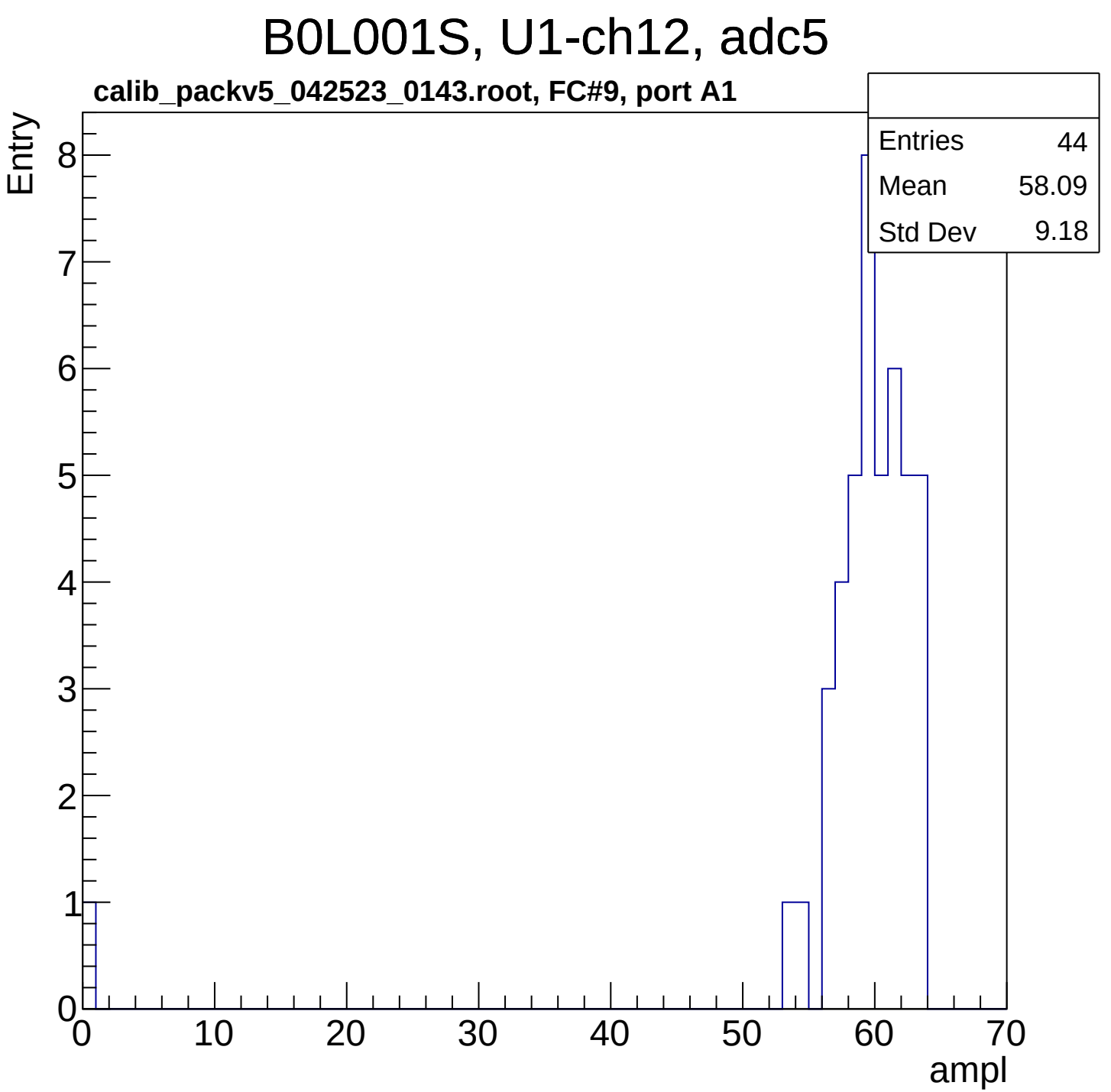
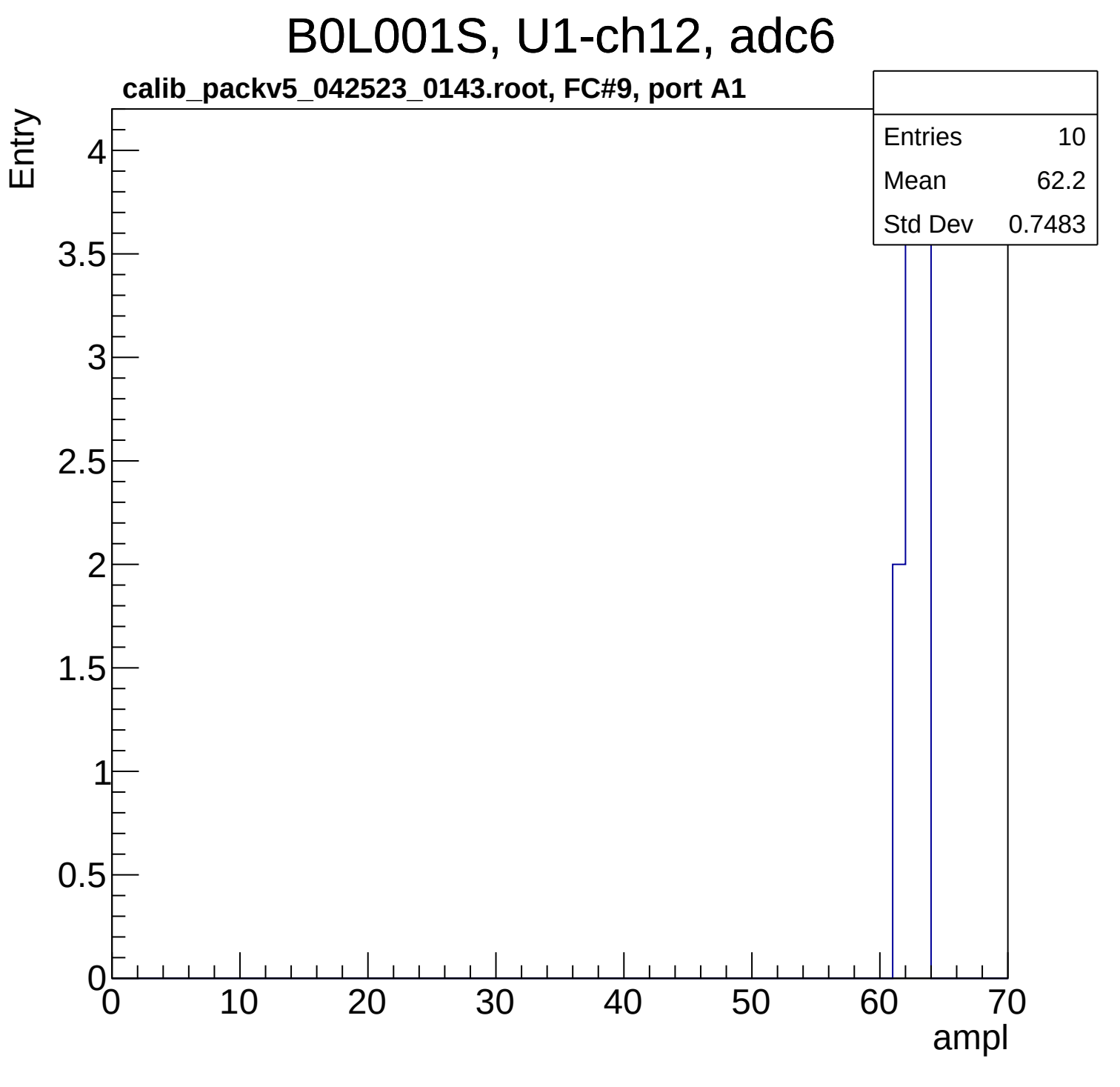
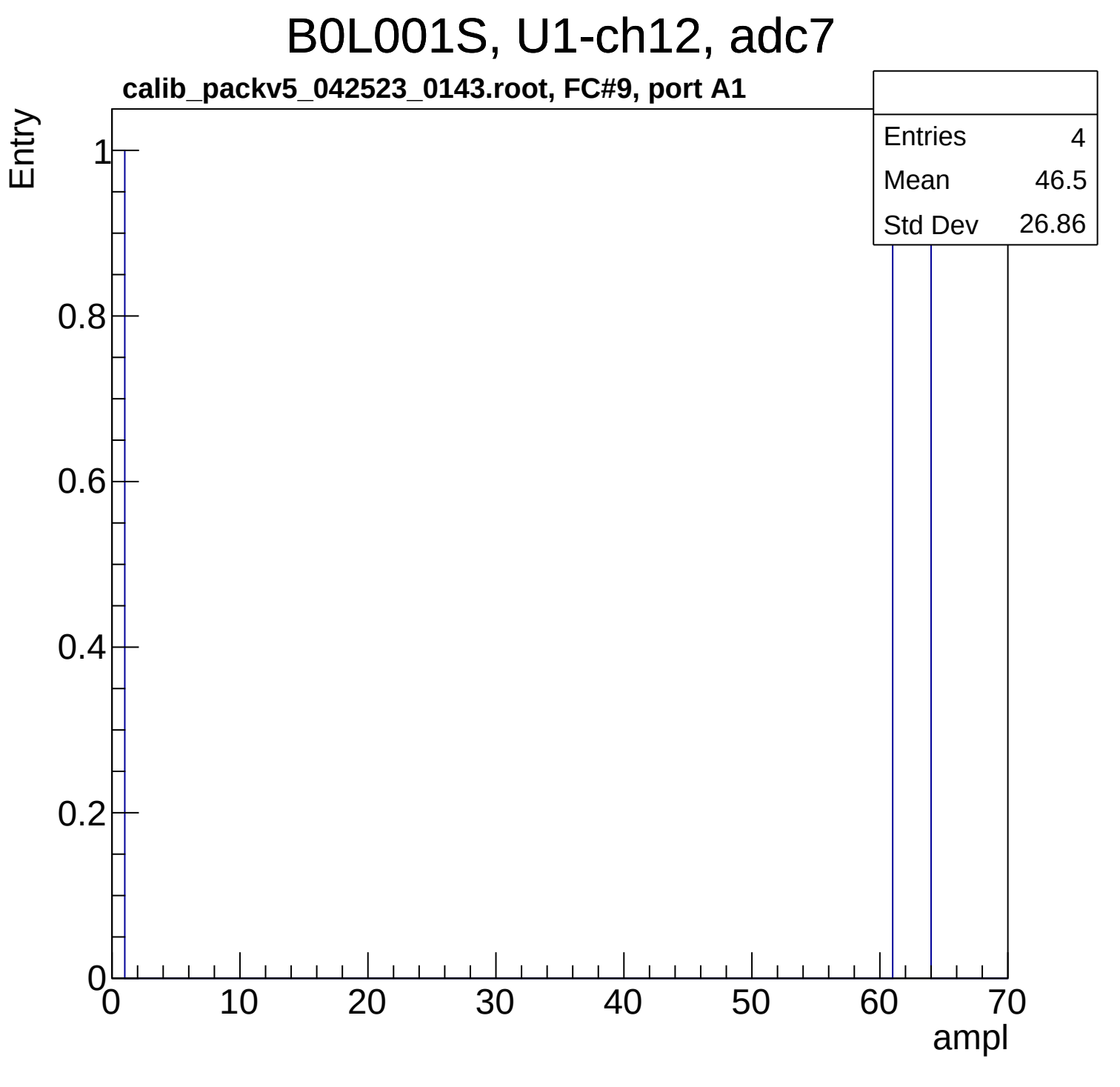

B0L001S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 48.85
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 54.6
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.09
Std Dev 9.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.5
Std Dev 26.86
Entry
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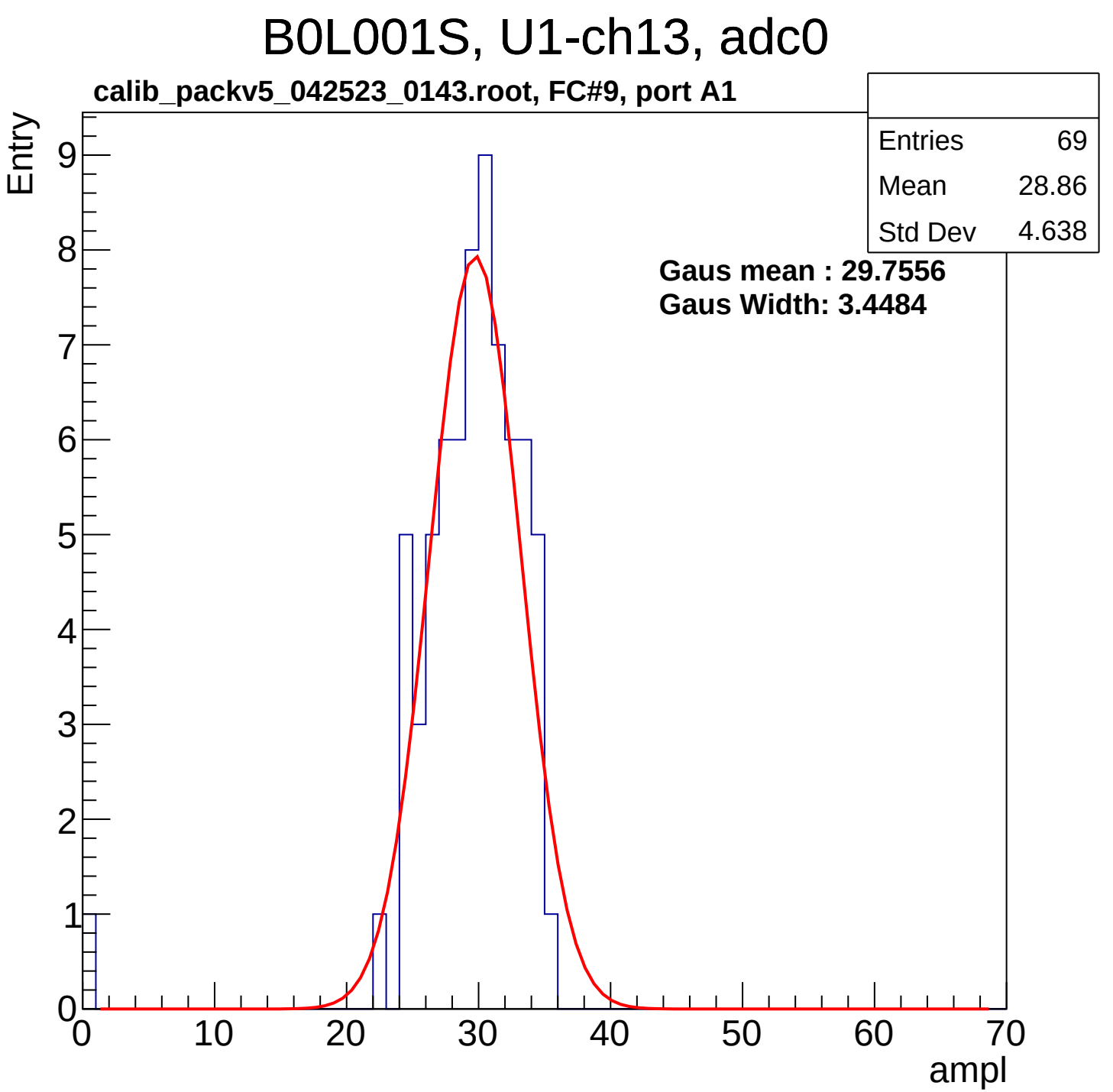
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 28.86
Std Dev 4.638
Gaus mean : 29.7556
Gaus Width: 3.4484
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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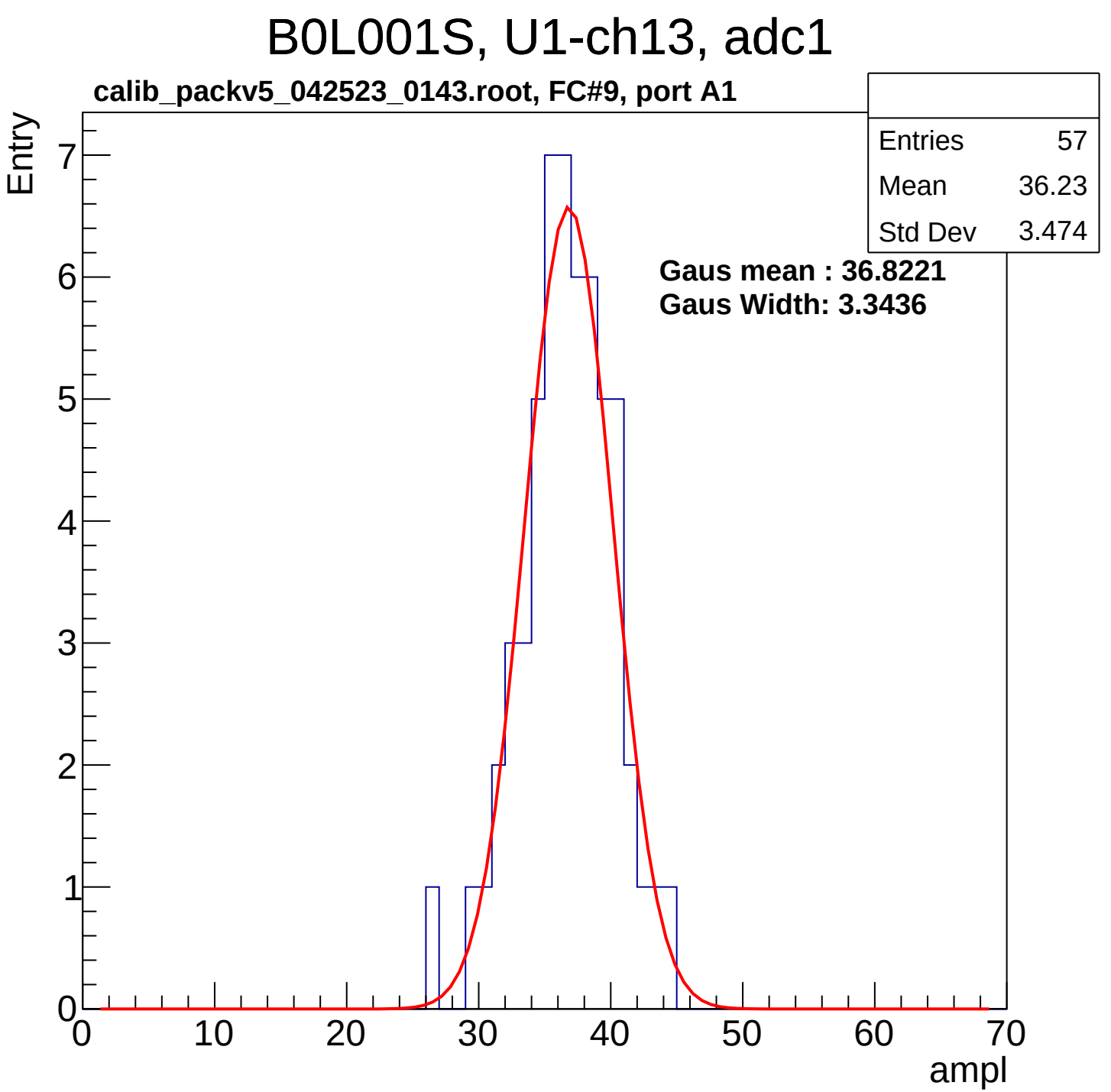
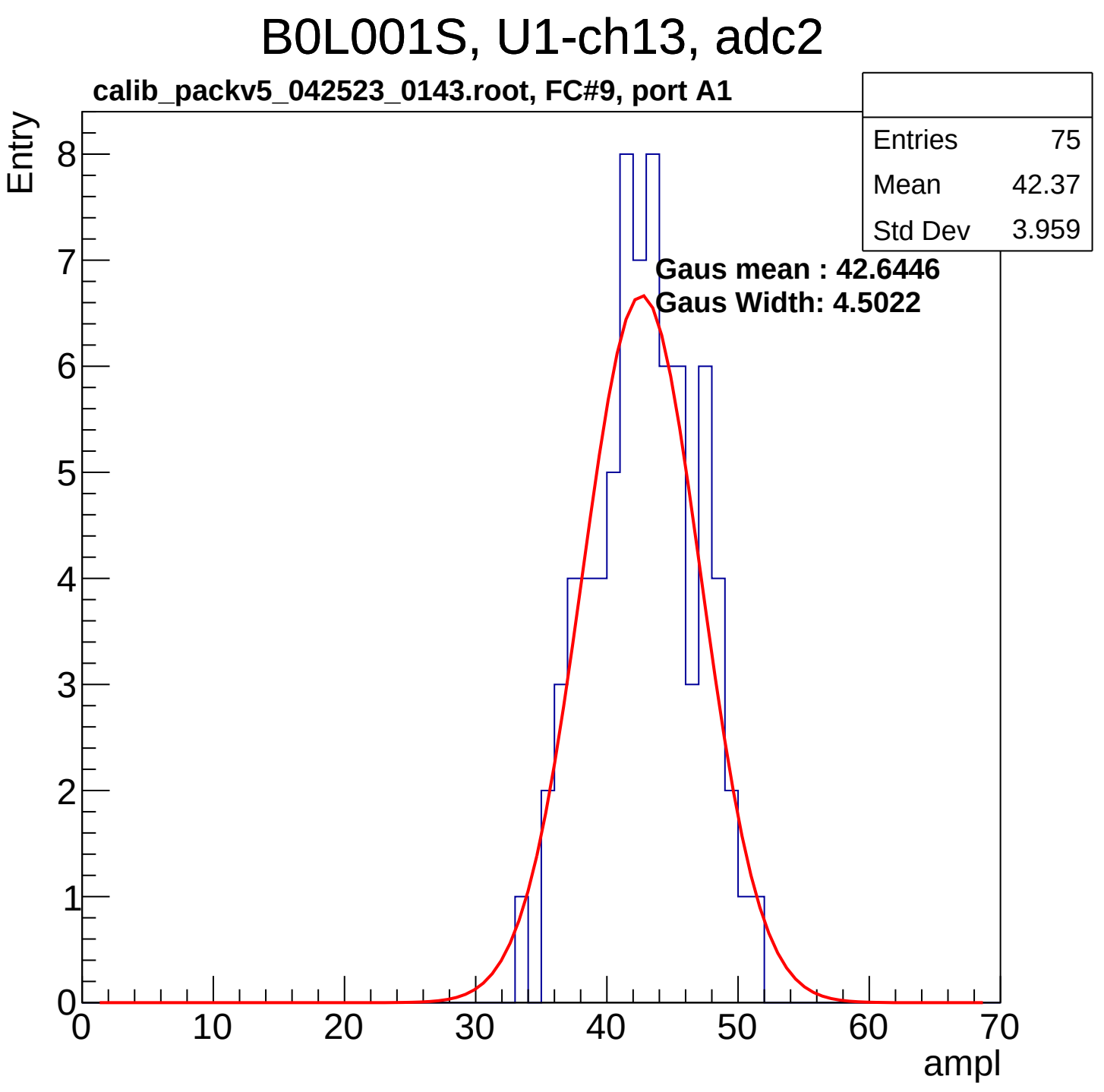
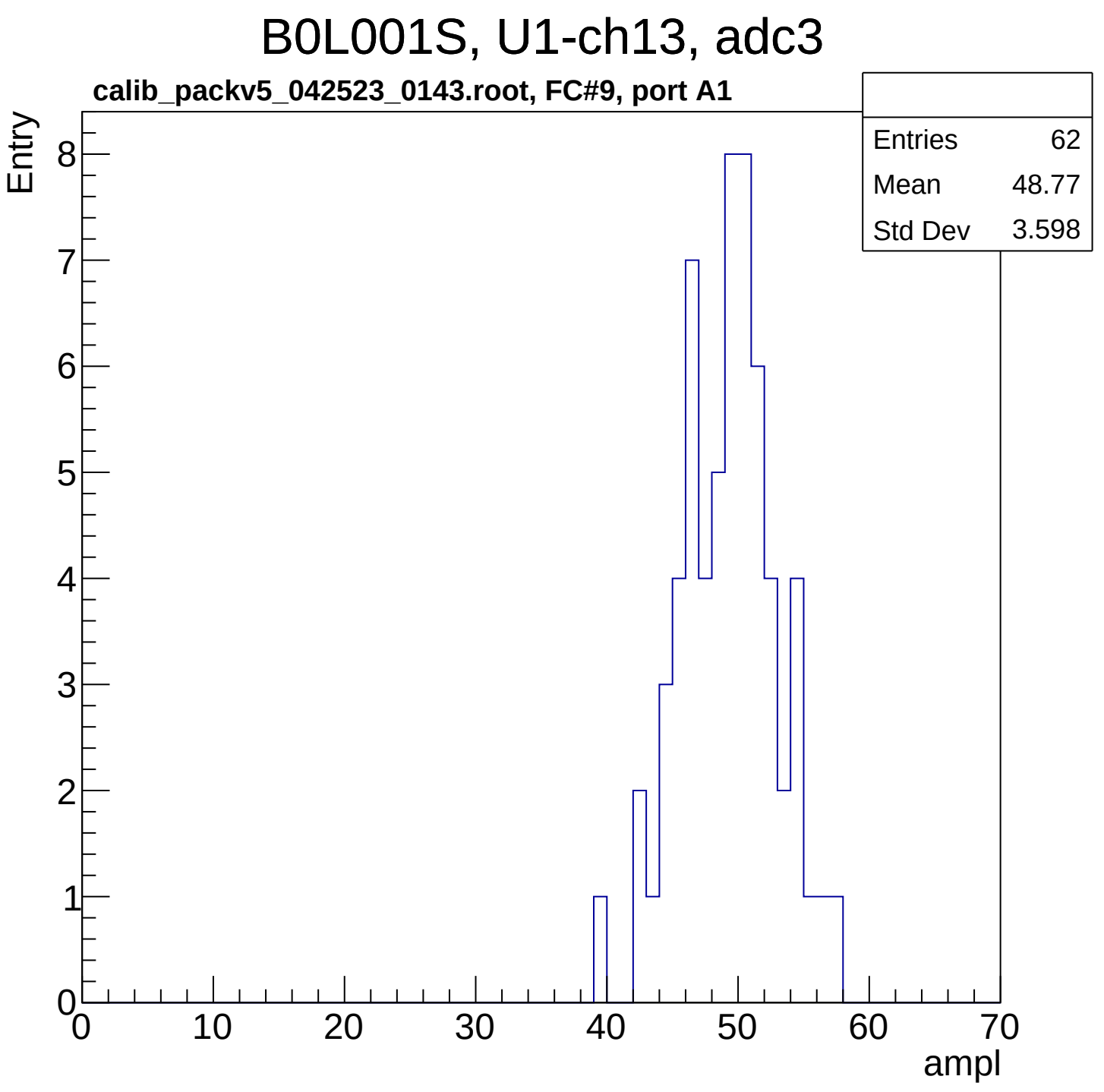
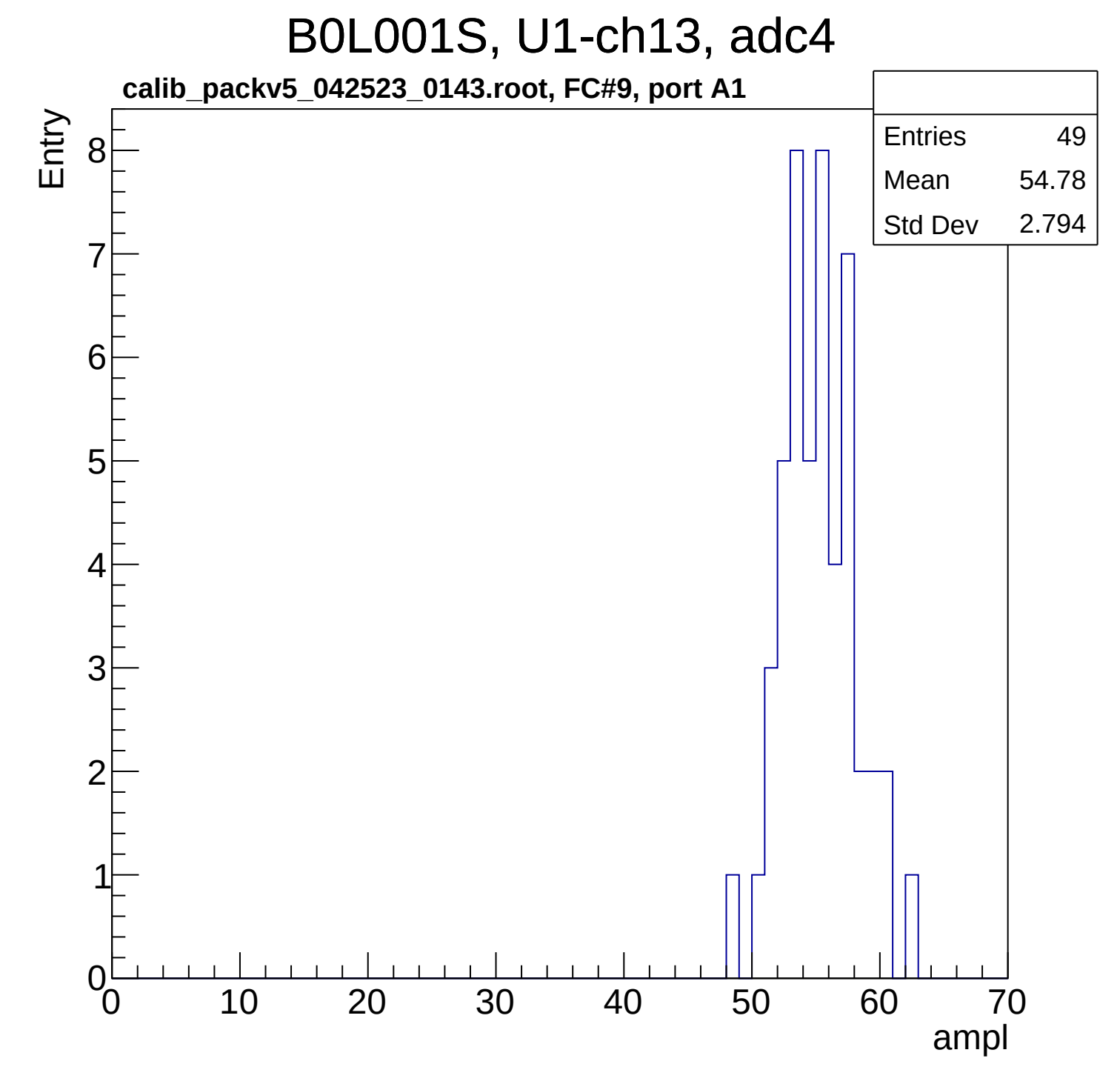
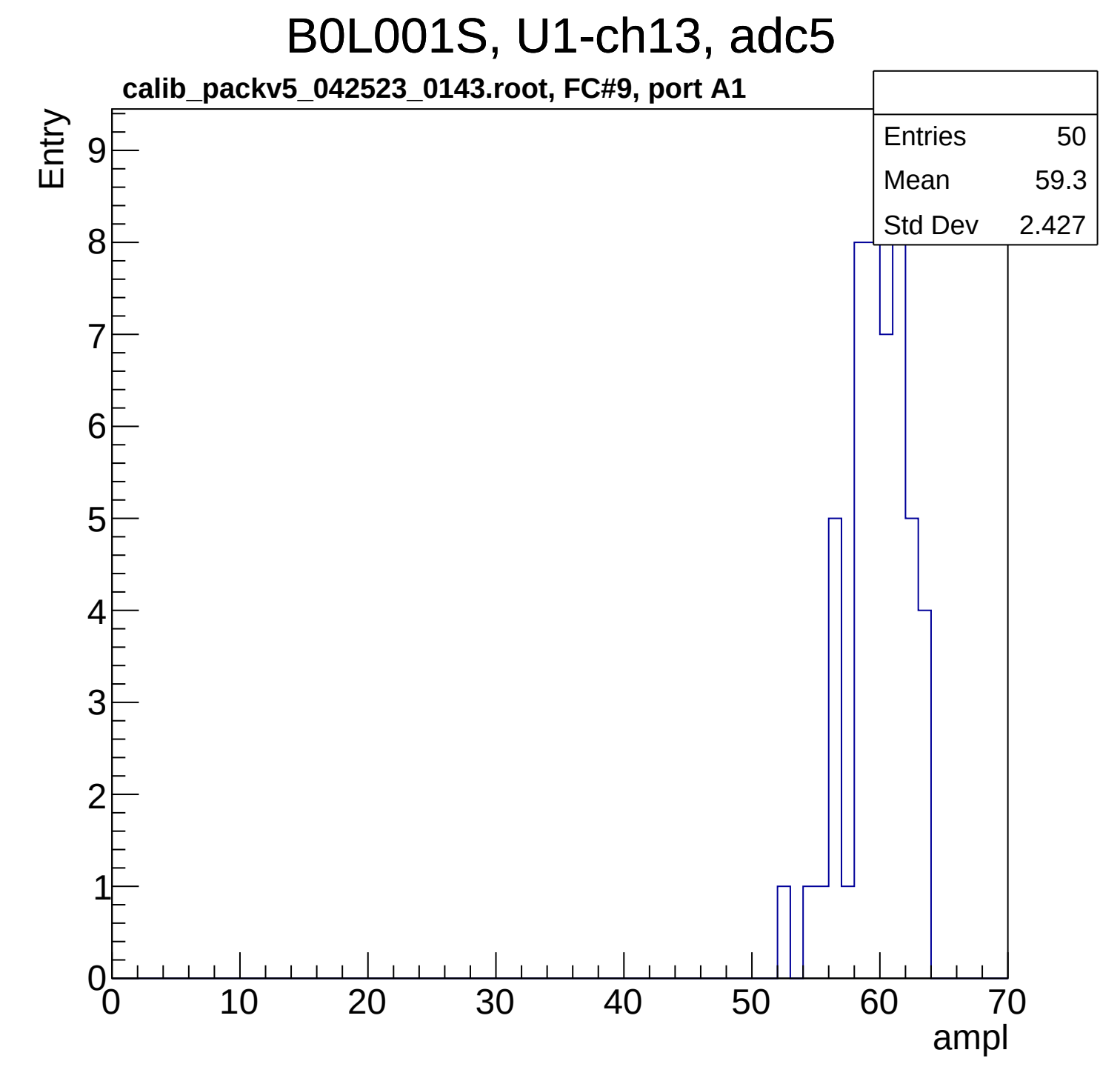
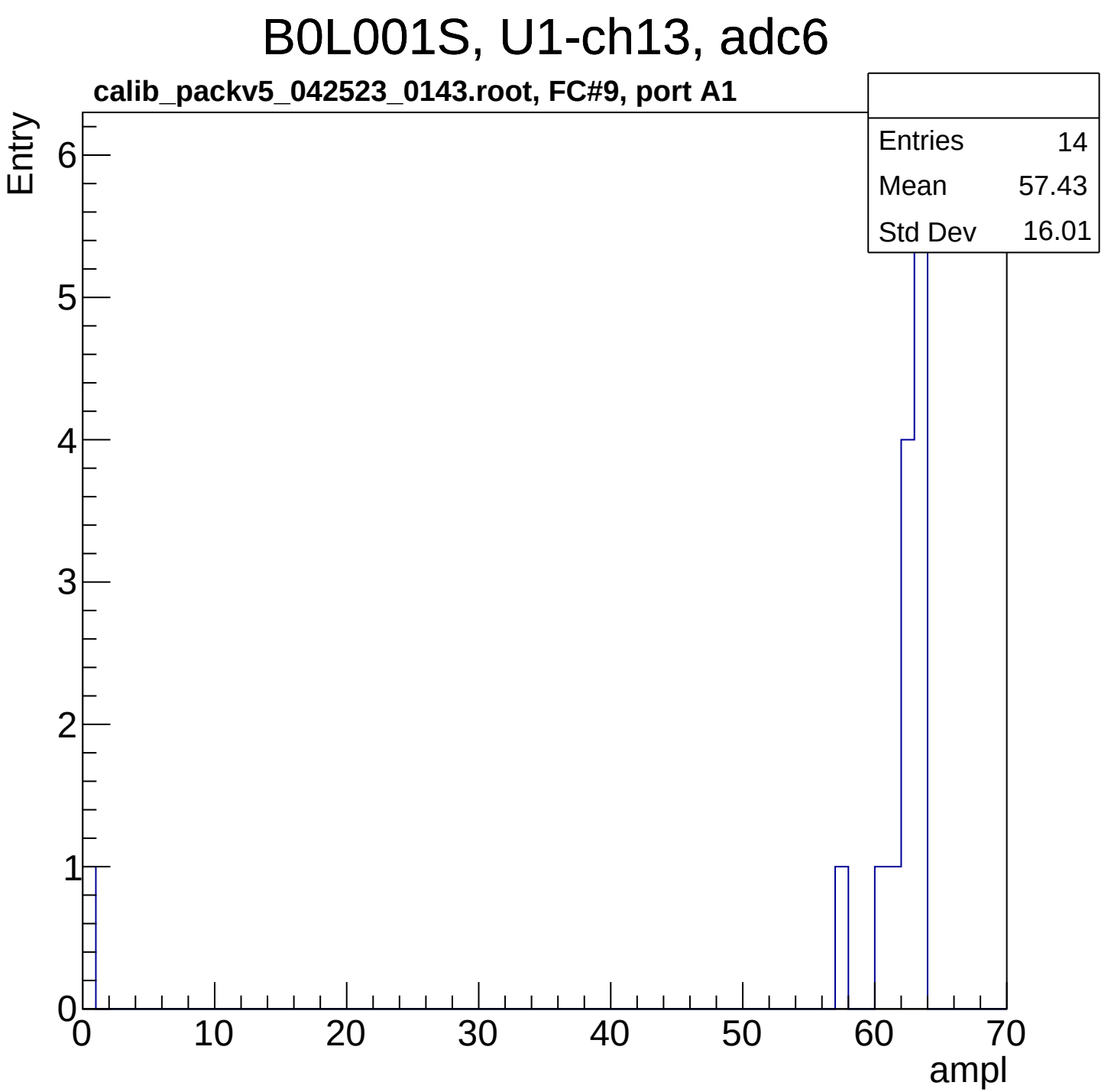

B0L001S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.23
Std Dev 3.474
Gaus mean : 36.8221
Gaus Width: 3.3436
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

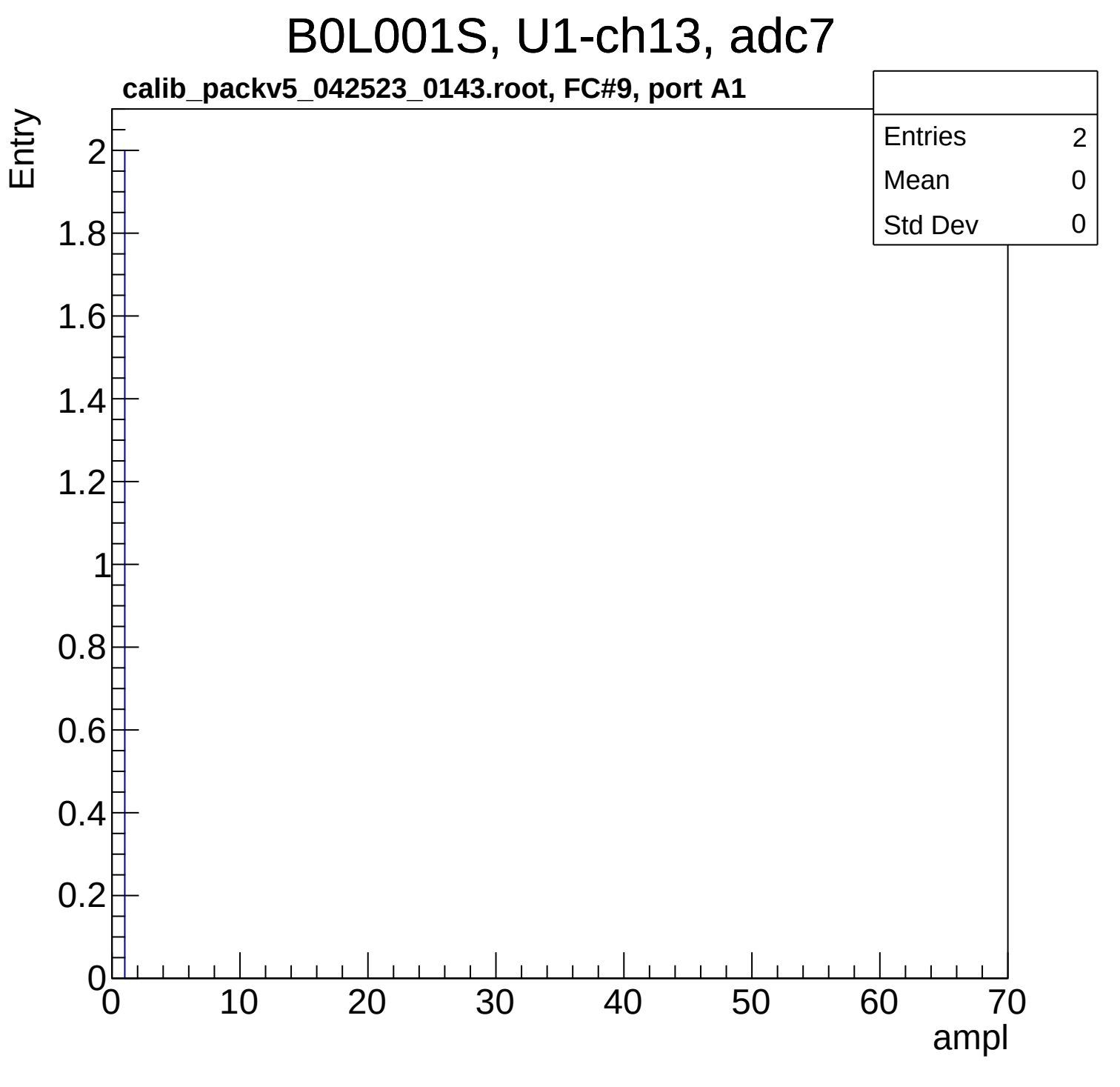

B0L001S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 42.37
Std Dev 3.959
Gaus mean : 42.6446
Gaus Width: 4.5022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

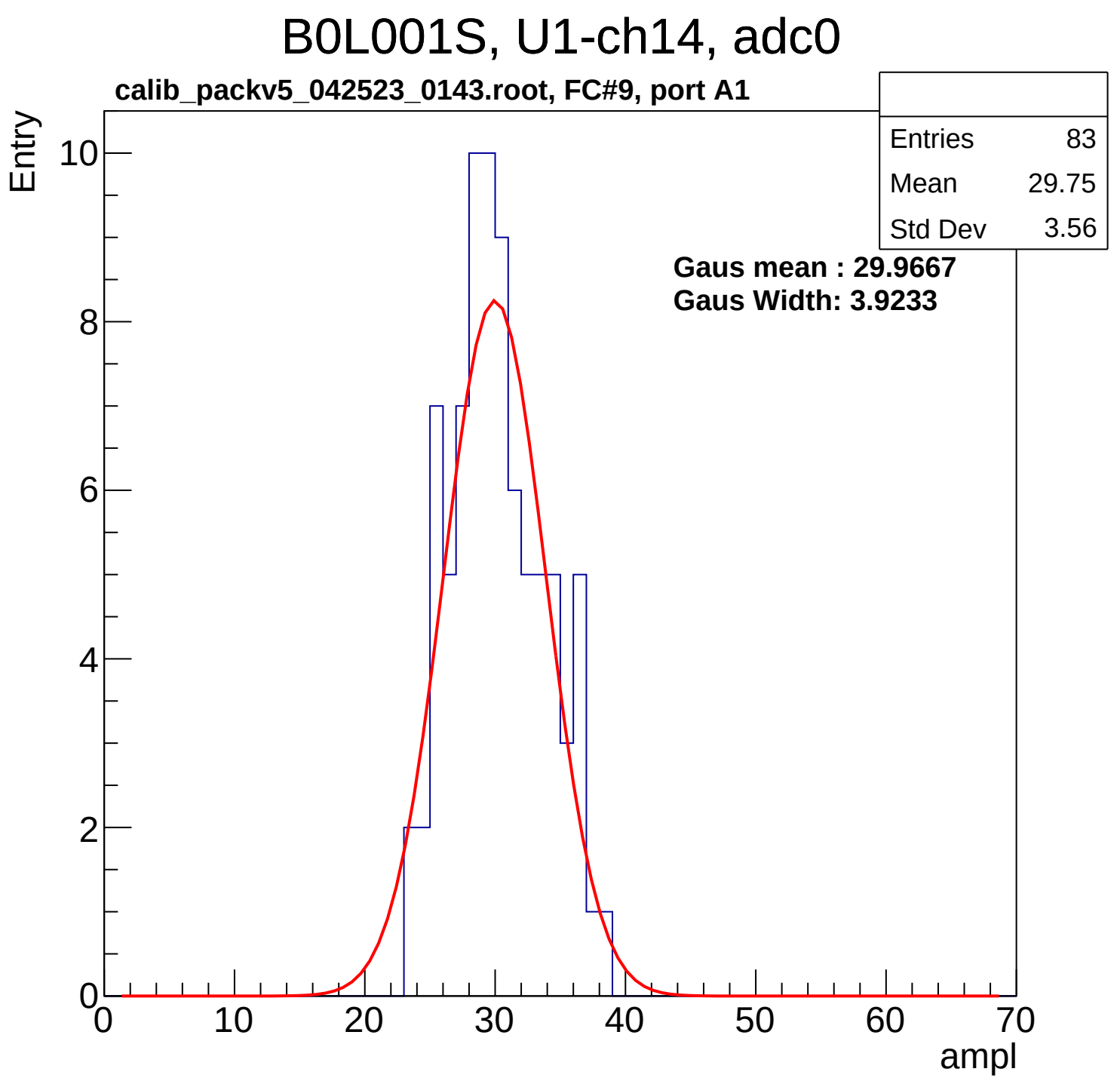

B0L001S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 48.77
Std Dev 3.598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

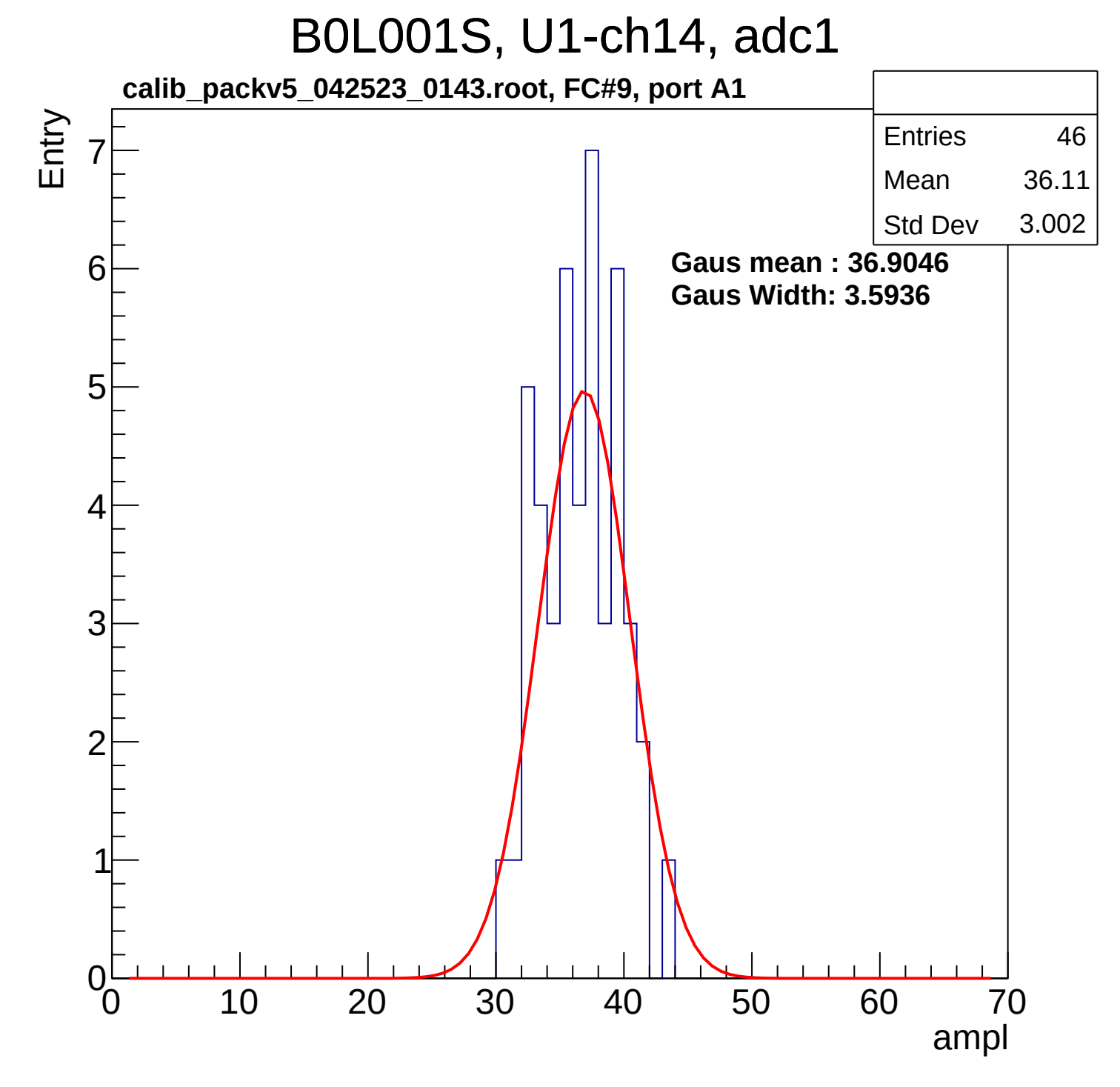

B0L001S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 54.78
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

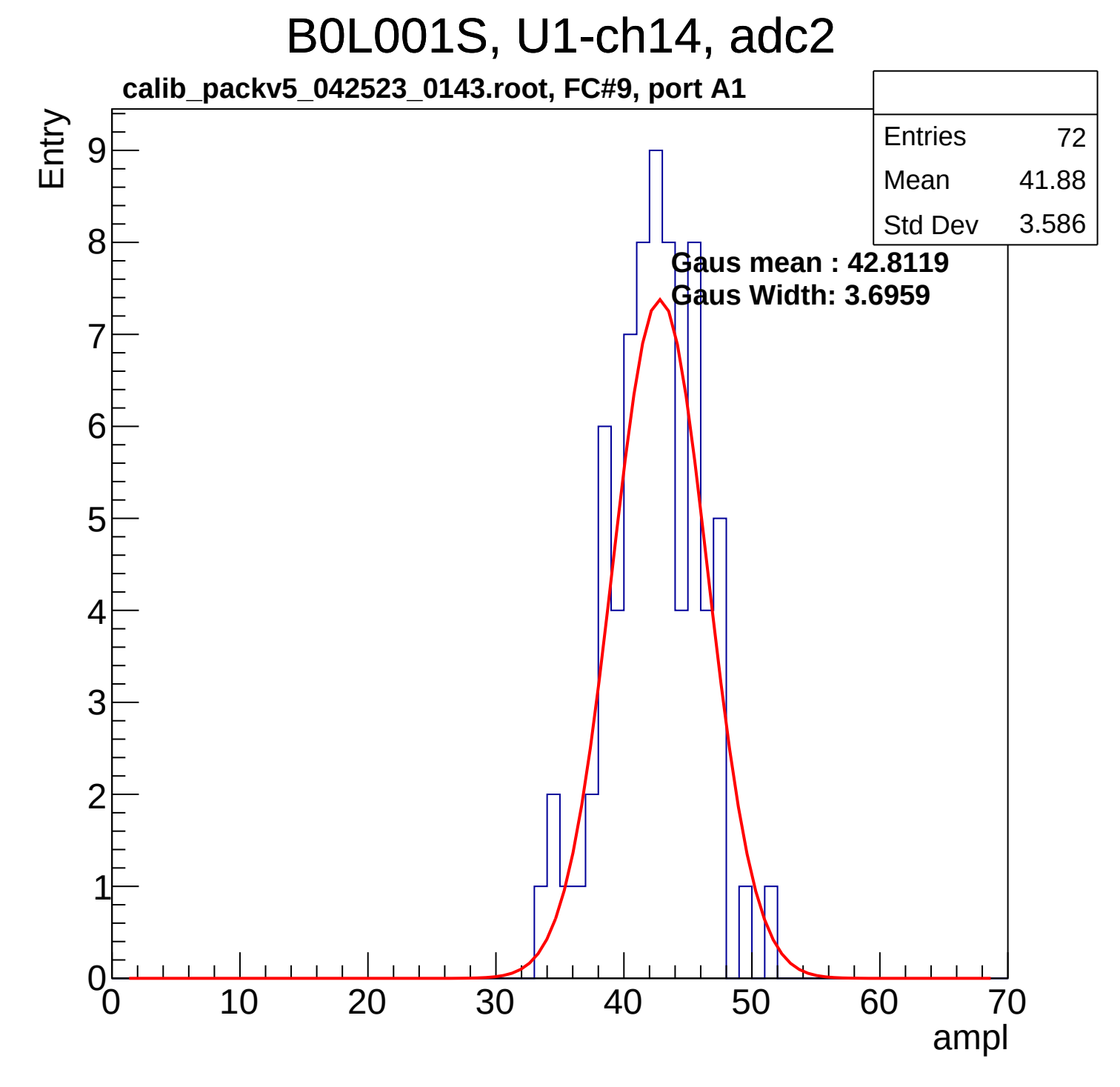

B0L001S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.3
Std Dev 2.427
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 57.43
Std Dev 16.01
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 29.75
Std Dev 3.56
Gaus mean : 29.9667
Gaus Width: 3.9233
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 36.11
Std Dev 3.002
Gaus mean : 36.9046
Gaus Width: 3.5936
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 41.88
Std Dev 3.586
Gaus mean : 42.8119
Gaus Width: 3.6959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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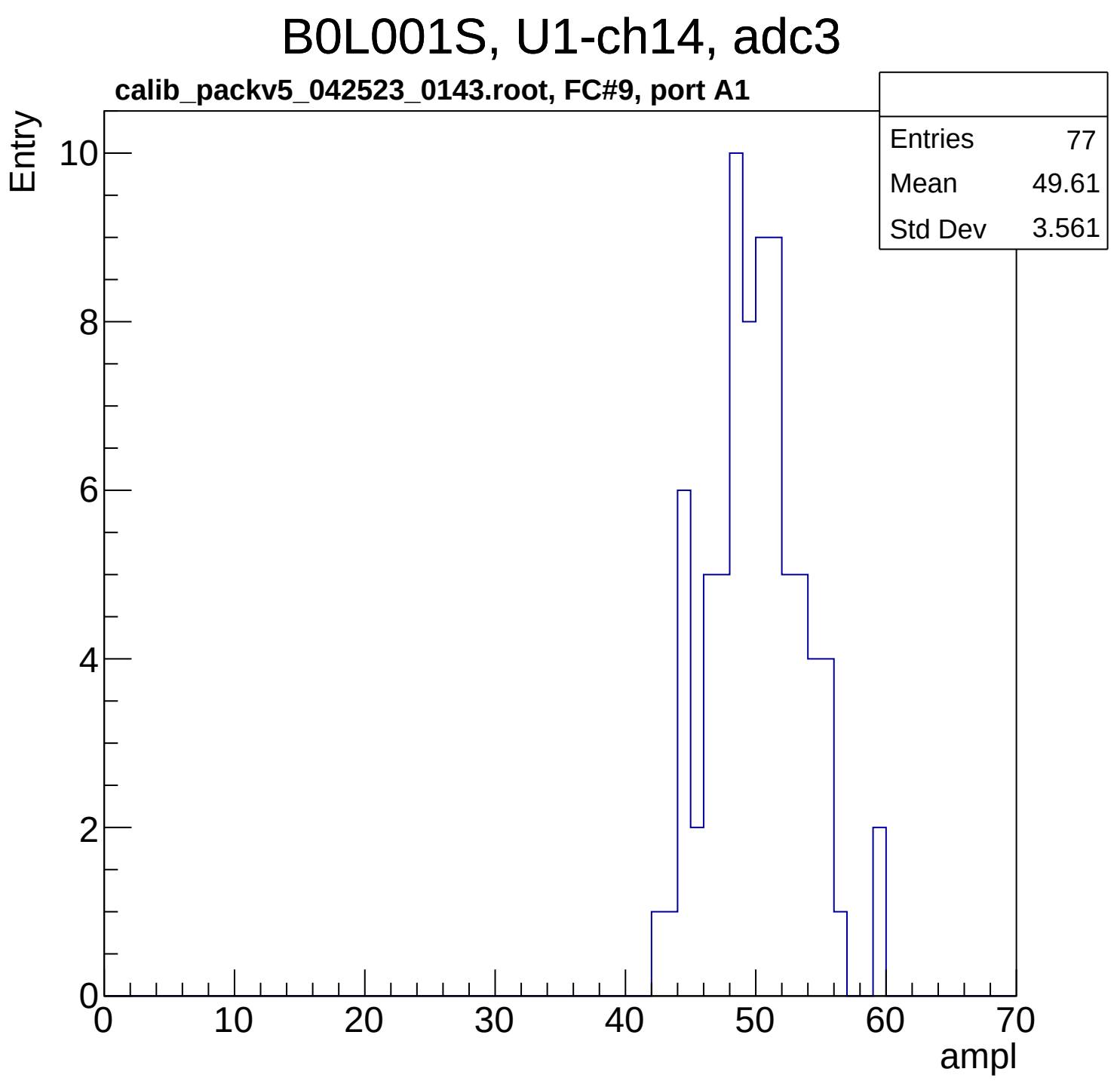
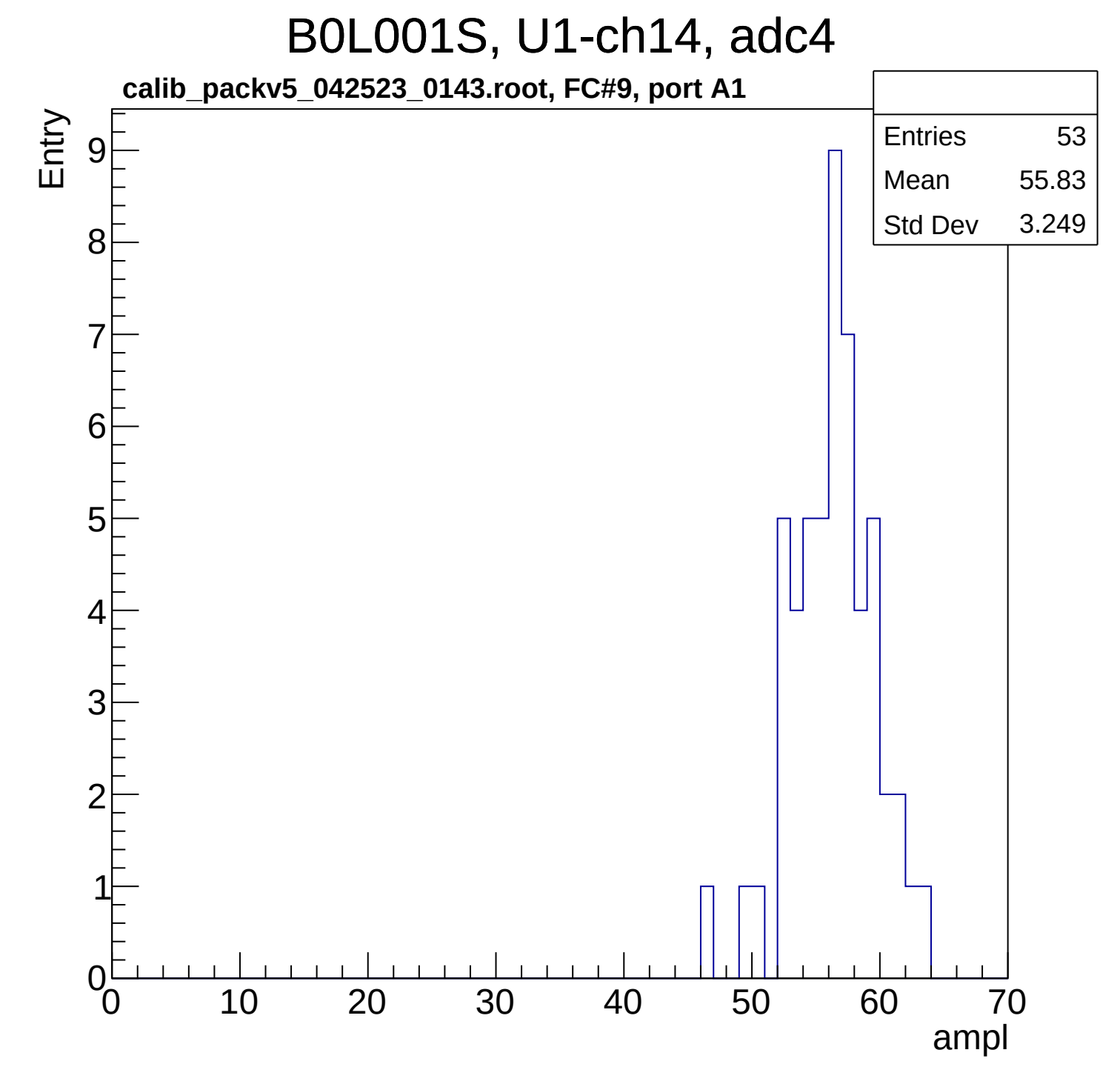

B0L001S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 49.61
Std Dev 3.561
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.83
Std Dev 3.249
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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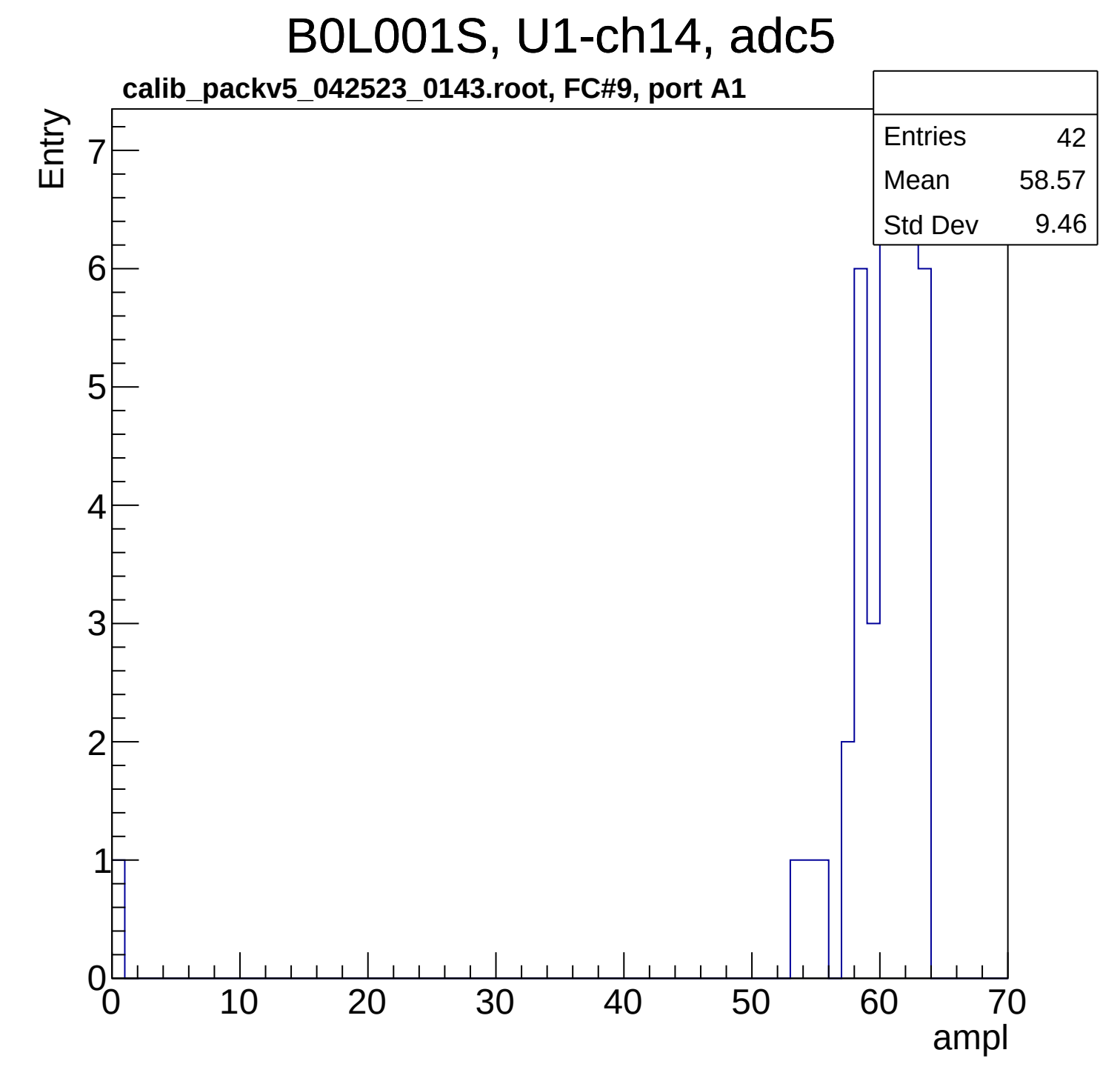
B0L001S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.57
Std Dev 9.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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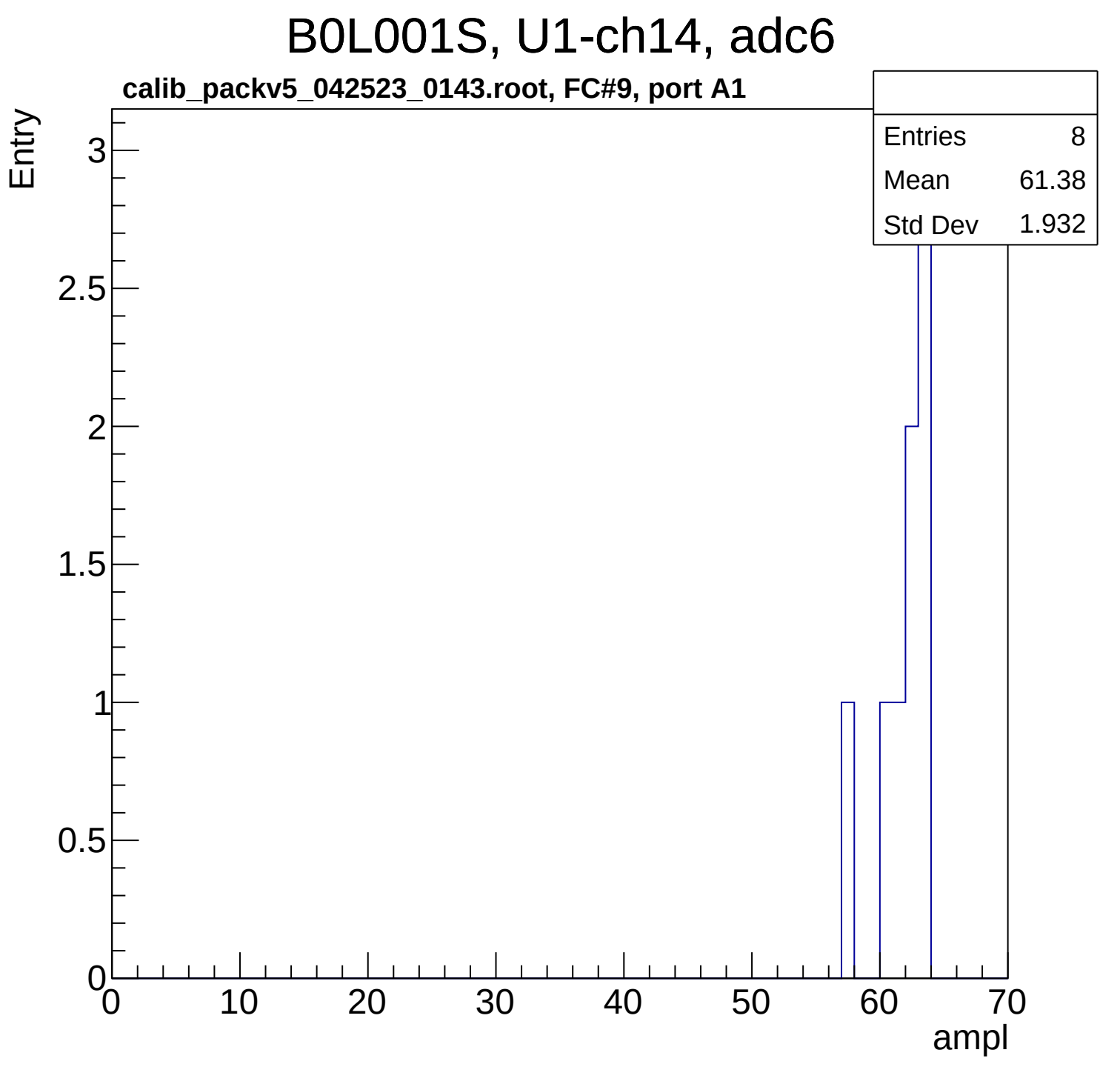
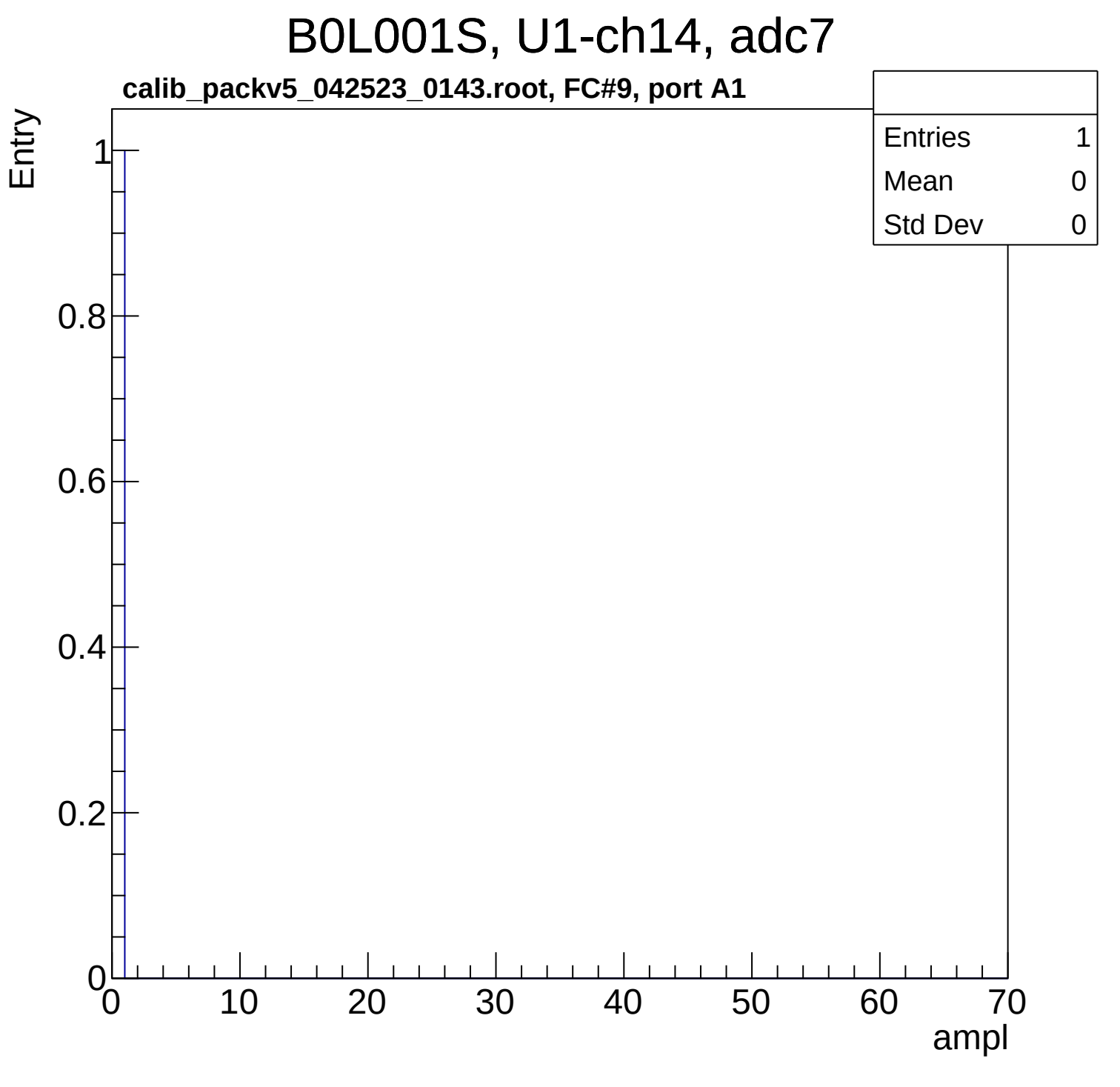
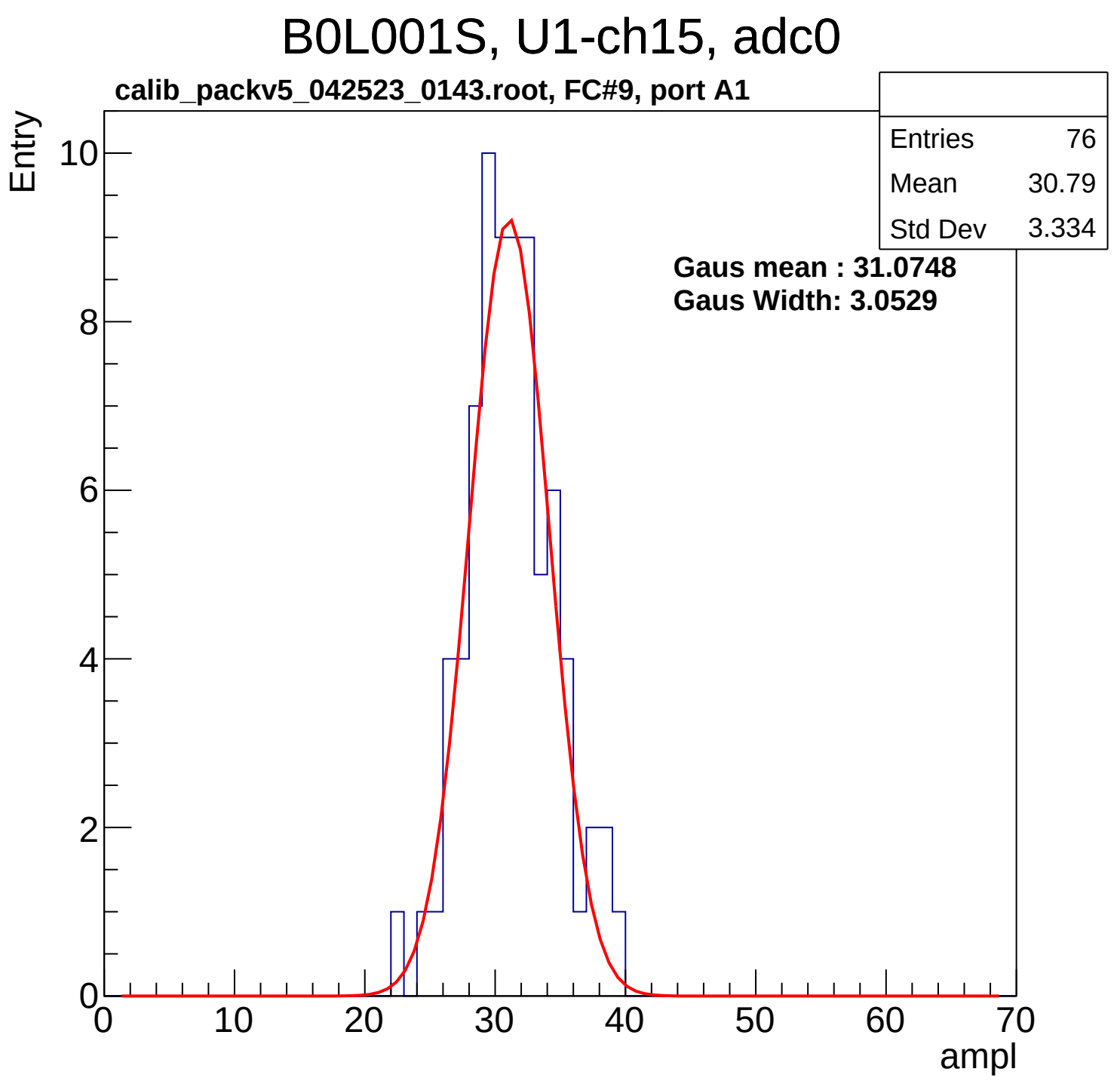
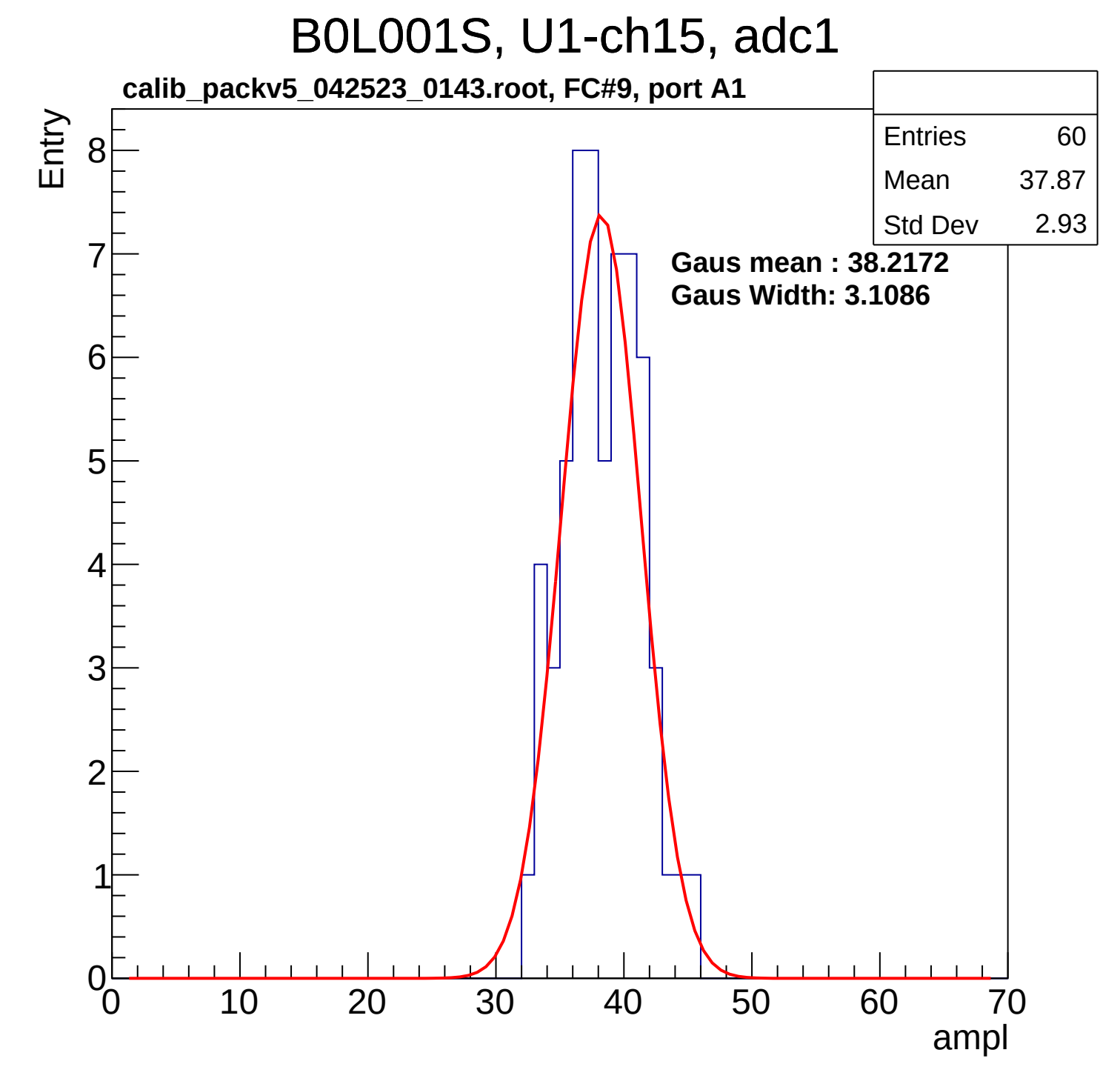

B0L001S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.38
Std Dev 1.932
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 30.79
Std Dev 3.334
Gaus mean : 31.0748
Gaus Width: 3.0529
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.87
Std Dev 2.93
Gaus mean : 38.2172
Gaus Width: 3.1086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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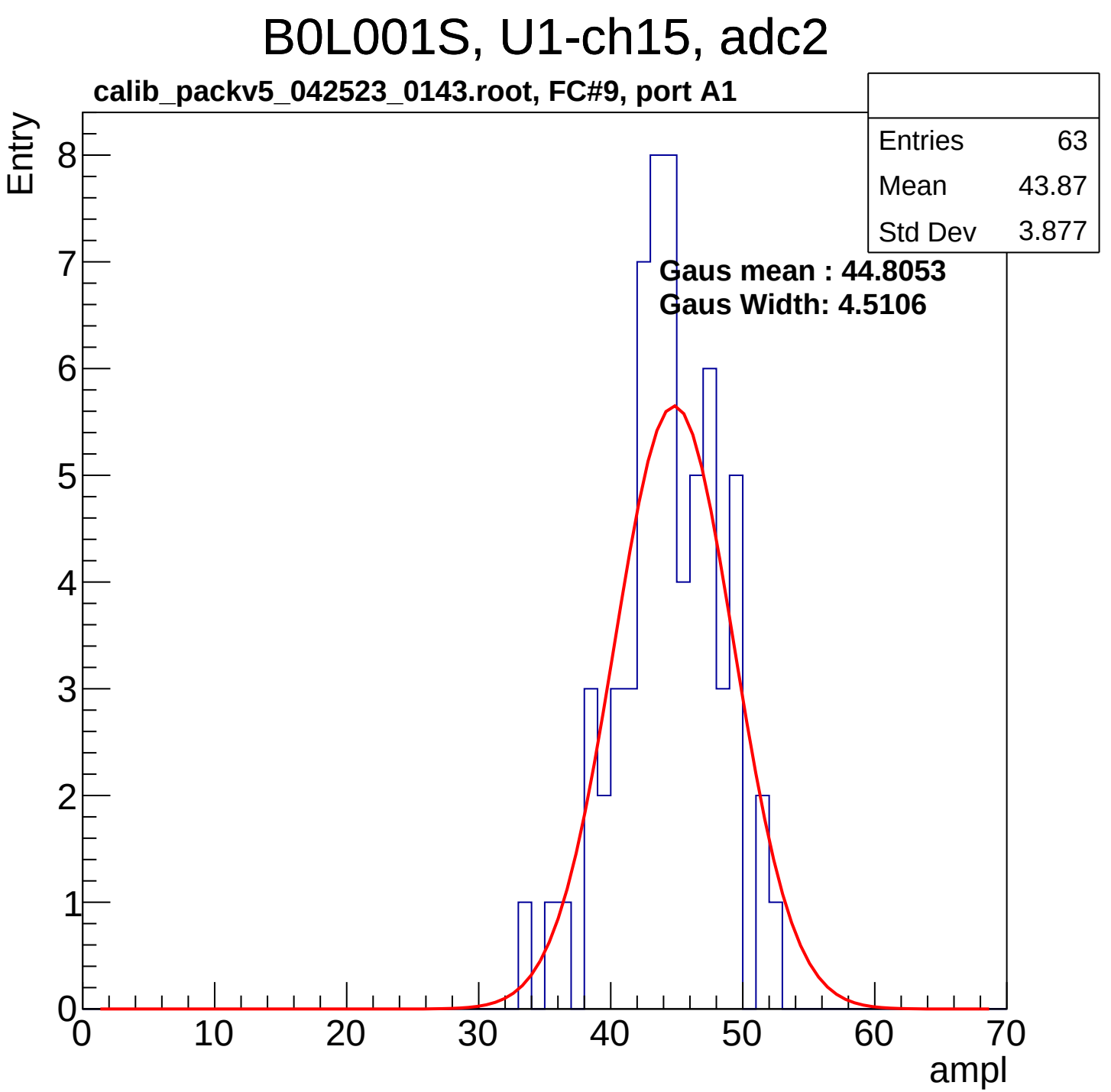
B0L001S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.87
Std Dev 3.877
Gaus mean : 44.8053
Gaus Width: 4.5106
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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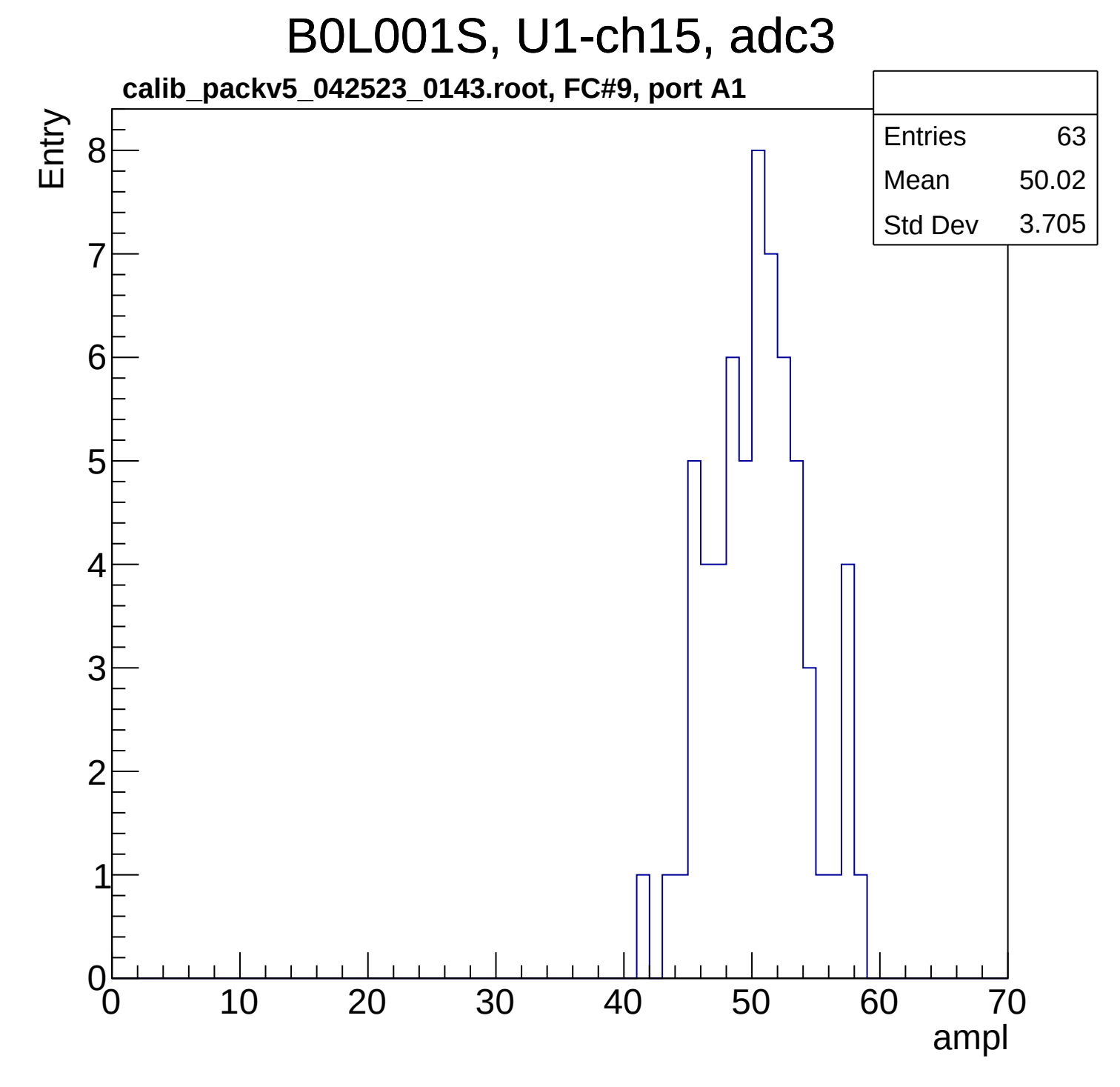
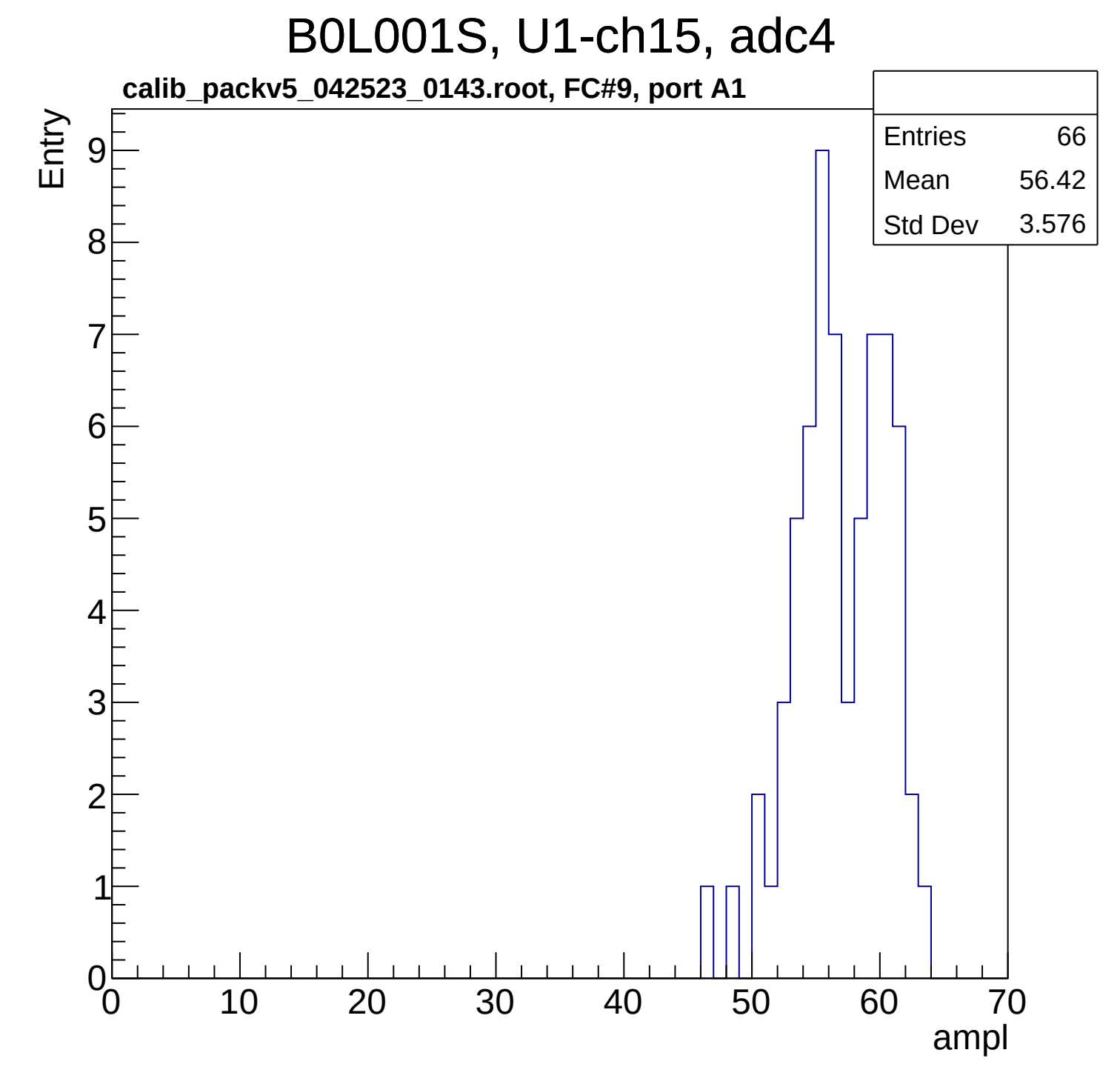
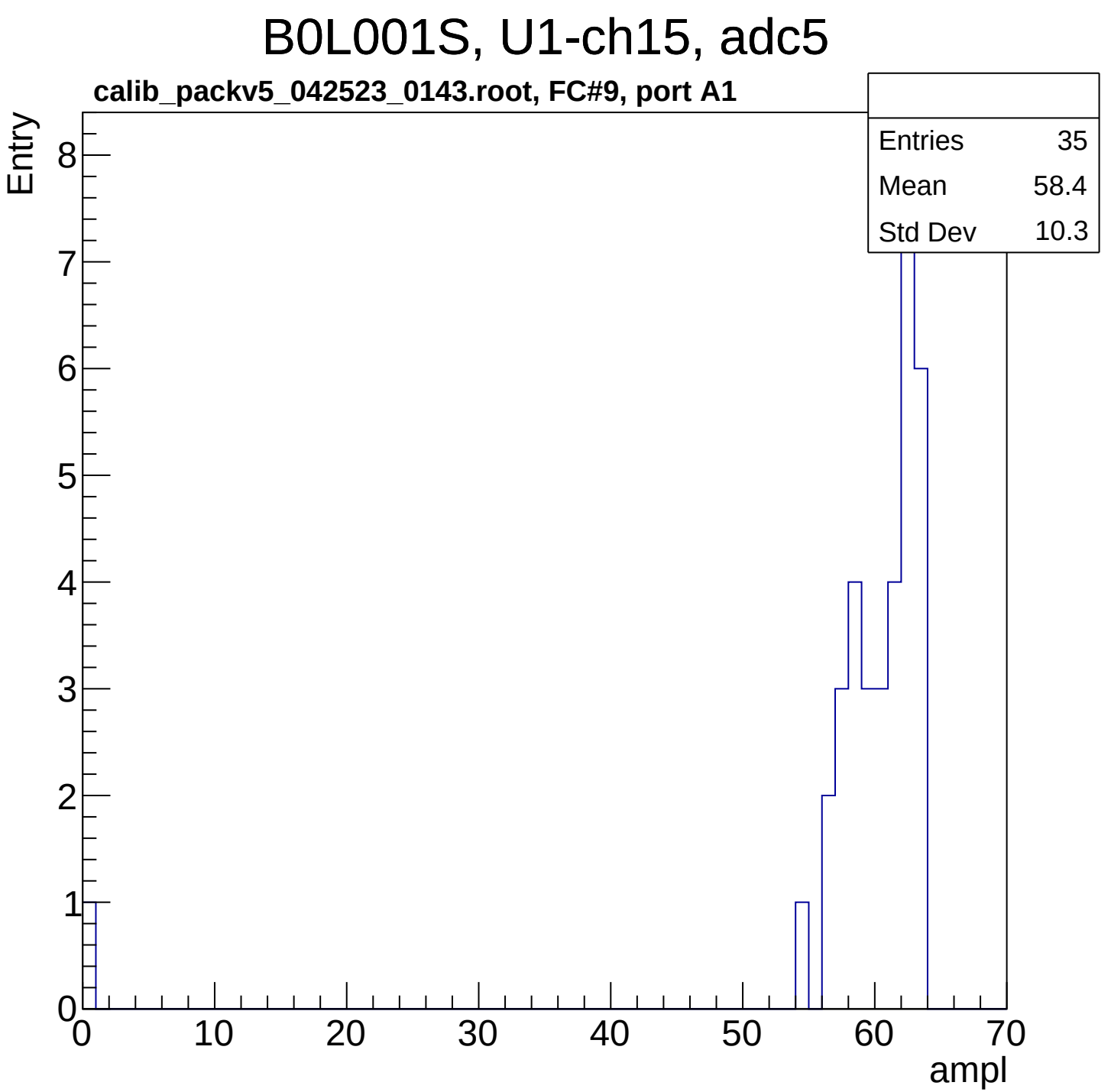
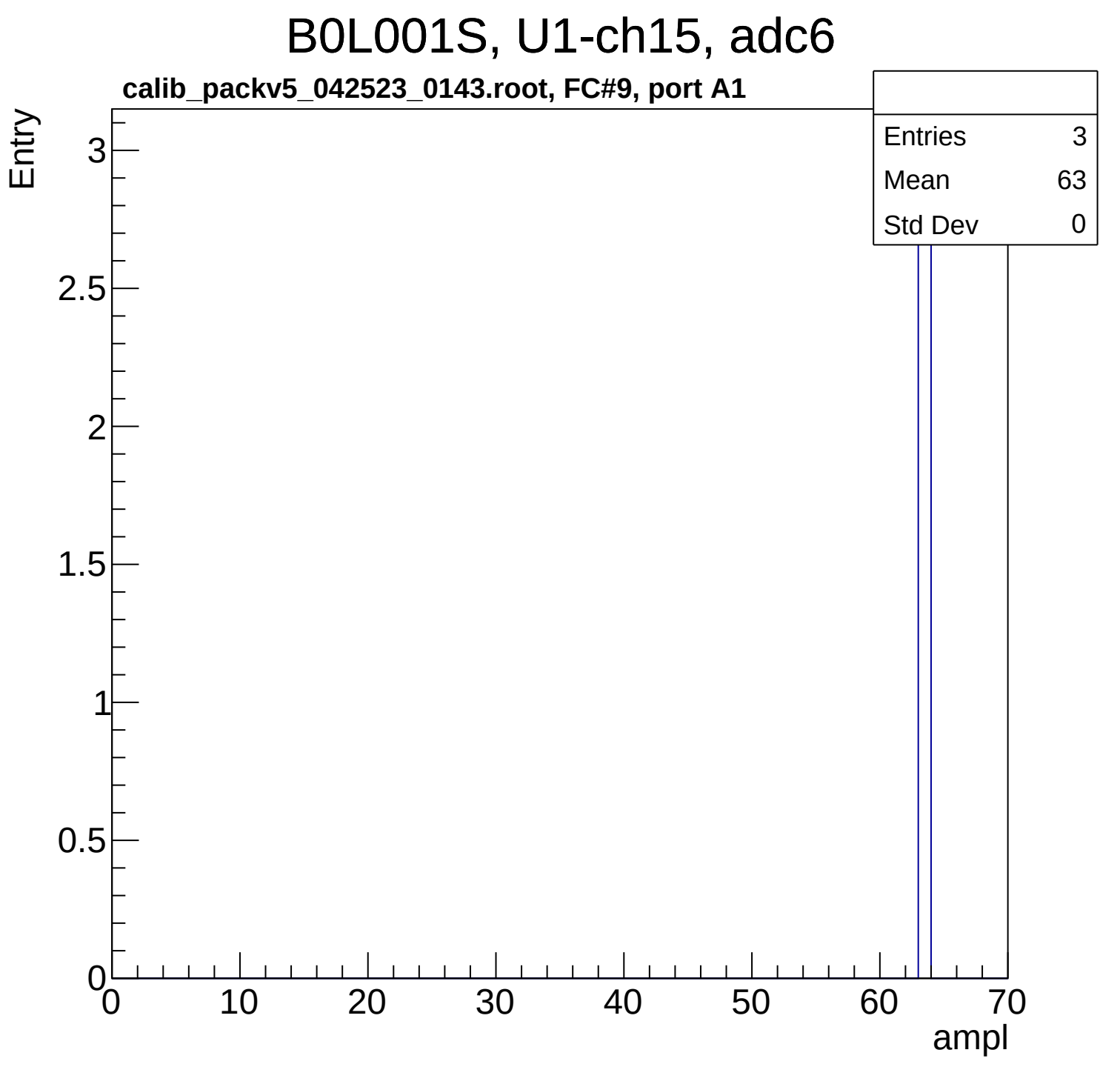
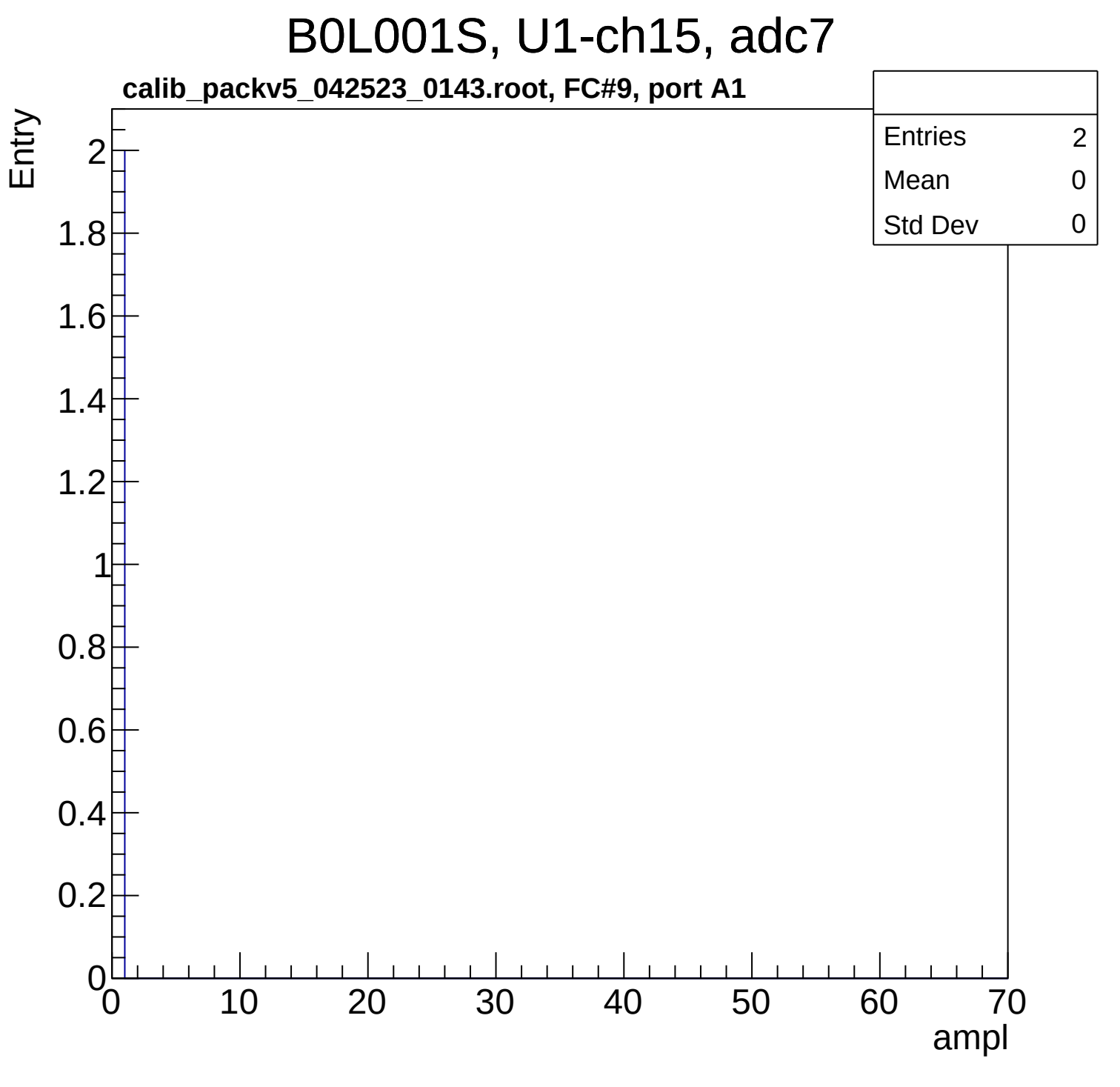
40
50
60
70

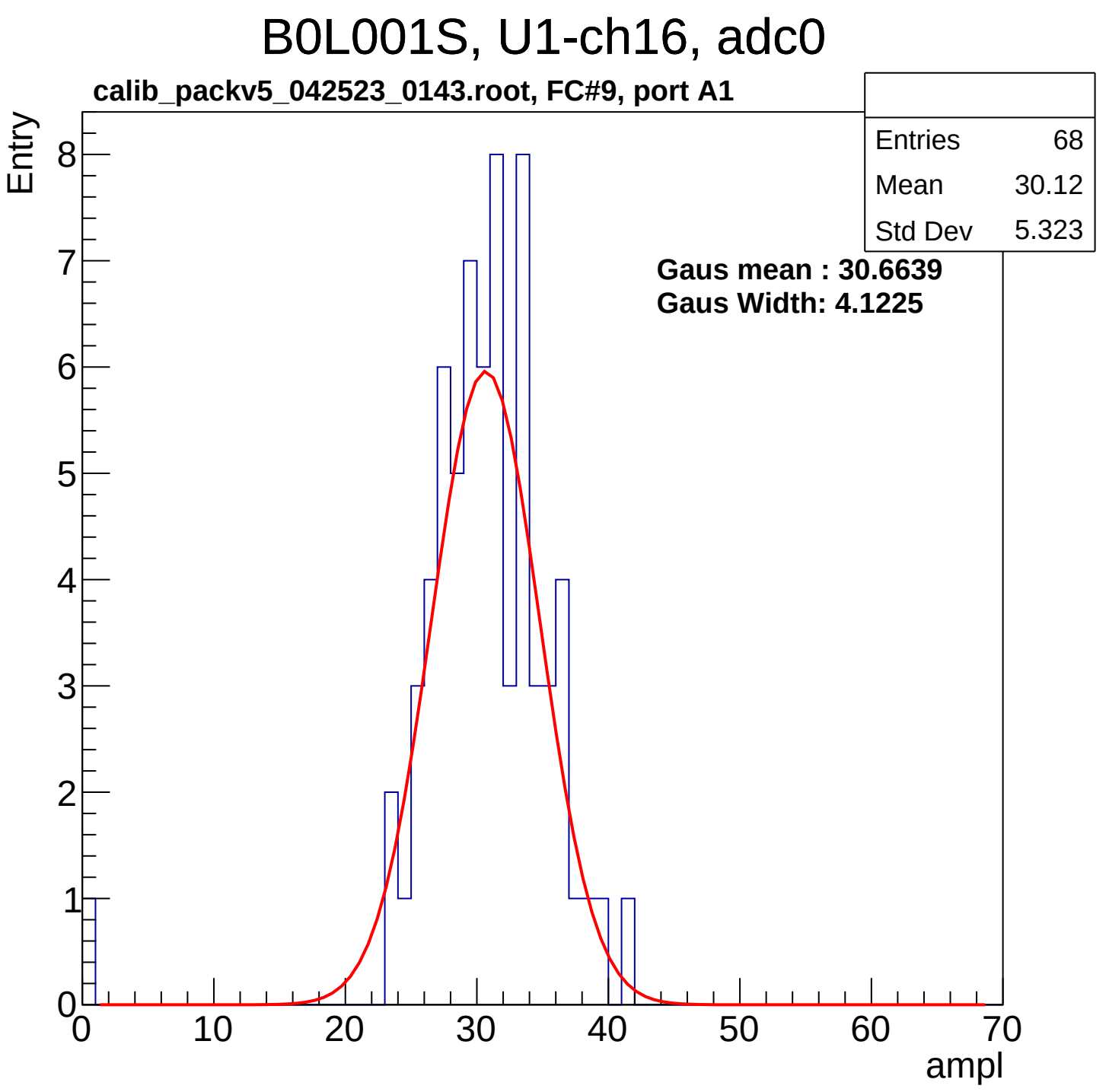

B0L001S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.02
Std Dev 3.705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

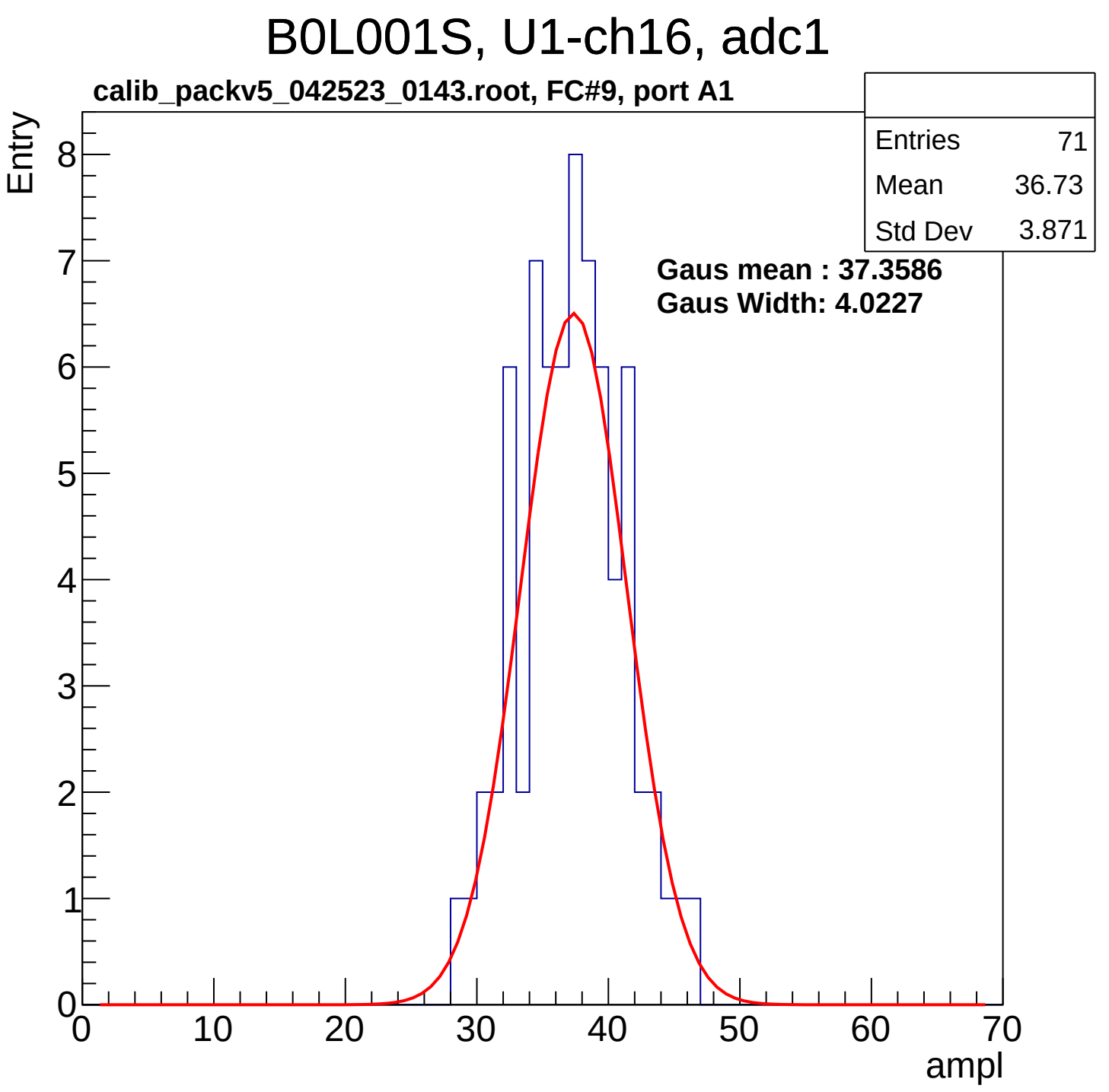

B0L001S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.42
Std Dev 3.576
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

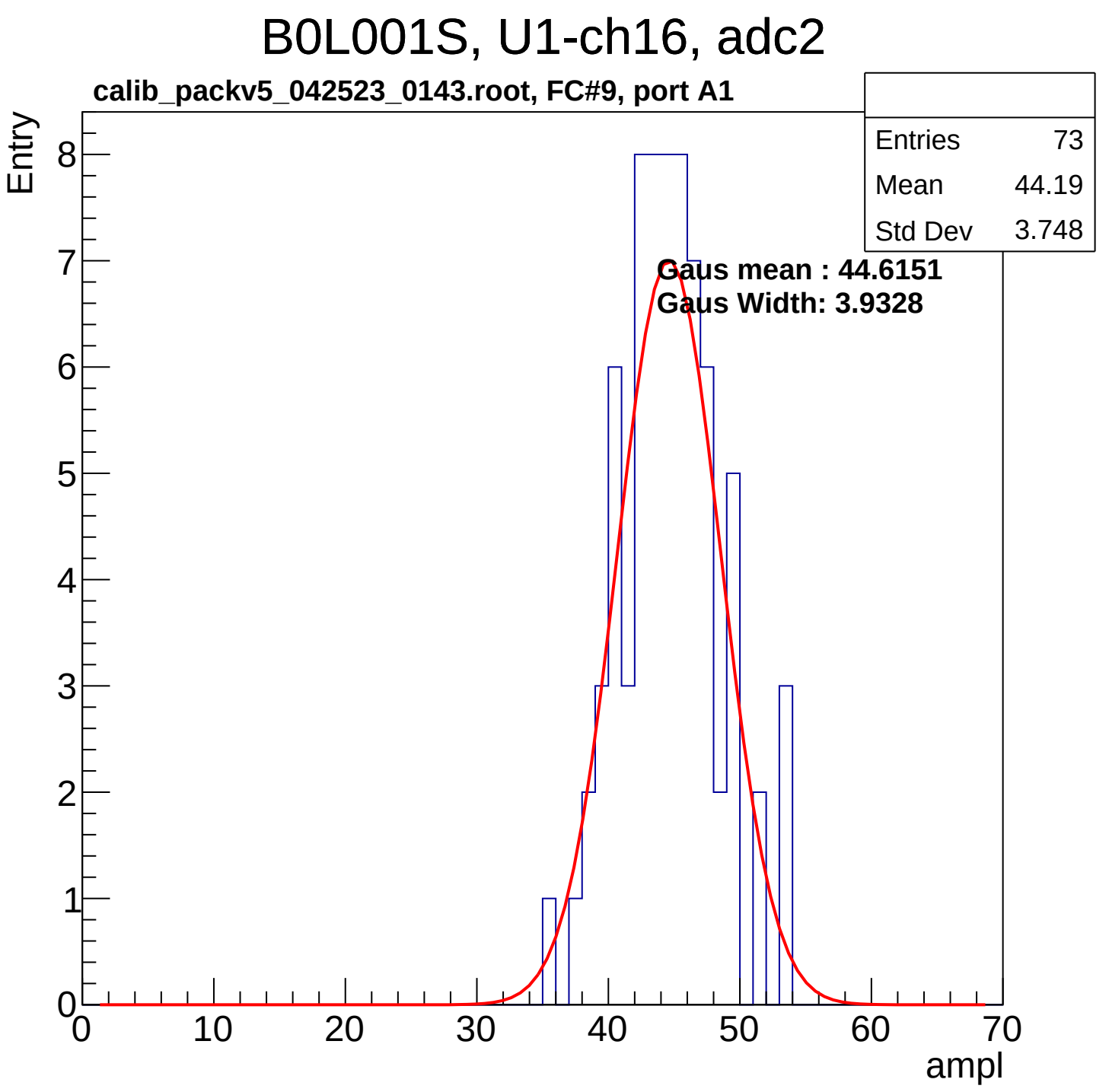

B0L001S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.4
Std Dev 10.3
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

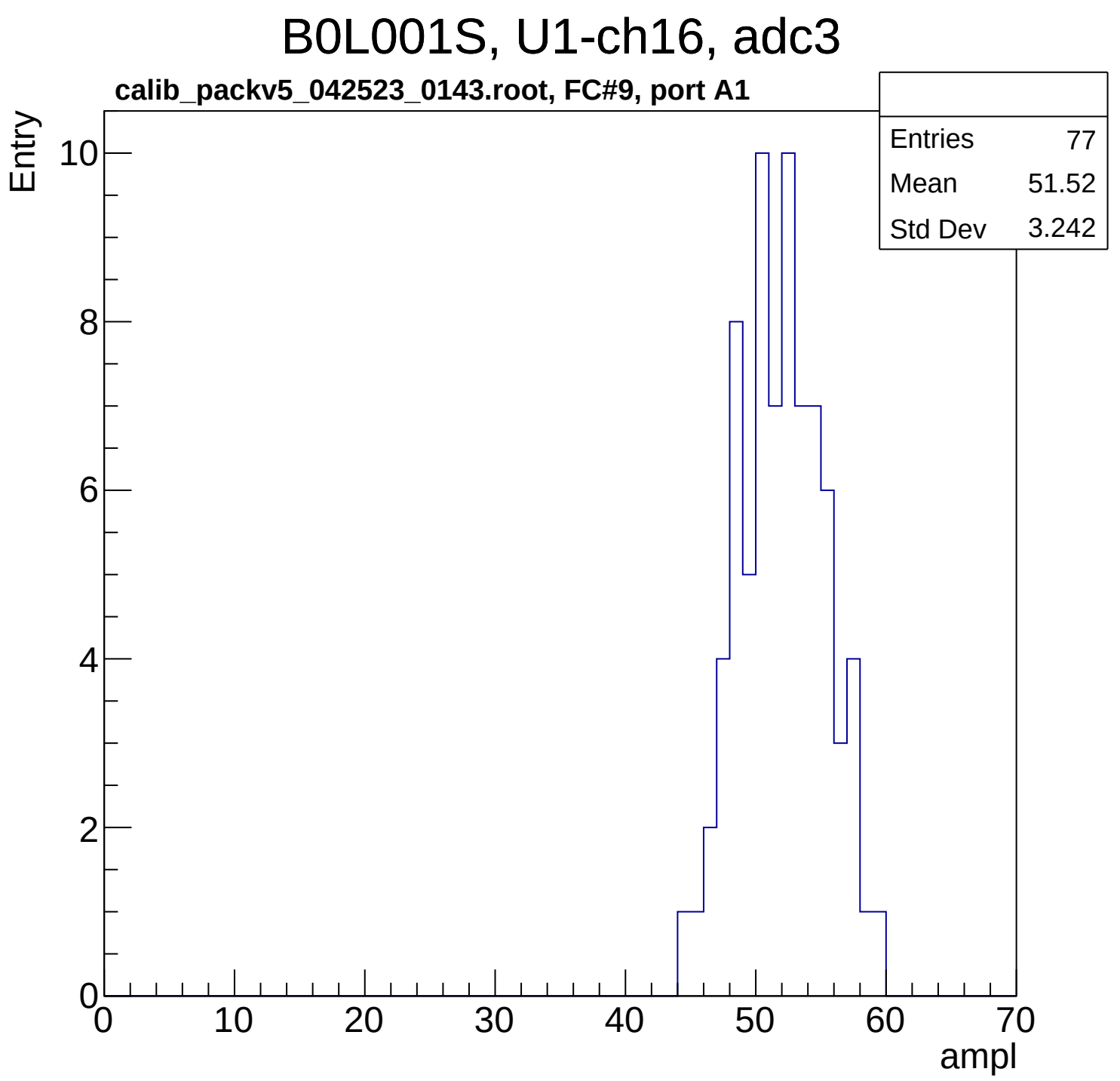

B0L001S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

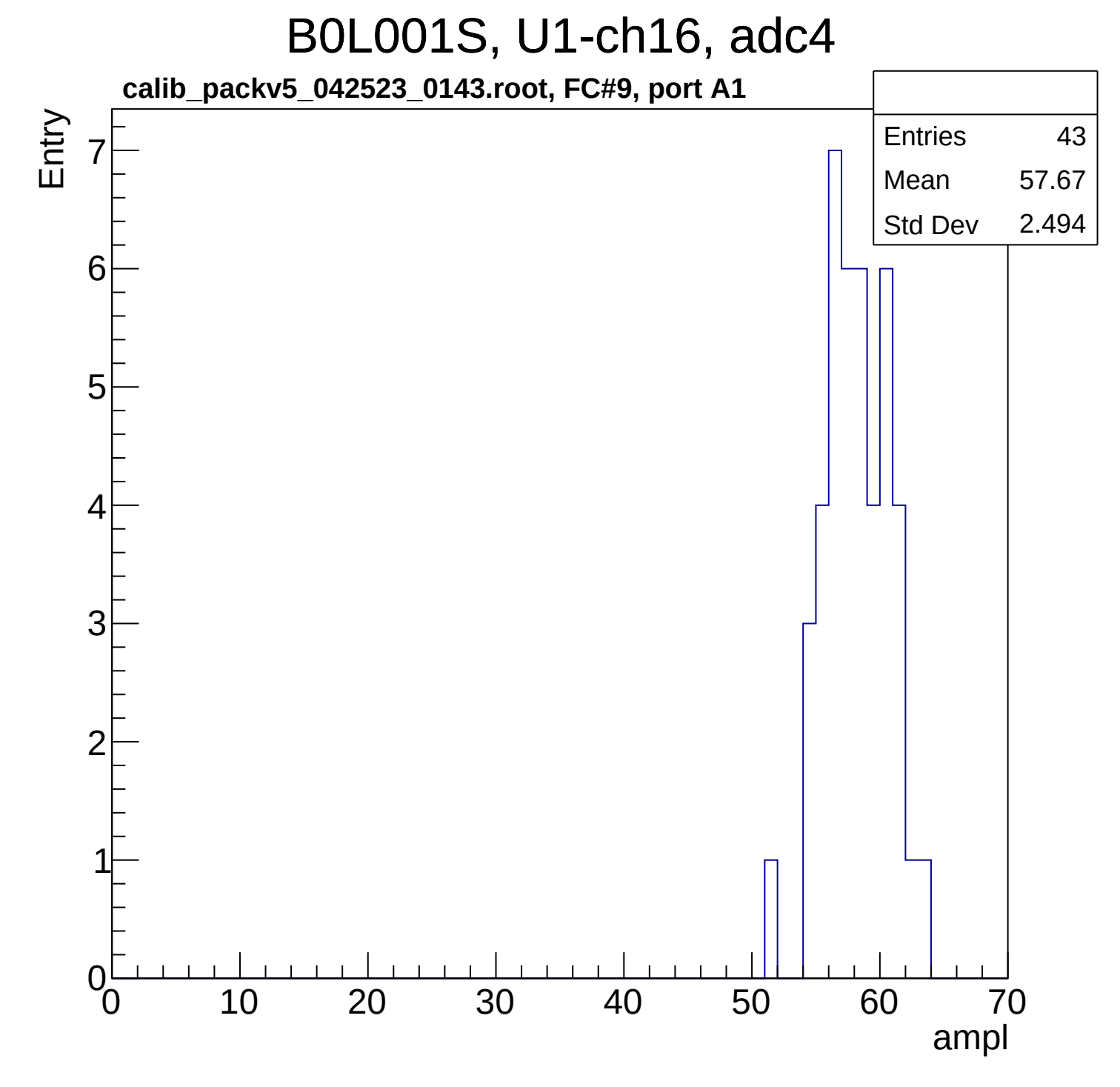

B0L001S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

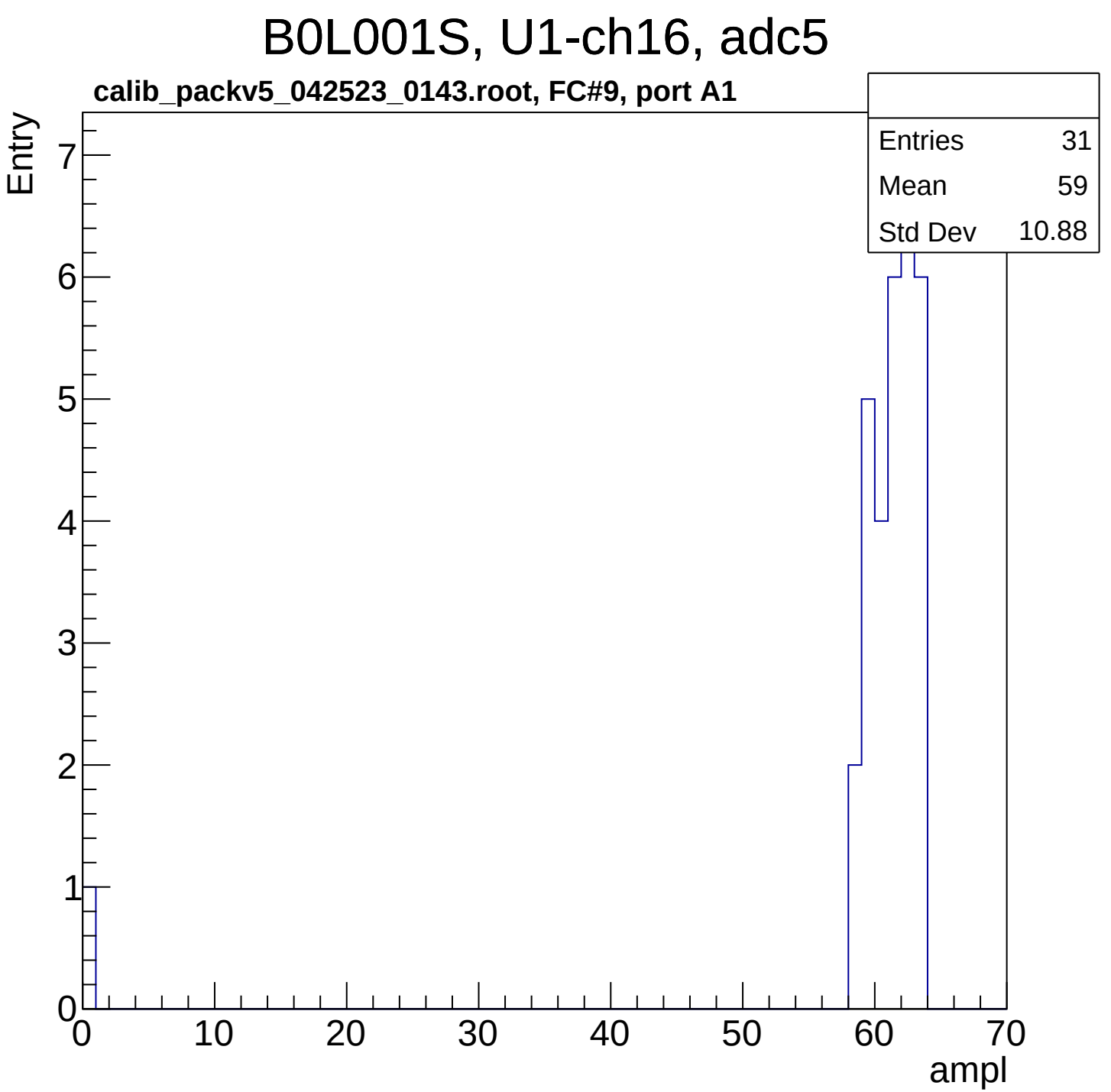

B0L001S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 30.12
Std Dev 5.323
Gaus mean : 30.6639
Gaus Width: 4.1225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

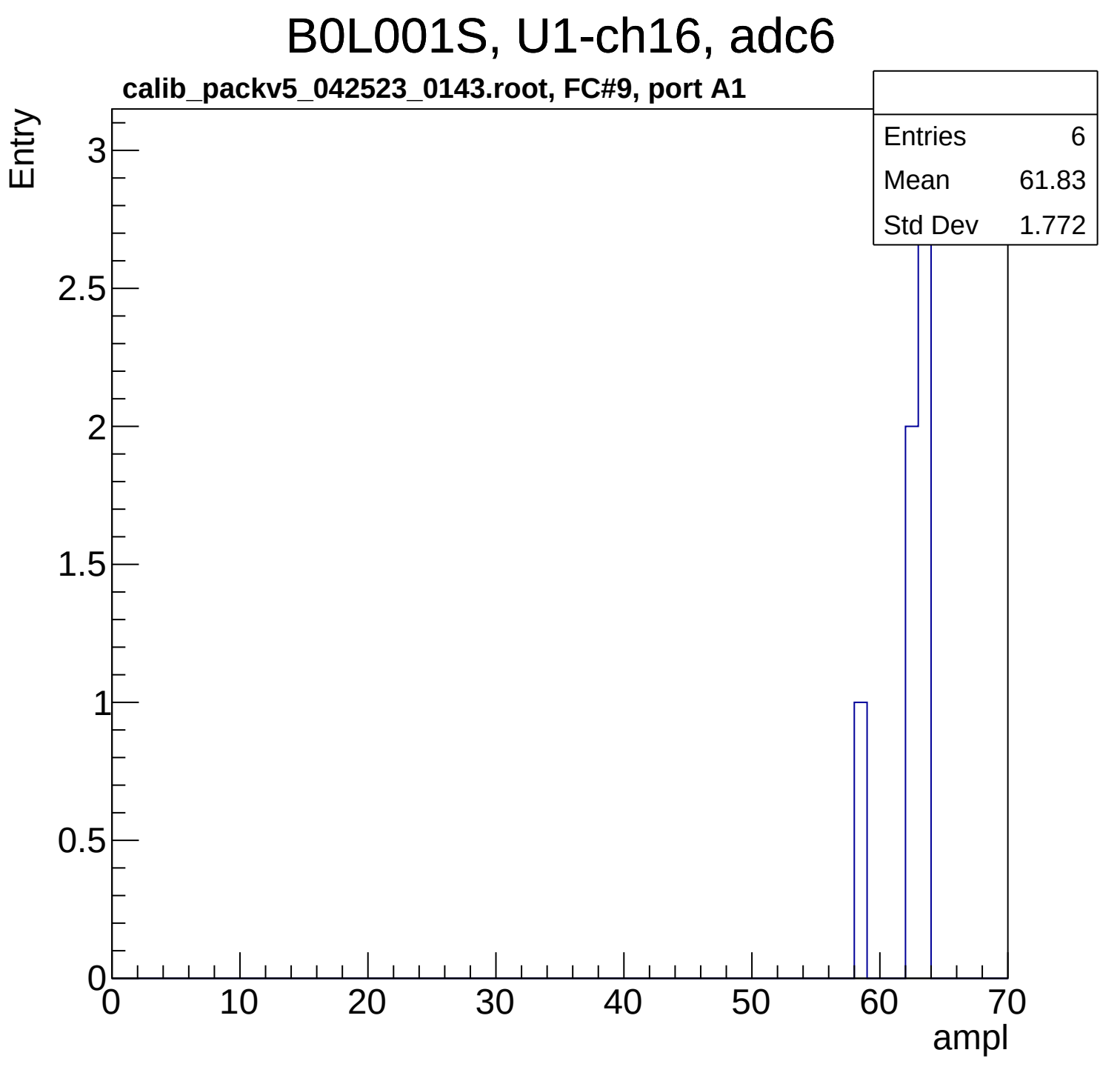

B0L001S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 36.73
Std Dev 3.871
Gaus mean : 37.3586
Gaus Width: 4.0227
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

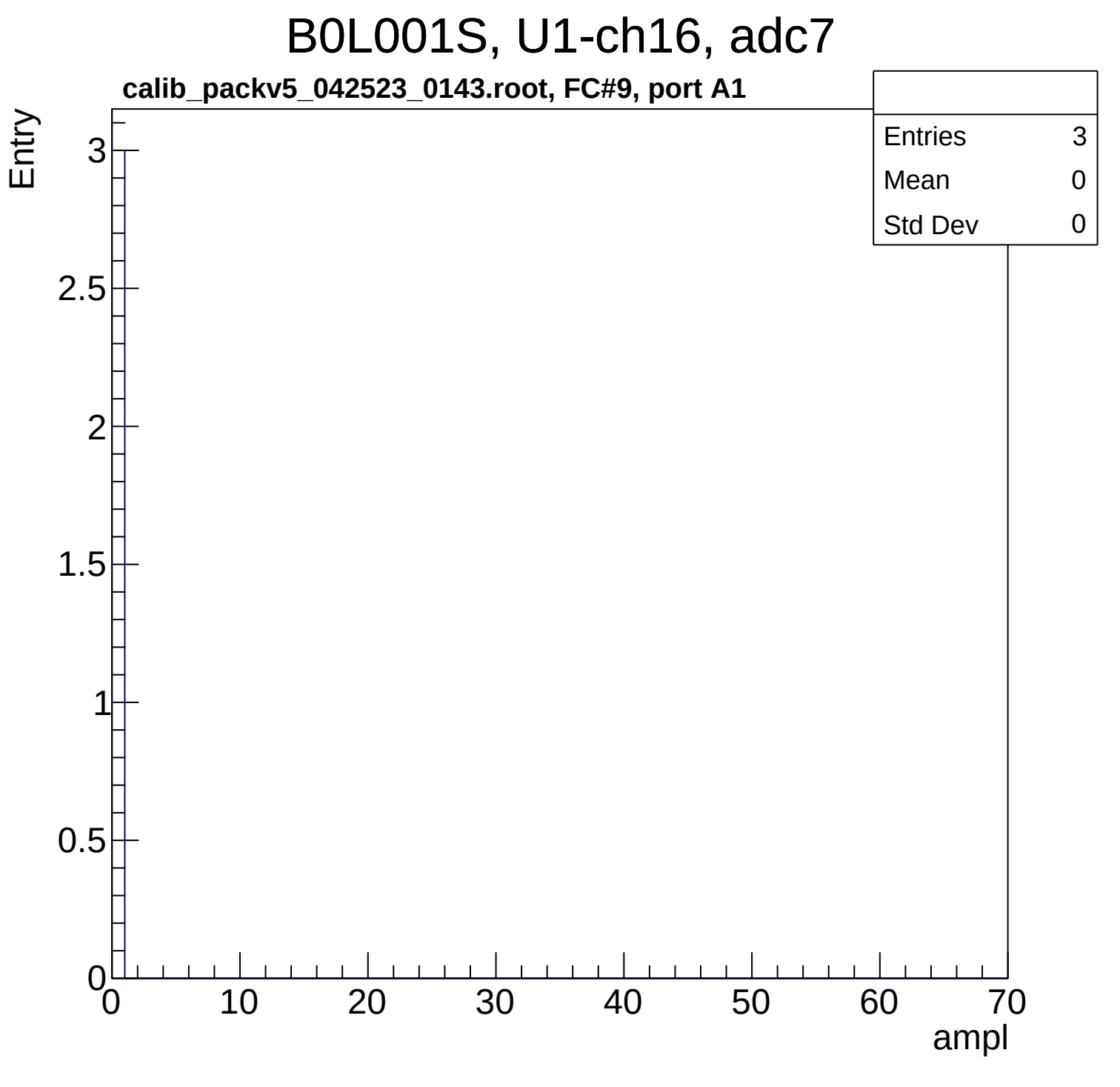

B0L001S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 44.19
Std Dev 3.748
Gaus mean : 44.6151
Gaus Width: 3.9328
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

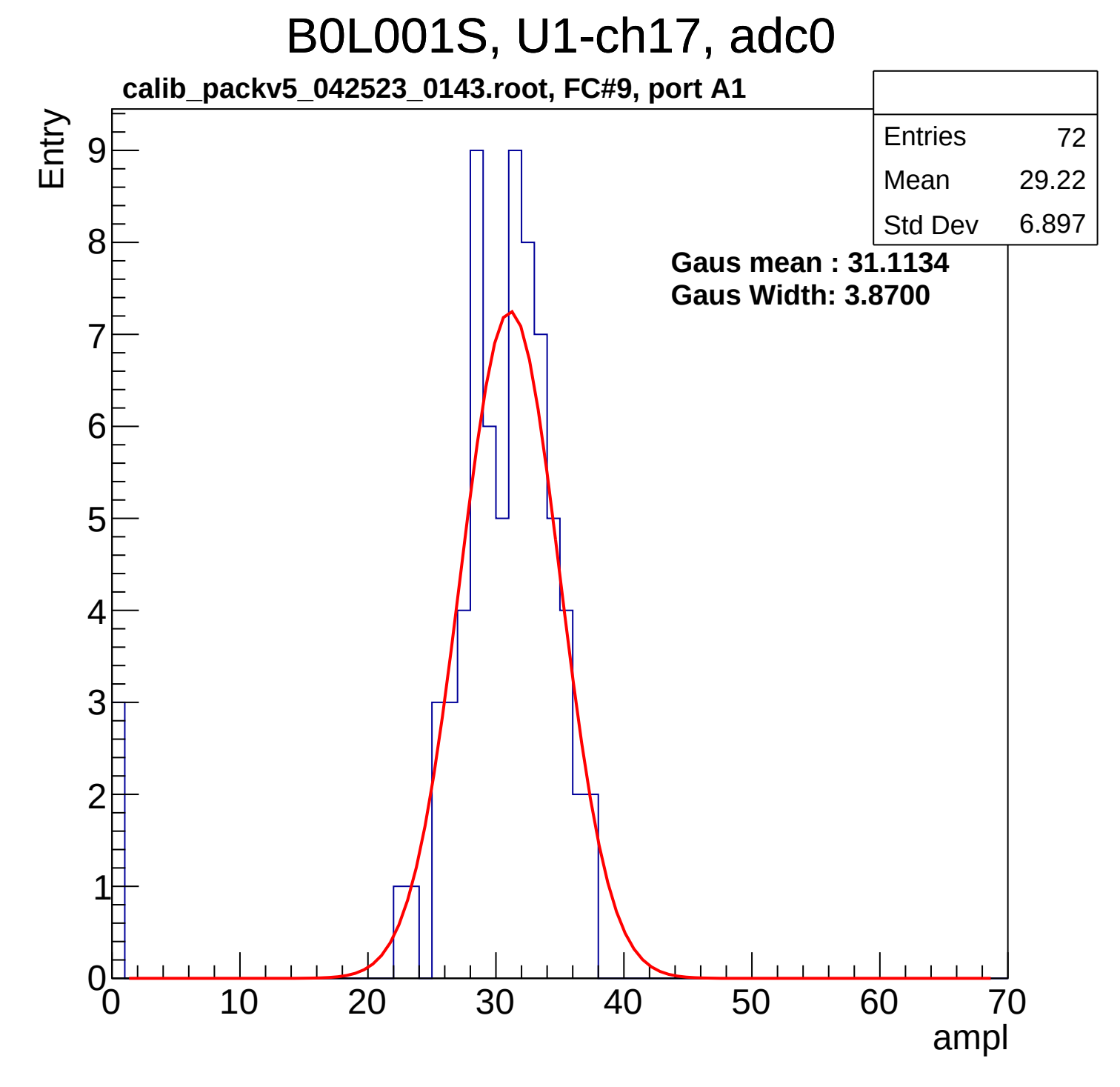

B0L001S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.52
Std Dev 3.242
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

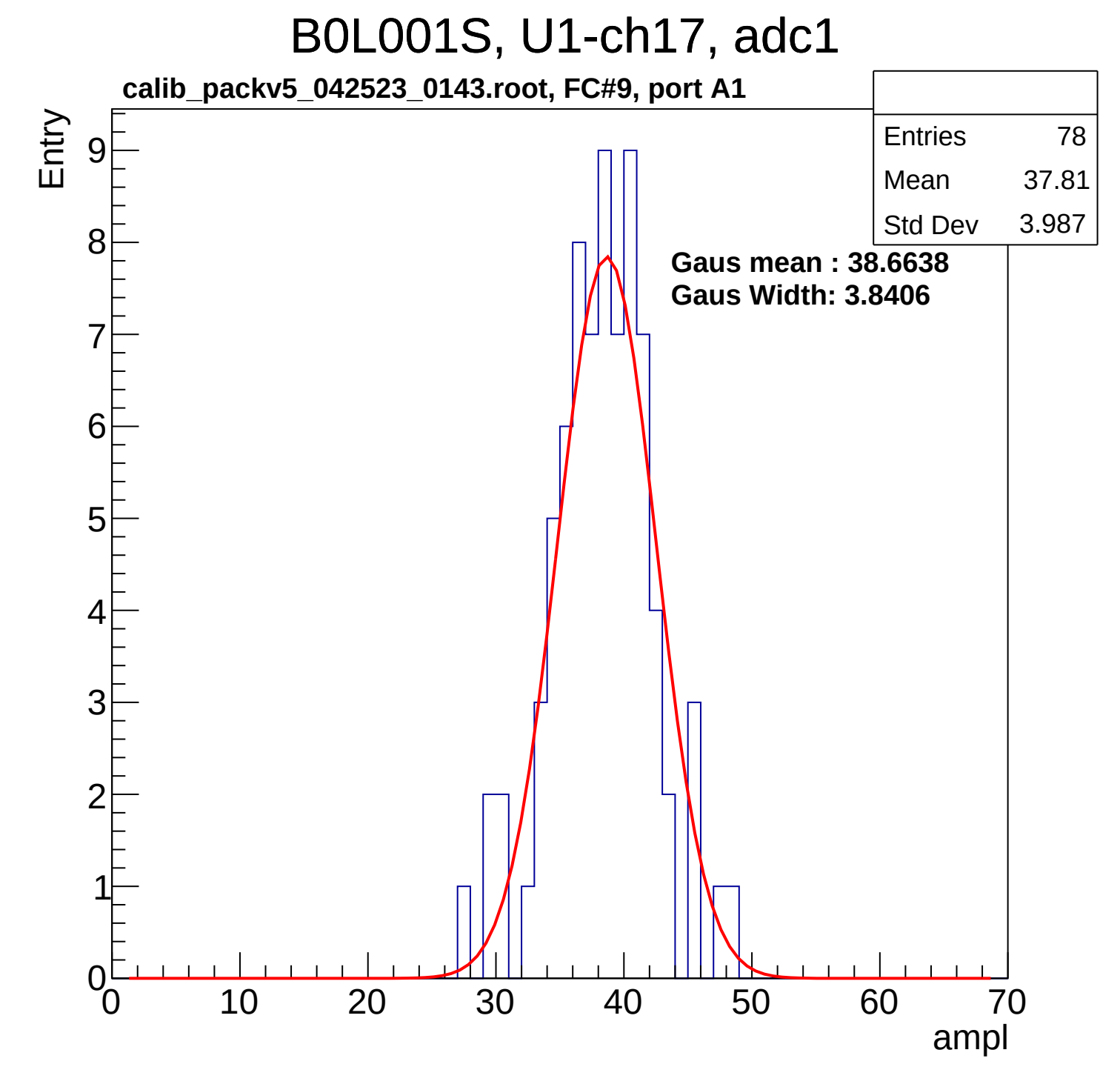

B0L001S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.67
Std Dev 2.494
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

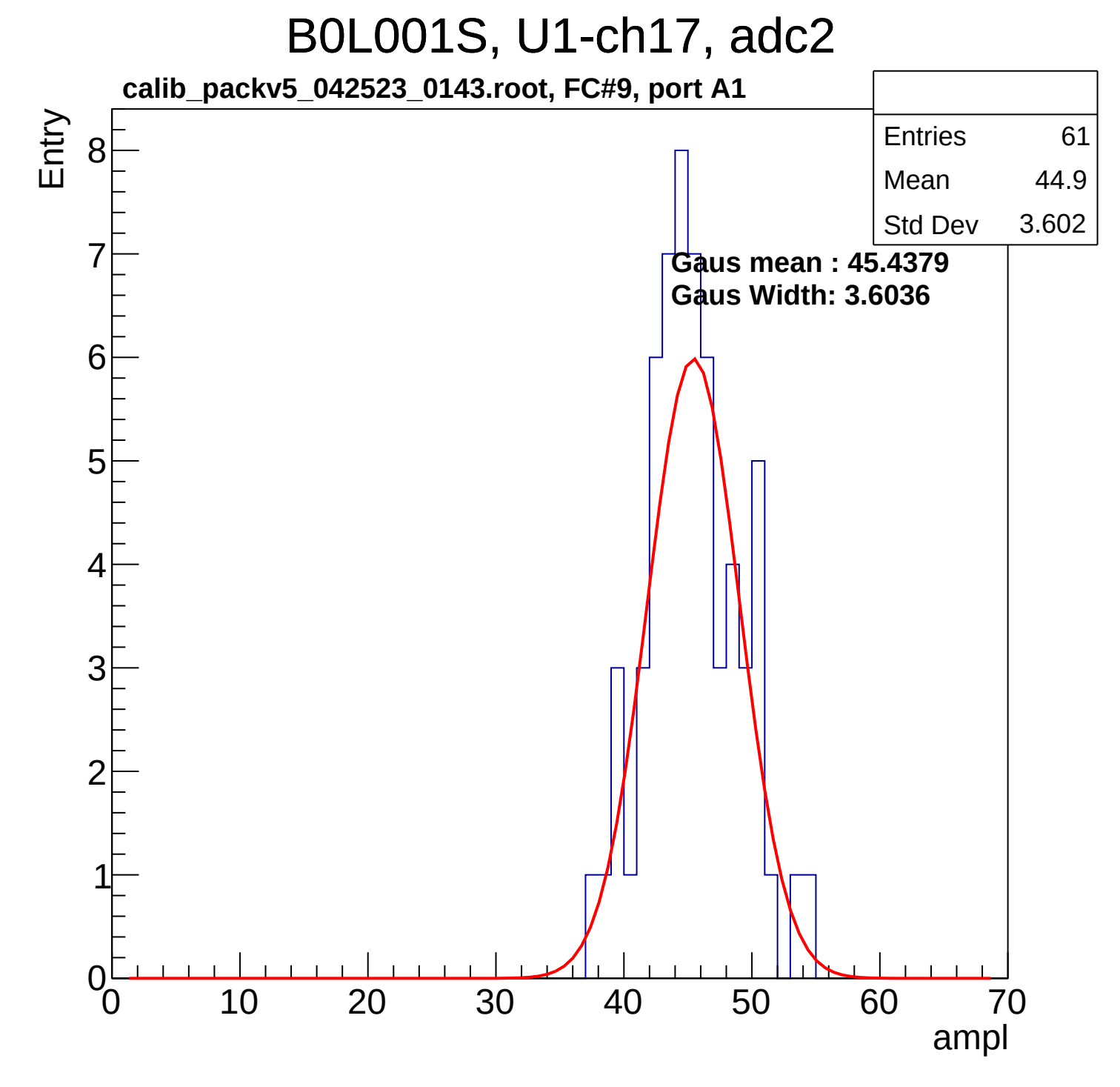

B0L001S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59
Std Dev 10.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

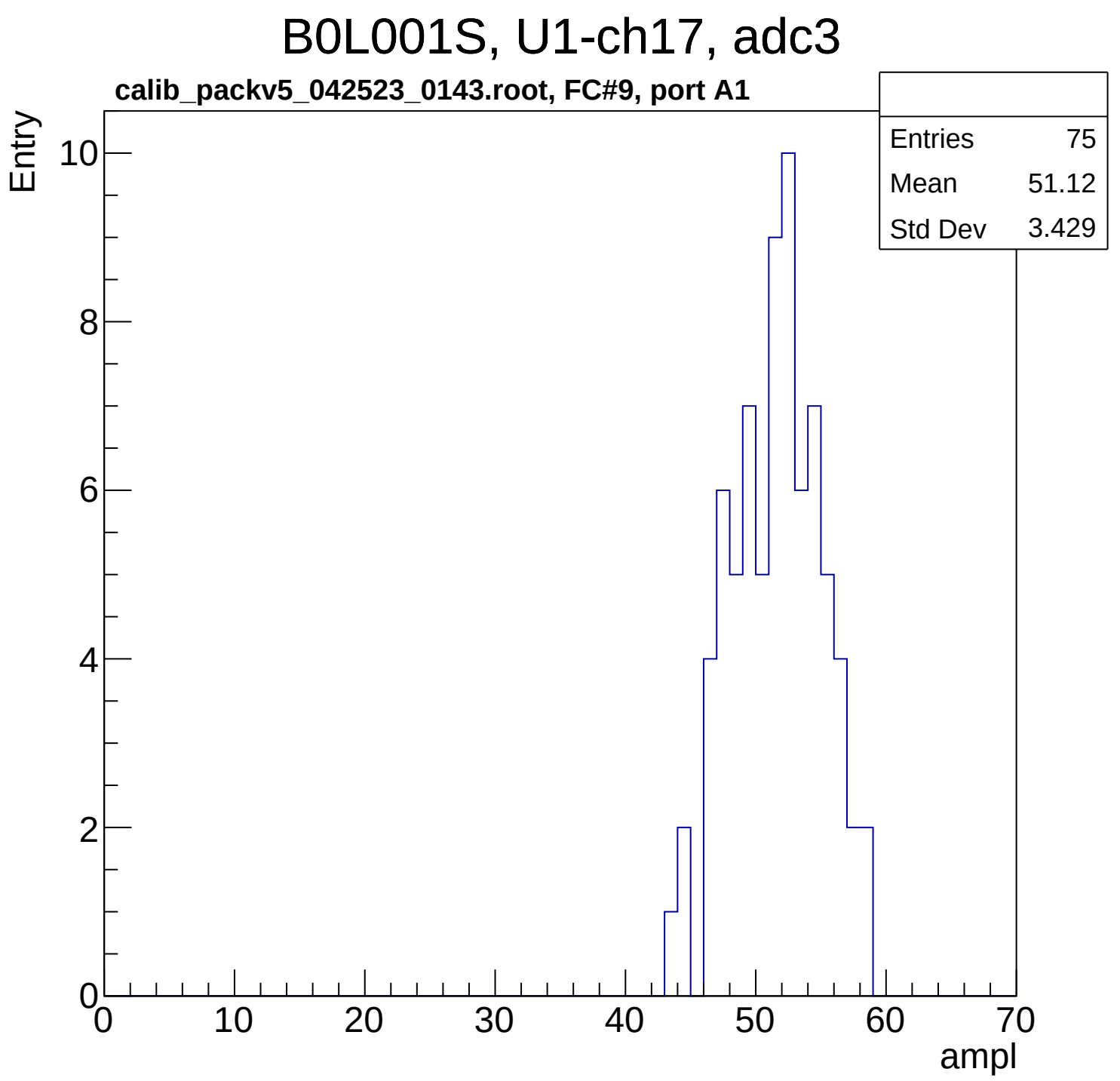

B0L001S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.83
Std Dev 1.772
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

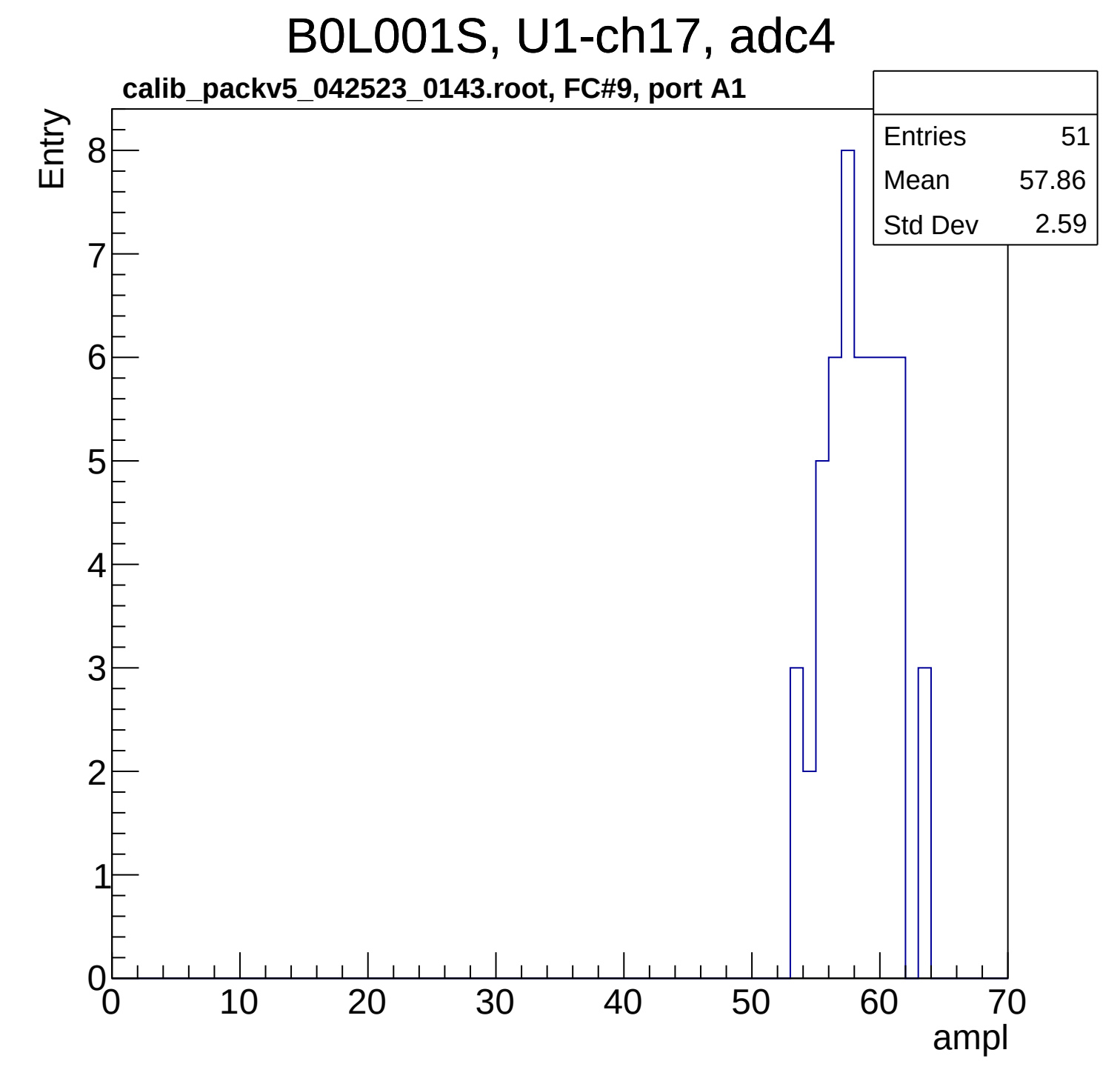

B0L001S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

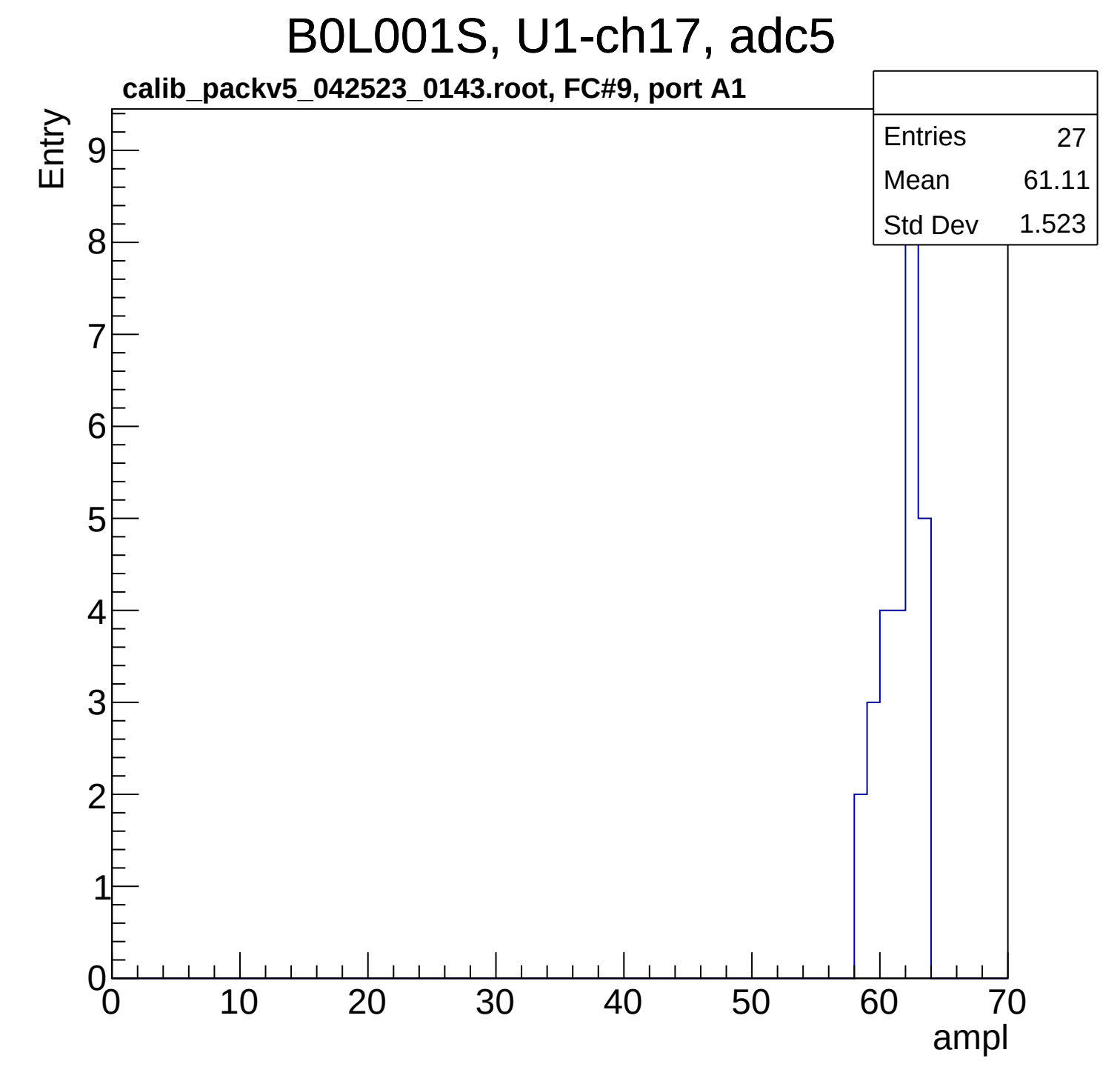

B0L001S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.22
Std Dev 6.897
Gaus mean : 31.1134
Gaus Width: 3.8700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

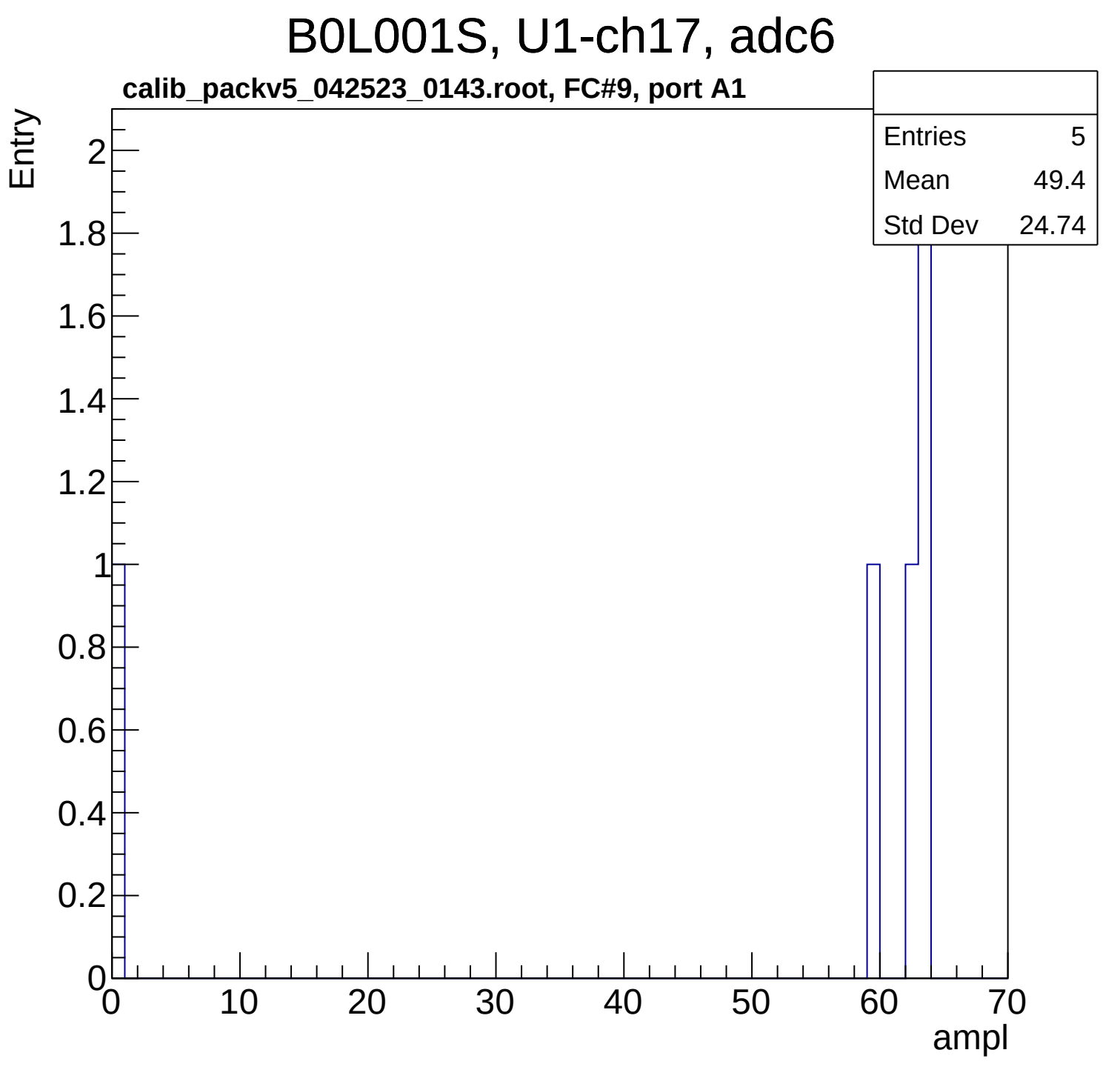

B0L001S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.81
Std Dev 3.987
Gaus mean : 38.6638
Gaus Width: 3.8406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

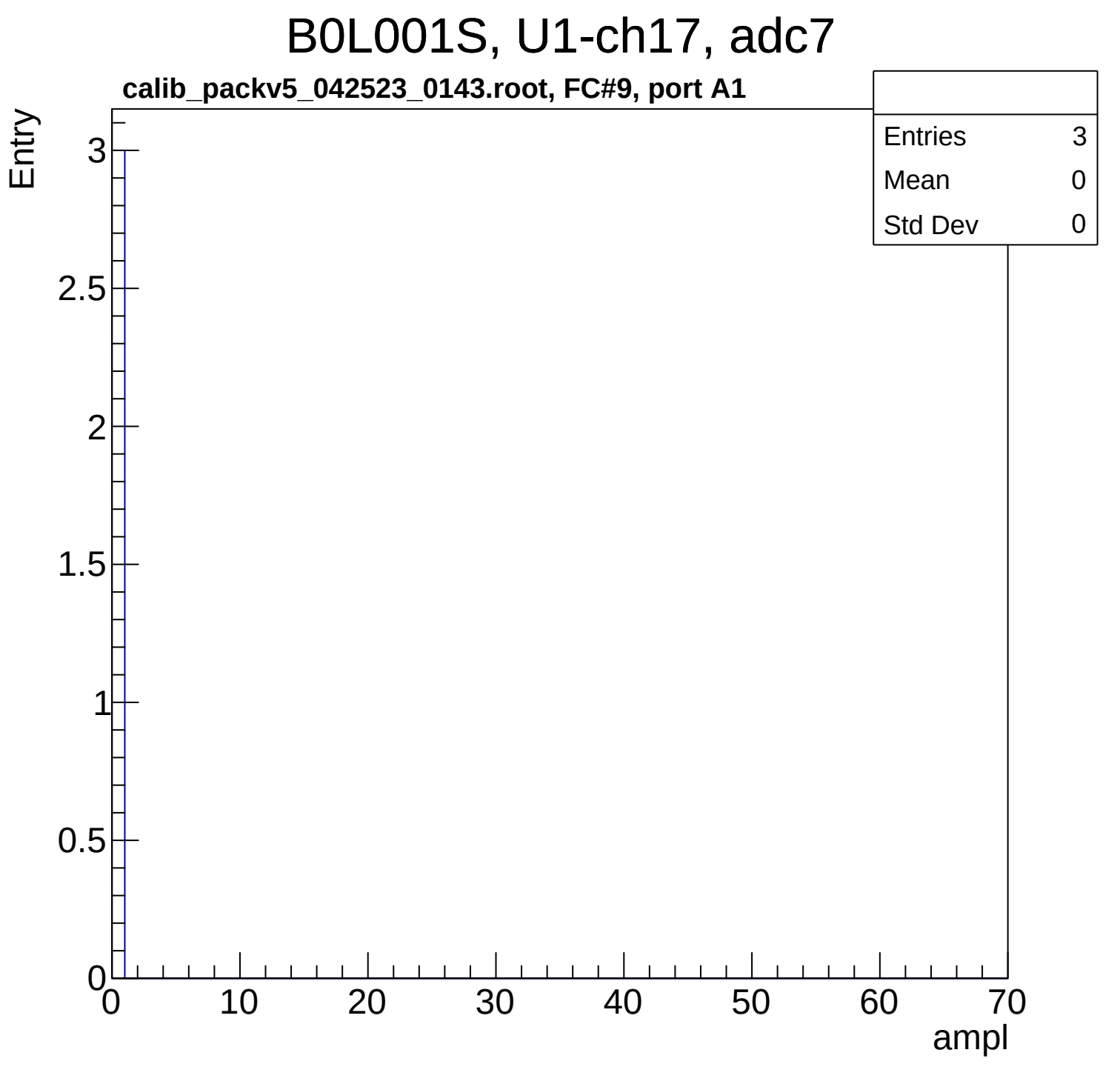

B0L001S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.9
Std Dev 3.602
Gaus mean : 45.4379
Gaus Width: 3.6036
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

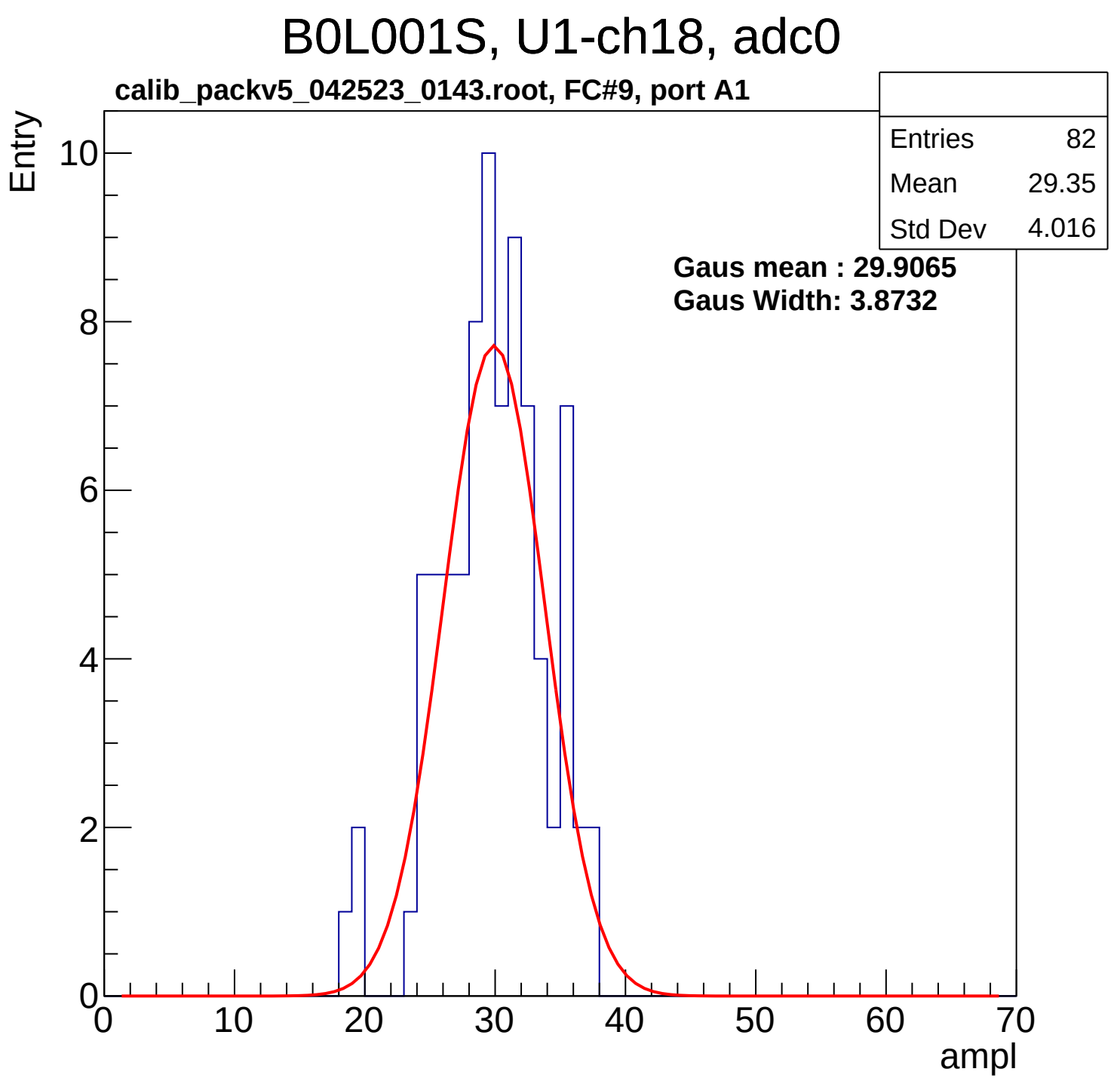

B0L001S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 51.12
Std Dev 3.429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

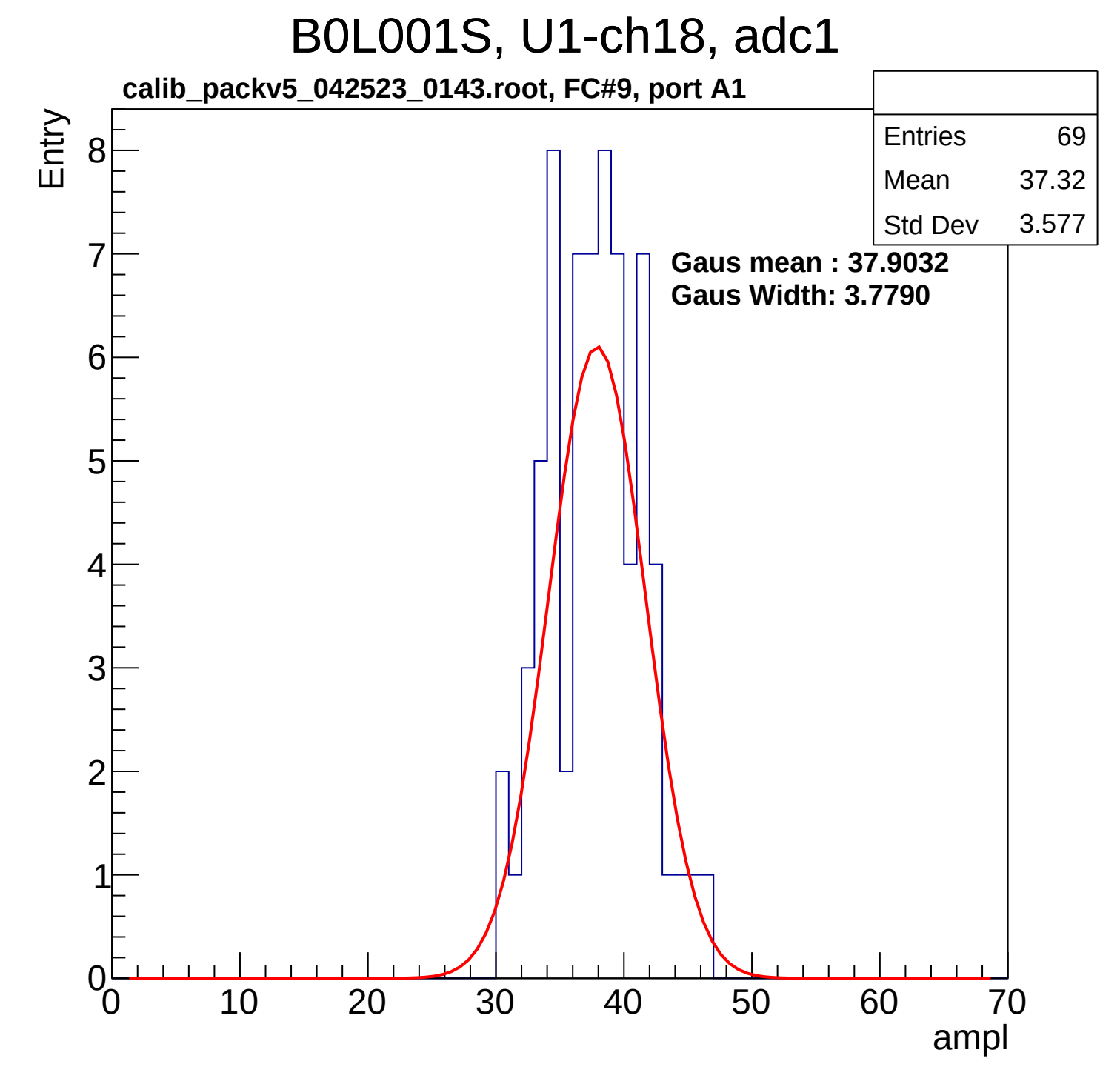

B0L001S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 57.86
Std Dev 2.59
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

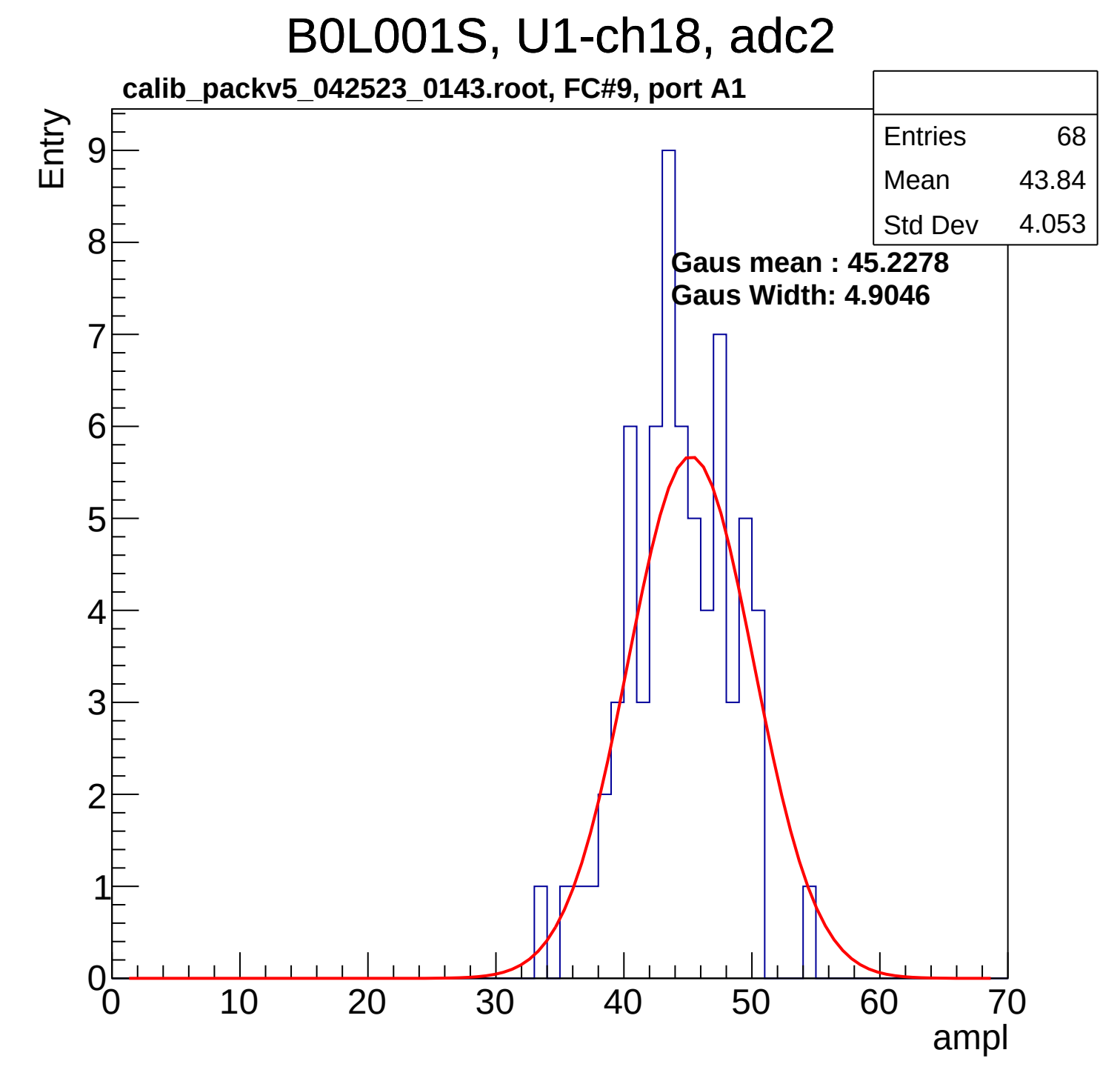

B0L001S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 61.11
Std Dev 1.523
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

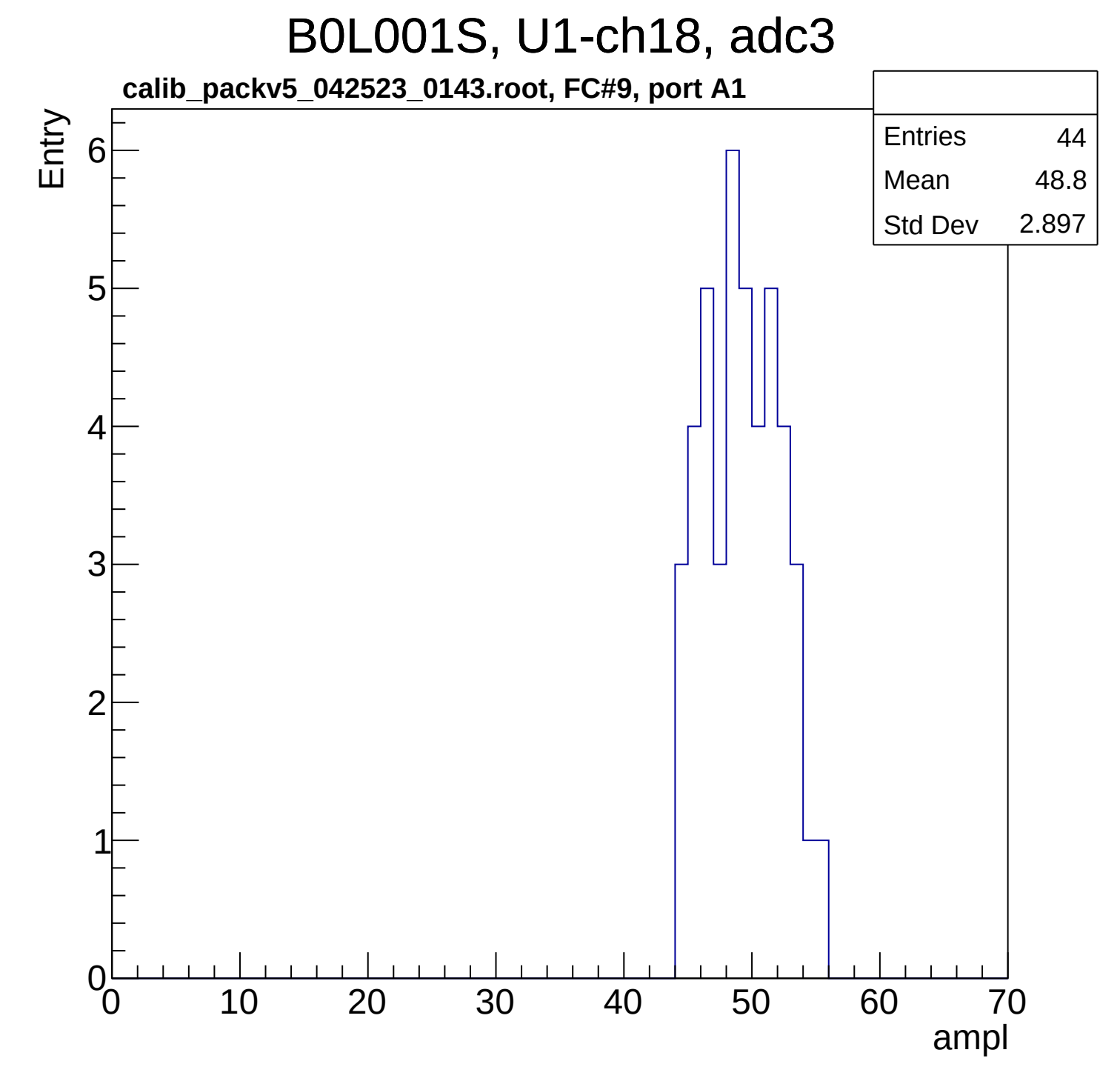

B0L001S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 49.4
Std Dev 24.74
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

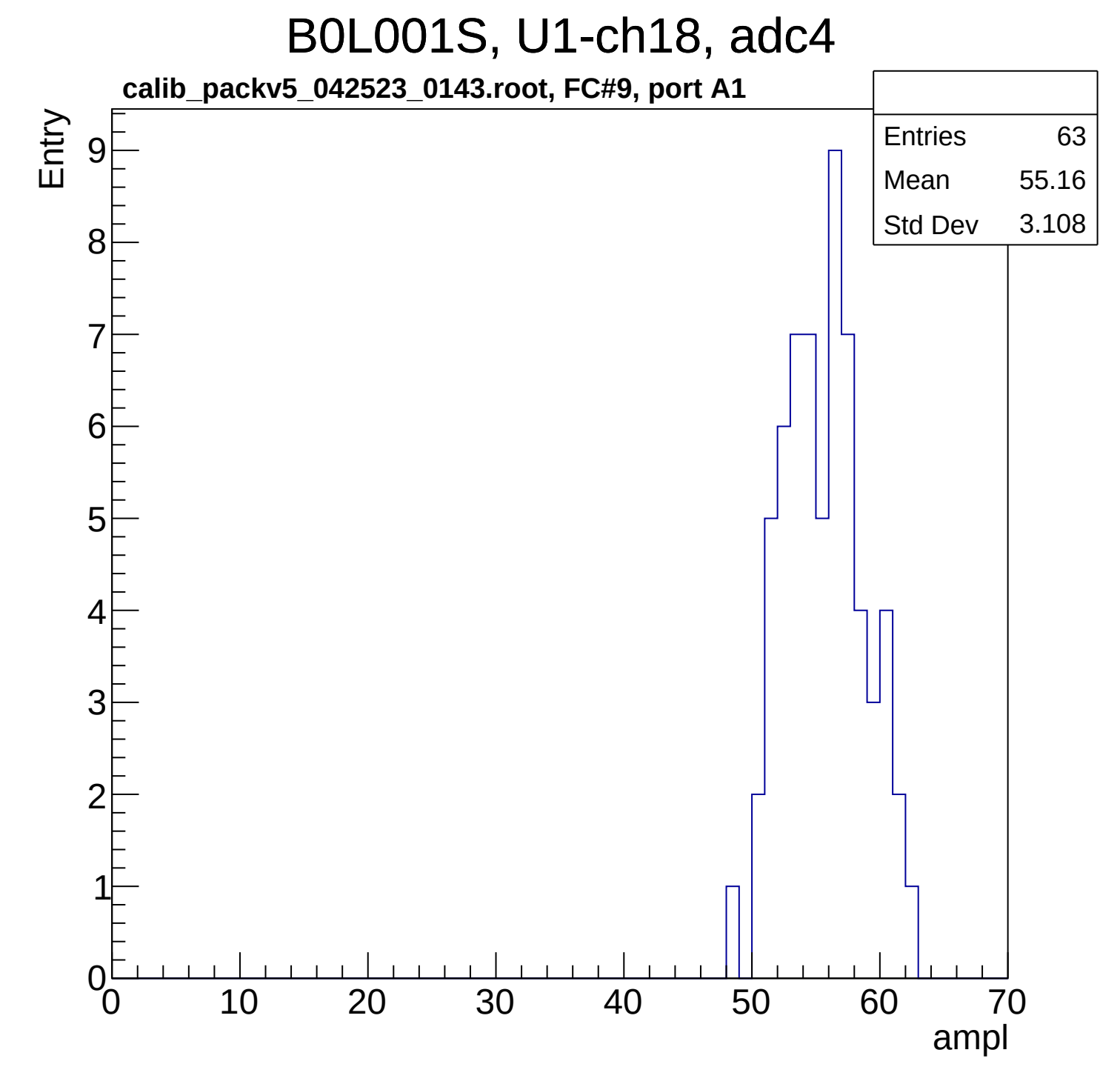

B0L001S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 29.35
Std Dev 4.016
Gaus mean : 29.9065
Gaus Width: 3.8732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37.32
Std Dev 3.577
Gaus mean : 37.9032
Gaus Width: 3.7790
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.84
Std Dev 4.053
Gaus mean : 45.2278
Gaus Width: 4.9046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 48.8
Std Dev 2.897
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.16
Std Dev 3.108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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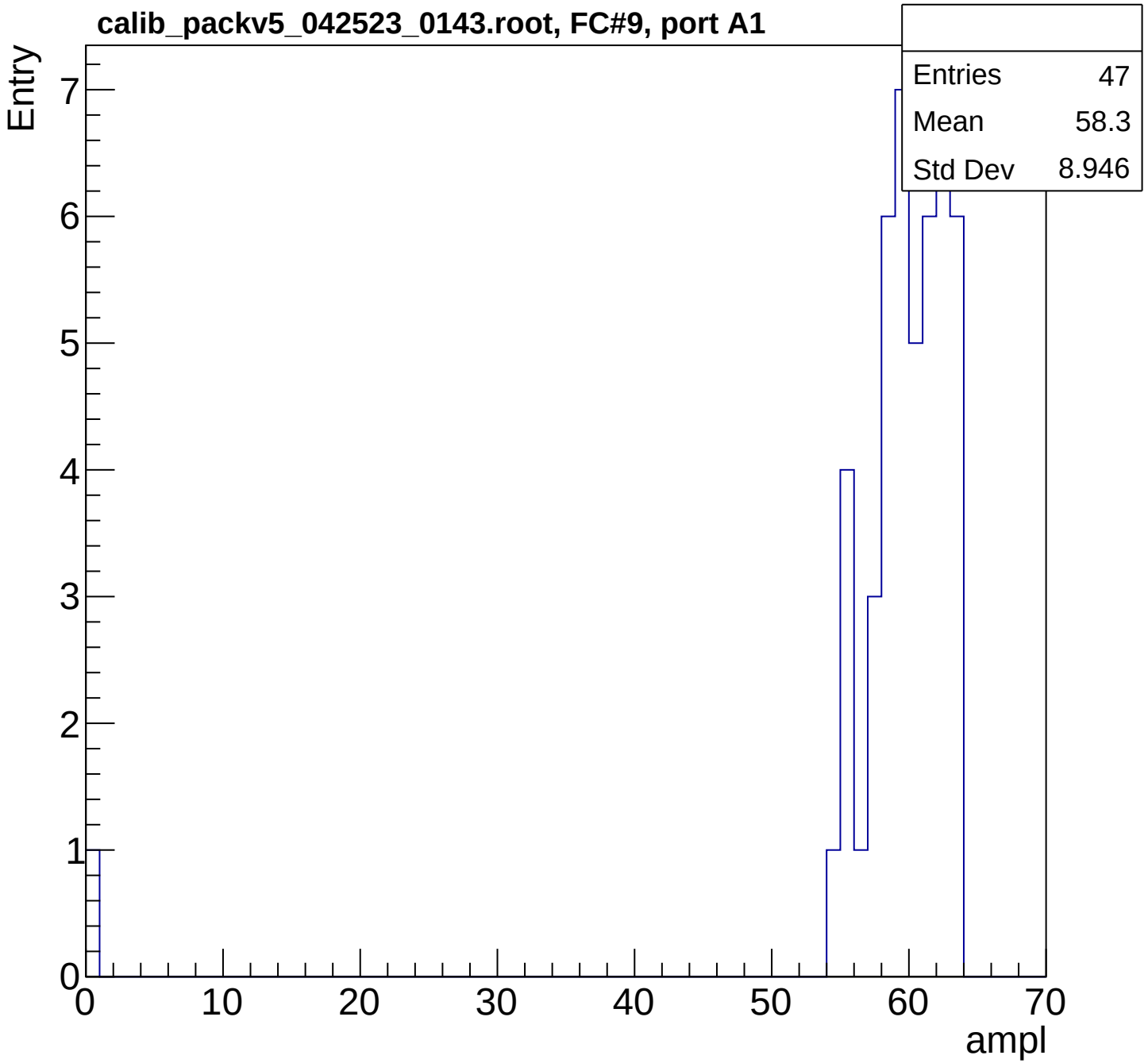
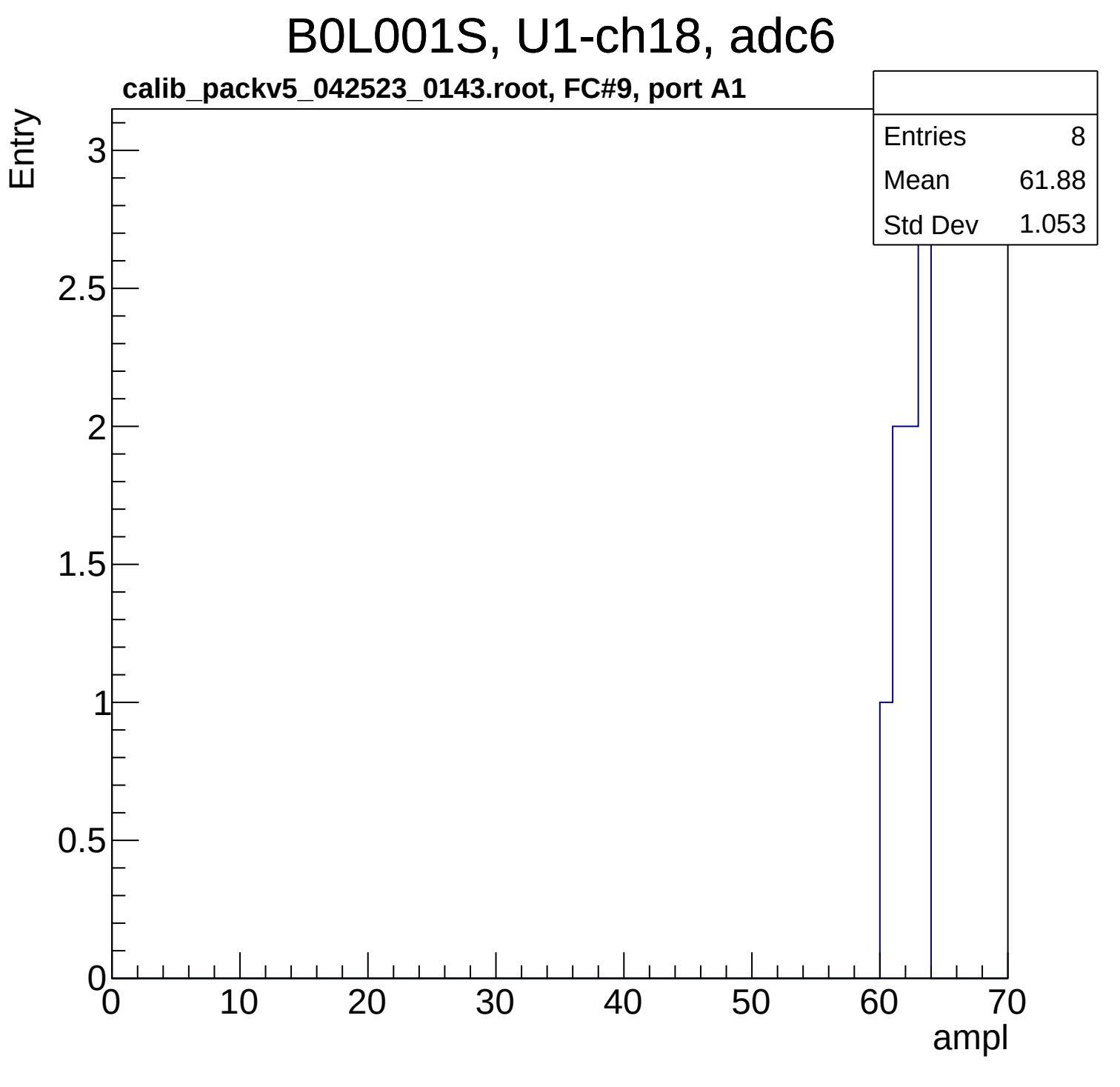
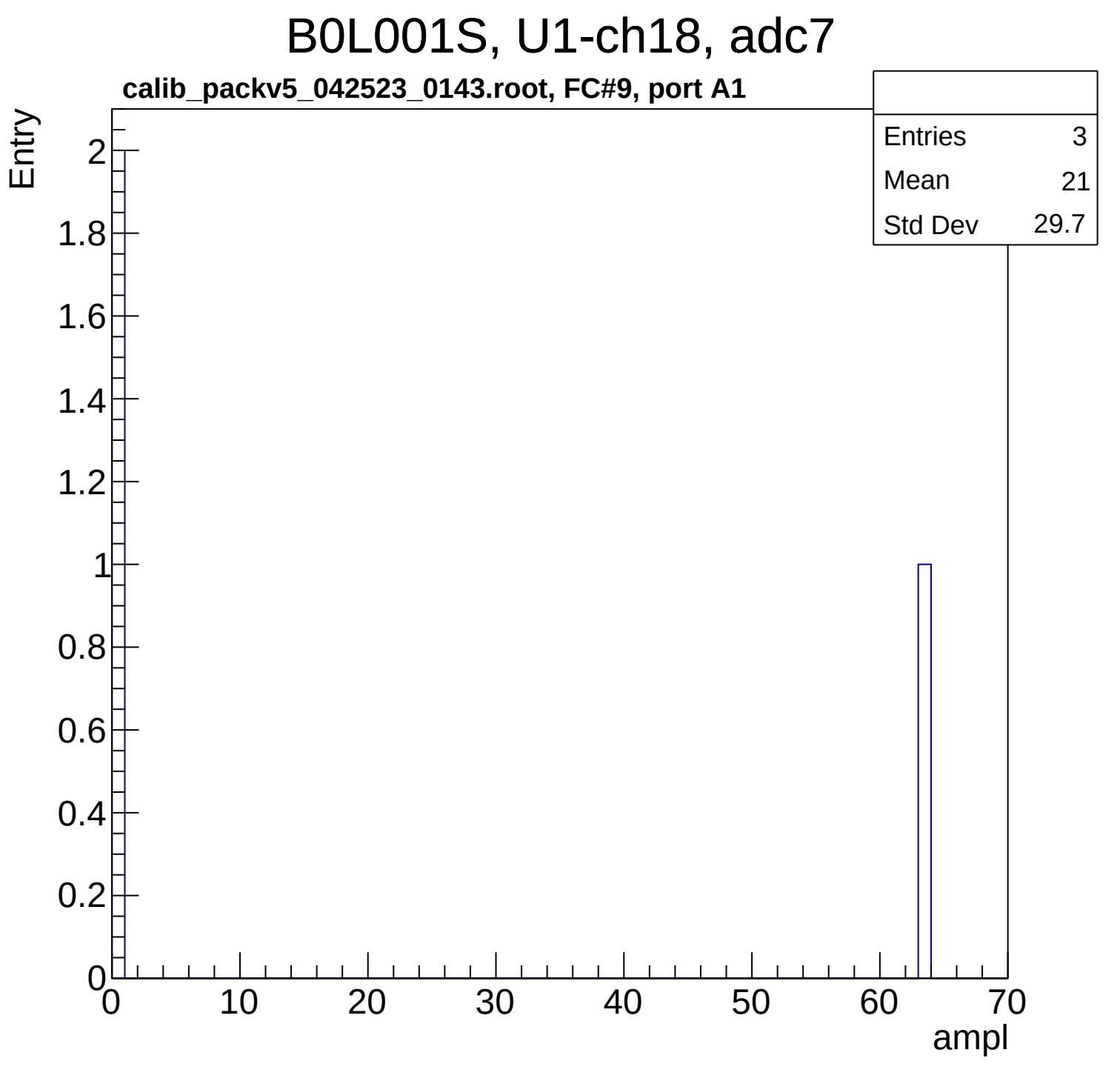
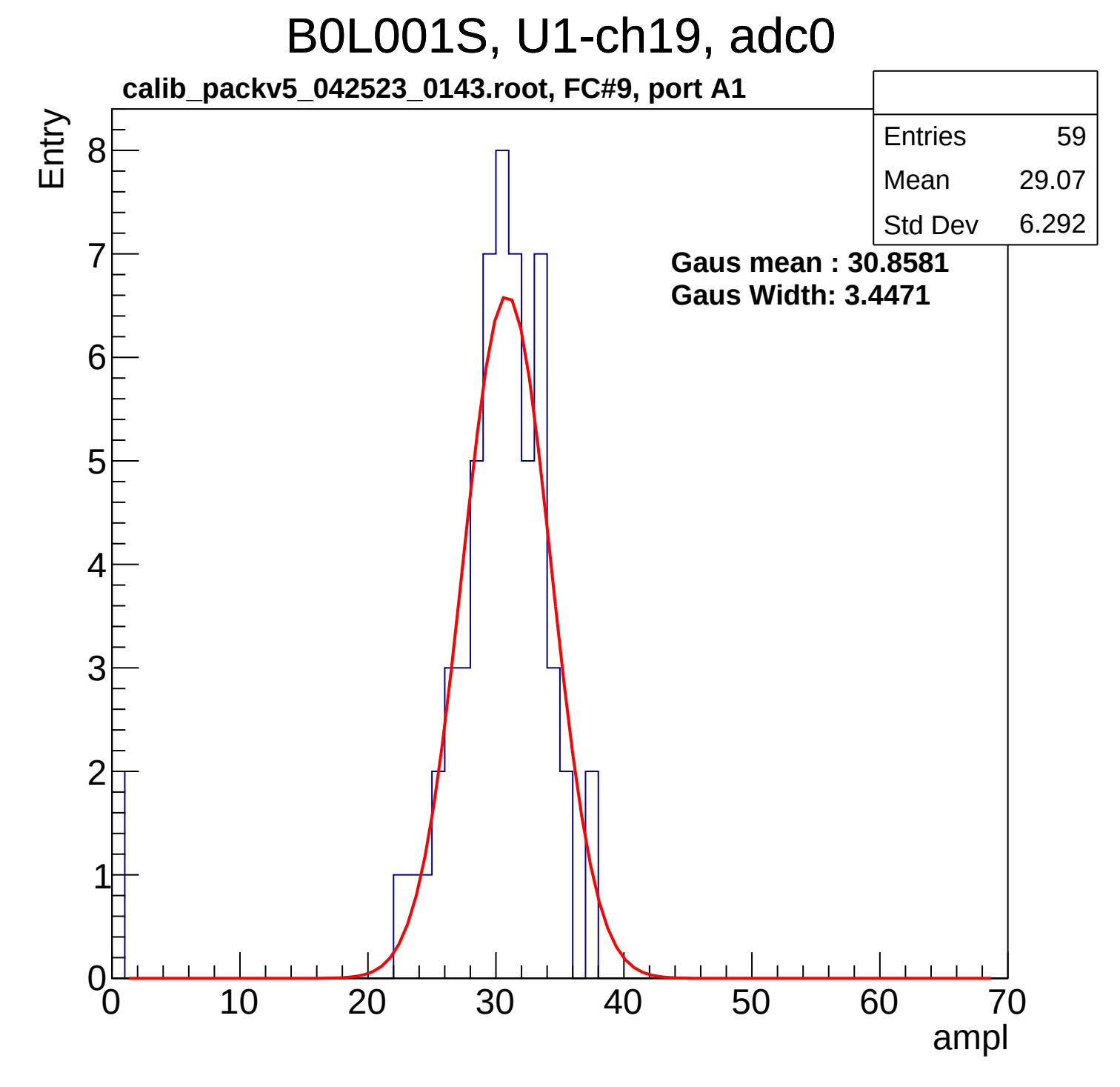
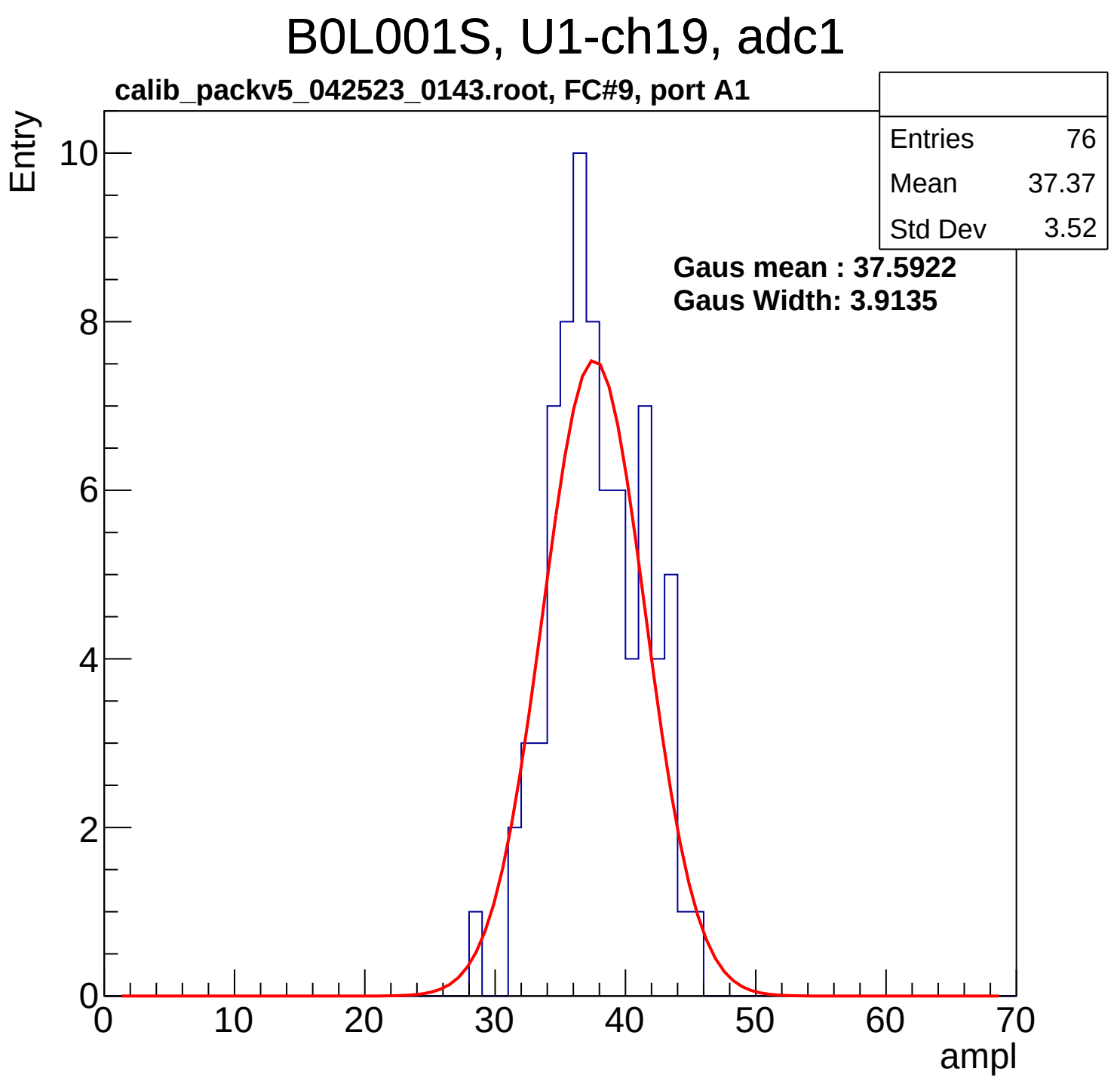
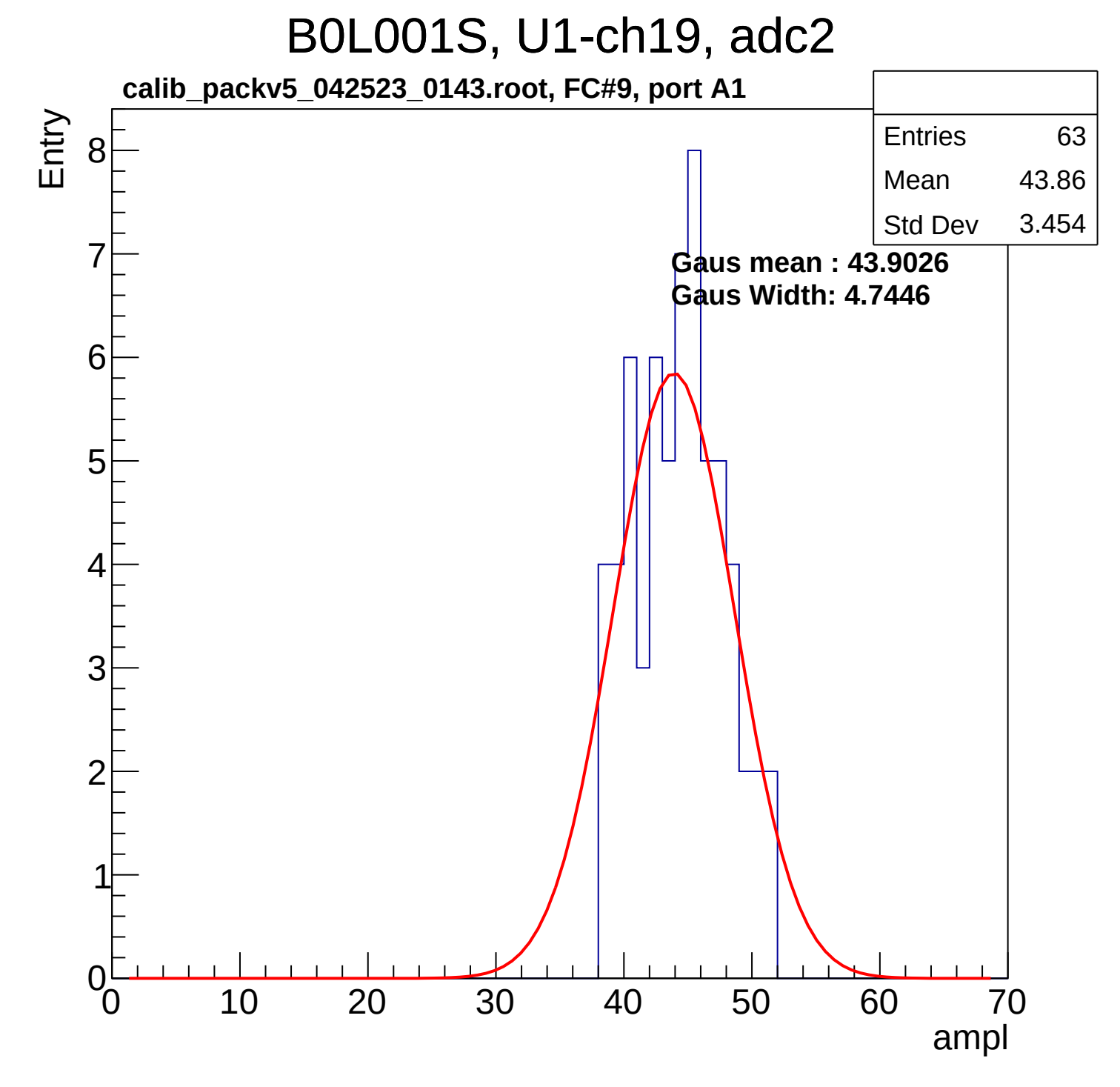

B0L001S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.3
Std Dev 8.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

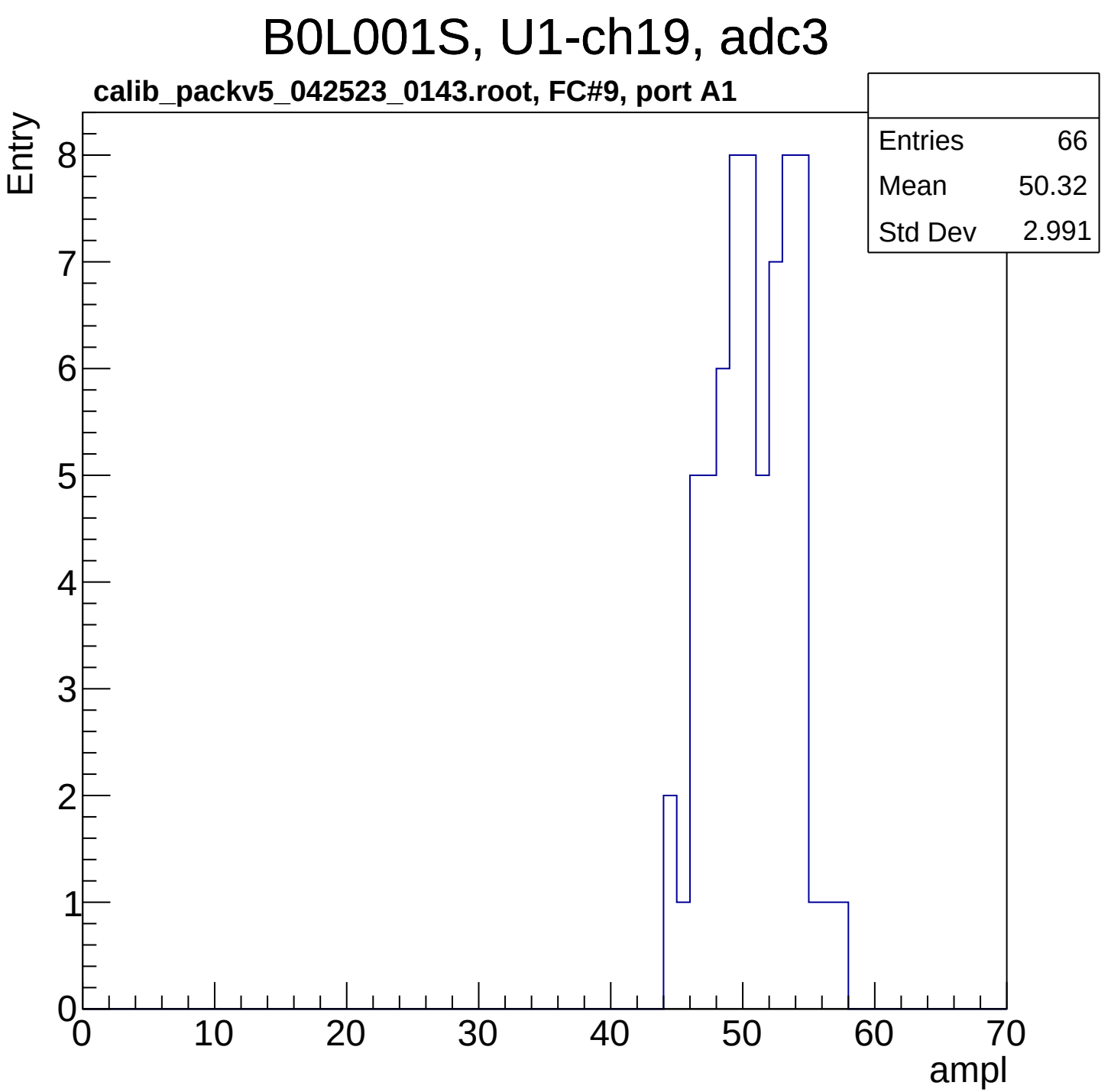

B0L001S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

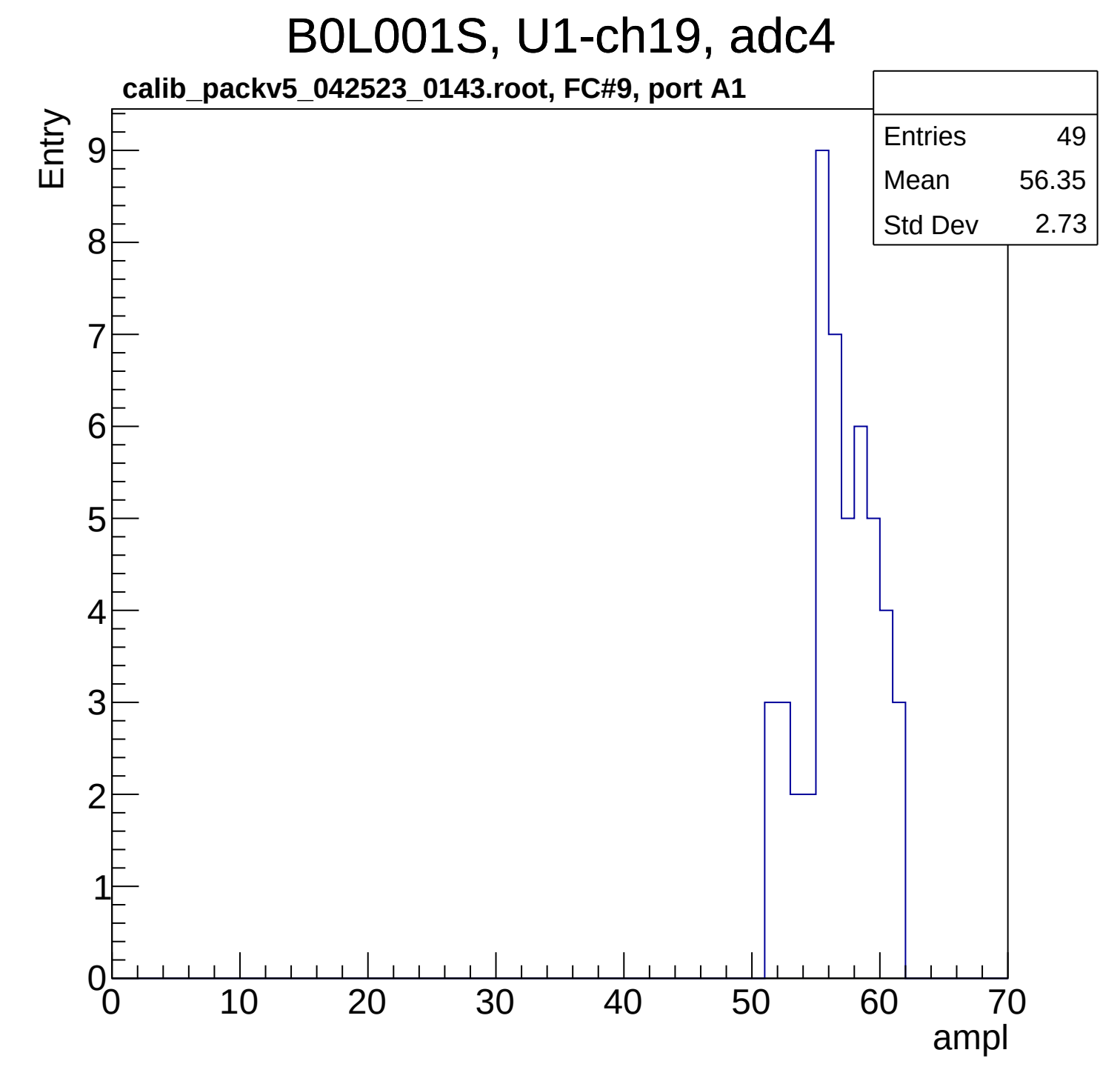

B0L001S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

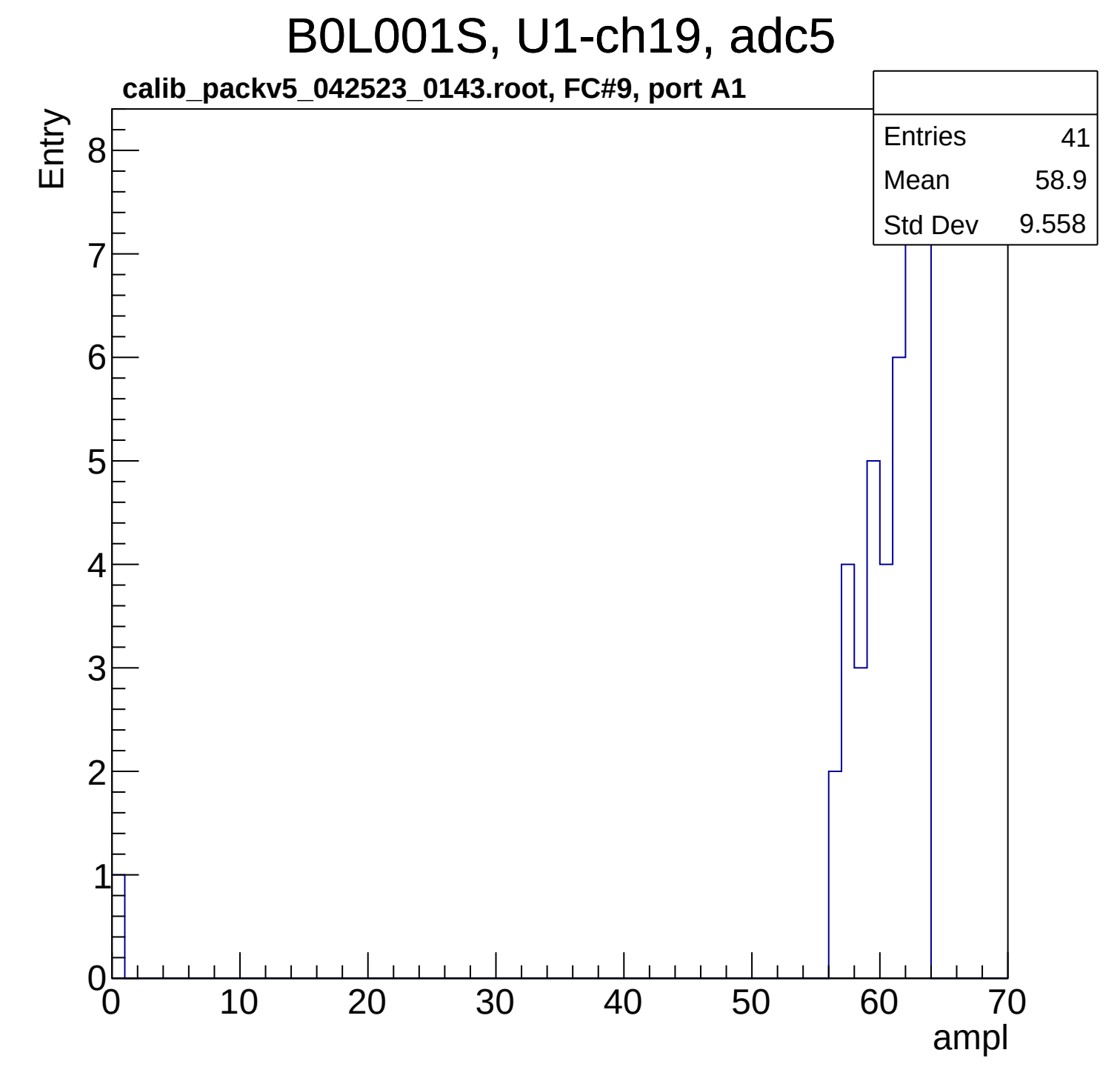

B0L001S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.07
Std Dev 6.292
Gaus mean : 30.8581
Gaus Width: 3.4471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

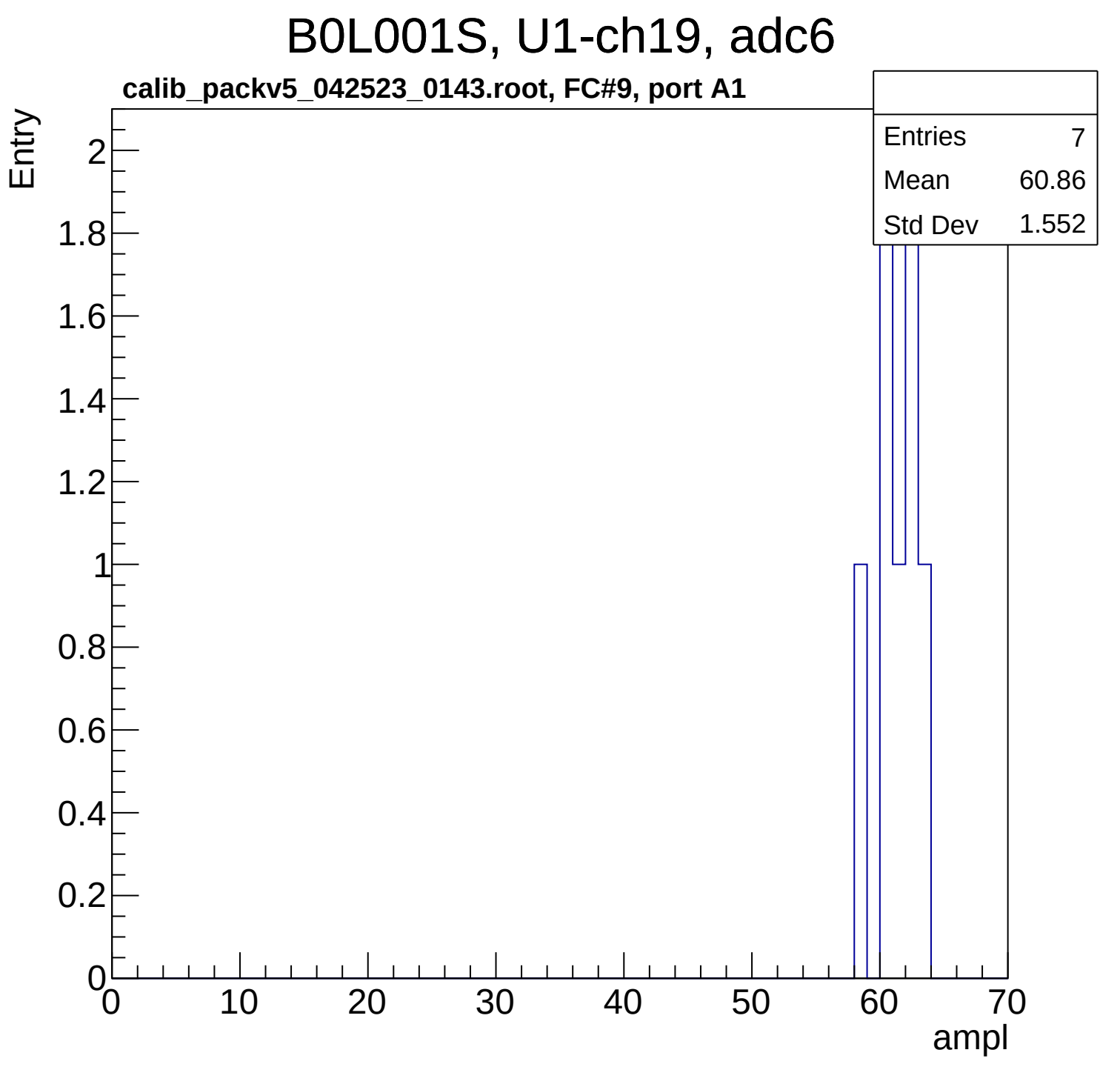

B0L001S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.37
Std Dev 3.52
Gaus mean : 37.5922
Gaus Width: 3.9135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

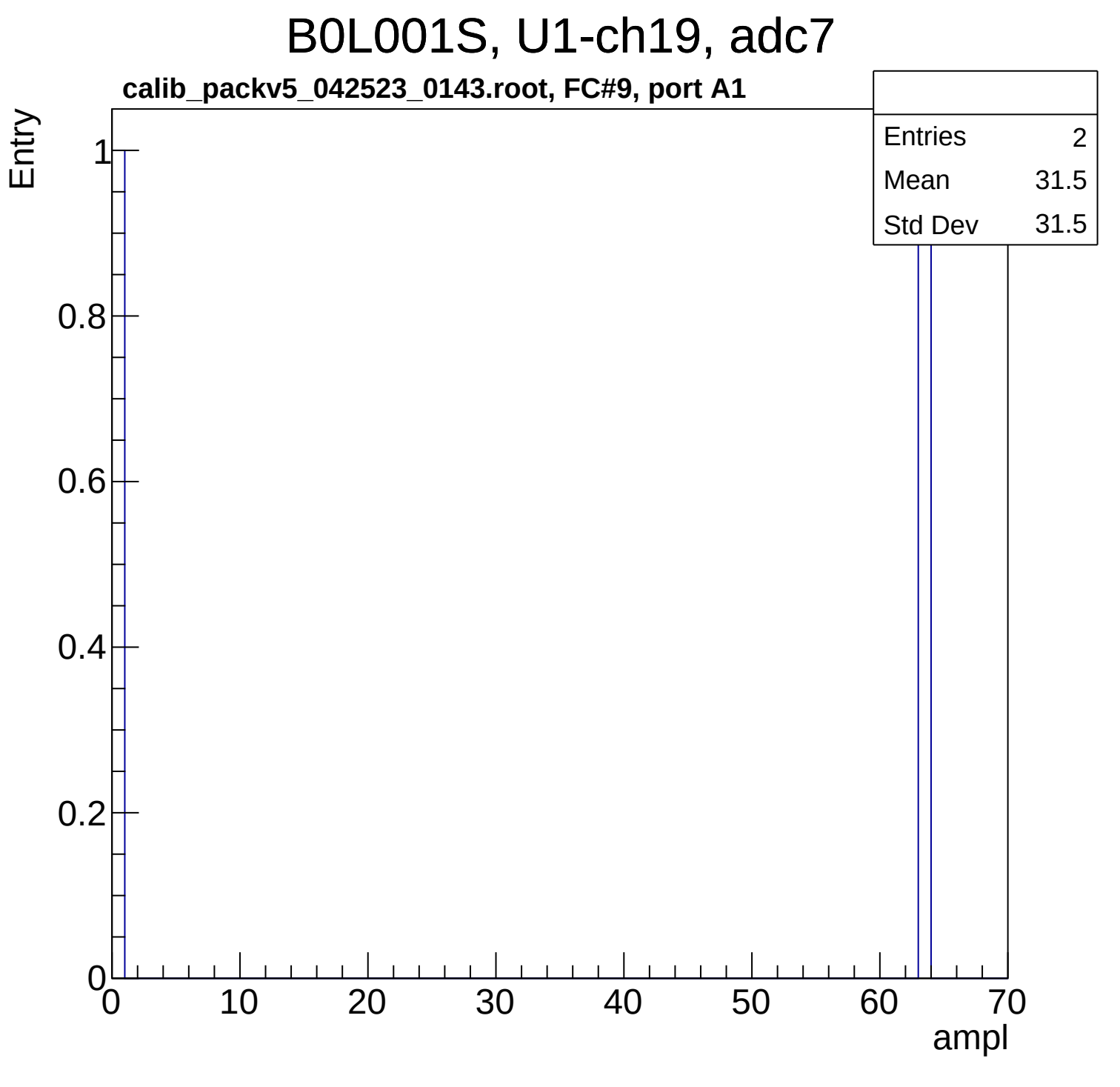

B0L001S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.86
Std Dev 3.454
Gaus mean : 43.9026
Gaus Width: 4.7446
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

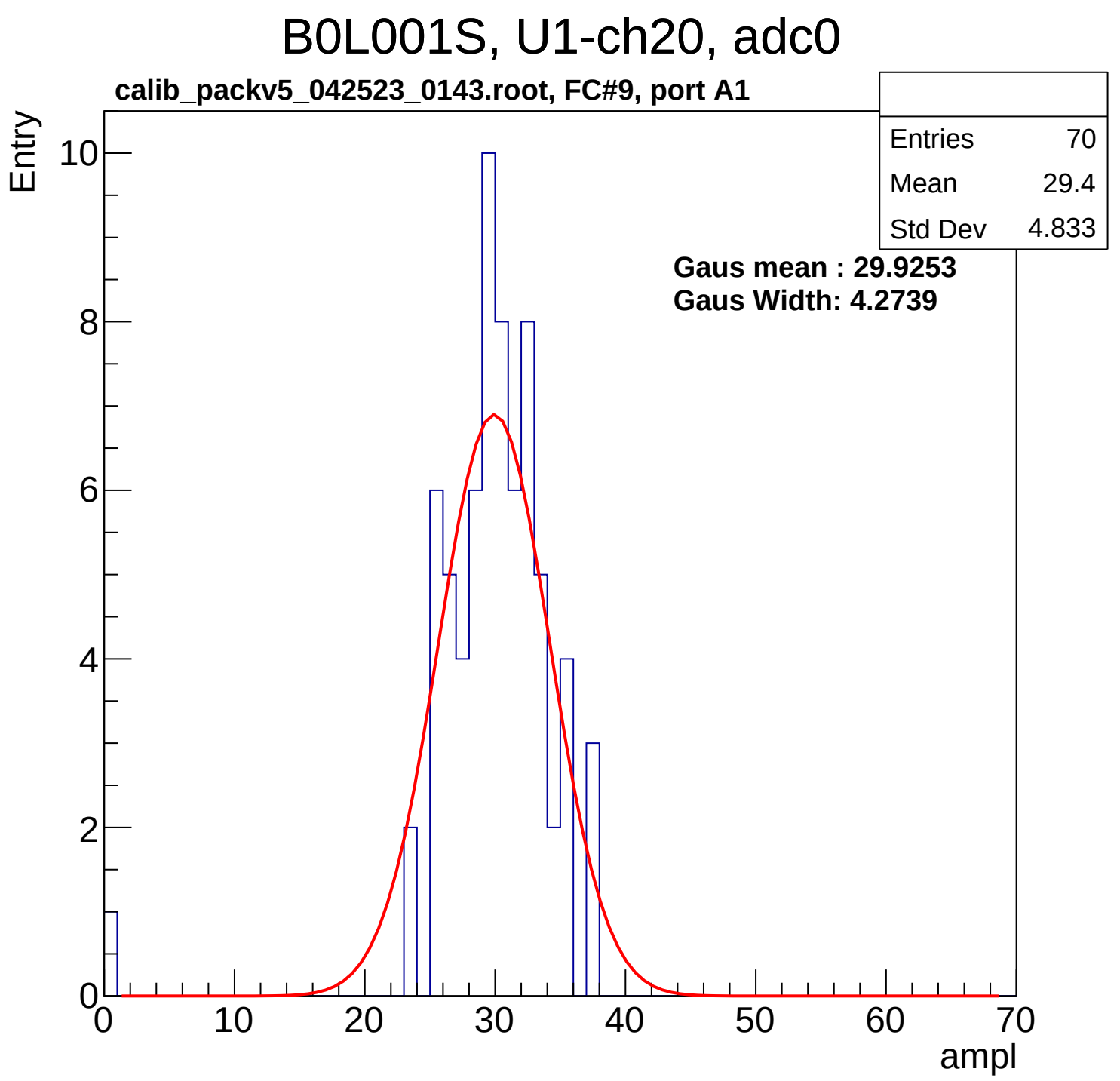

B0L001S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.32
Std Dev 2.991
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

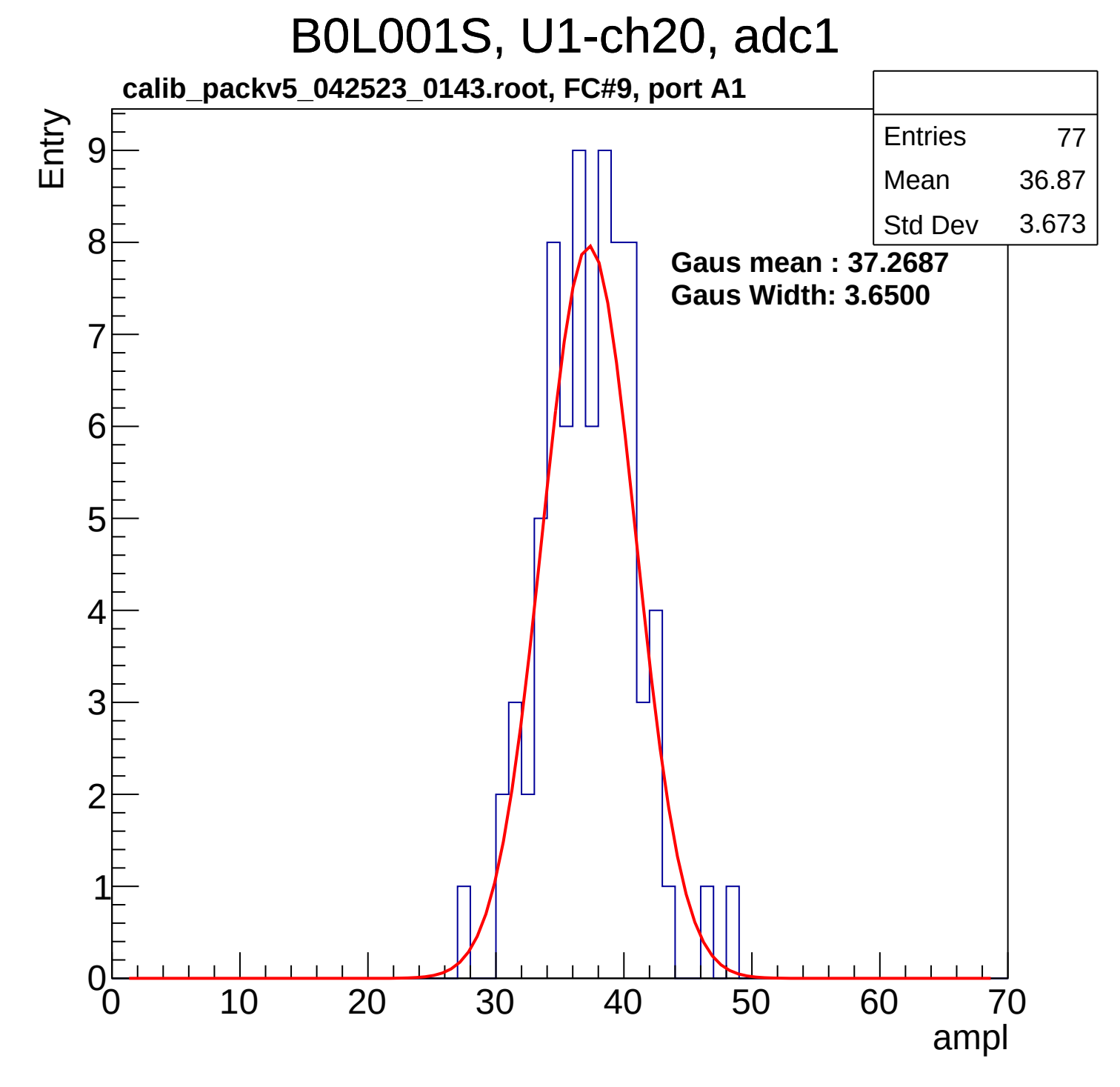

B0L001S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 56.35
Std Dev 2.73
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

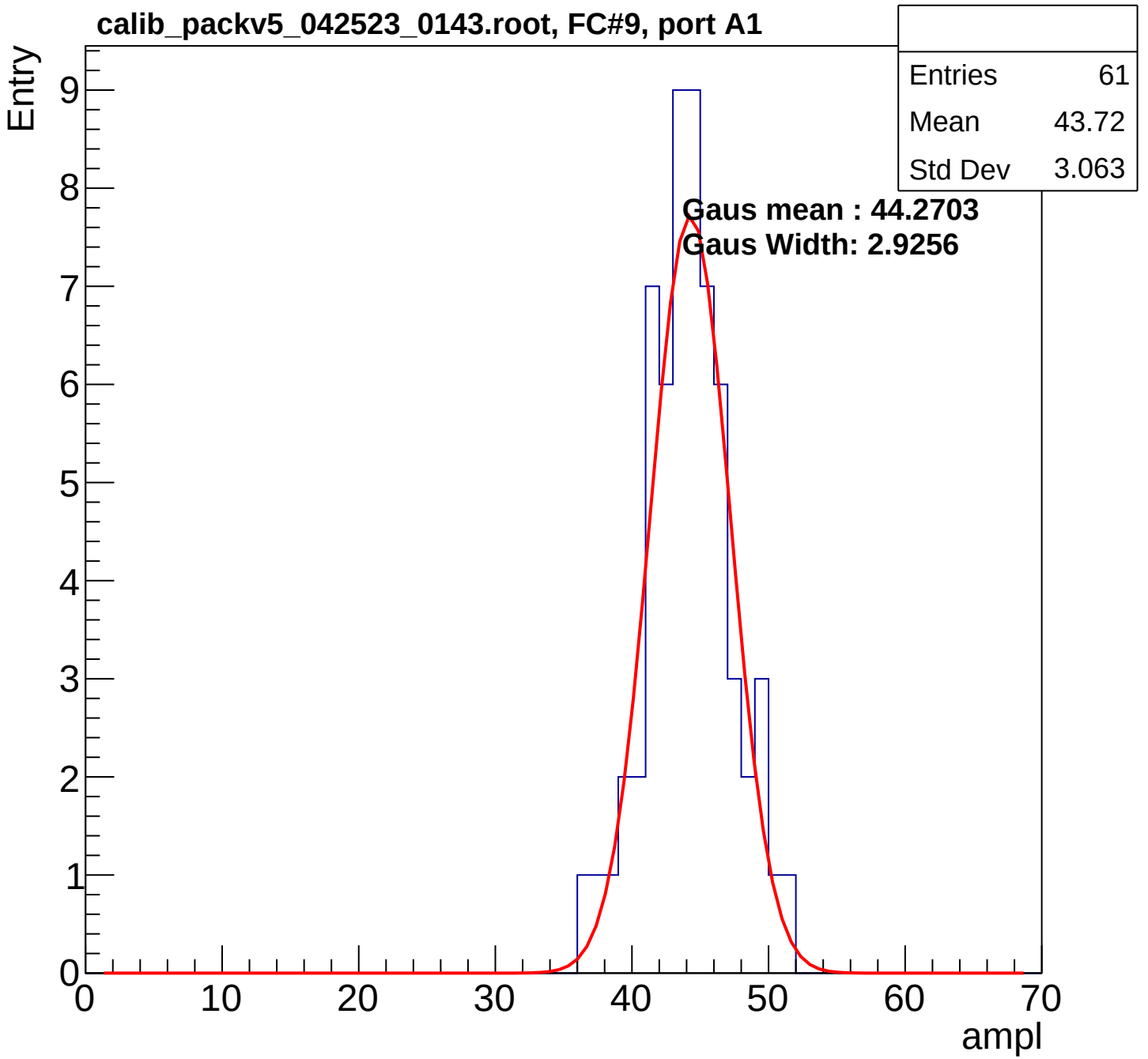

B0L001S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.9
Std Dev 9.558
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

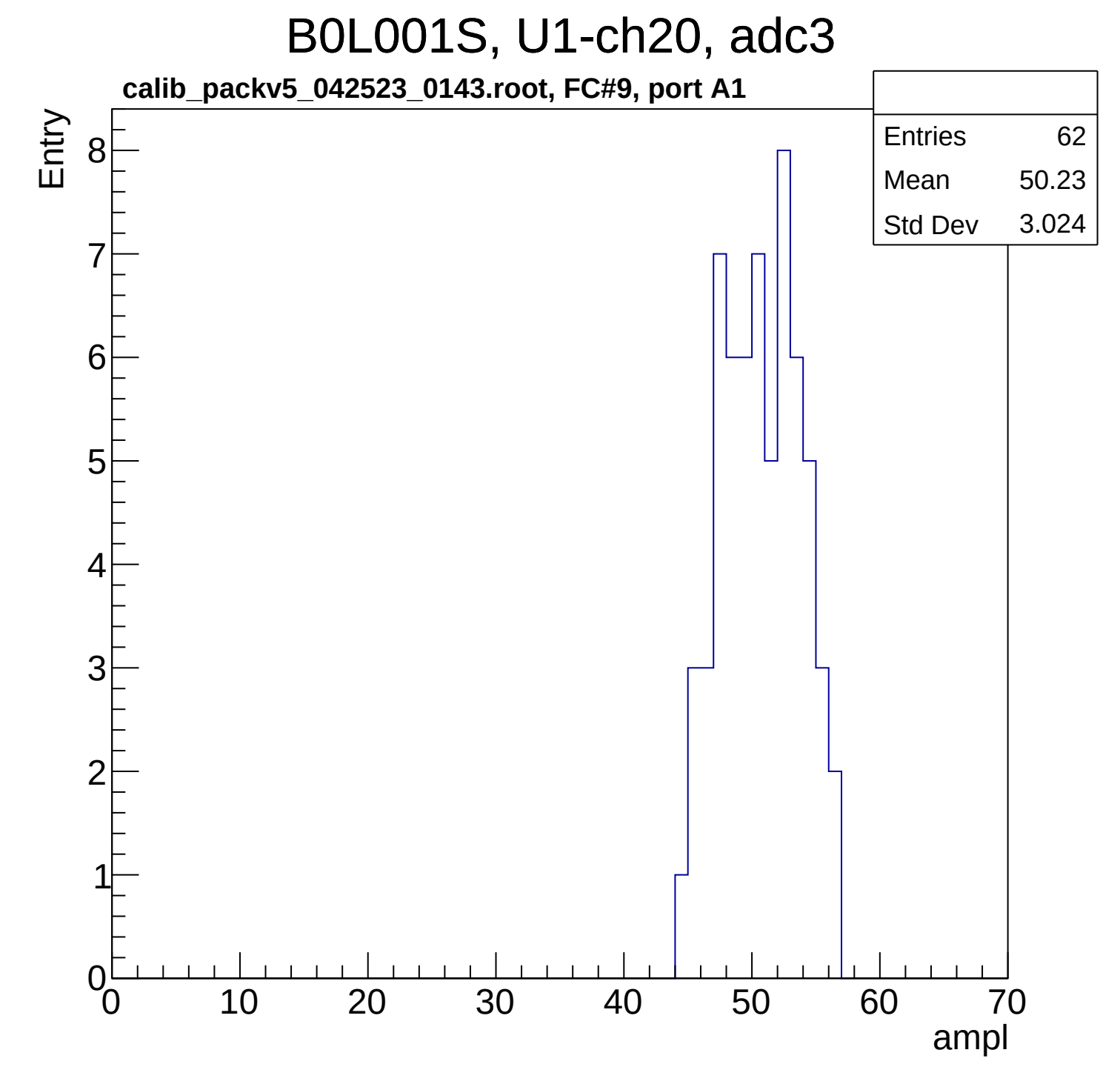

B0L001S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 60.86
Std Dev 1.552
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

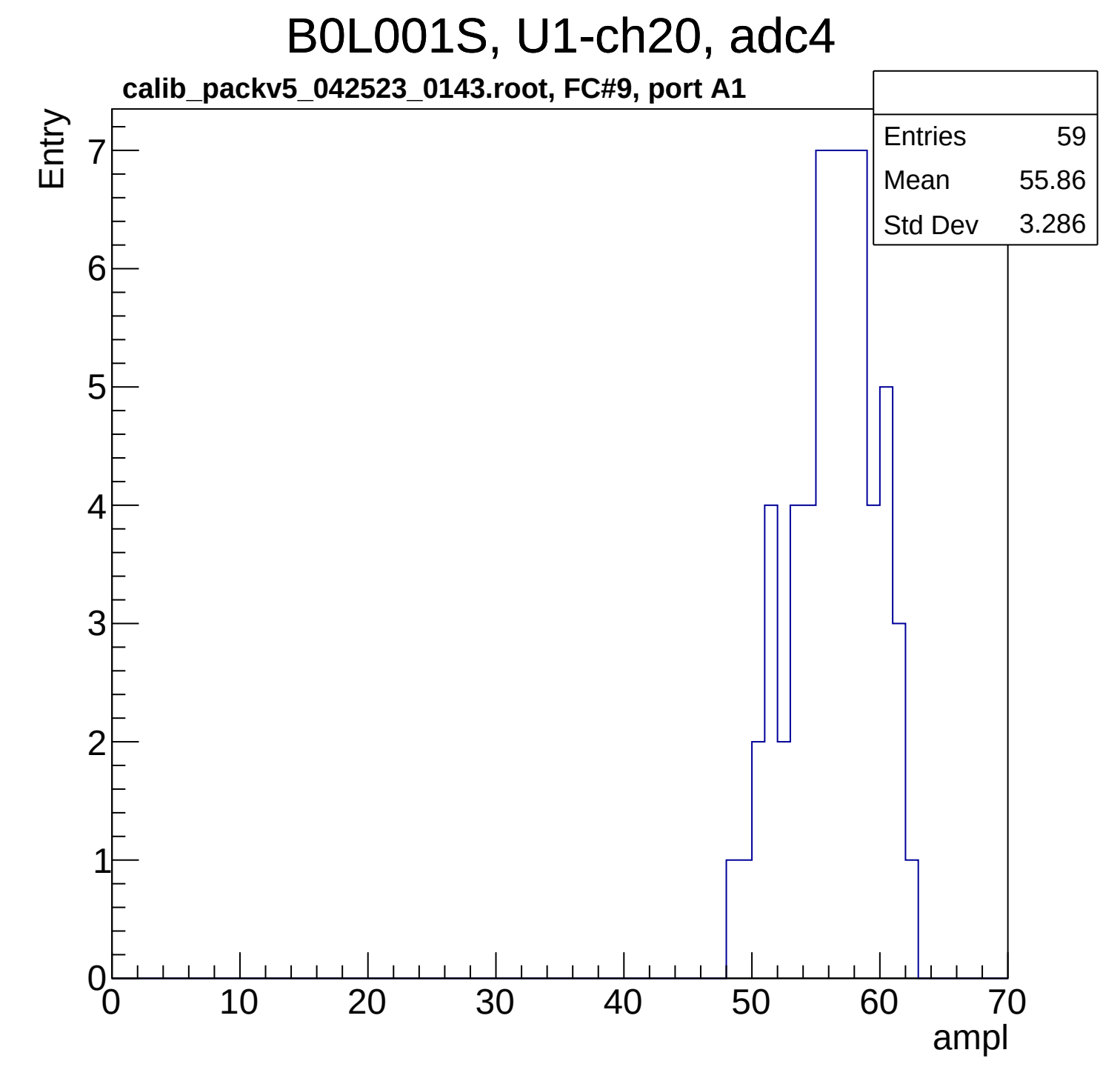

B0L001S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

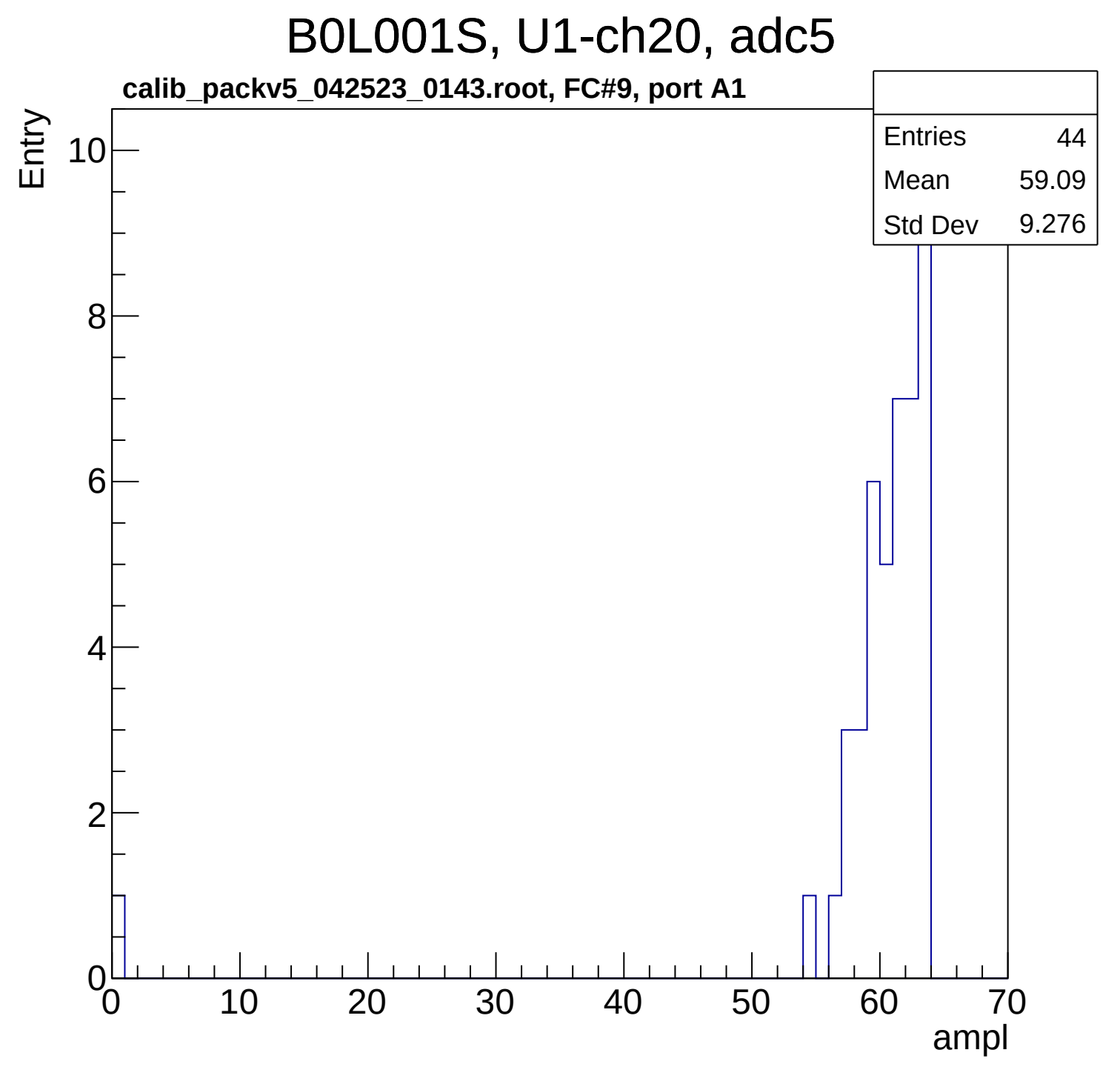

B0L001S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.4
Std Dev 4.833
Gaus mean : 29.9253
Gaus Width: 4.2739
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

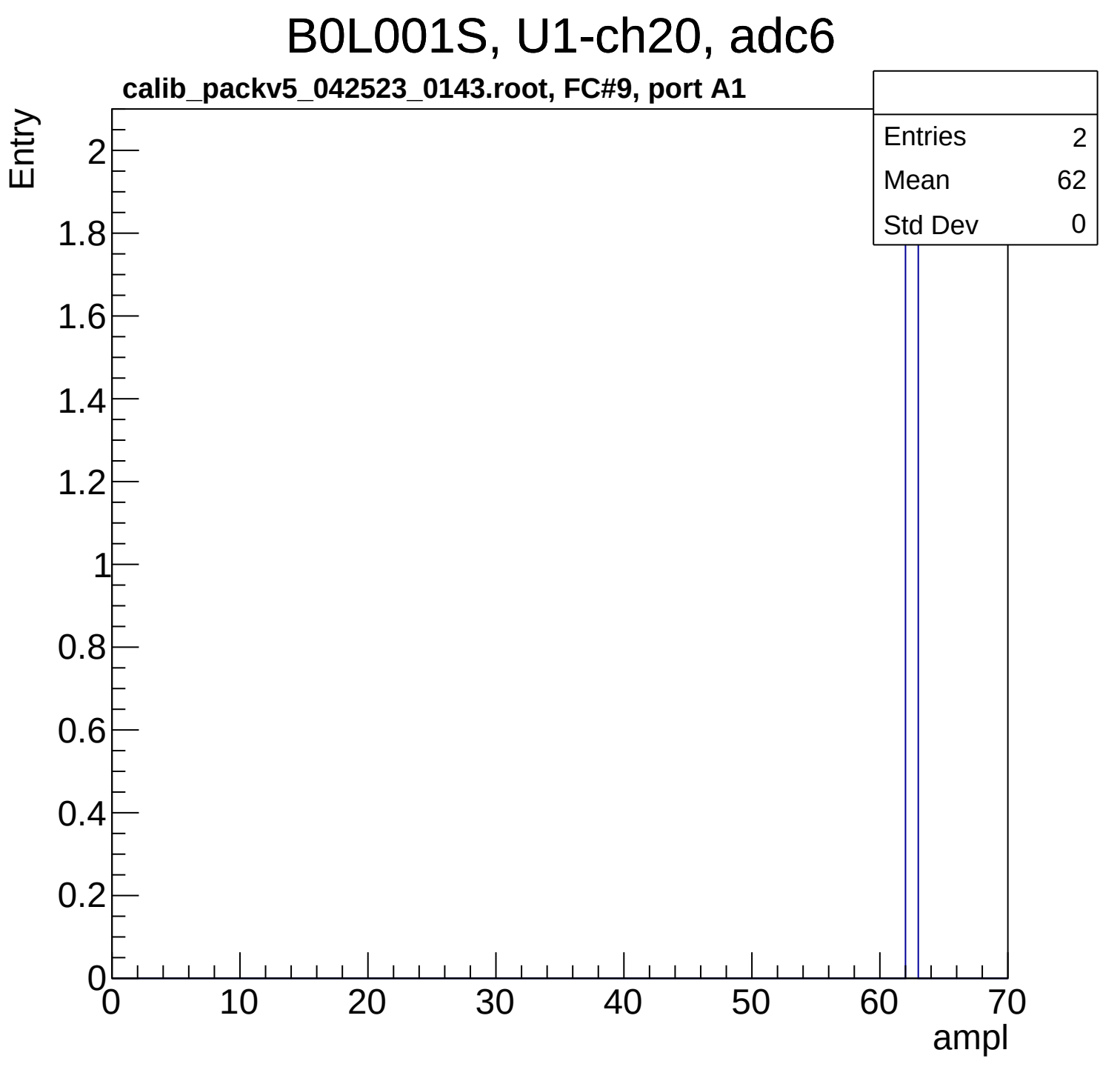

B0L001S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 36.87
Std Dev 3.673
Gaus mean : 37.2687
Gaus Width: 3.6500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

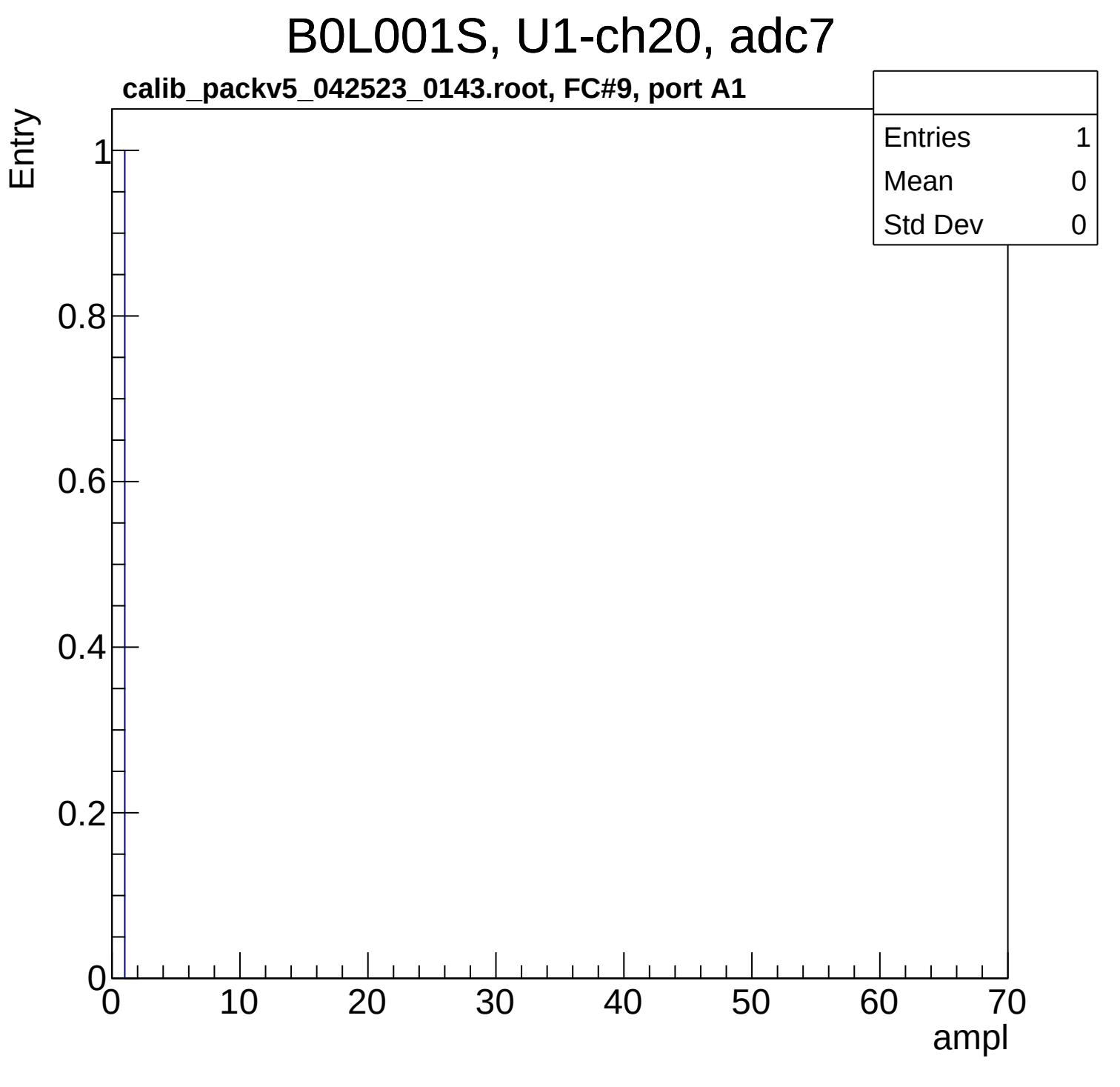

B0L001S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.72
Std Dev 3.063
Gaus mean : 44.2703
Gaus Width: 2.9256
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

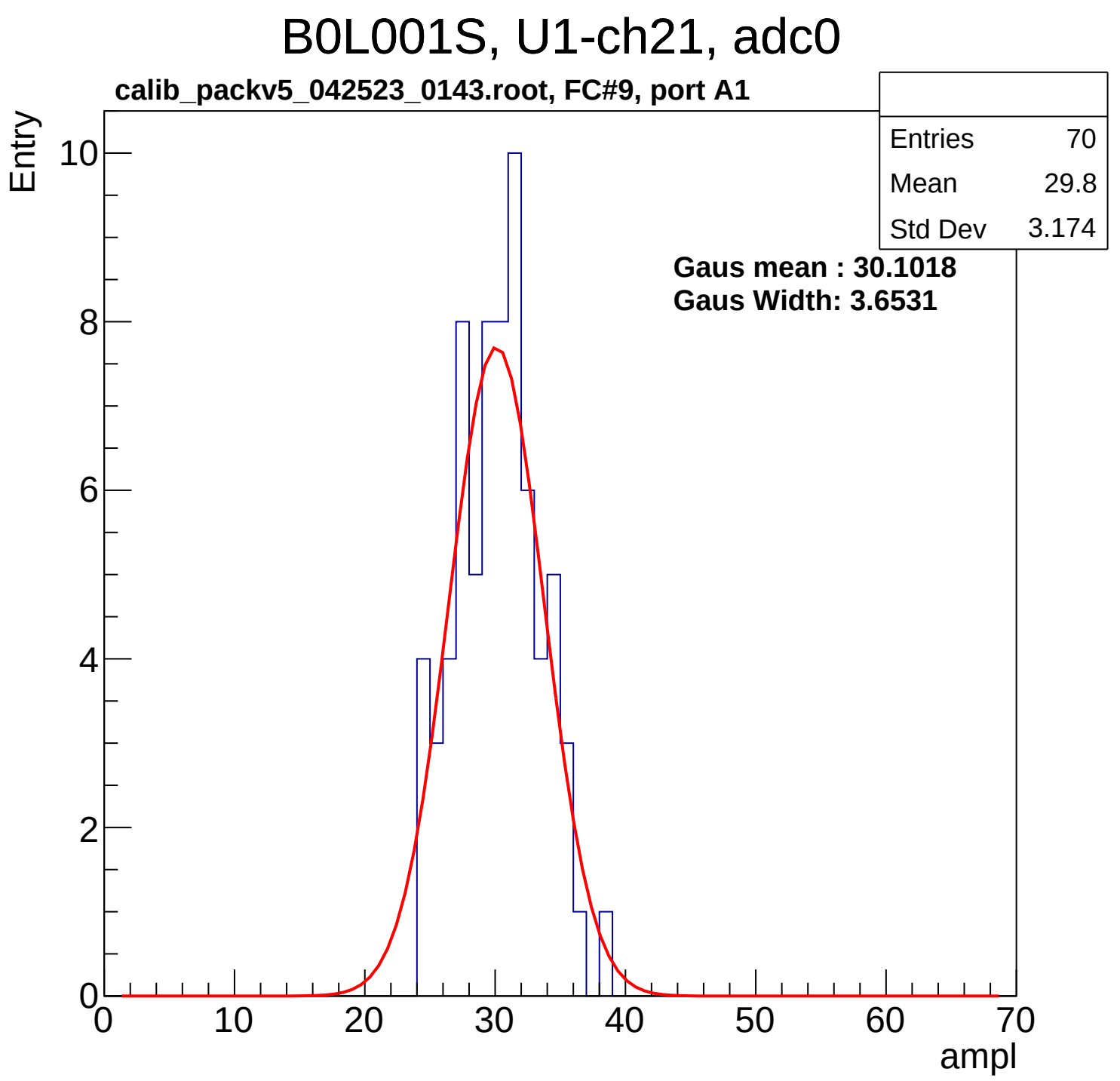

B0L001S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.23
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

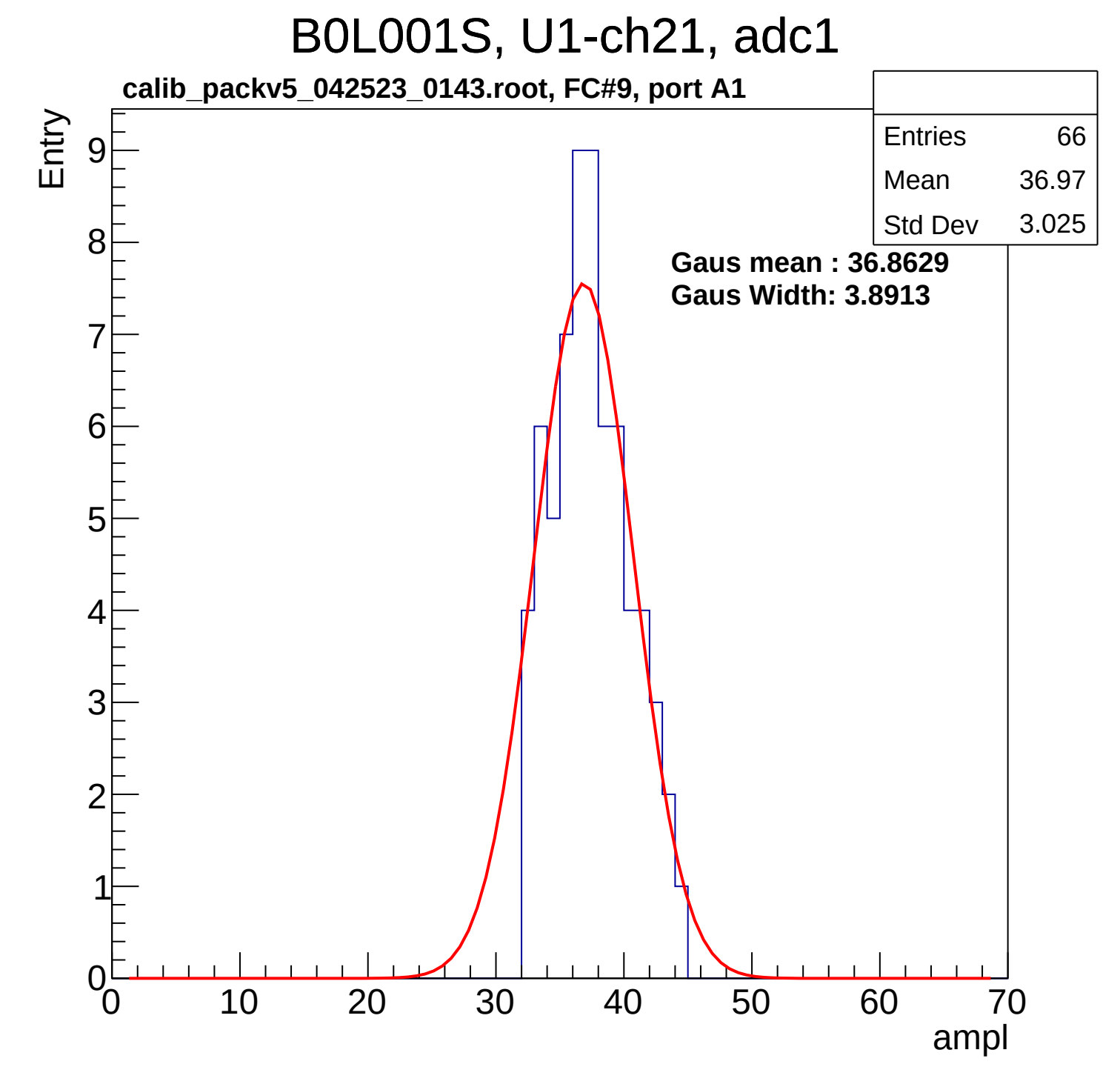

B0L001S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.86
Std Dev 3.286
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

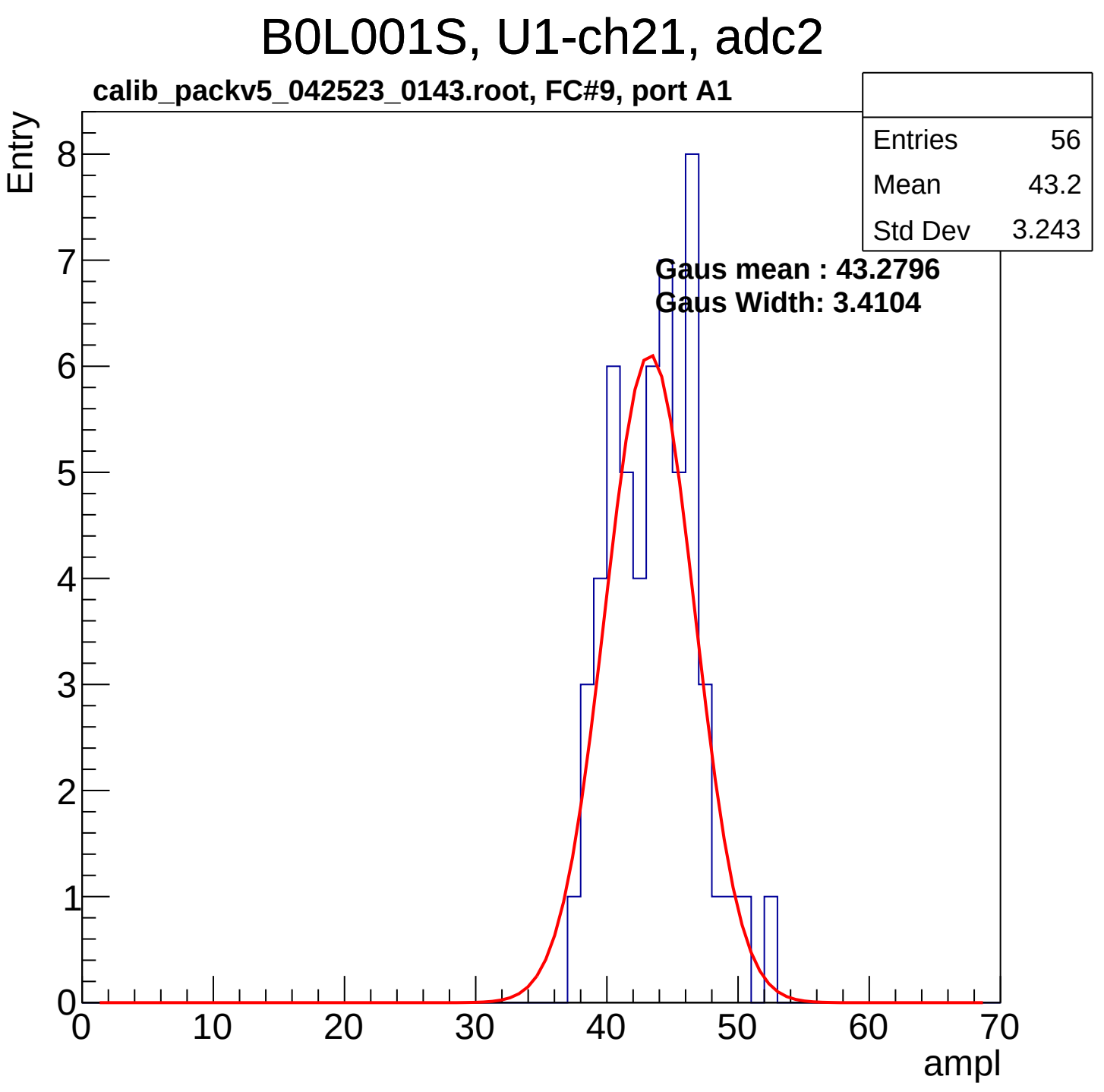

B0L001S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.09
Std Dev 9.276
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

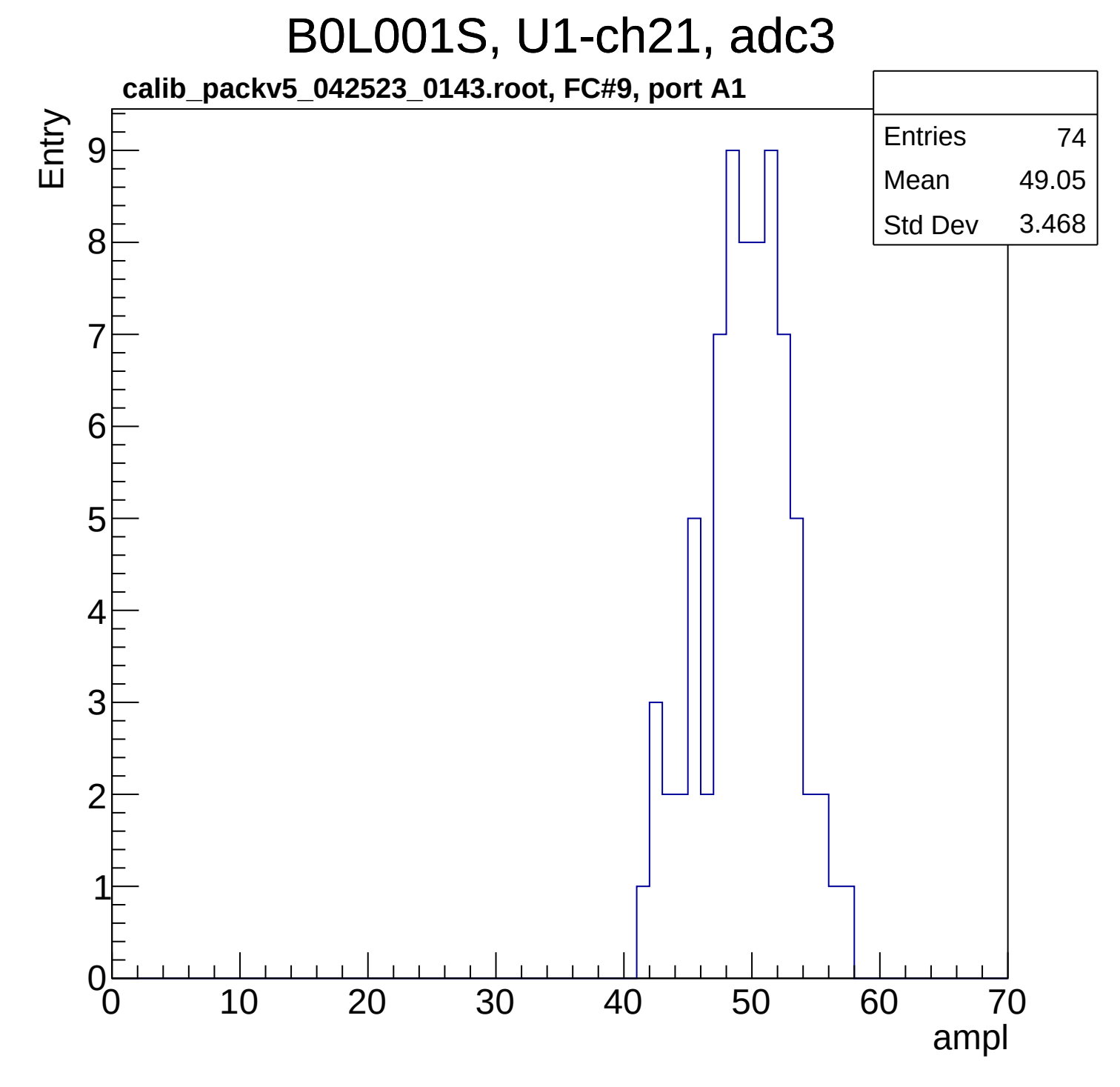

B0L001S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

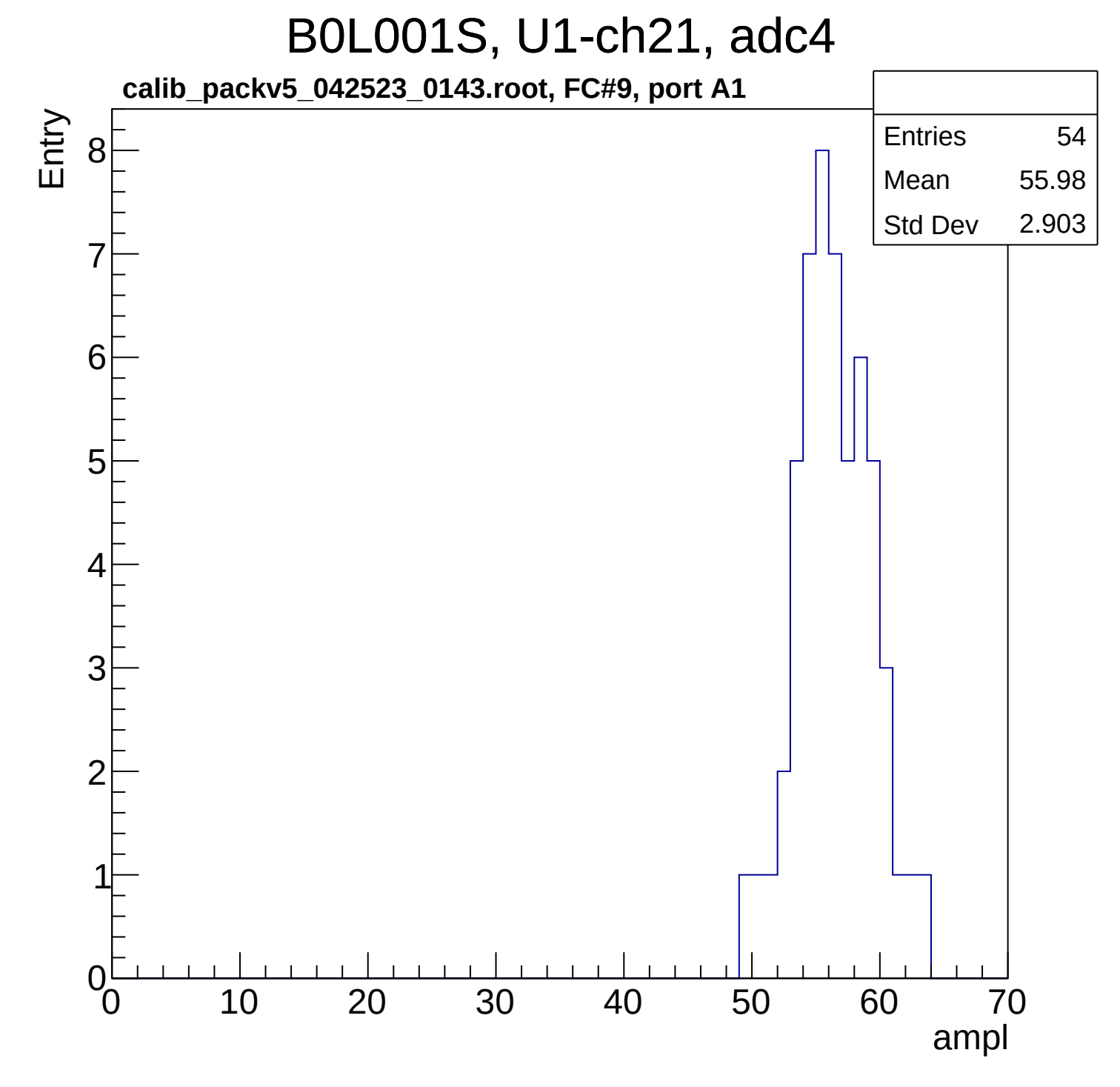

B0L001S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.8
Std Dev 3.174
Gaus mean : 30.1018
Gaus Width: 3.6531
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.97
Std Dev 3.025
Gaus mean : 36.8629
Gaus Width: 3.8913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.2
Std Dev 3.243
Gaus mean : 43.2796
Gaus Width: 3.4104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 49.05
Std Dev 3.468
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.98
Std Dev 2.903
Entry
ampl
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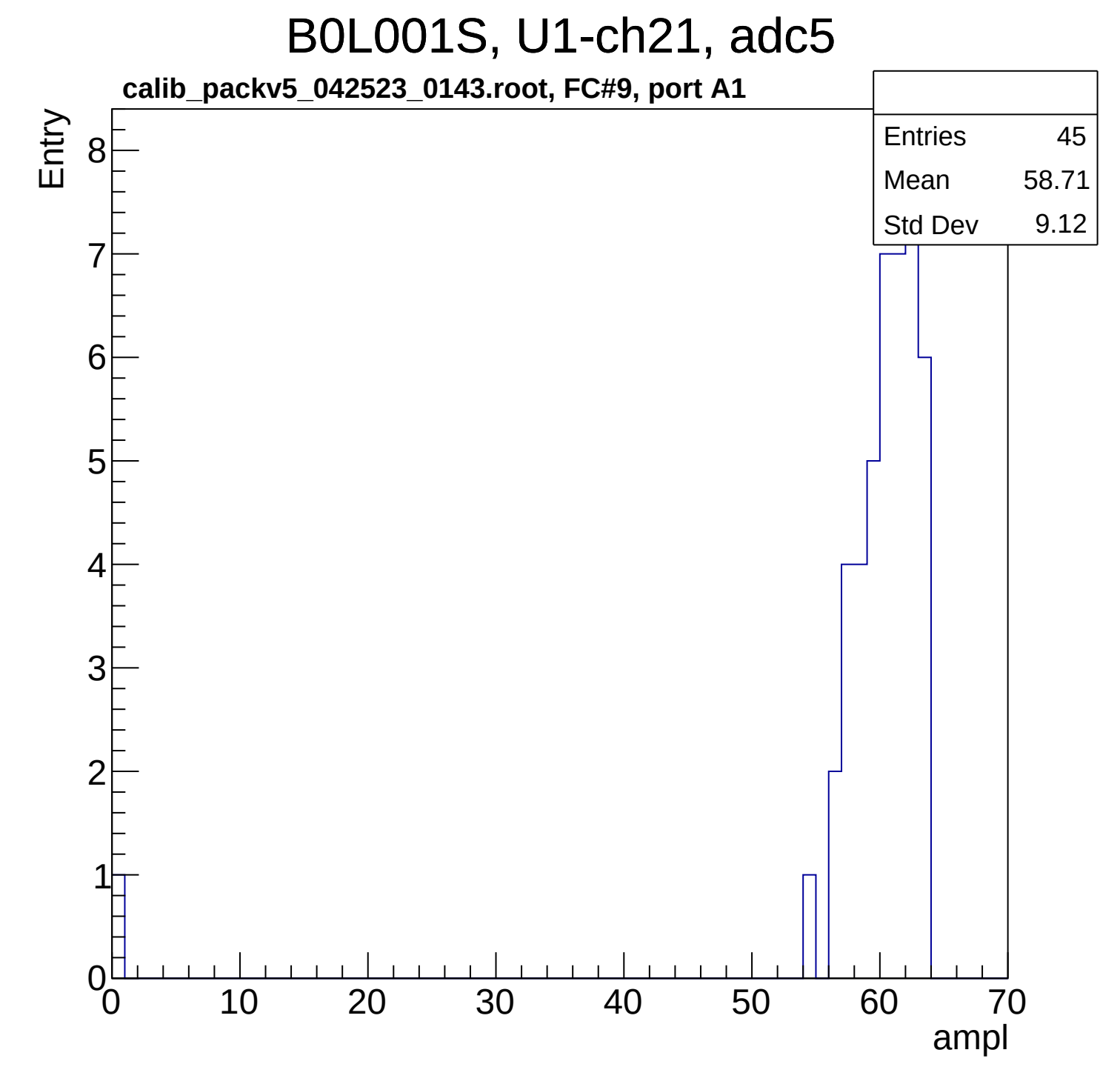
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.71
Std Dev 9.12
Entry
ampl
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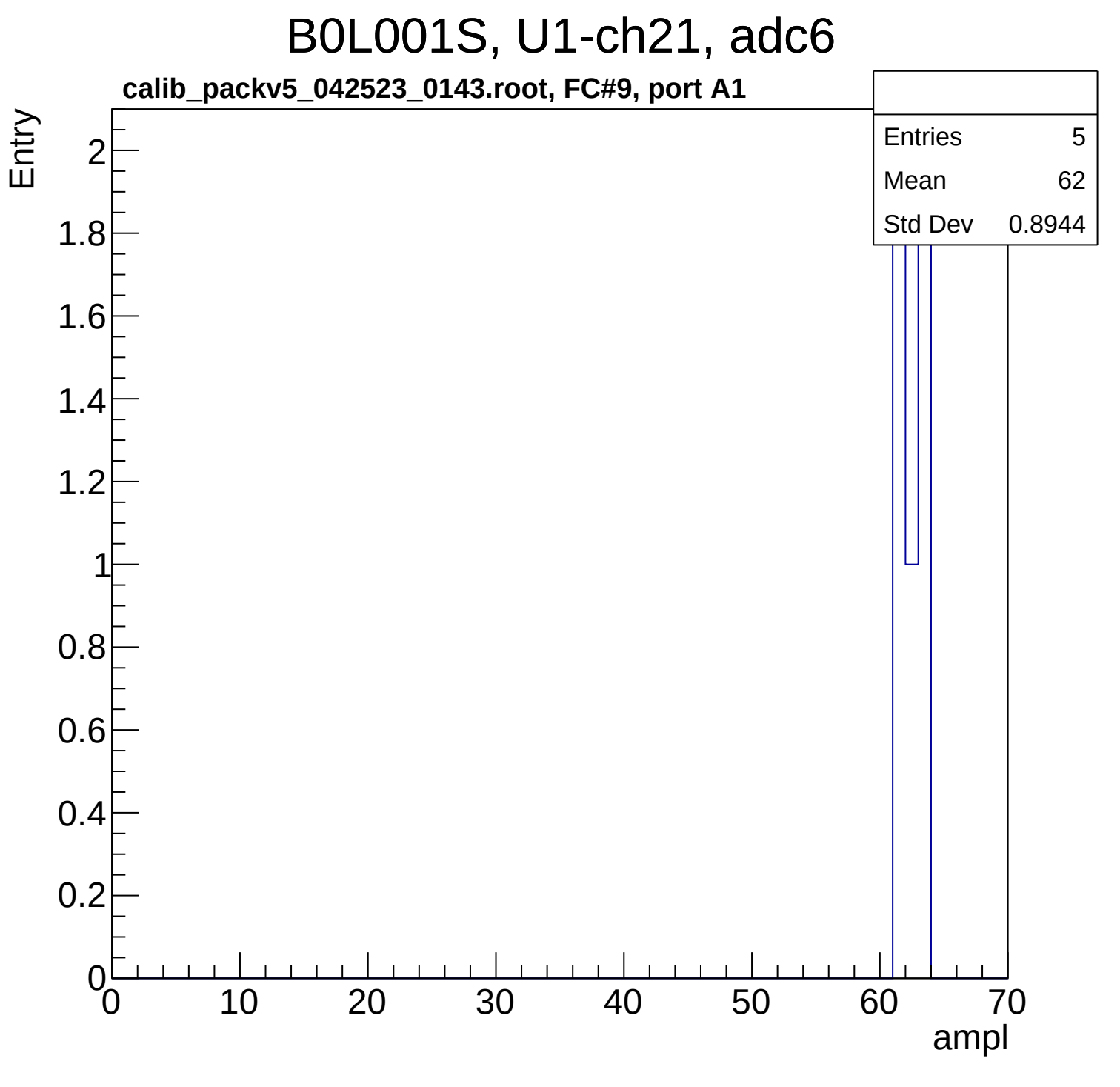
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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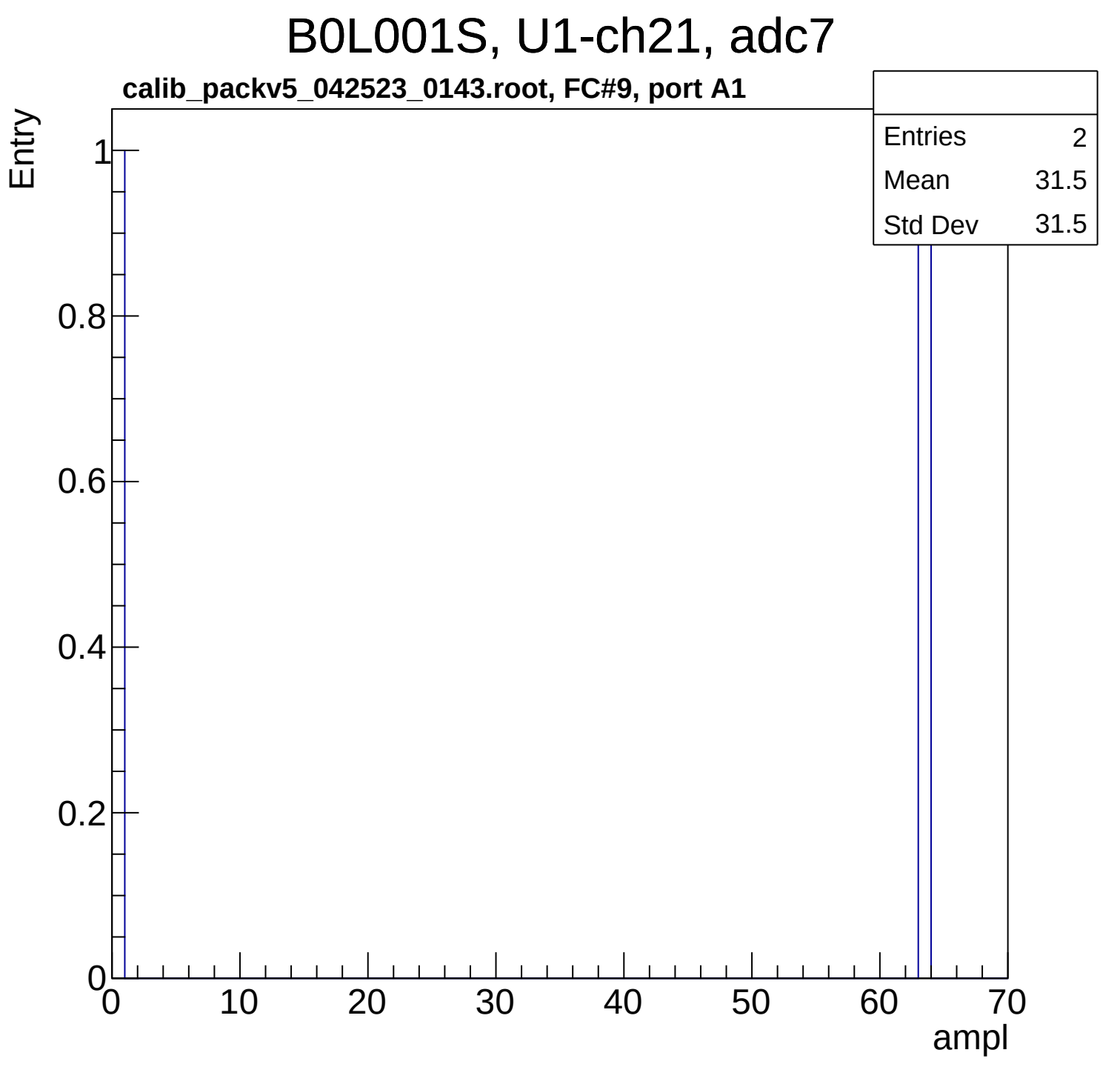
20
30
40
50
60
70


B0L001S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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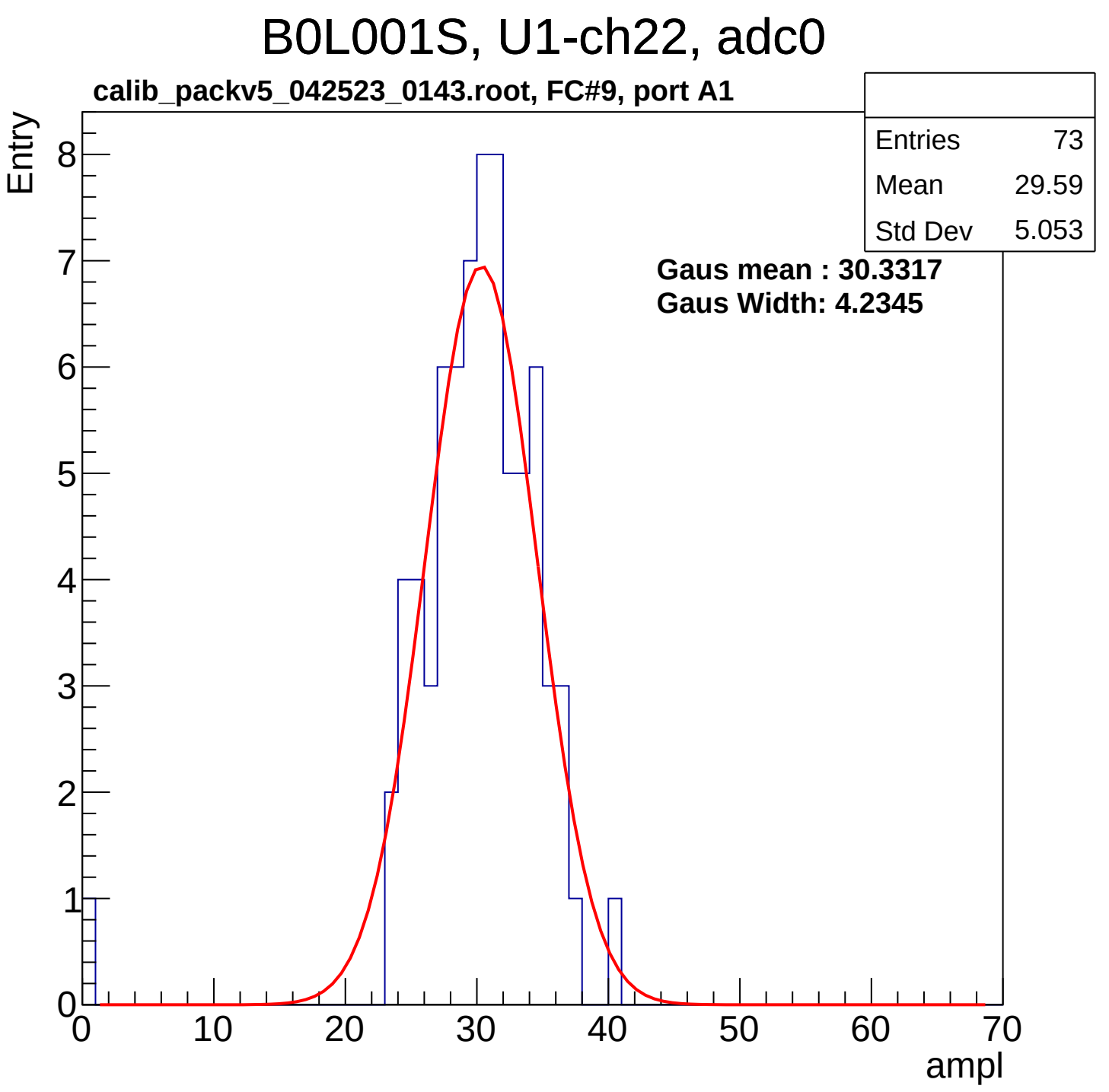
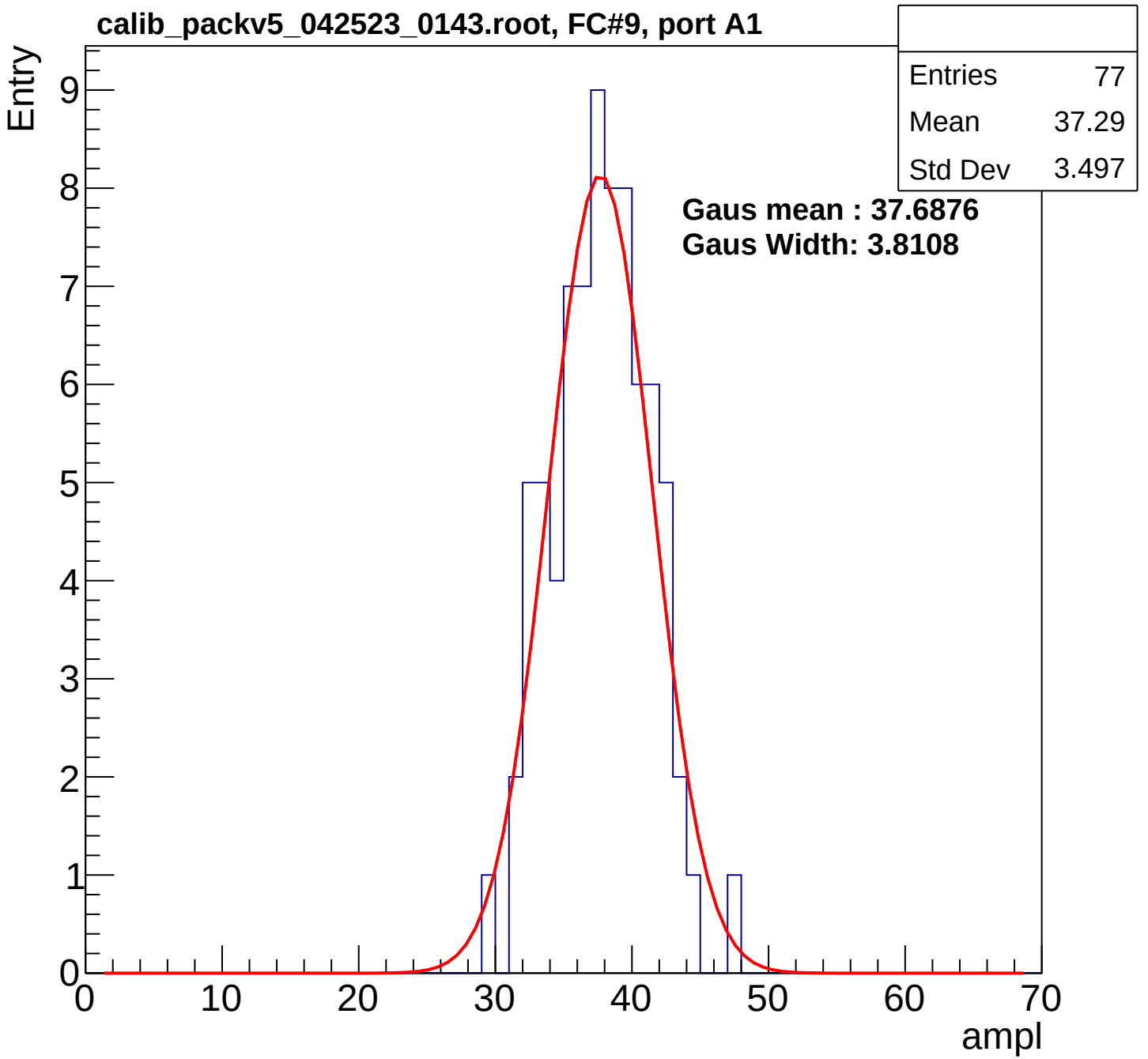
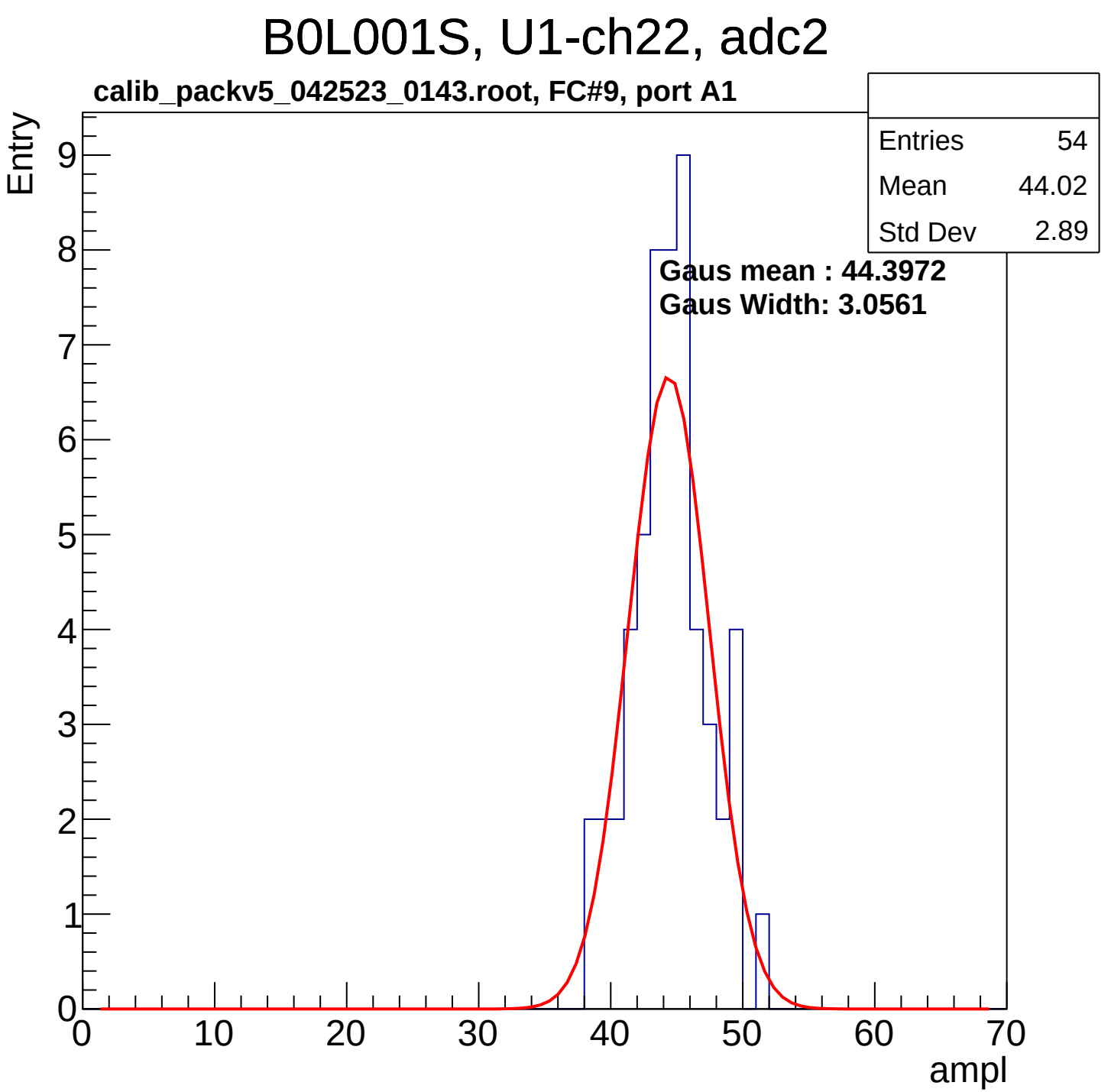
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.59
Std Dev 5.053
Gaus mean : 30.3317
Gaus Width: 4.2345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.29
Std Dev 3.497
Gaus mean : 37.6876
Gaus Width: 3.8108
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 44.02
Std Dev 2.89
Gaus mean : 44.3972
Gaus Width: 3.0561
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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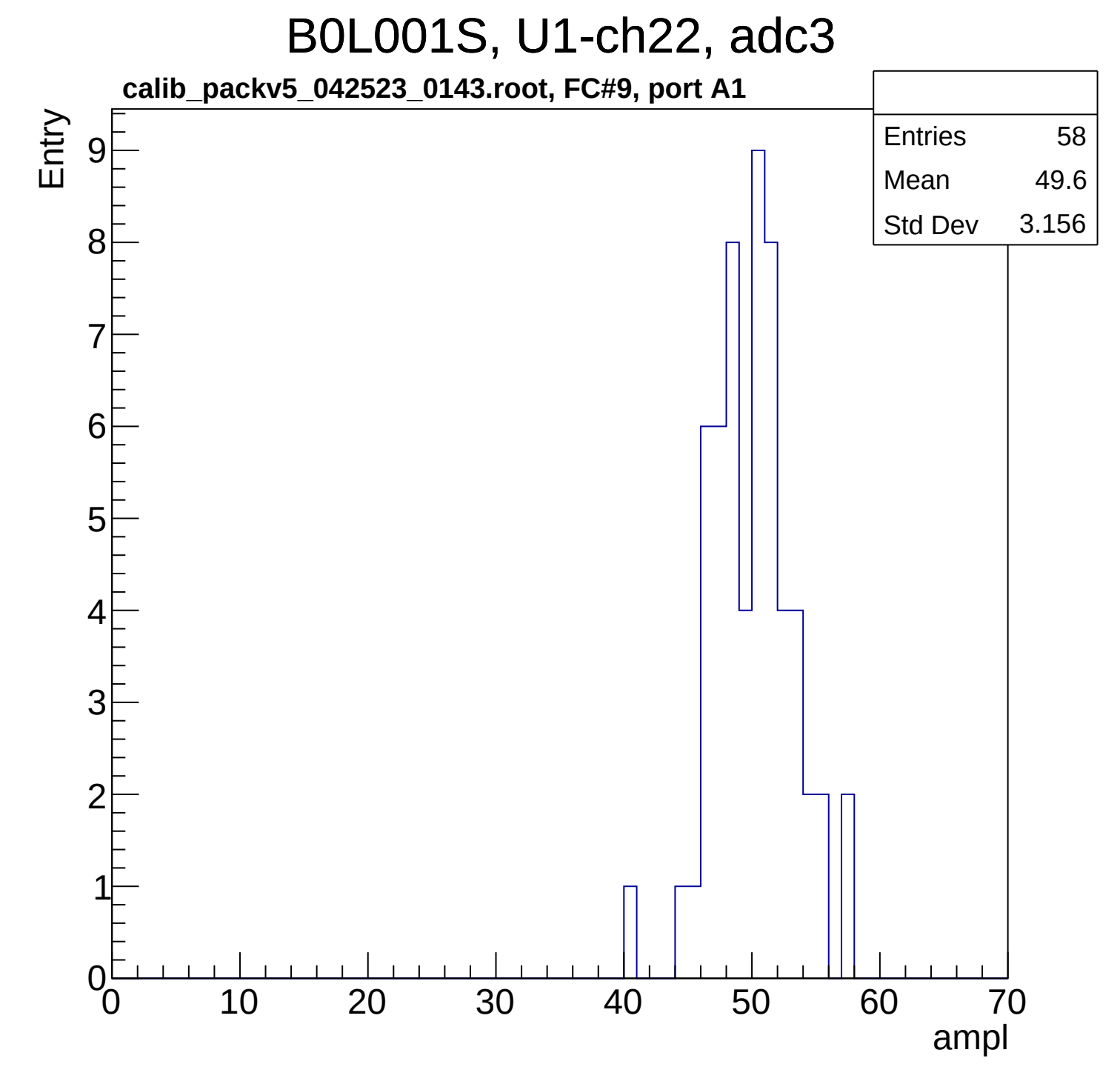
B0L001S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 49.6
Std Dev 3.156
Entry
ampl
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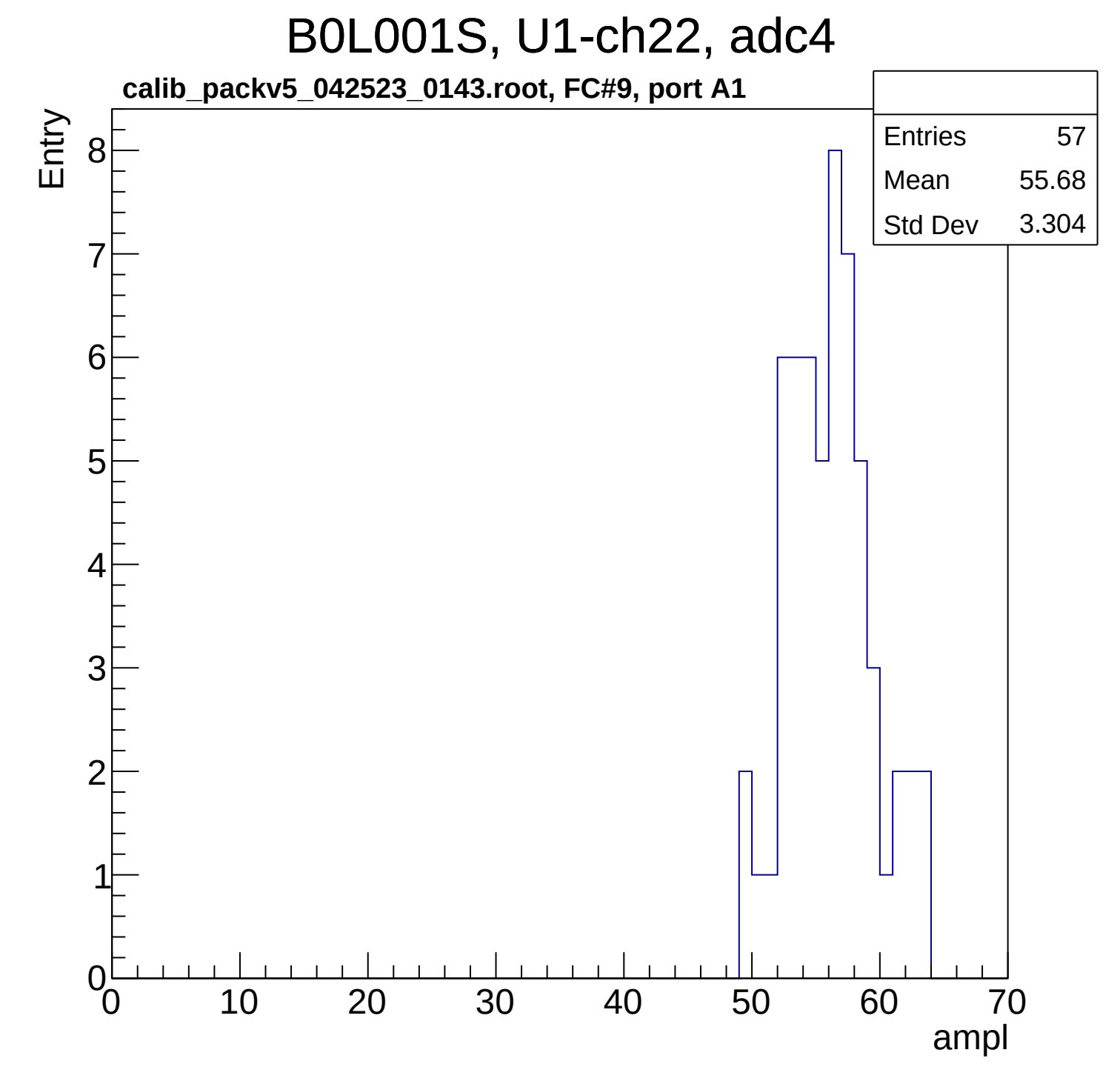
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.68
Std Dev 3.304
Entry
0
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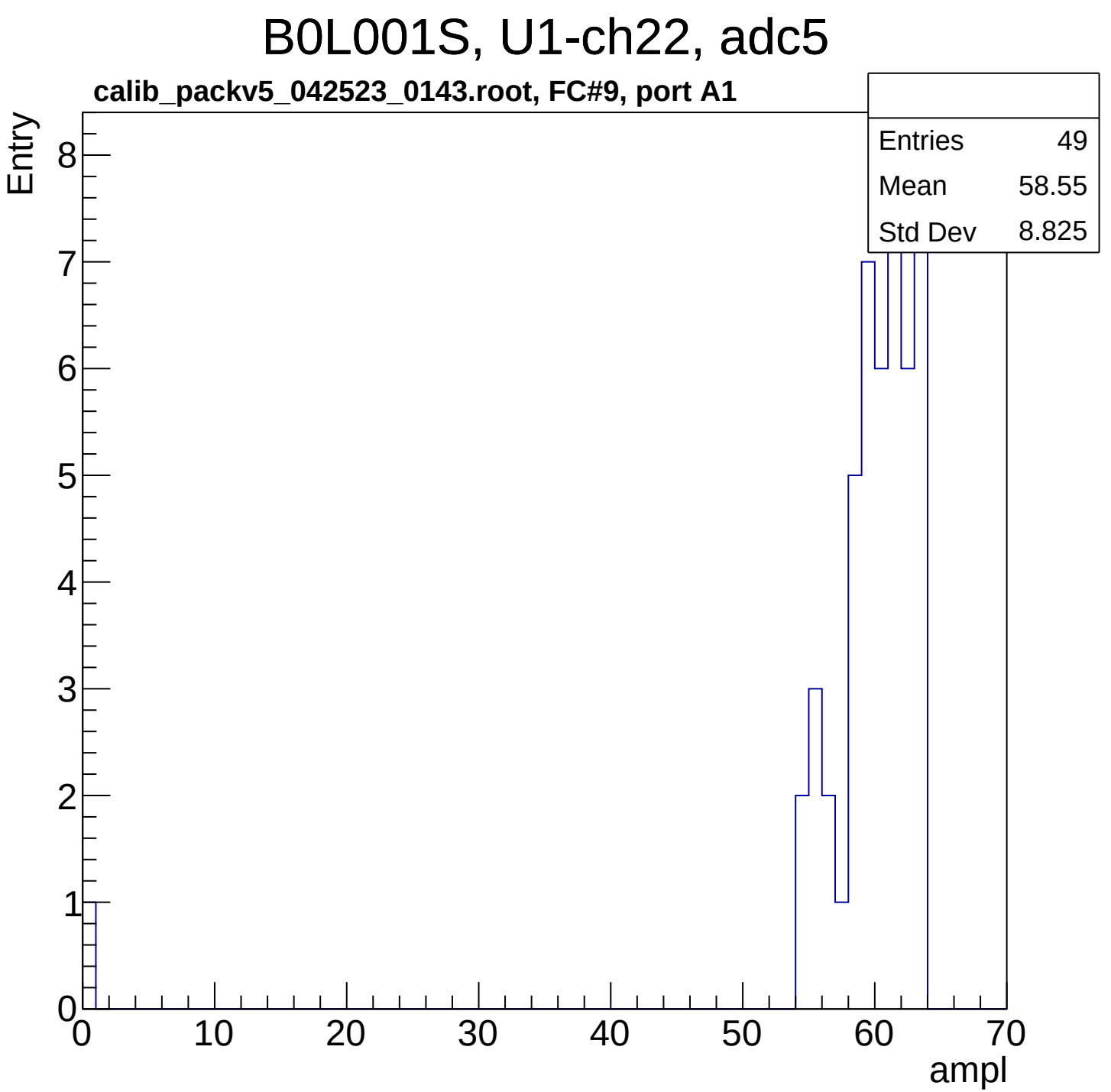
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.55
Std Dev 8.825
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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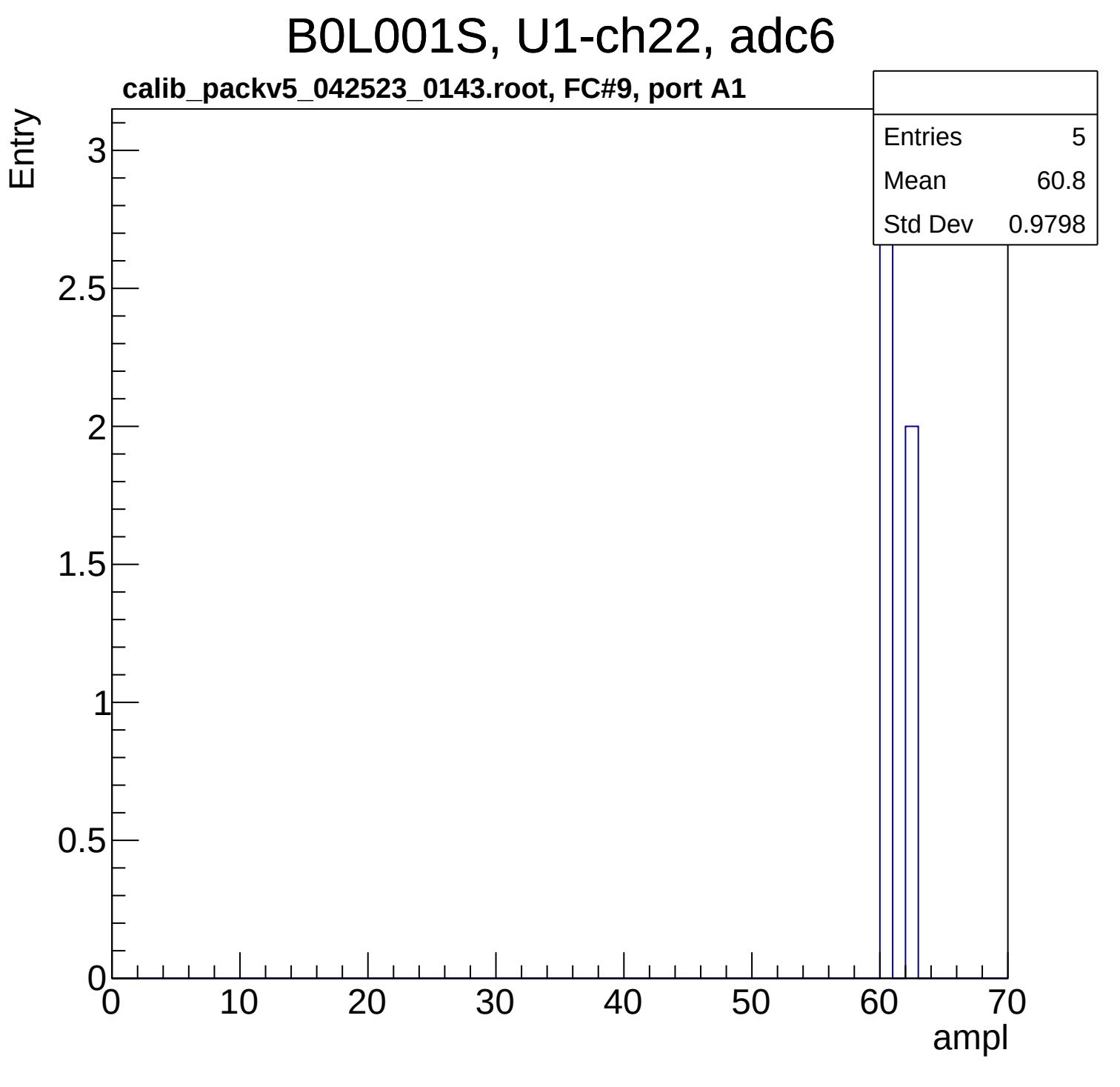
B0L001S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 60.8
Std Dev 0.9798
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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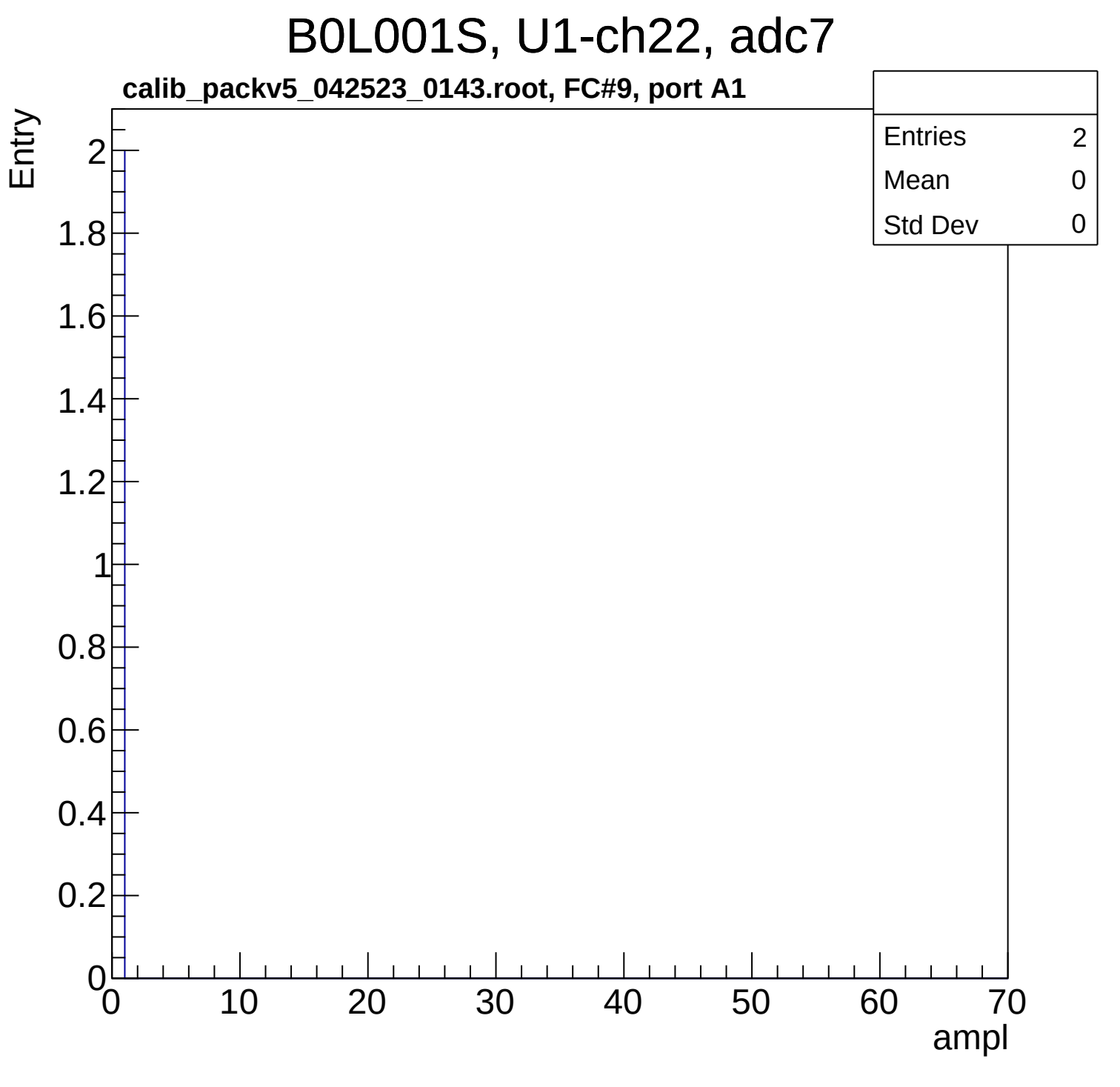
40
50
60
70


B0L001S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
10
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30
40
50
60
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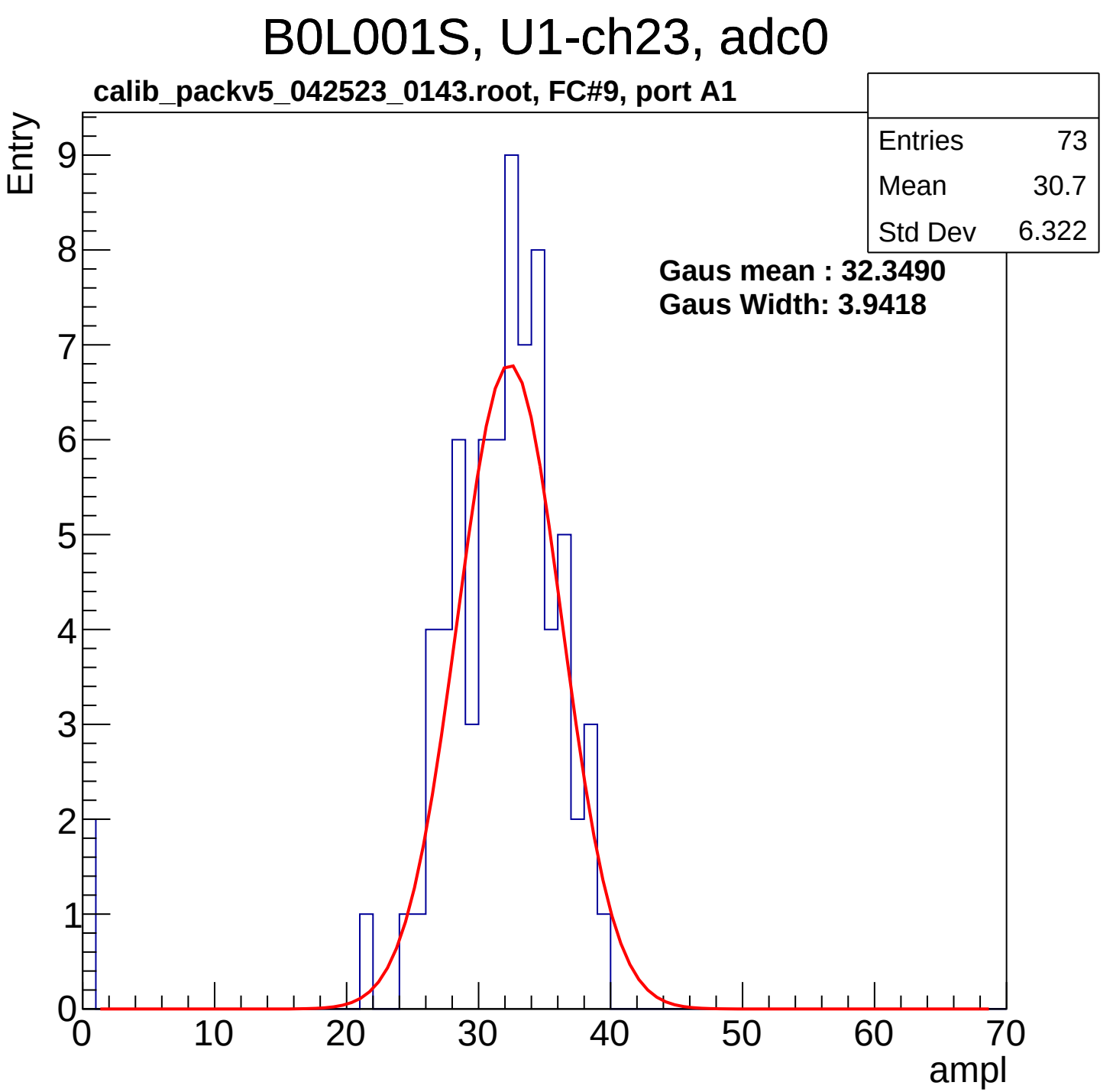
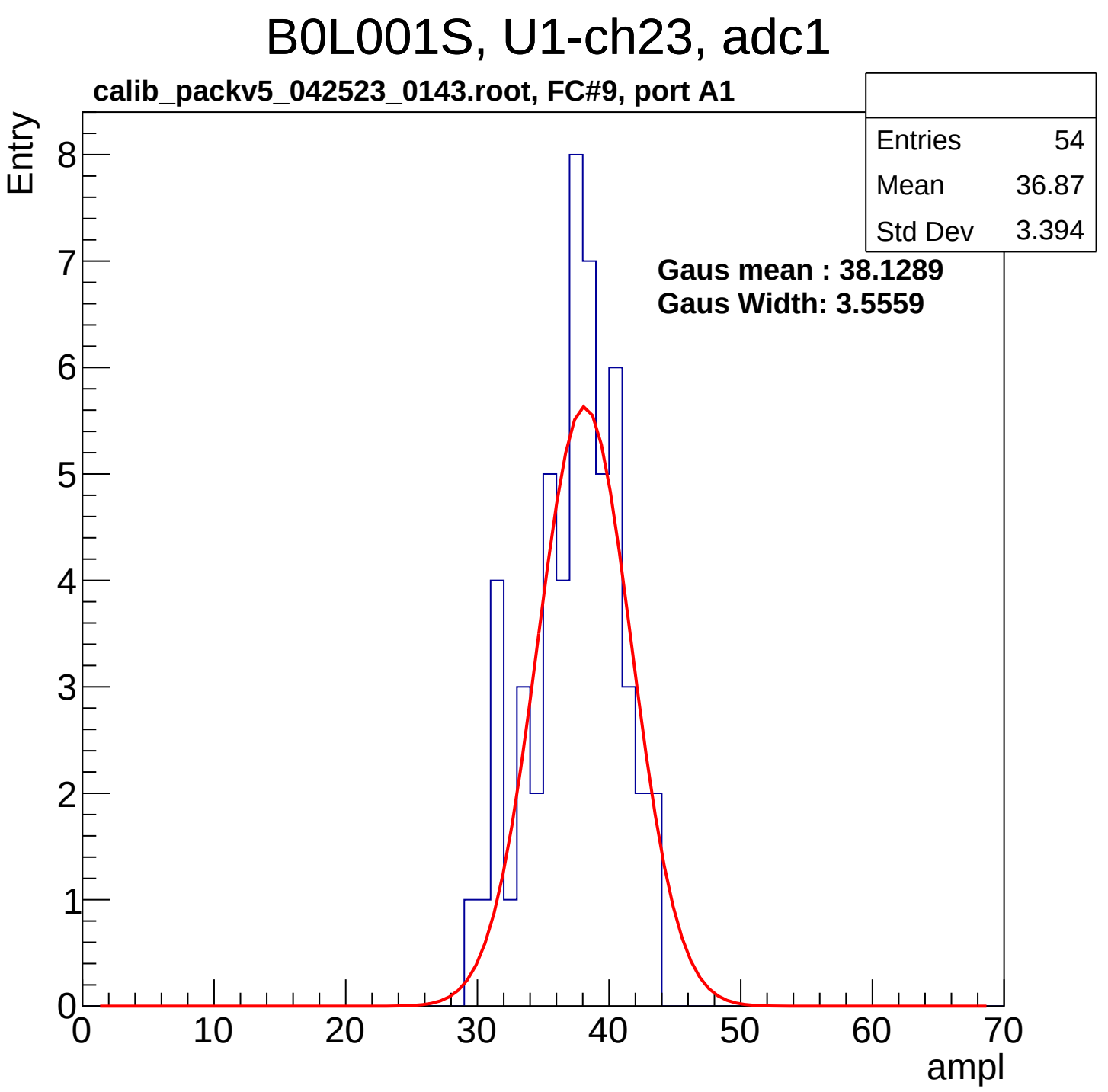

B0L001S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.7
Std Dev 6.322
Gaus mean : 32.3490
Gaus Width: 3.9418
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 36.87
Std Dev 3.394
Gaus mean : 38.1289
Gaus Width: 3.5559
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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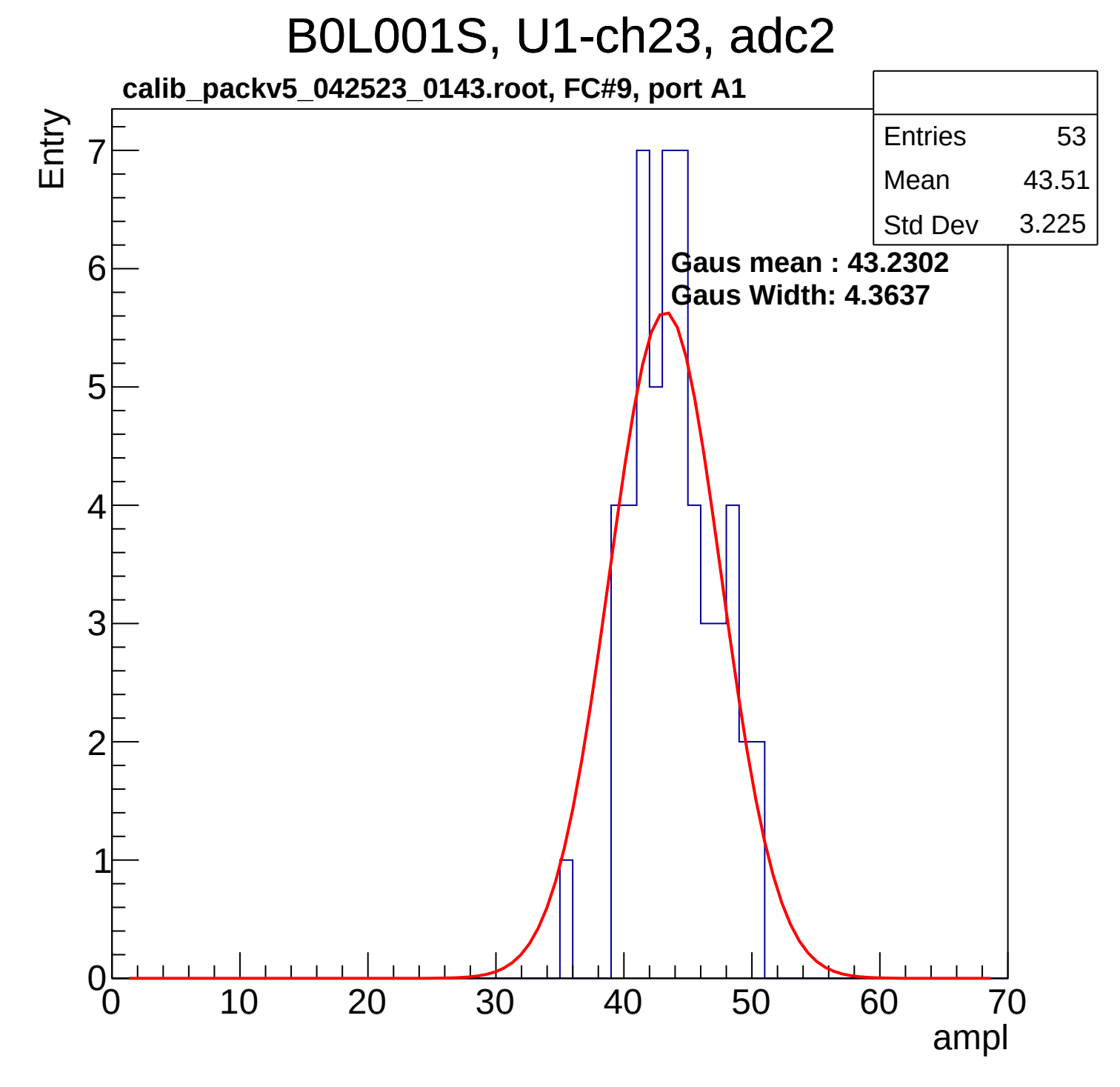
B0L001S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 43.51
Std Dev 3.225
Gaus mean : 43.2302
Gaus Width: 4.3637
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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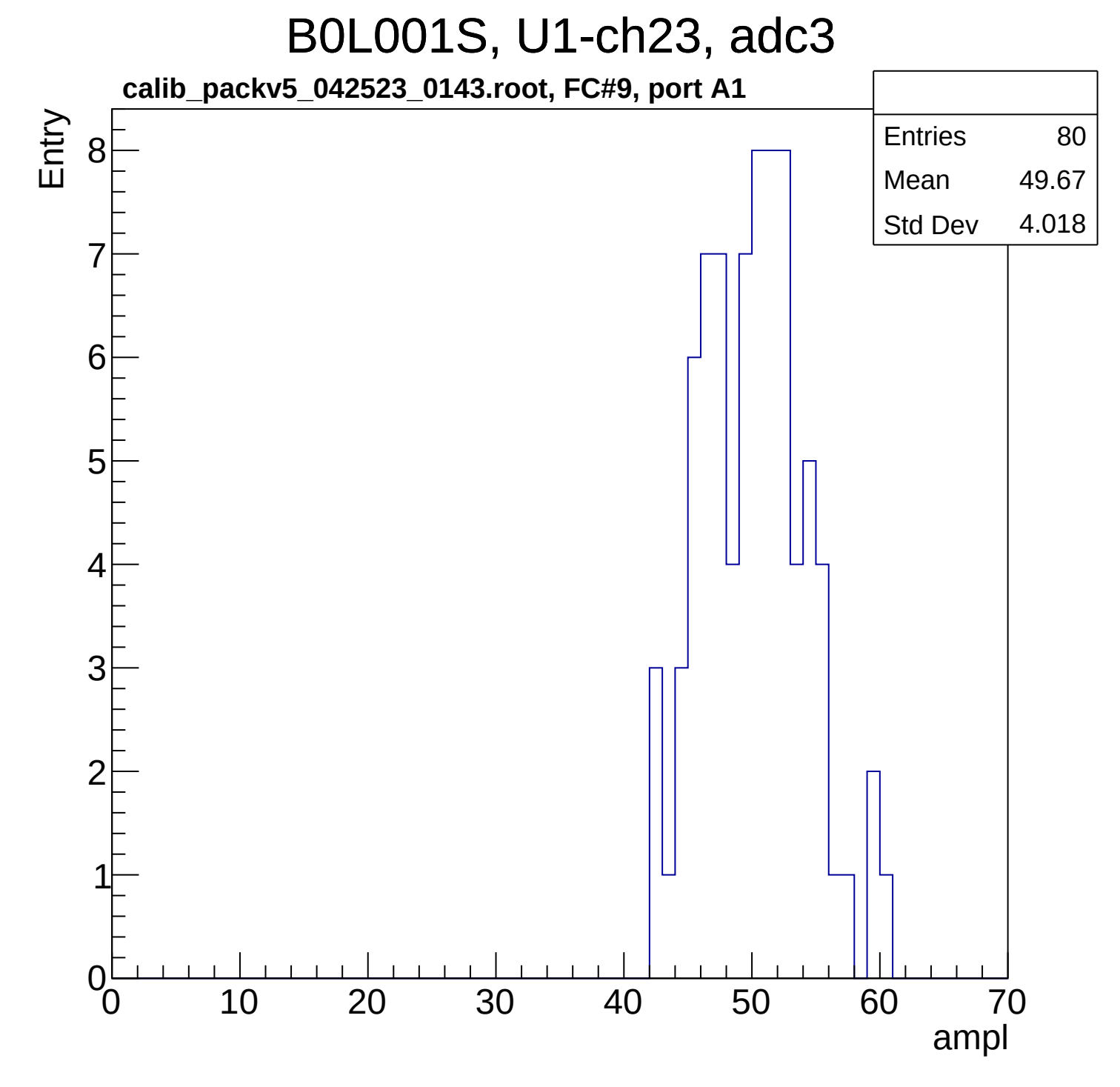
B0L001S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 49.67
Std Dev 4.018
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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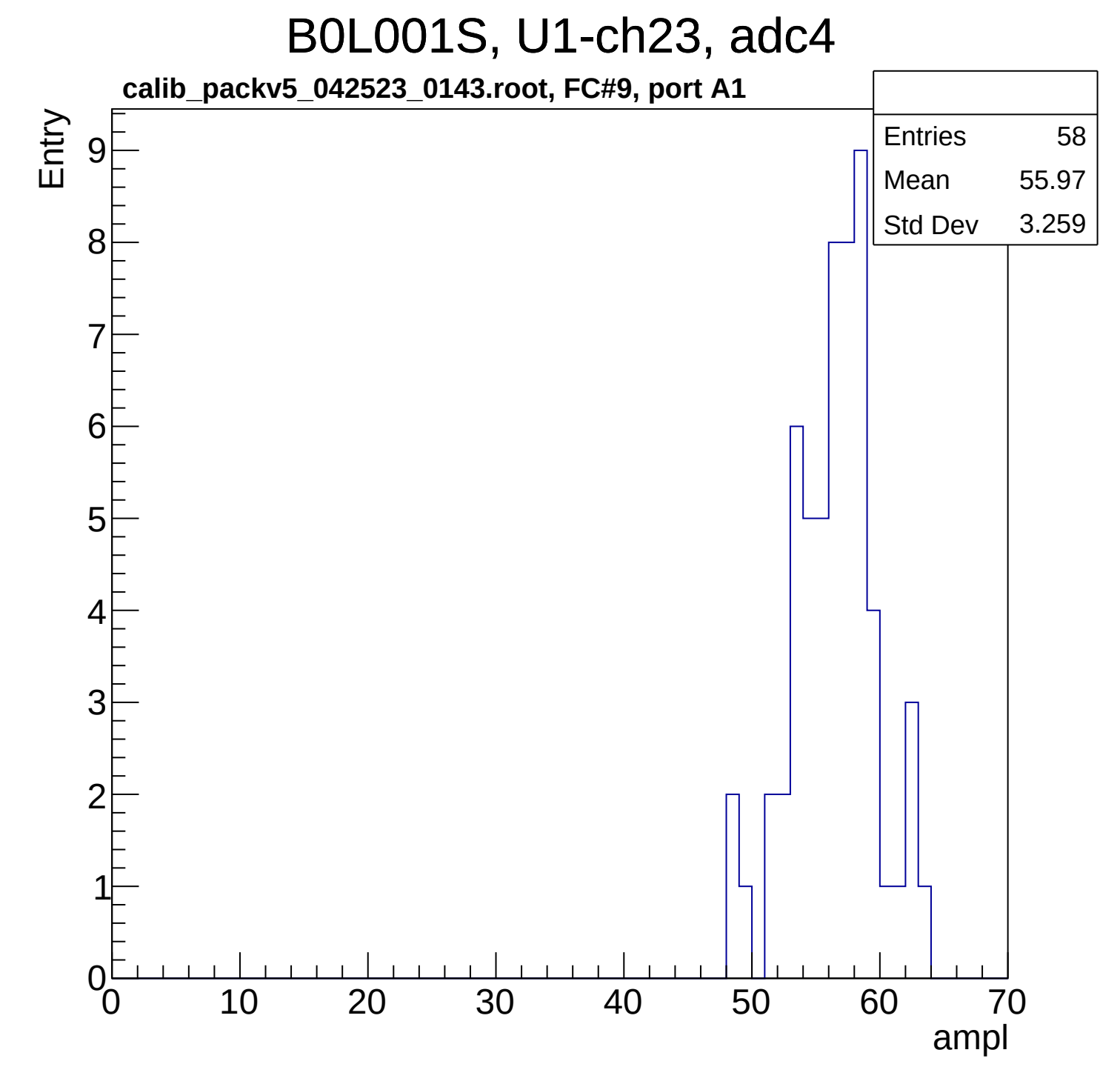
B0L001S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.97
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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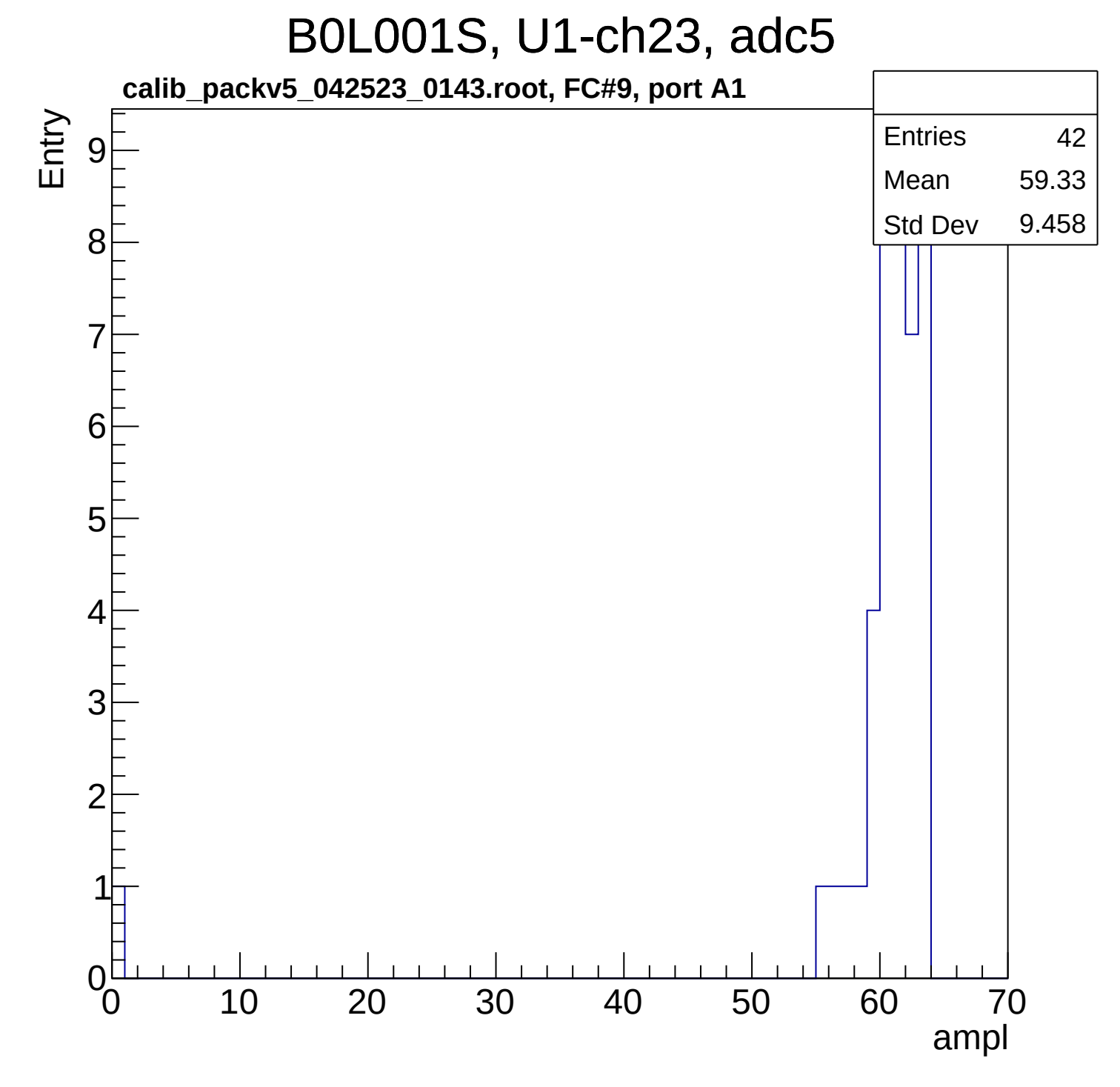
B0L001S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.33
Std Dev 9.458
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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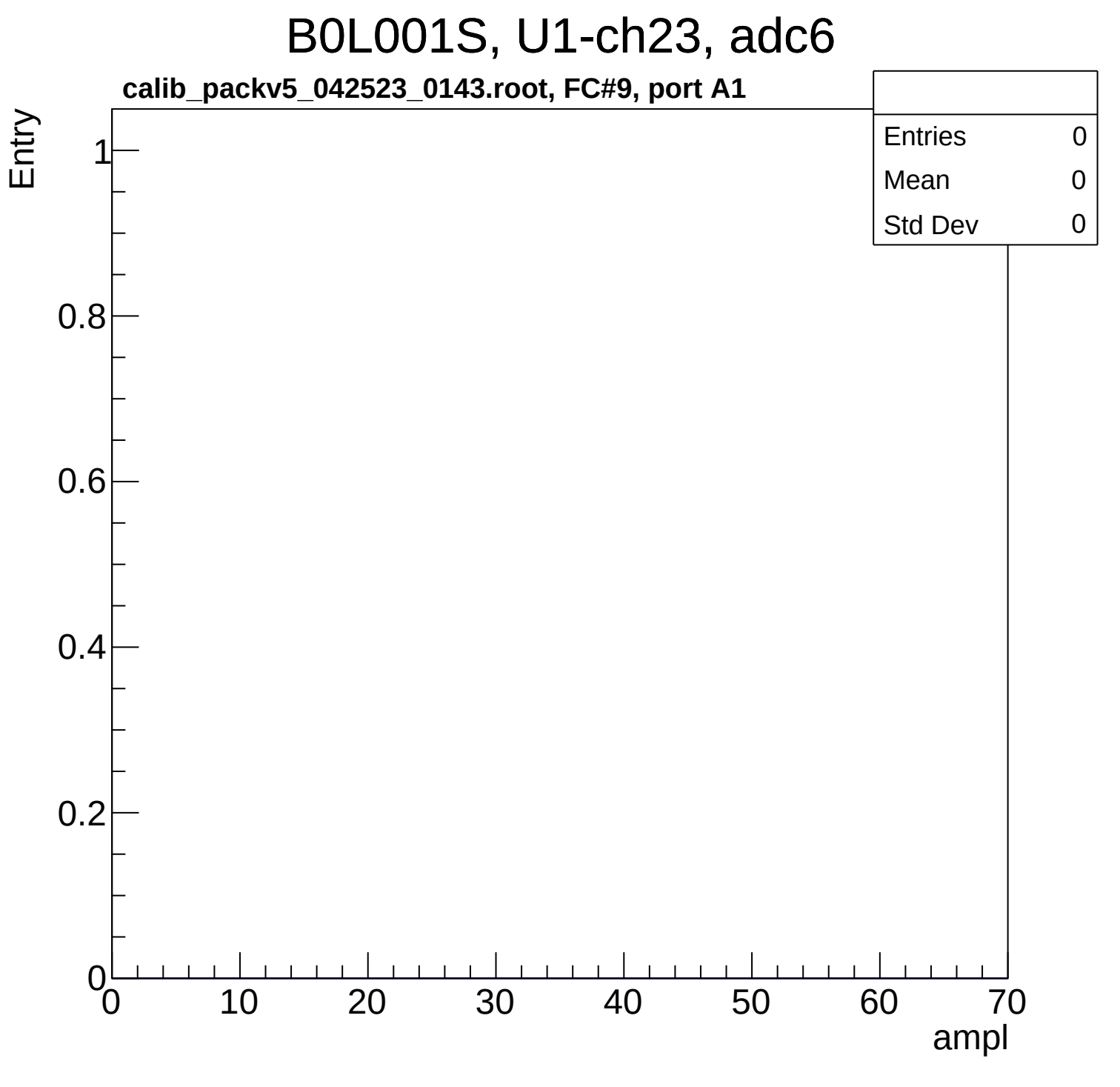
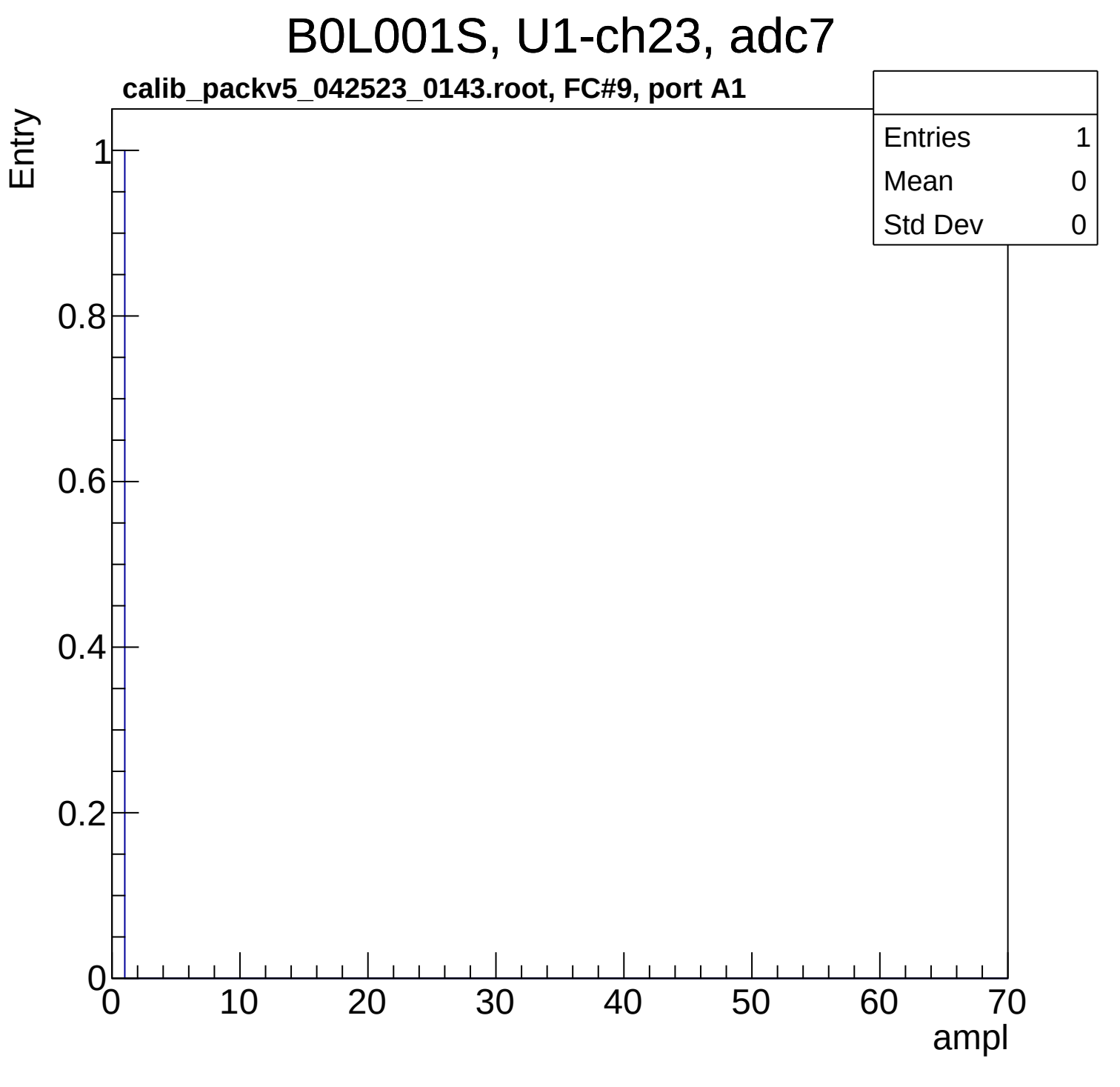
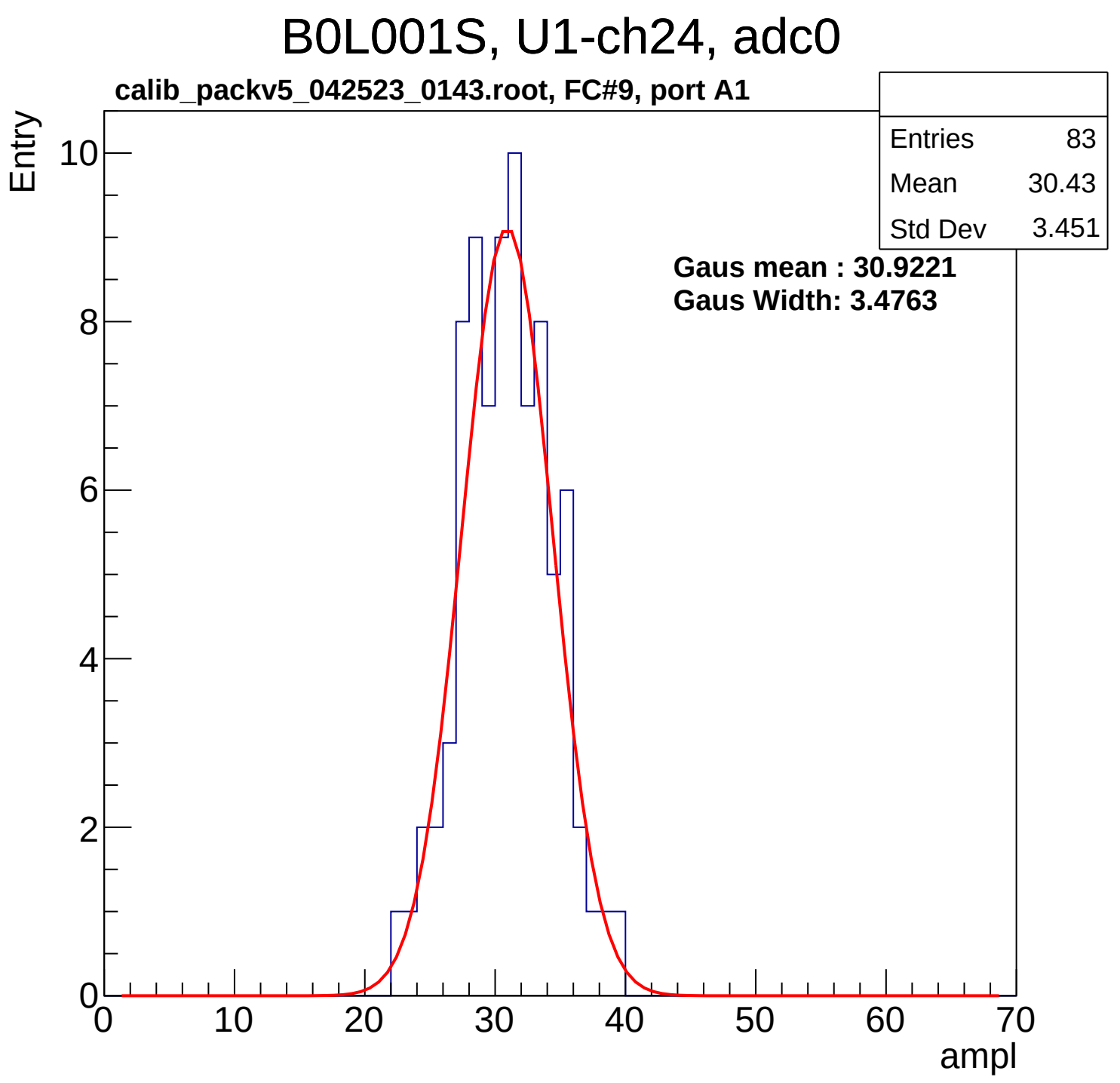
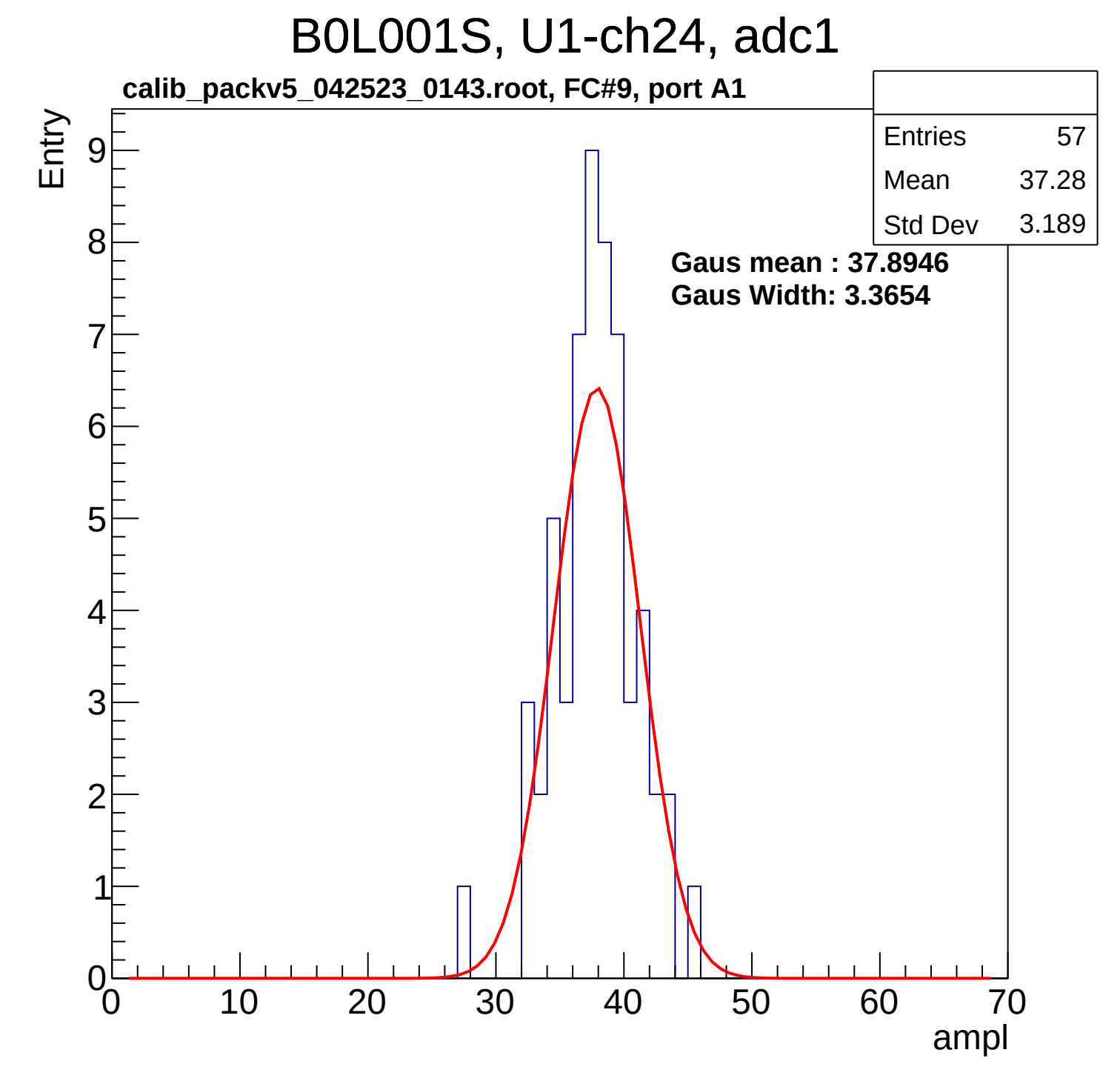
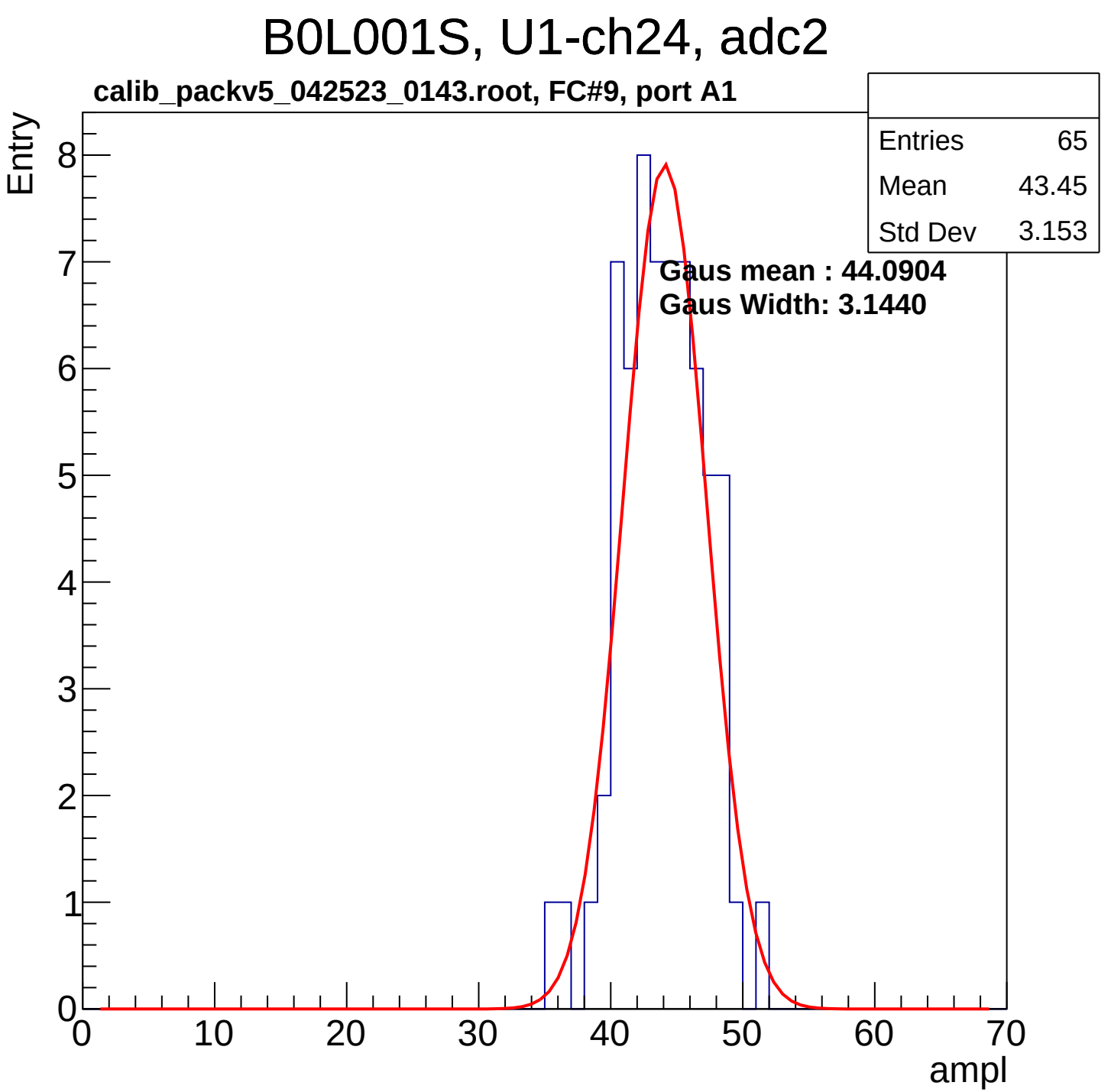
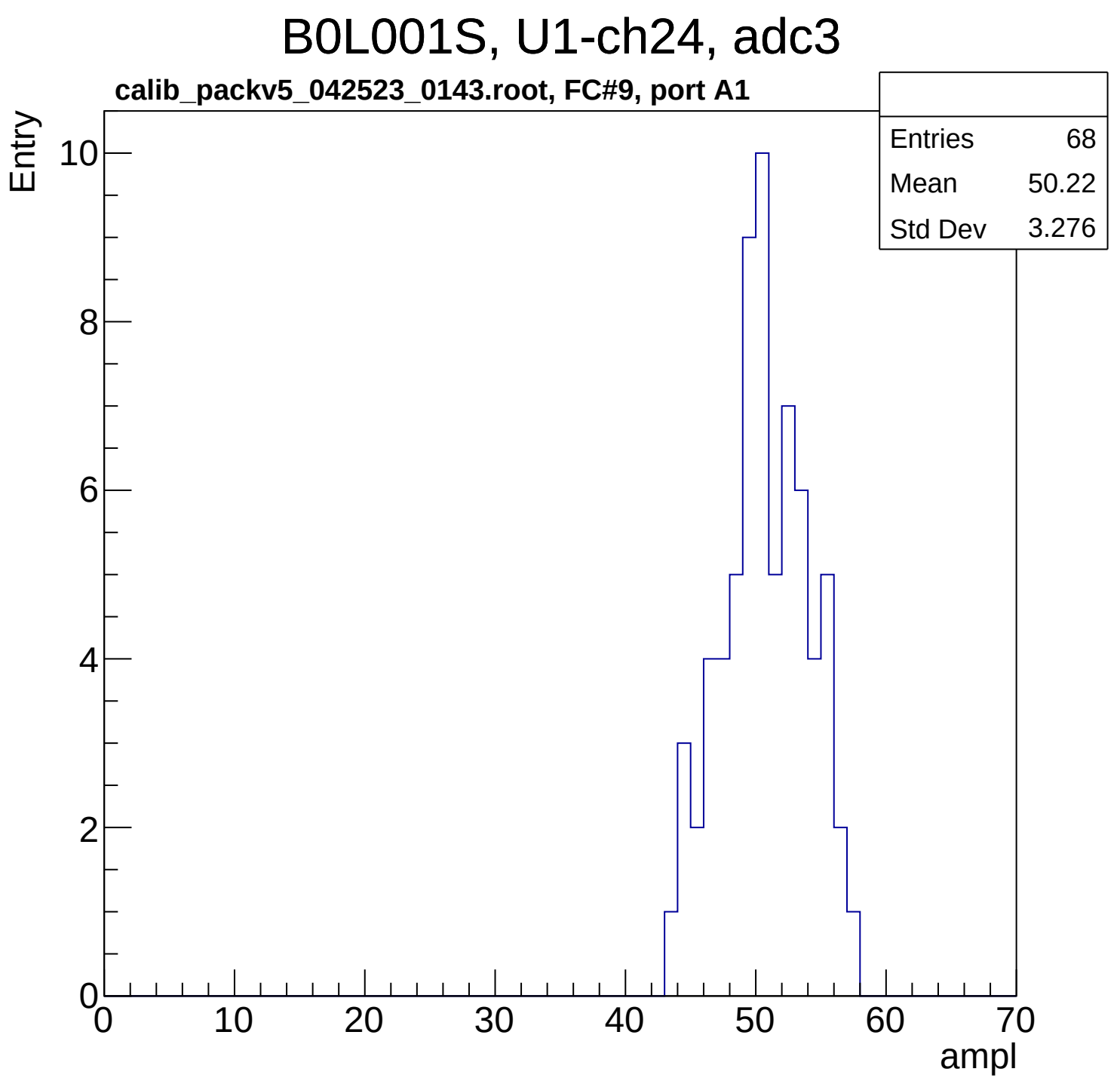

B0L001S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

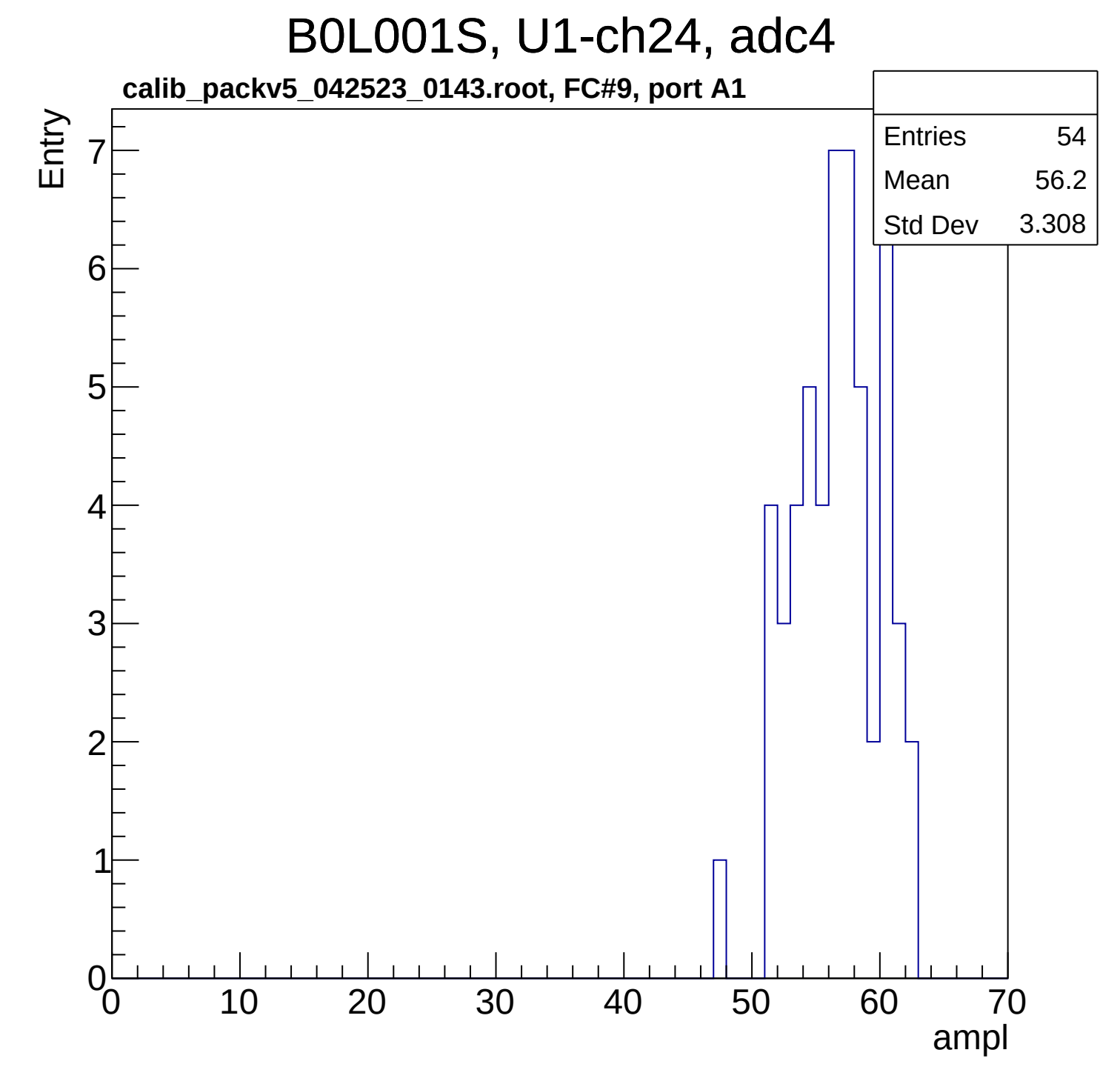

B0L001S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

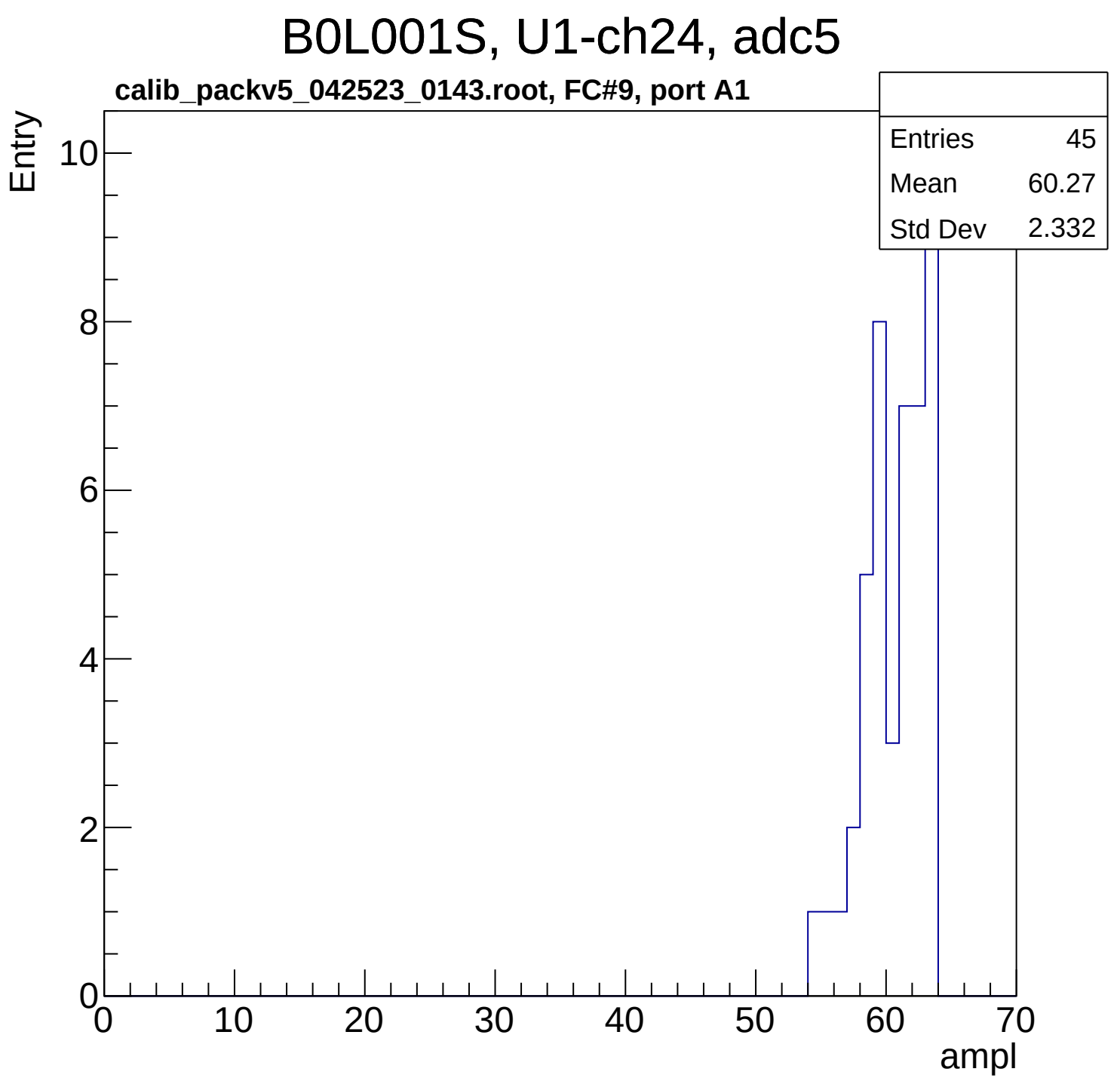

B0L001S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 30.43
Std Dev 3.451
Gaus mean : 30.9221
Gaus Width: 3.4763
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

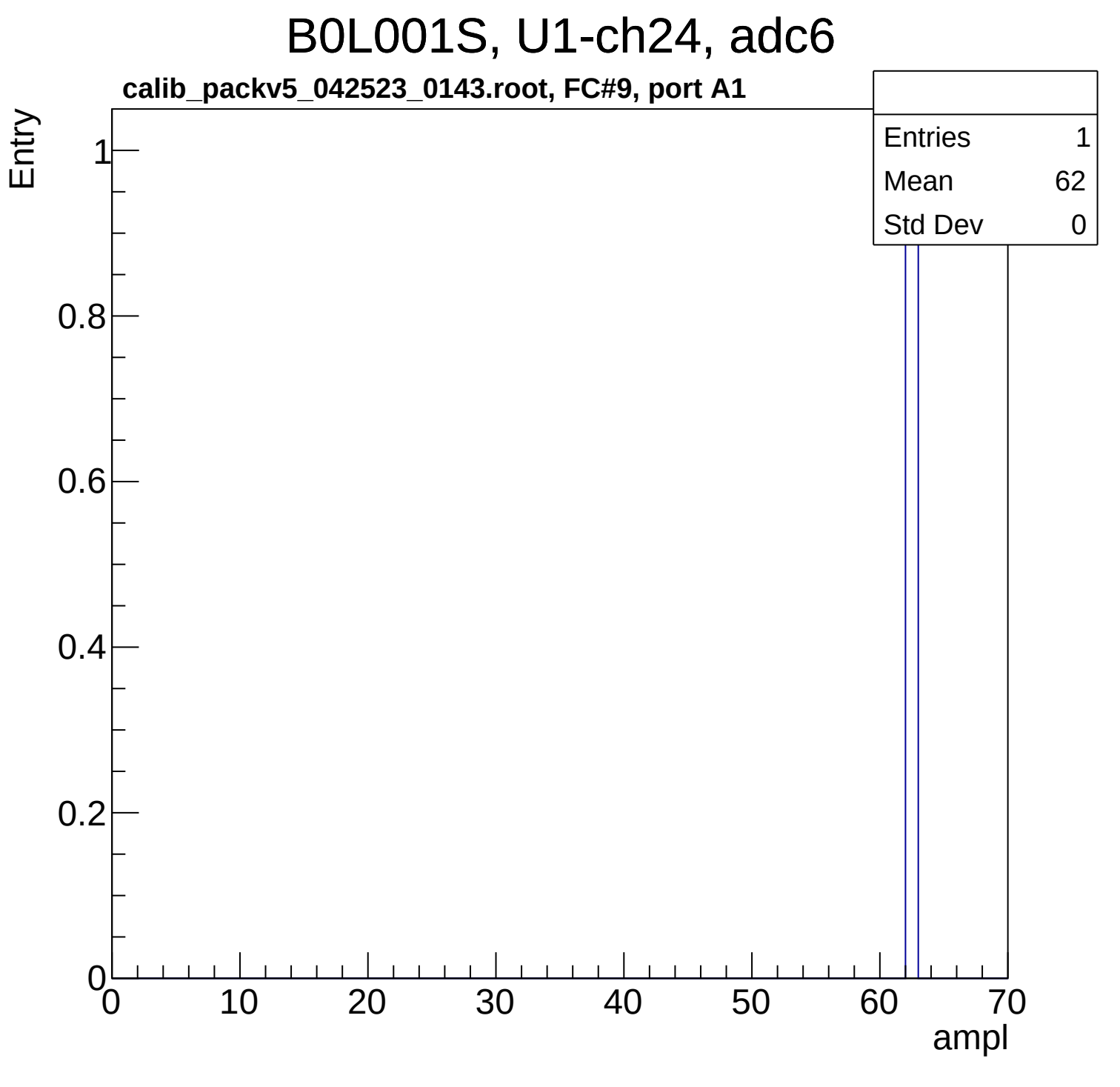

B0L001S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.28
Std Dev 3.189
Gaus mean : 37.8946
Gaus Width: 3.3654
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.45
Std Dev 3.153
Gaus mean : 44.0904
Gaus Width: 3.1440
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 50.22
Std Dev 3.276
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.2
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 60.27
Std Dev 2.332
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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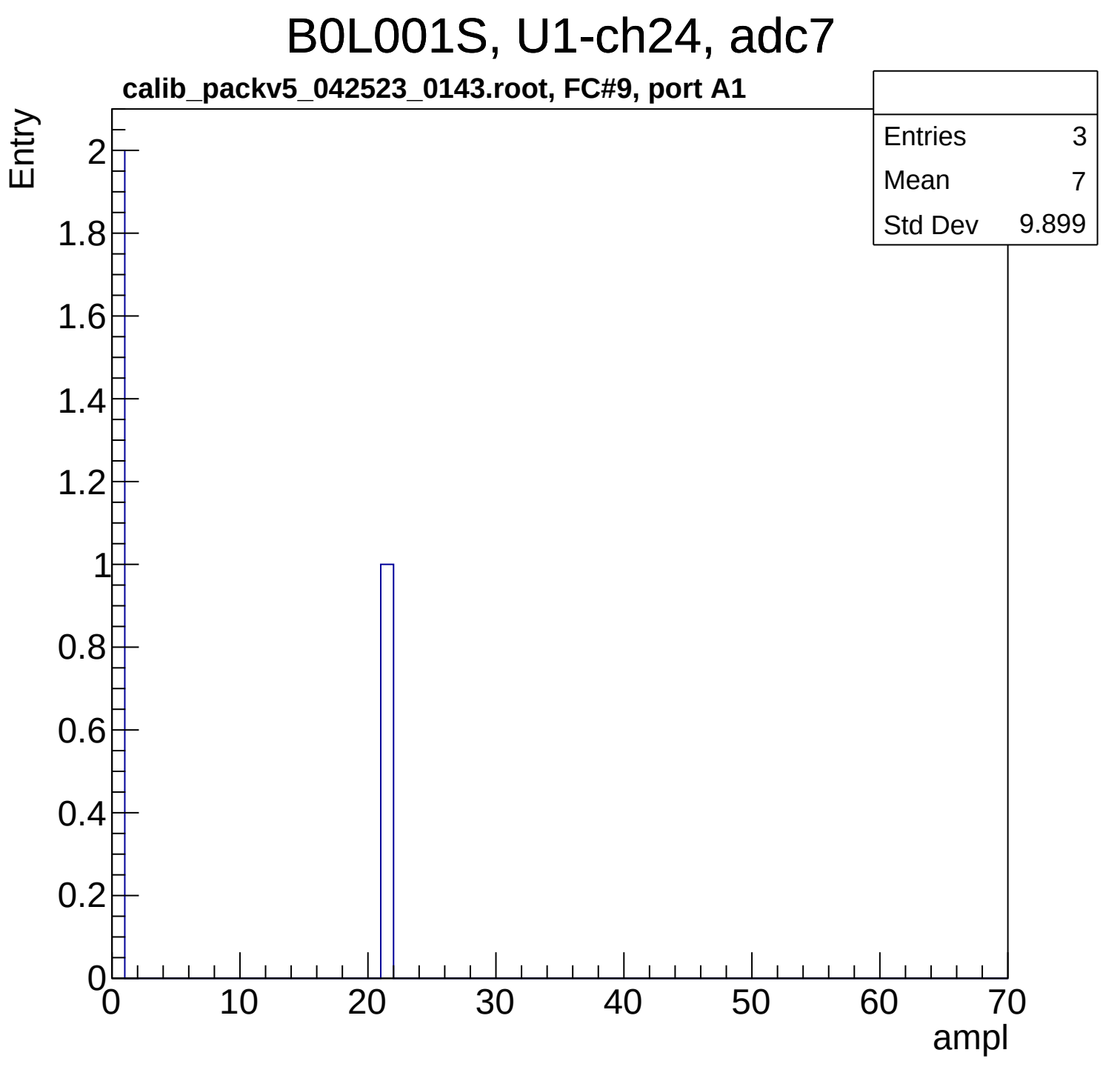
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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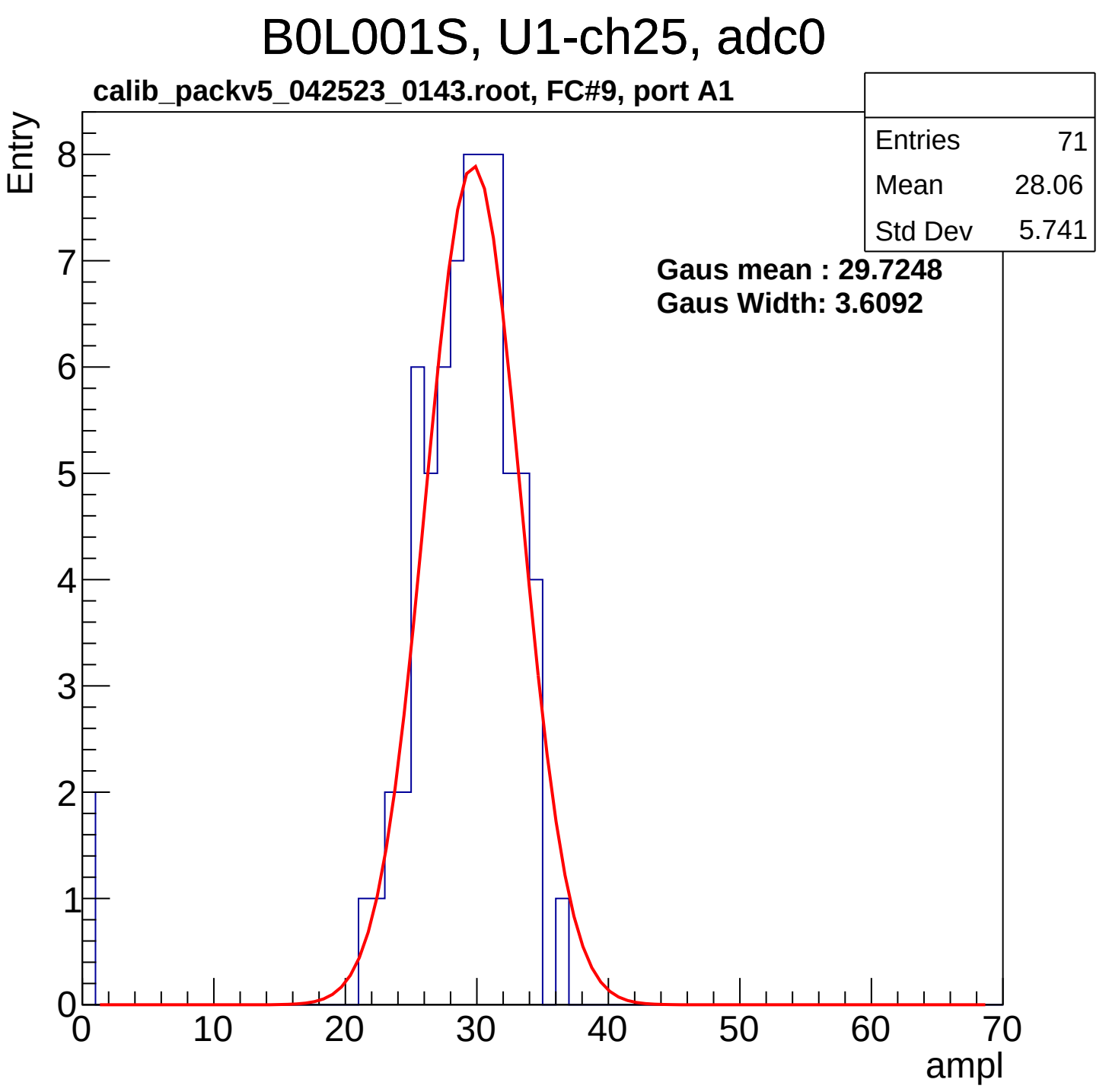
B0L001S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.06
Std Dev 5.741
Gaus mean : 29.7248
Gaus Width: 3.6092
Entry
ampl
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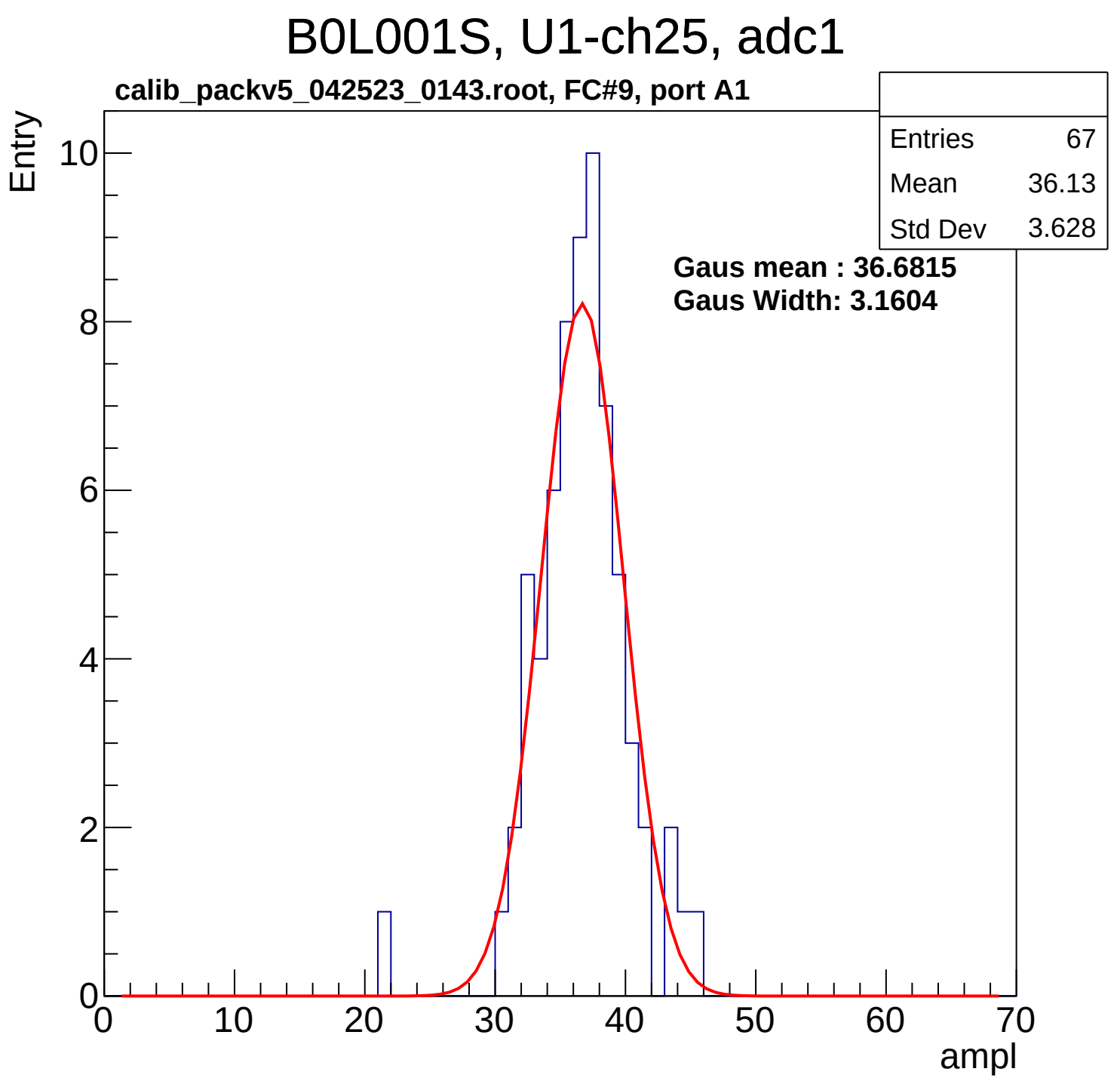
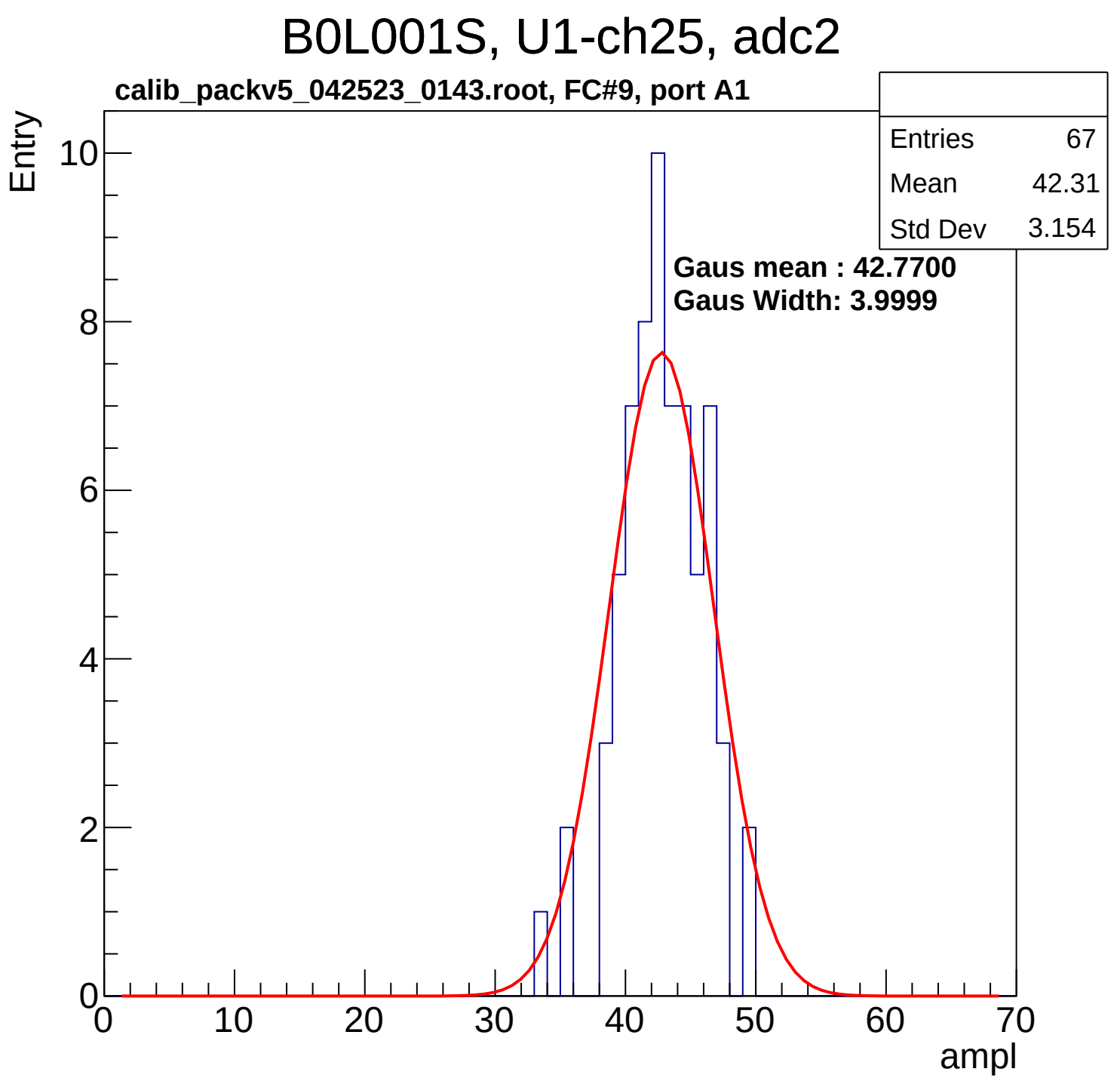
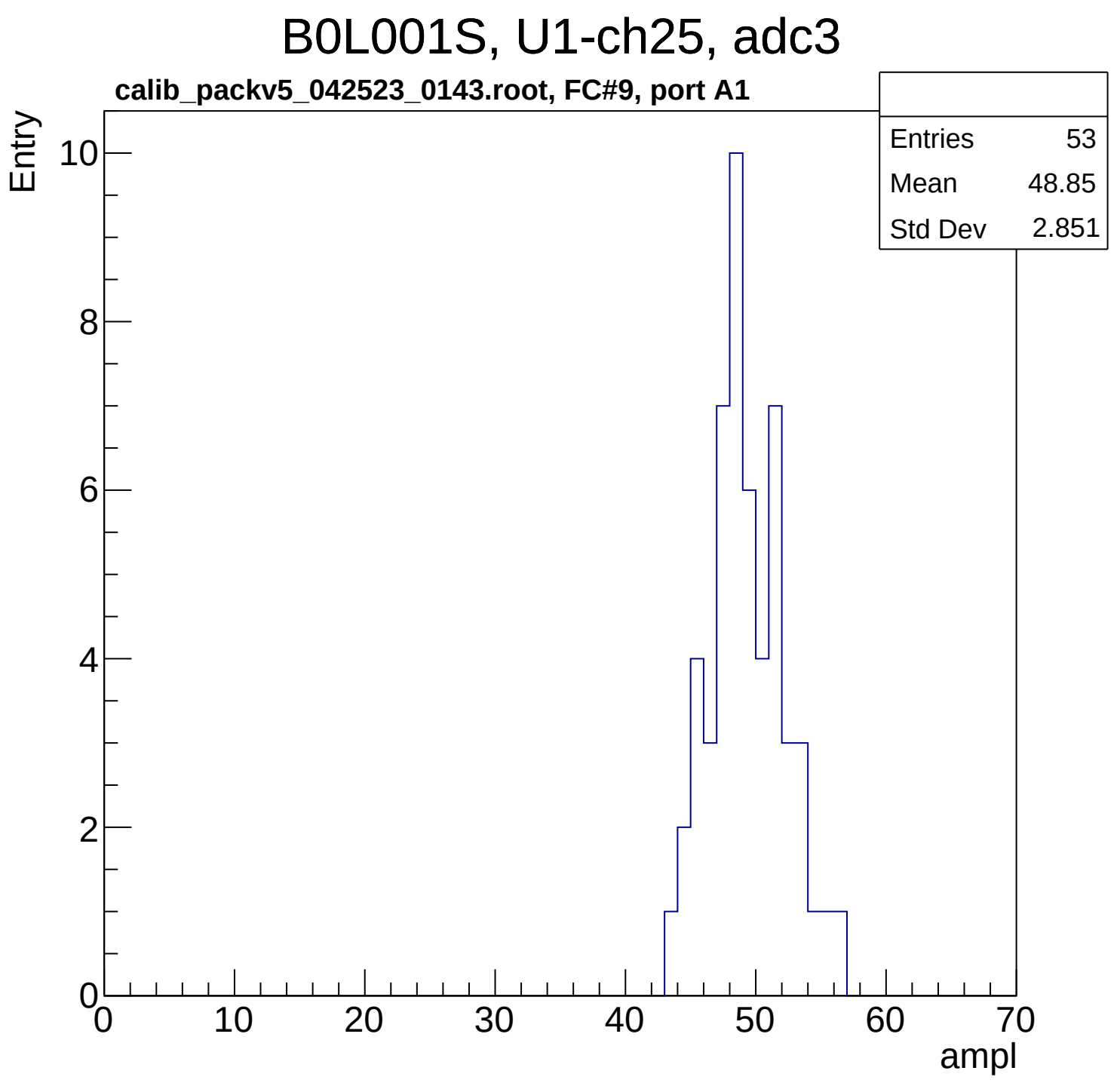
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.13
Std Dev 3.628
Gaus mean : 36.6815
Gaus Width: 3.1604
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 42.31
Std Dev 3.154
Gaus mean : 42.7700
Gaus Width: 3.9999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 48.85
Std Dev 2.851
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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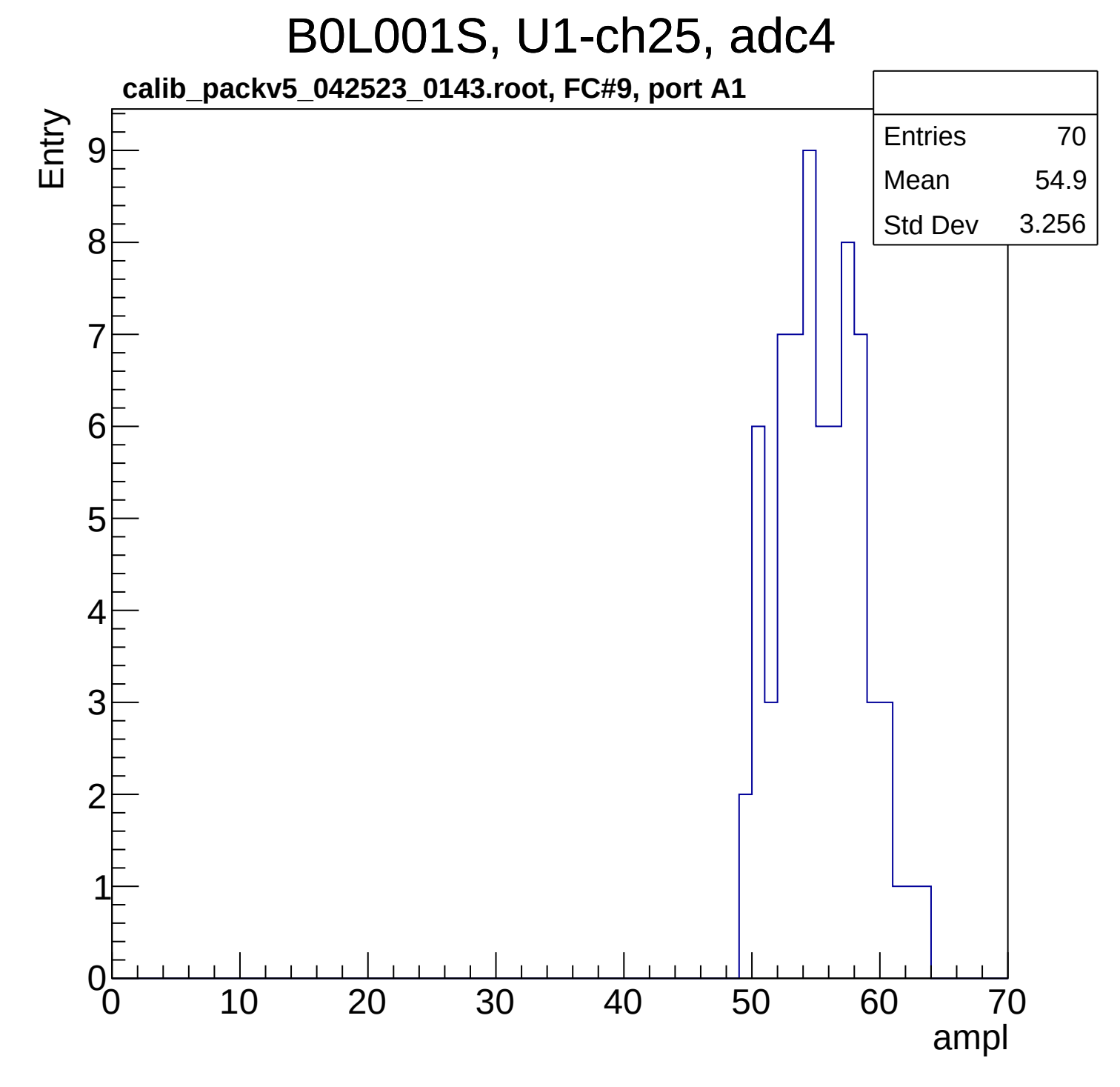
B0L001S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.9
Std Dev 3.256
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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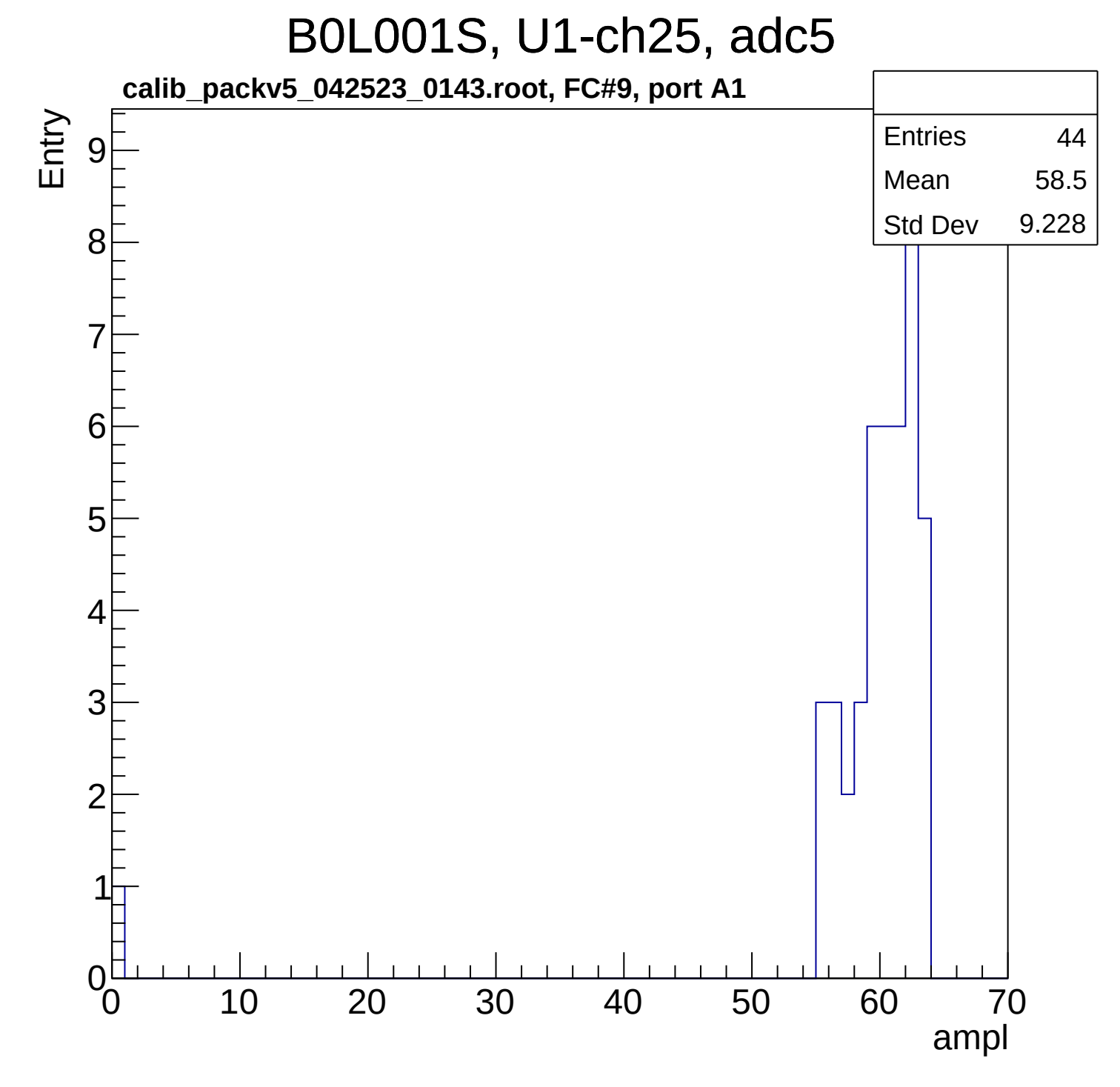
B0L001S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.5
Std Dev 9.228
Entry
ampl
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6
7
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0
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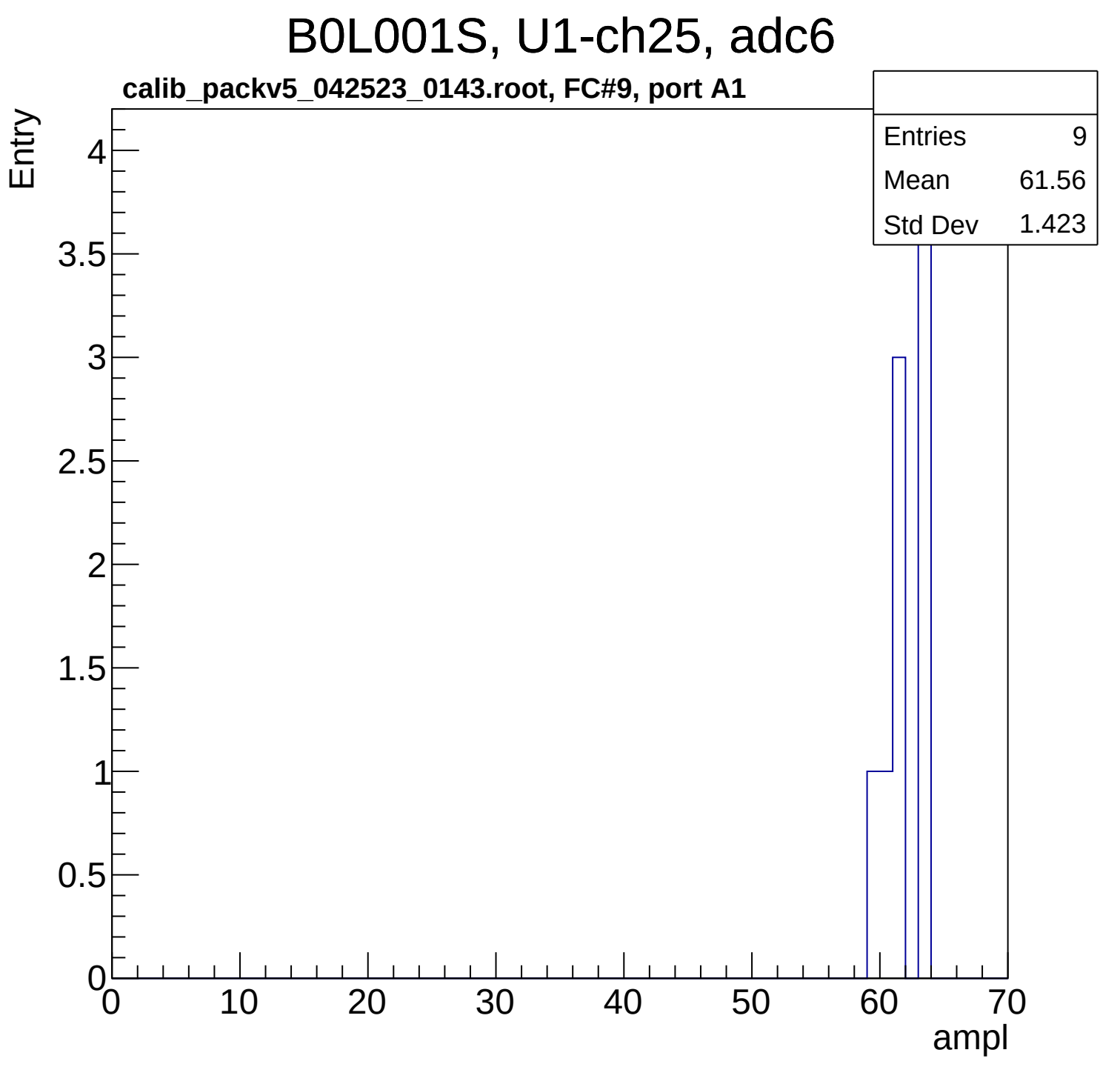
50
60
70


B0L001S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.56
Std Dev 1.423
Entry
ampl
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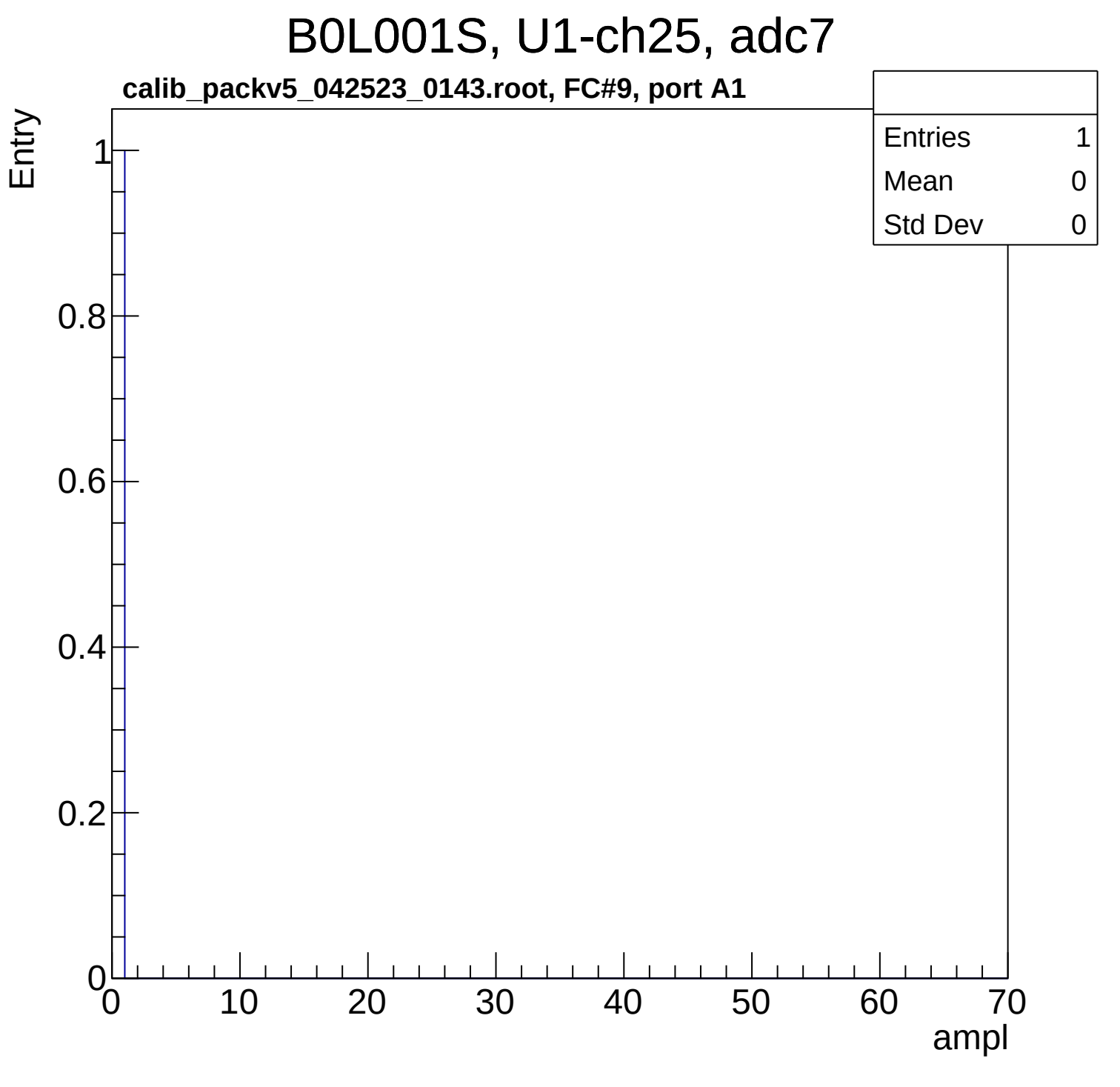
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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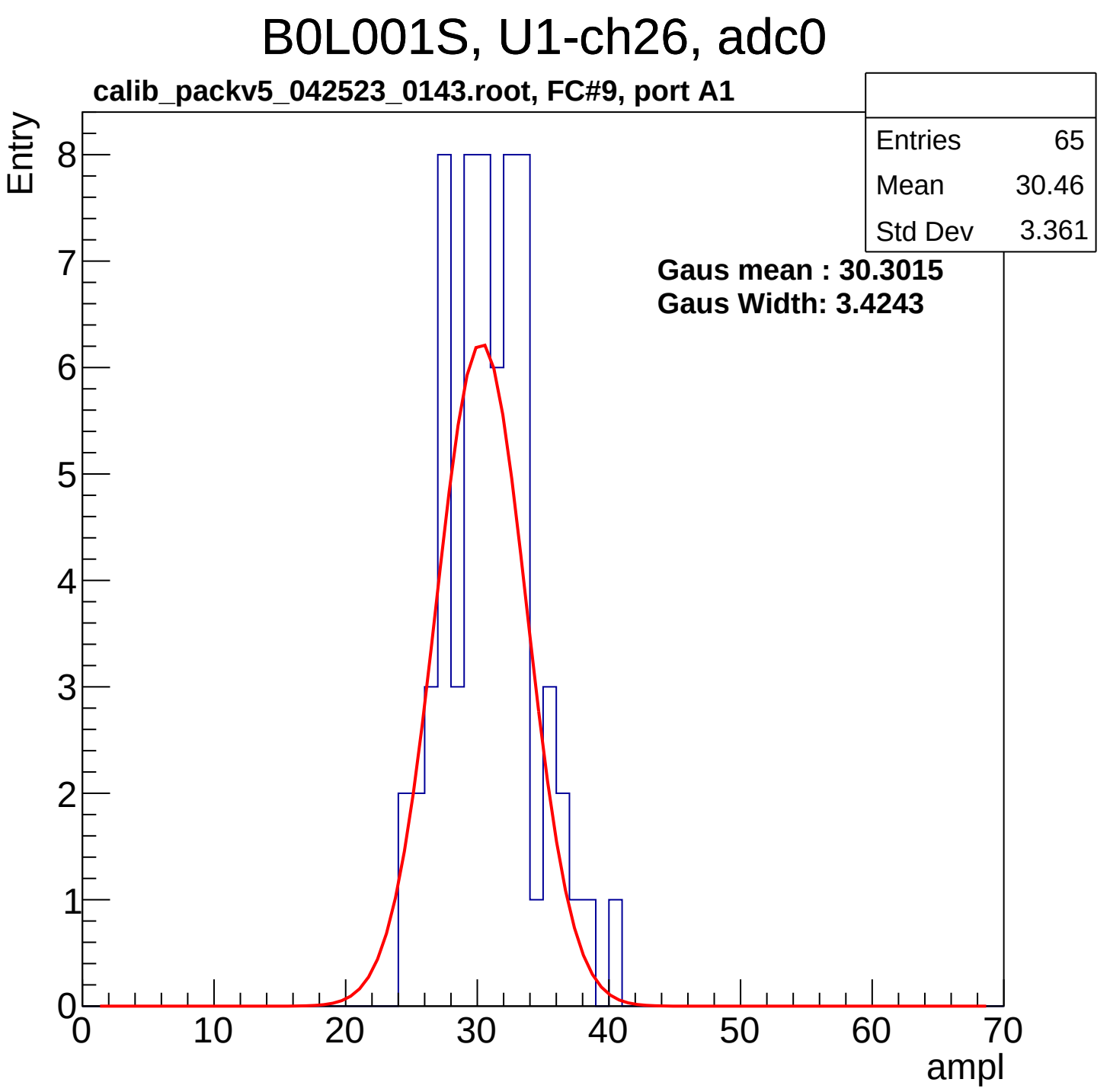
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.46
Std Dev 3.361
Gaus mean : 30.3015
Gaus Width: 3.4243
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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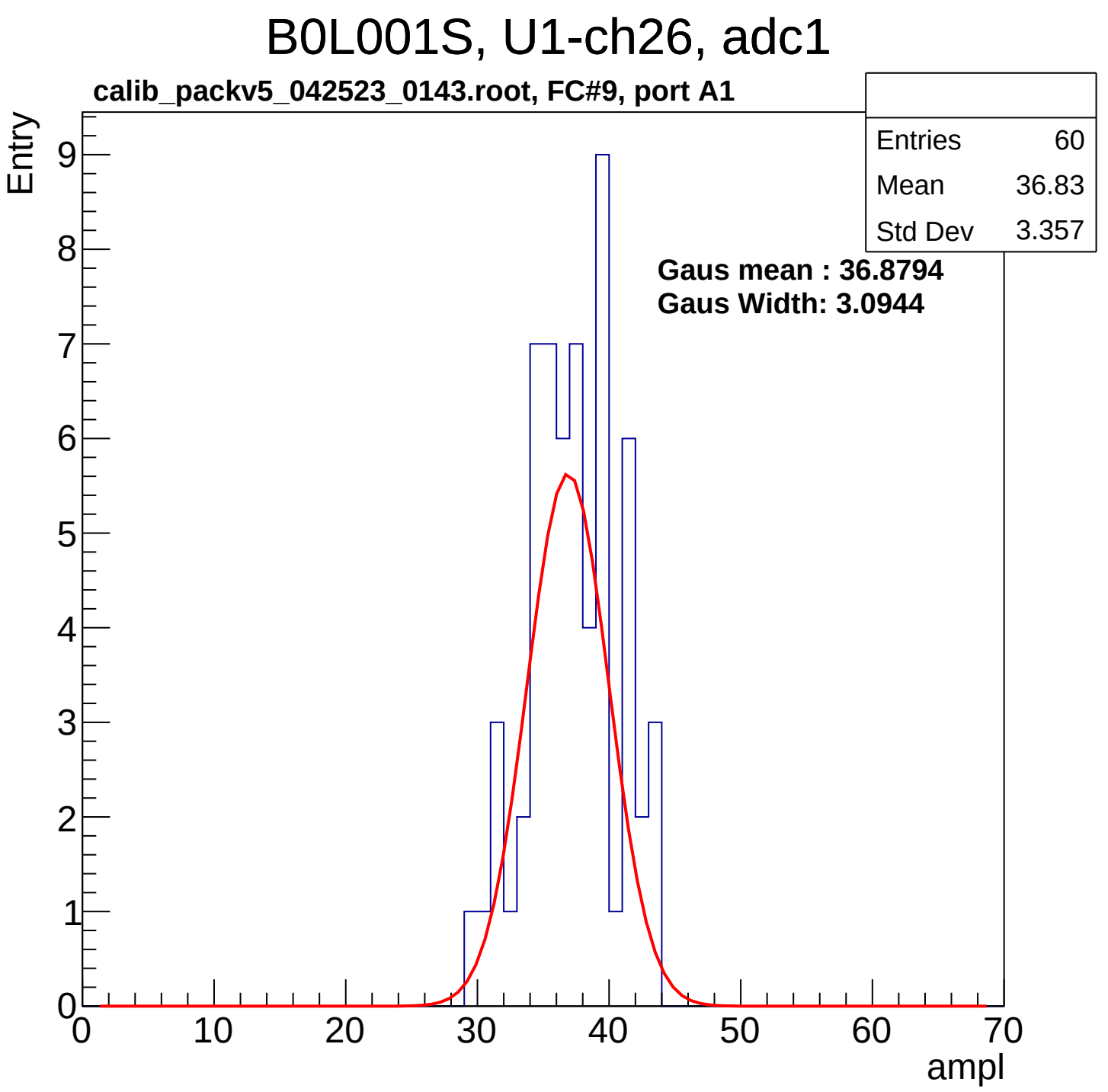
B0L001S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 36.83
Std Dev 3.357
Gaus mean : 36.8794
Gaus Width: 3.0944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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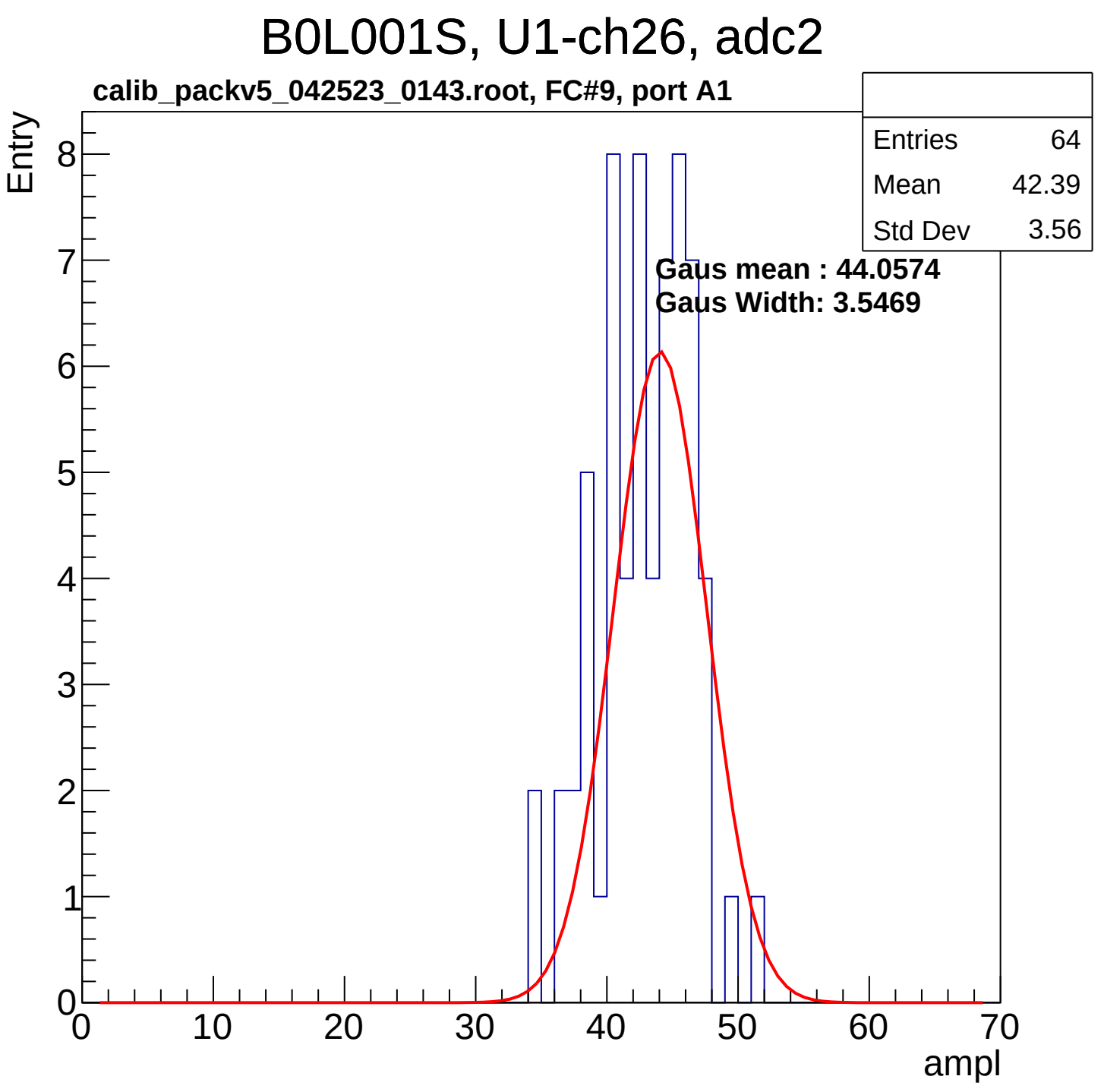
B0L001S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 42.39
Std Dev 3.56
Gaus mean : 44.0574
Gaus Width: 3.5469
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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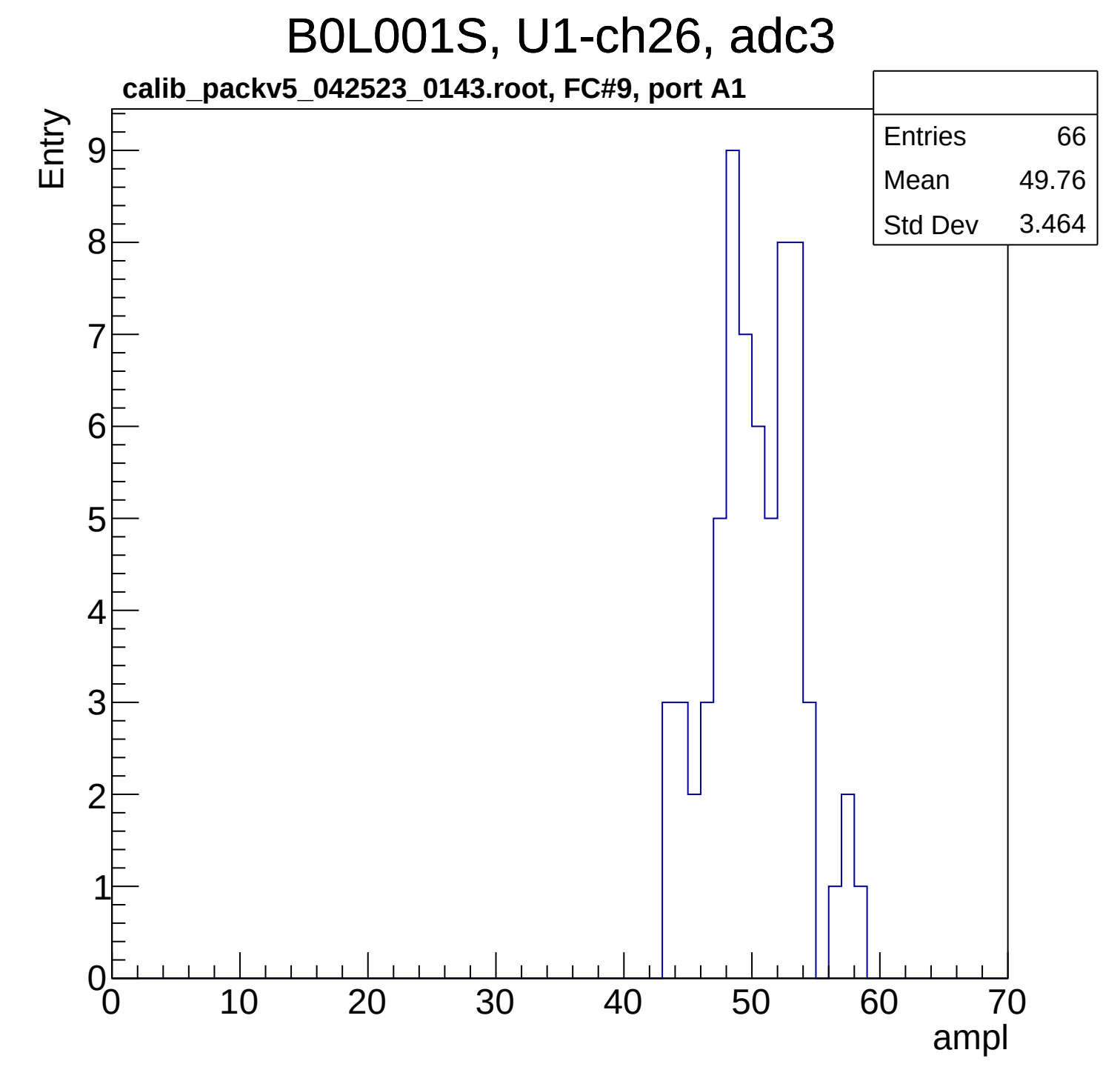
B0L001S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.76
Std Dev 3.464
Entry
ampl
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3
4
5
6
7
8
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0
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60
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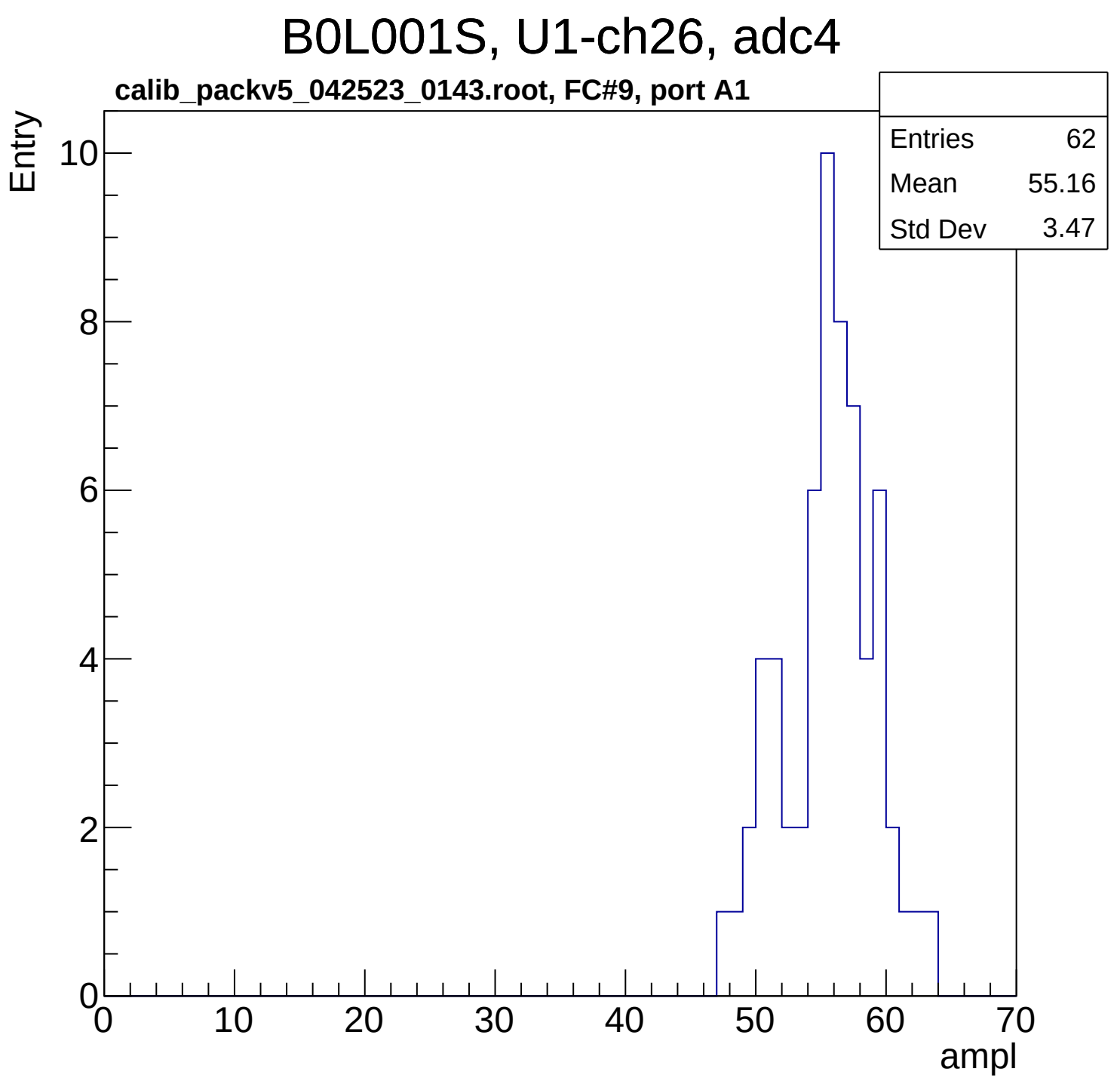
B0L001S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.16
Std Dev 3.47
Entry
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2
4
6
8
10
0
10
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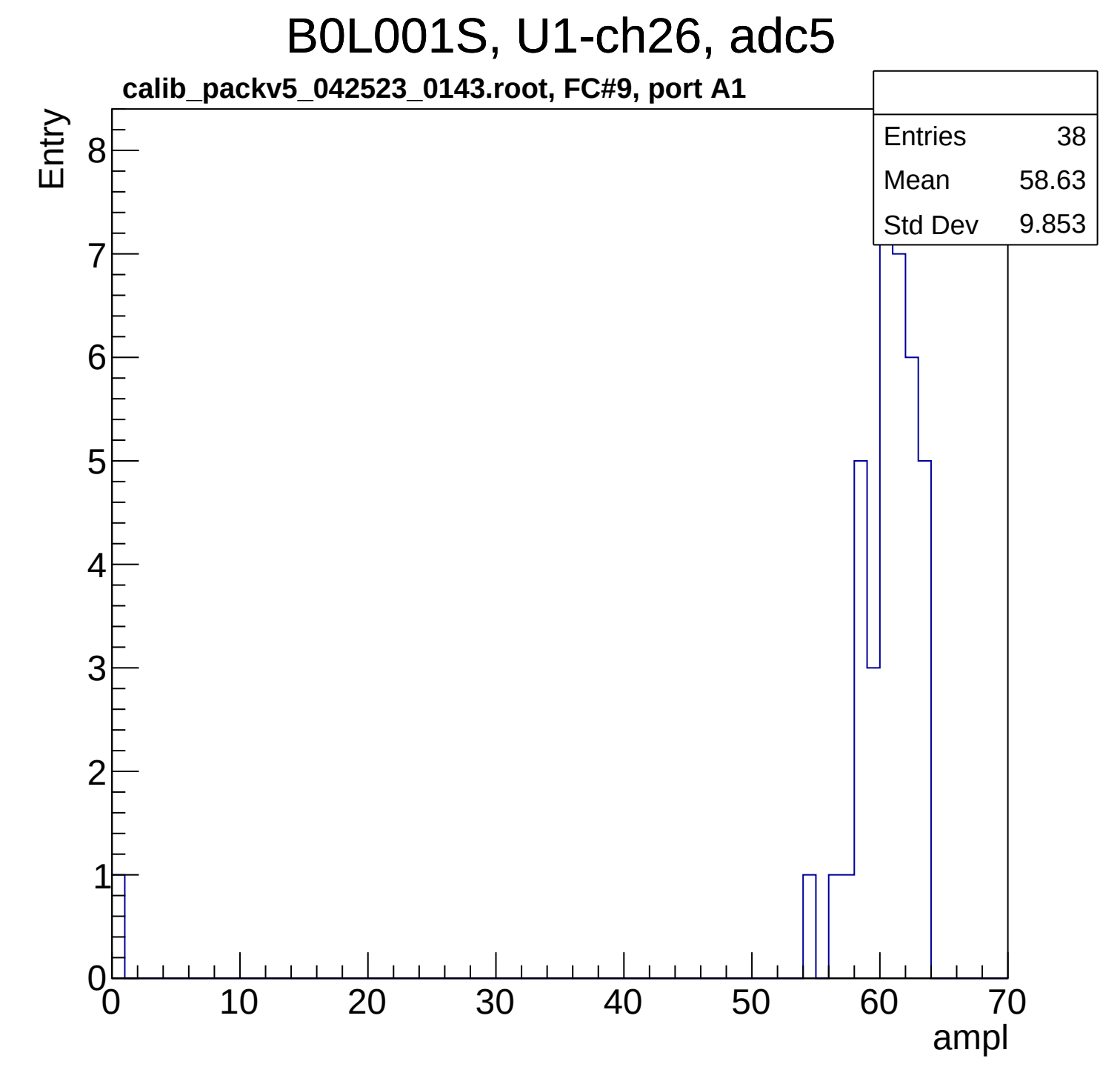
30
40
50
60
70
ampl


B0L001S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.63
Std Dev 9.853
Entry
ampl
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1
2
3
4
5
6
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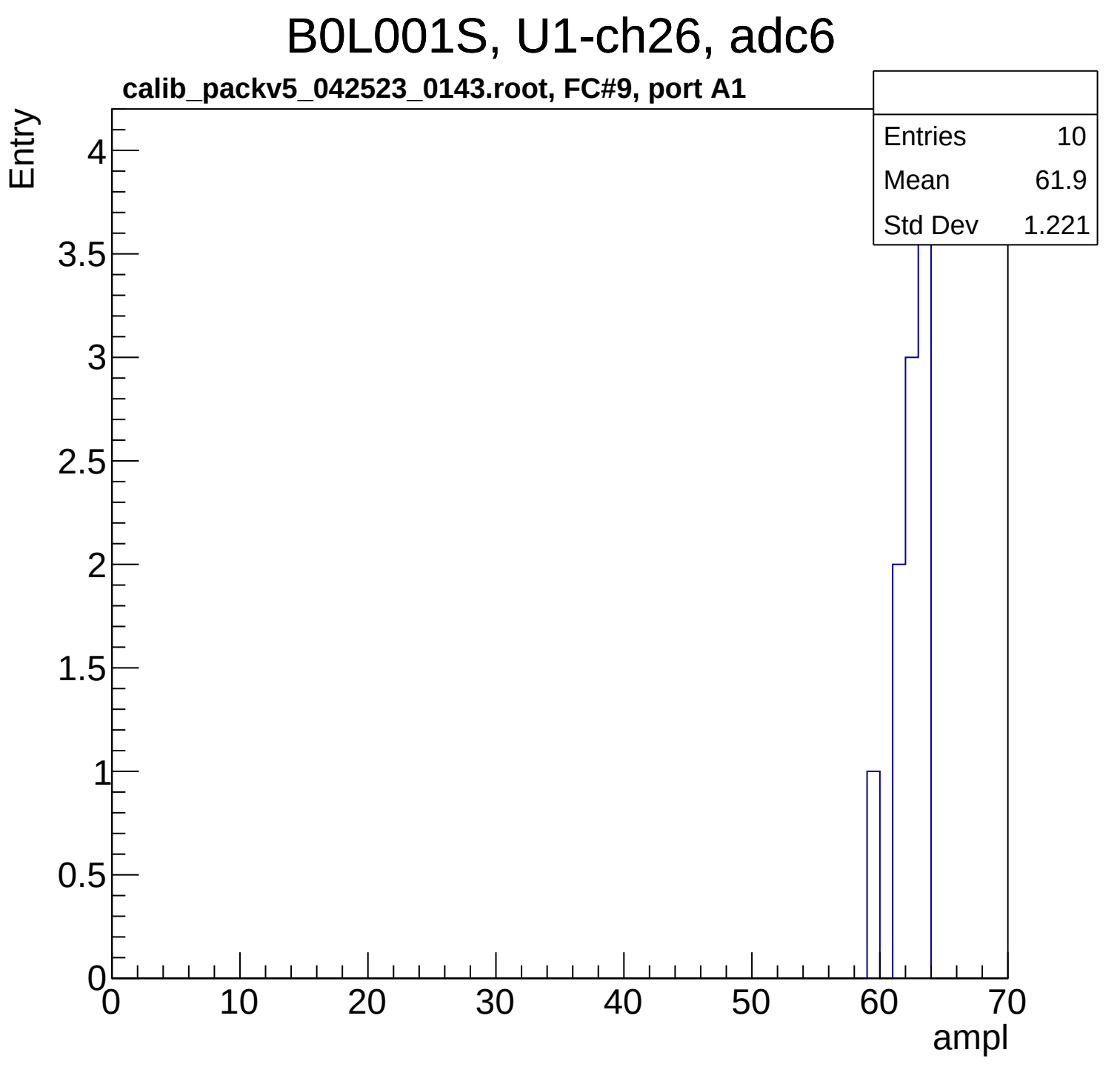
B0L001S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 61.9
Std Dev 1.221
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
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60
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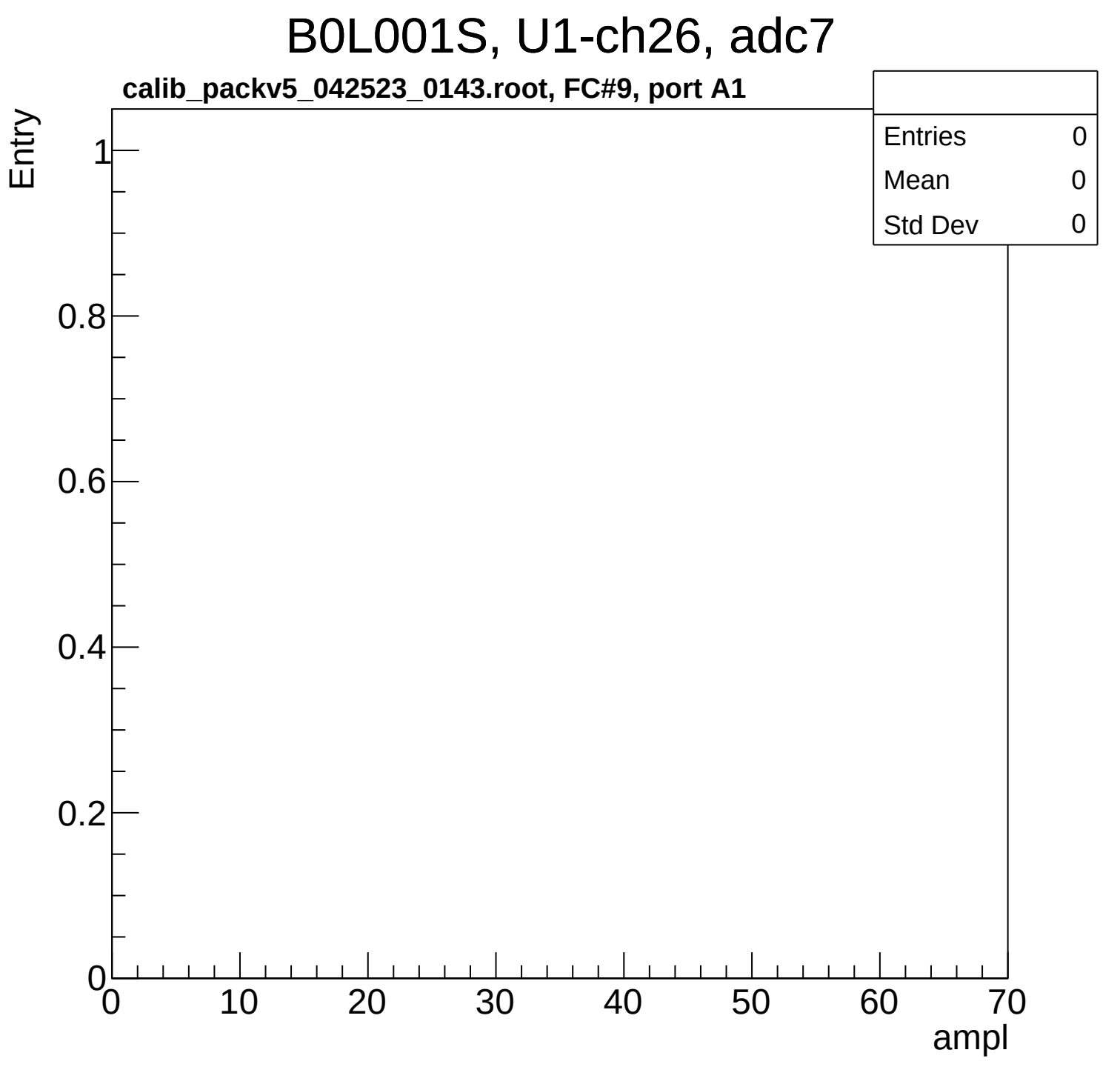
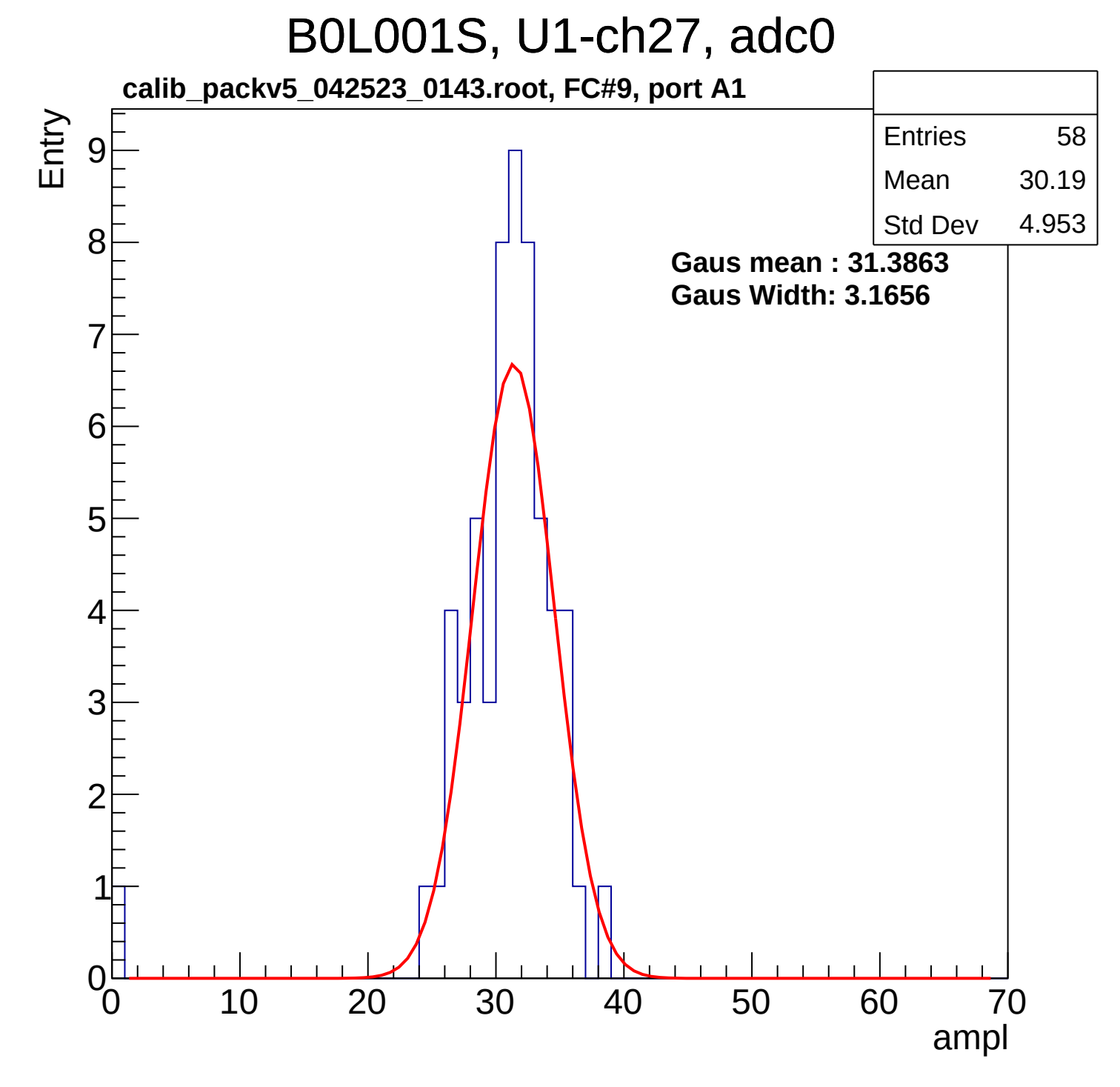

B0L001S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 30.19
Std Dev 4.953
Gaus mean : 31.3863
Gaus Width: 3.1656
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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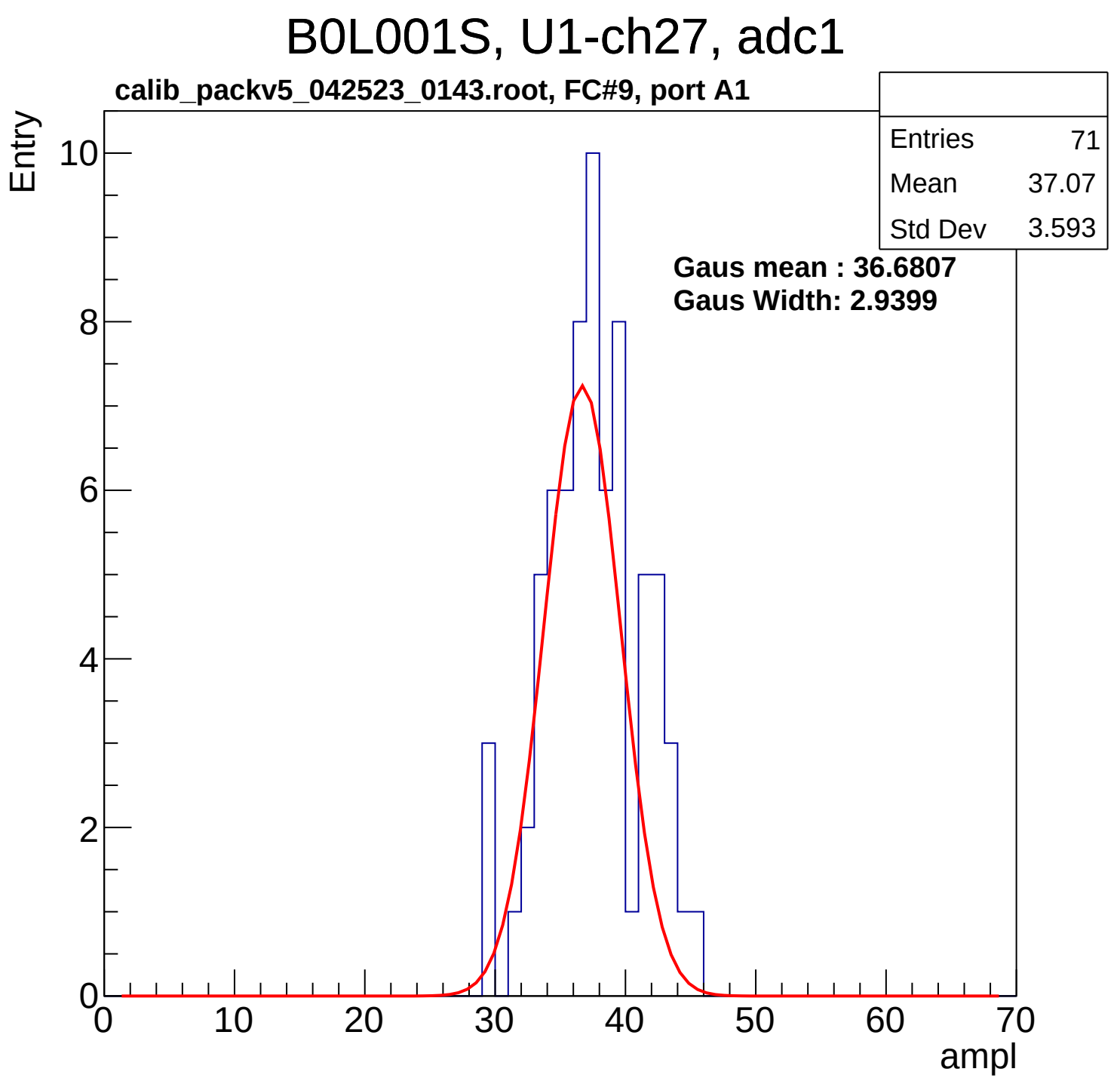
B0L001S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.07
Std Dev 3.593
Gaus mean : 36.6807
Gaus Width: 2.9399
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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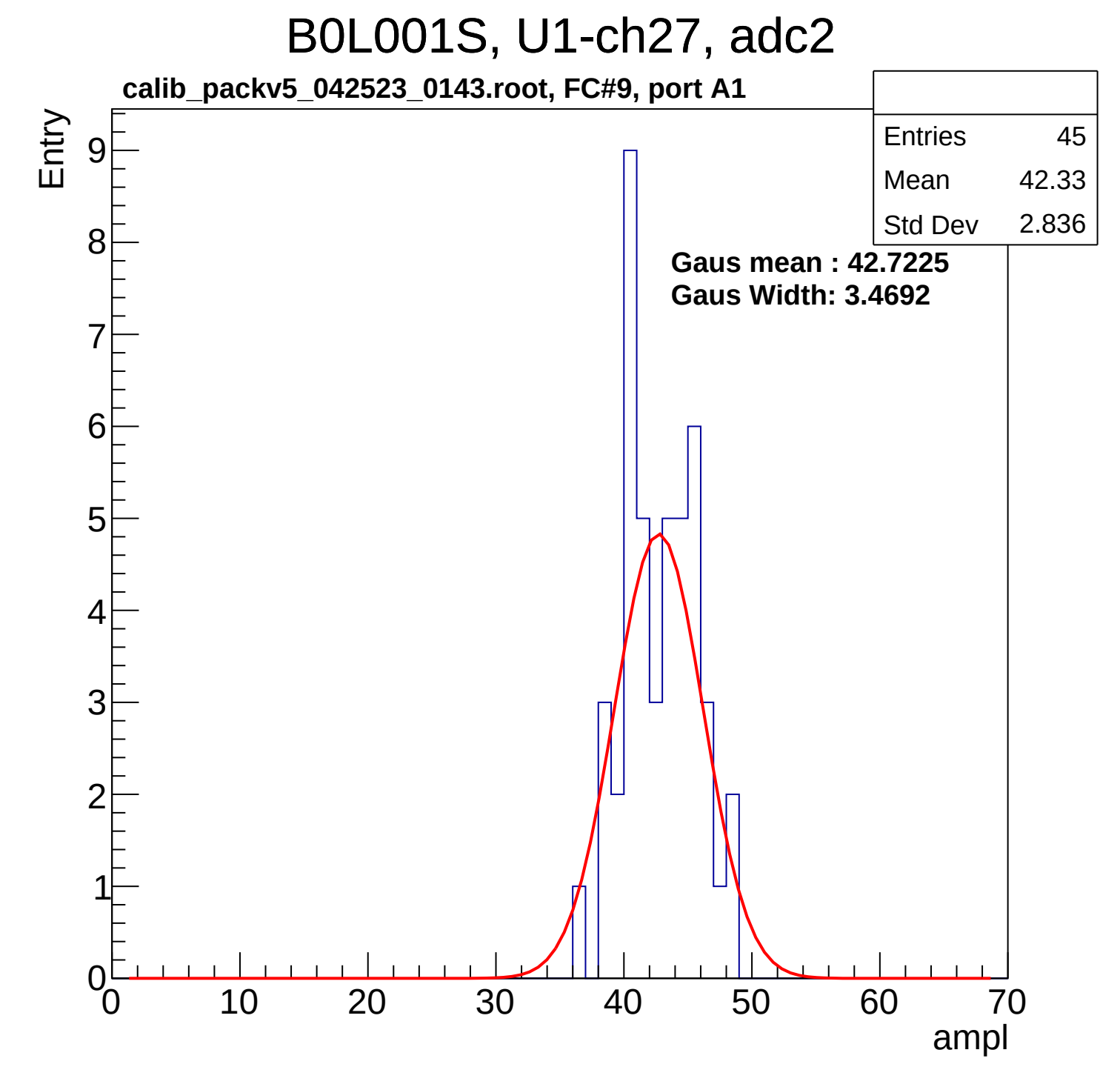
B0L001S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 42.33
Std Dev 2.836
Gaus mean : 42.7225
Gaus Width: 3.4692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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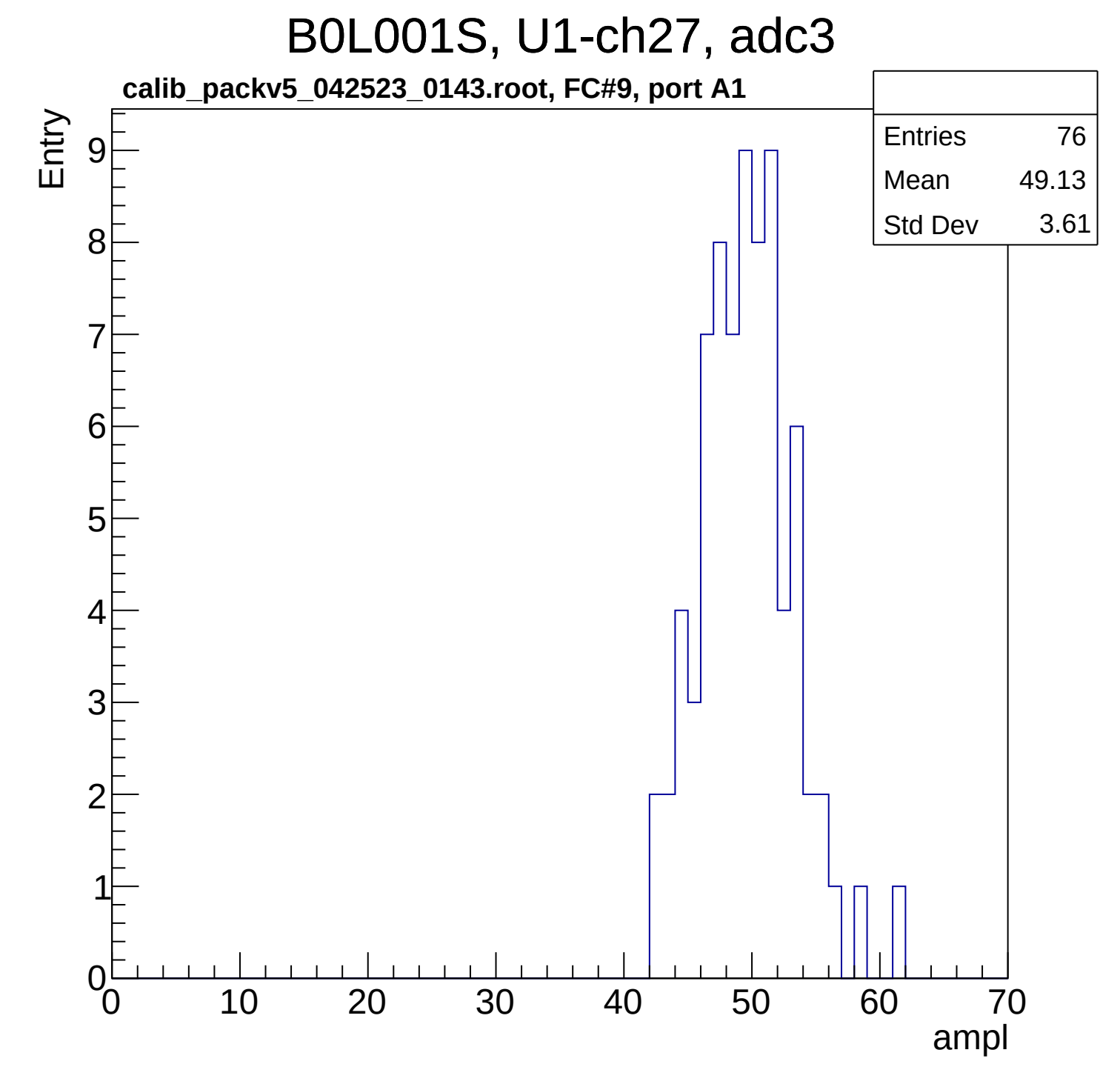
B0L001S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 49.13
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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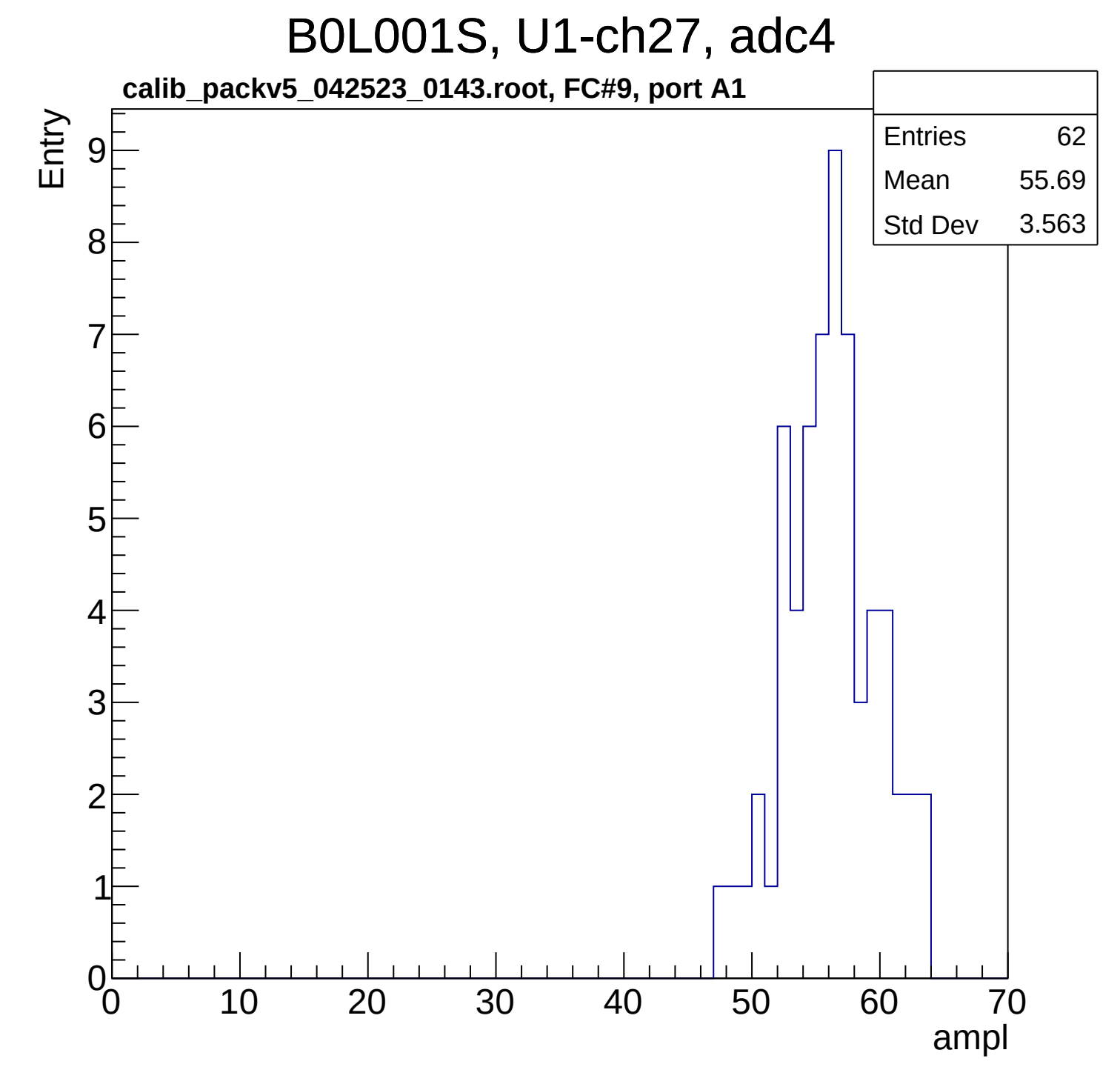
B0L001S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 55.69
Std Dev 3.563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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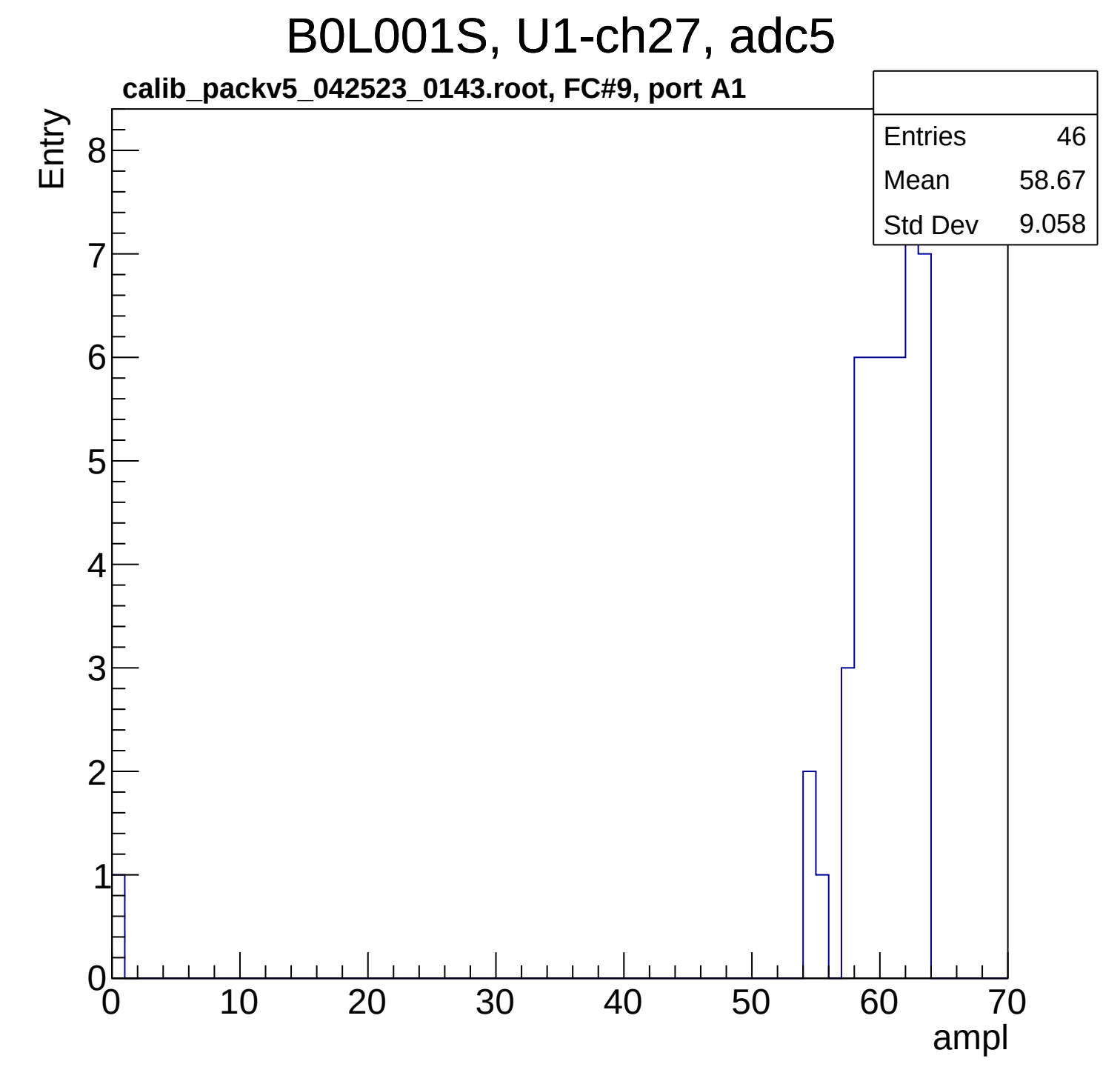
B0L001S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.67
Std Dev 9.058
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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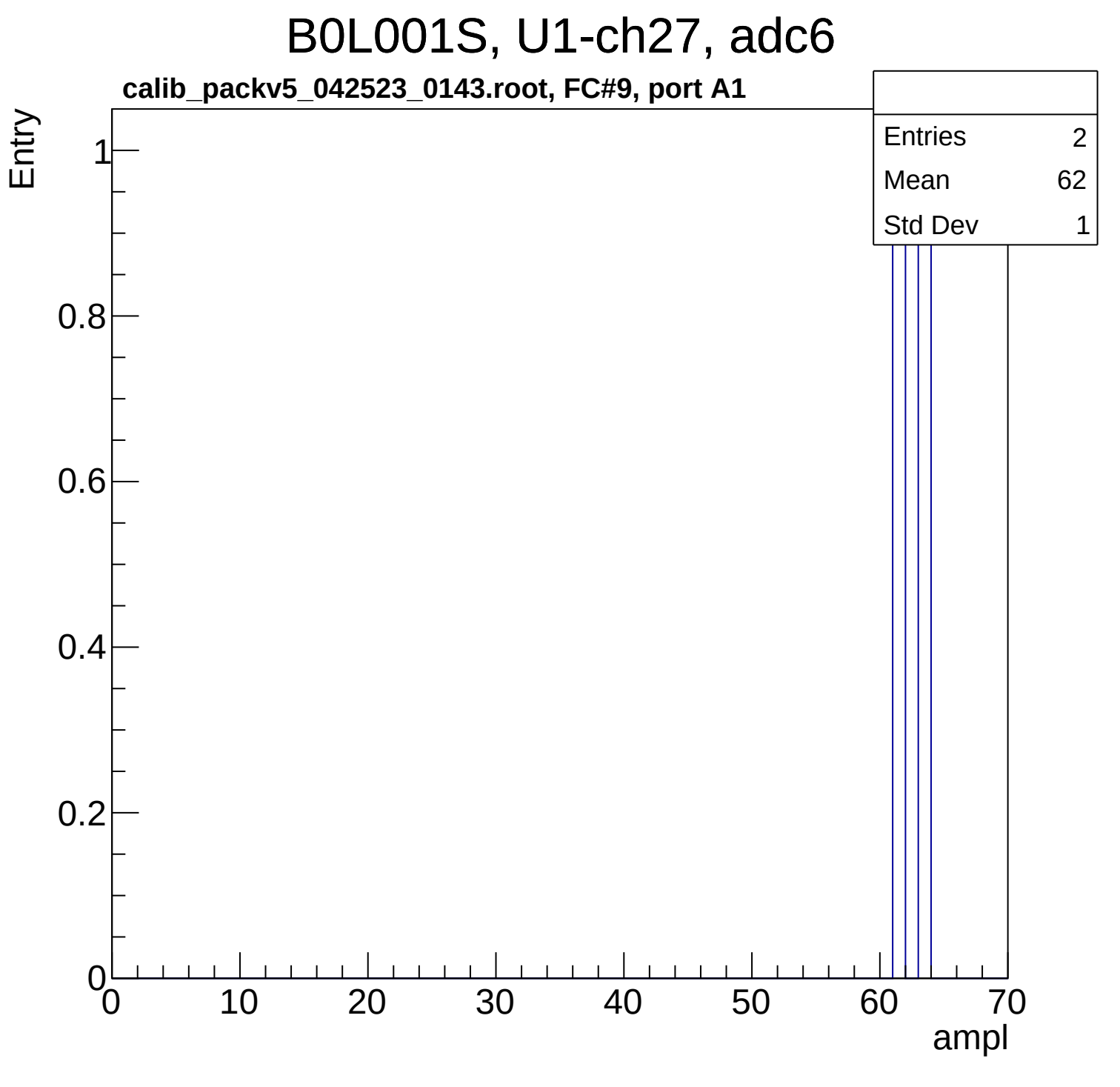
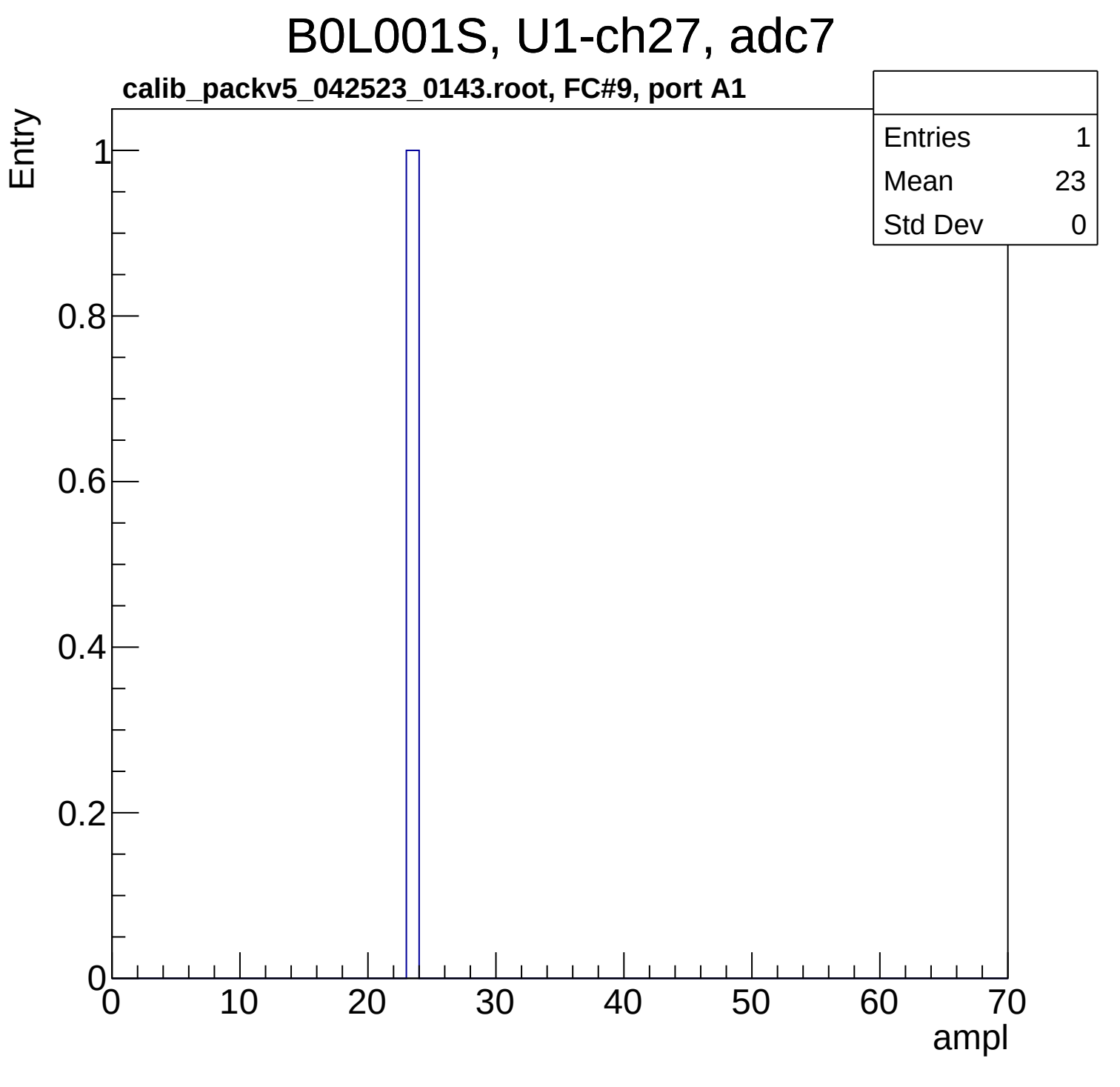

B0L001S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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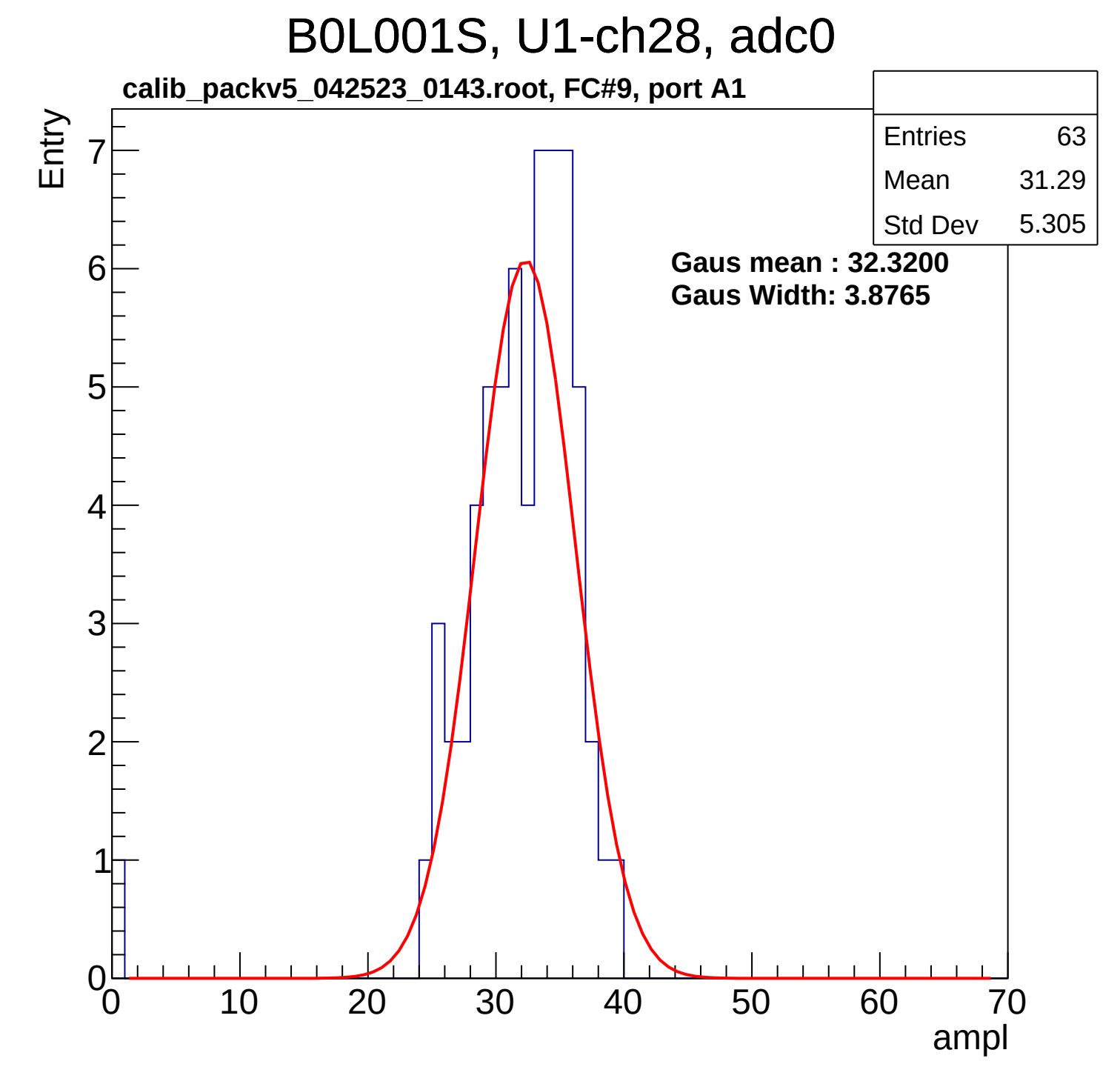
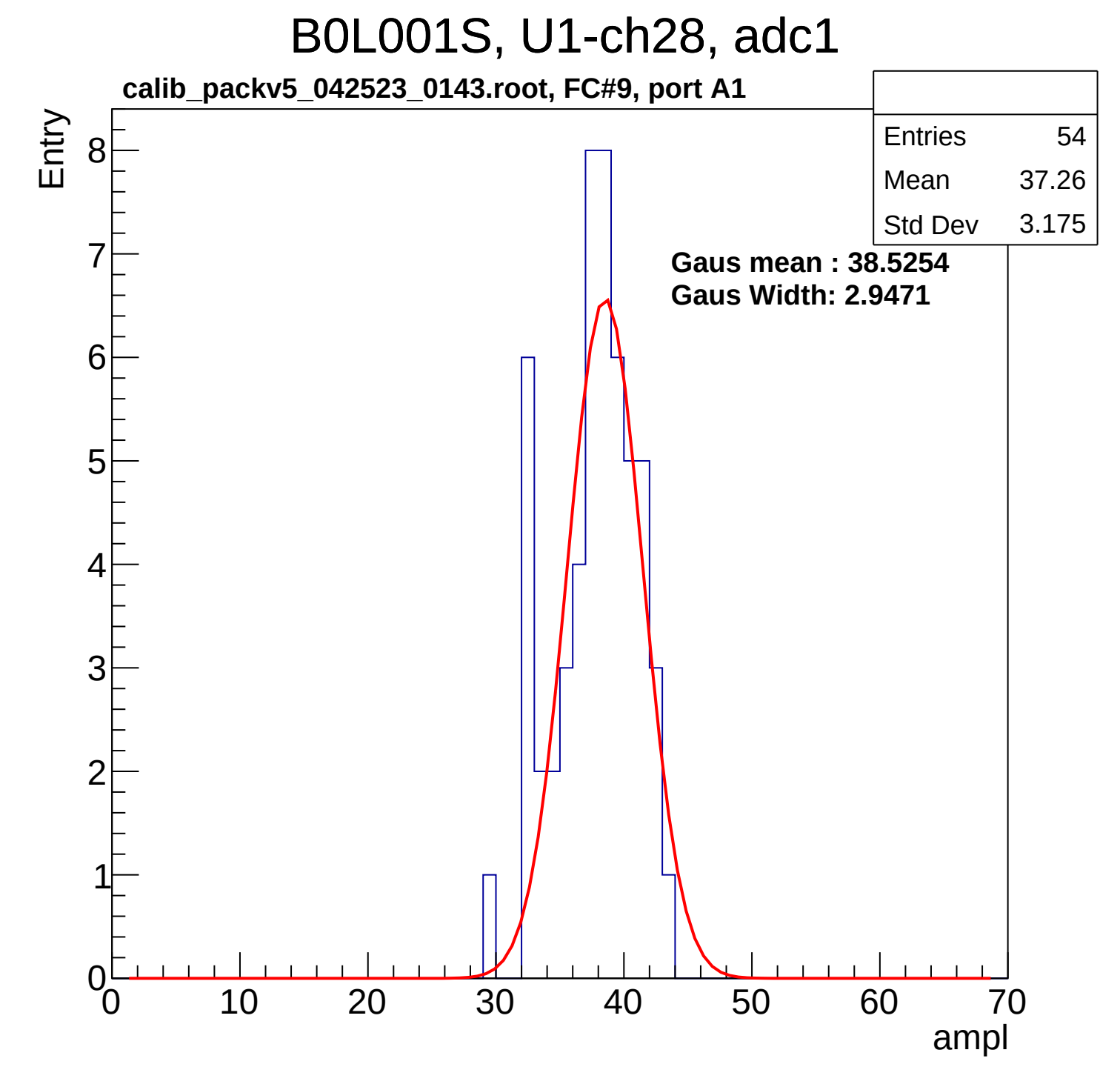
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 31.29
Std Dev 5.305
Gaus mean : 32.3200
Gaus Width: 3.8765
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 37.26
Std Dev 3.175
Gaus mean : 38.5254
Gaus Width: 2.9471
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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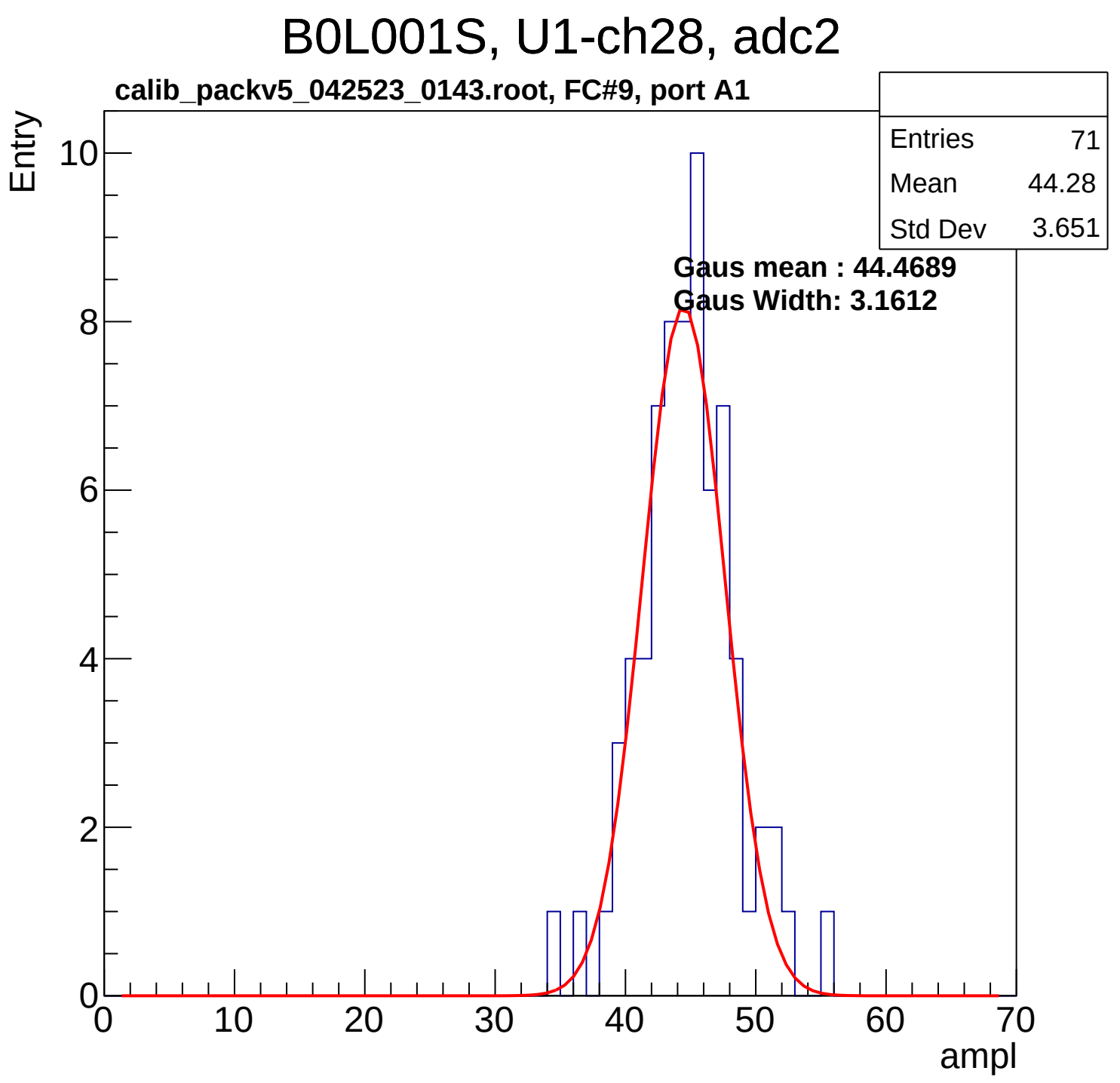
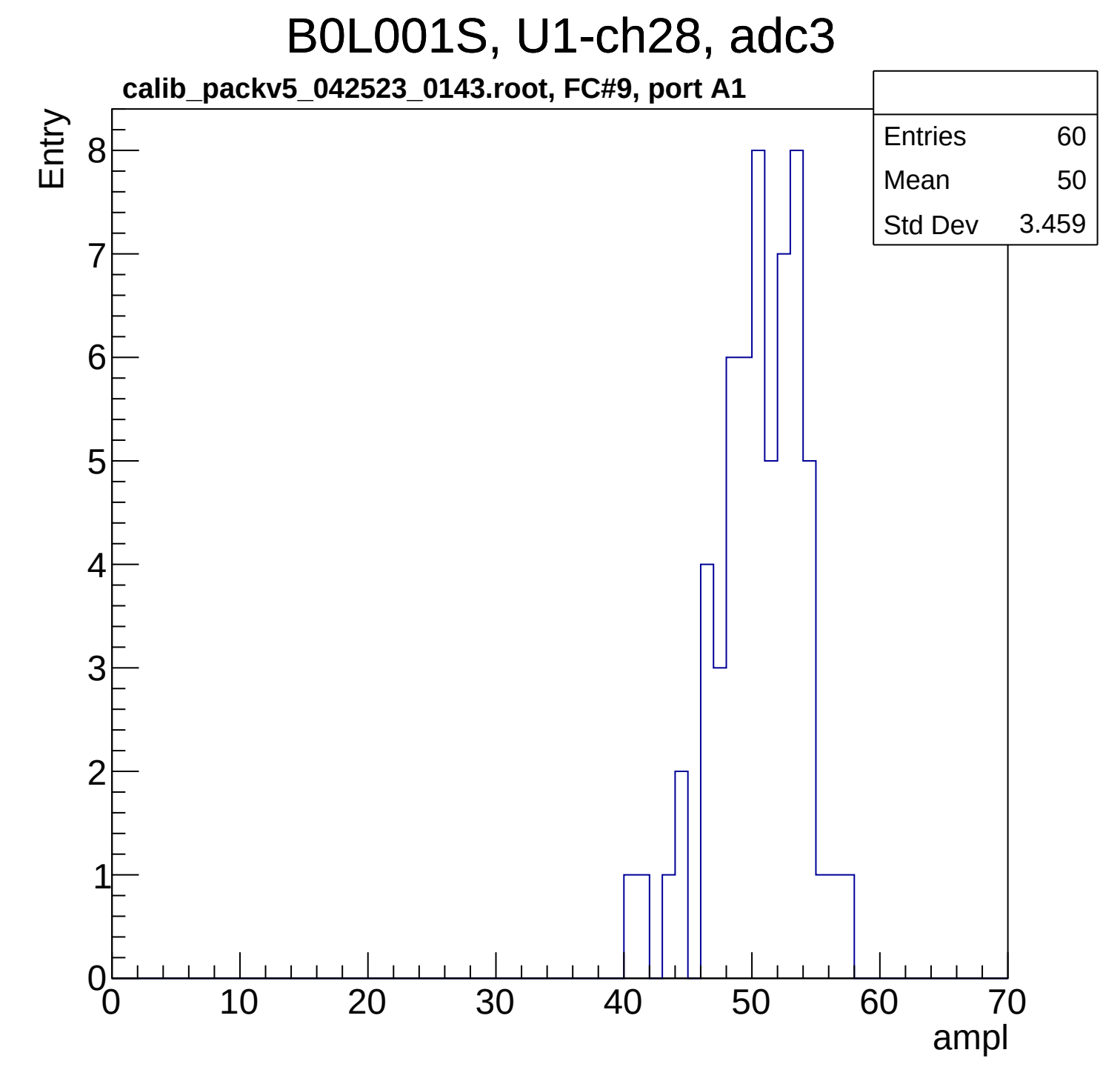
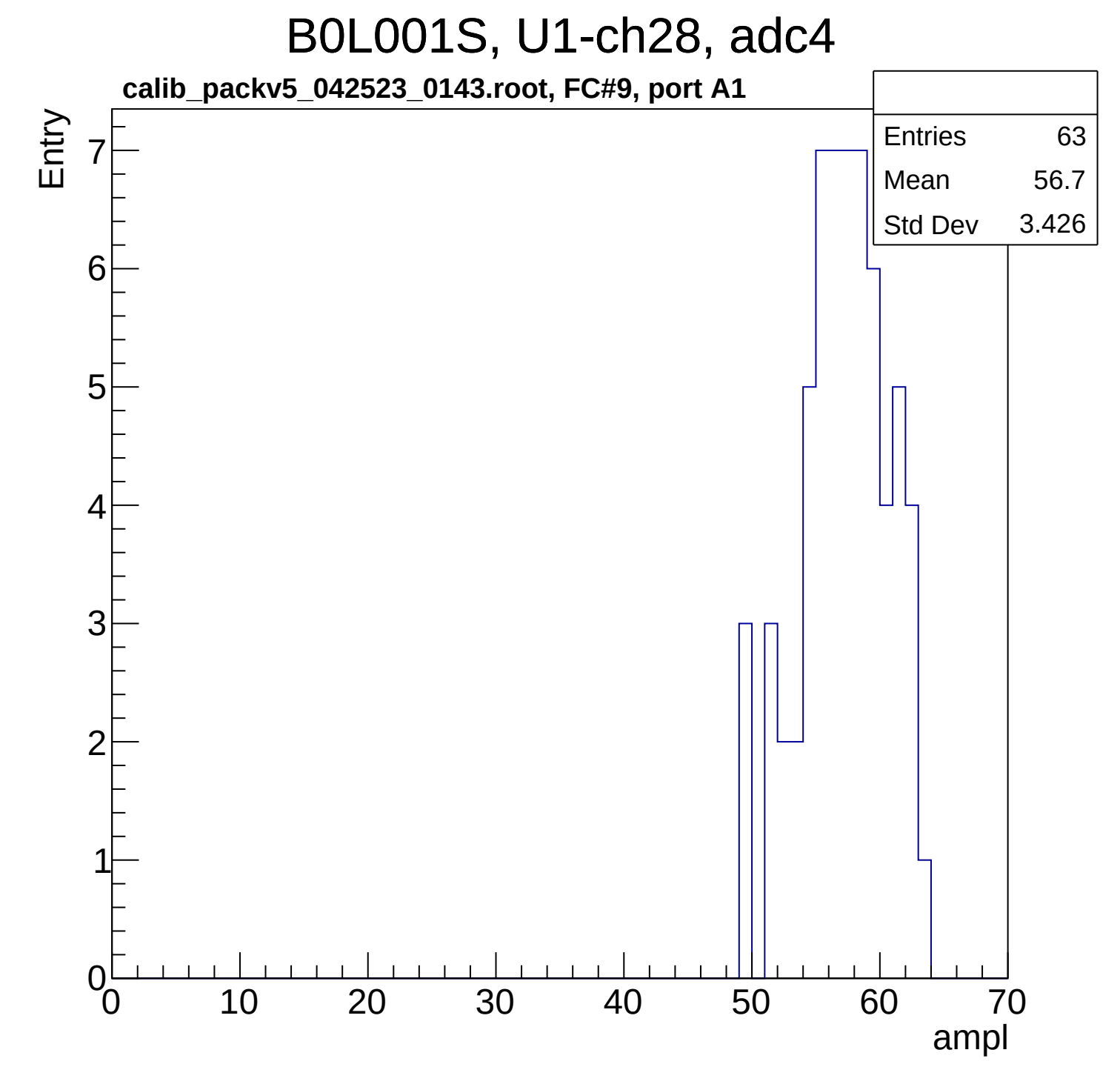
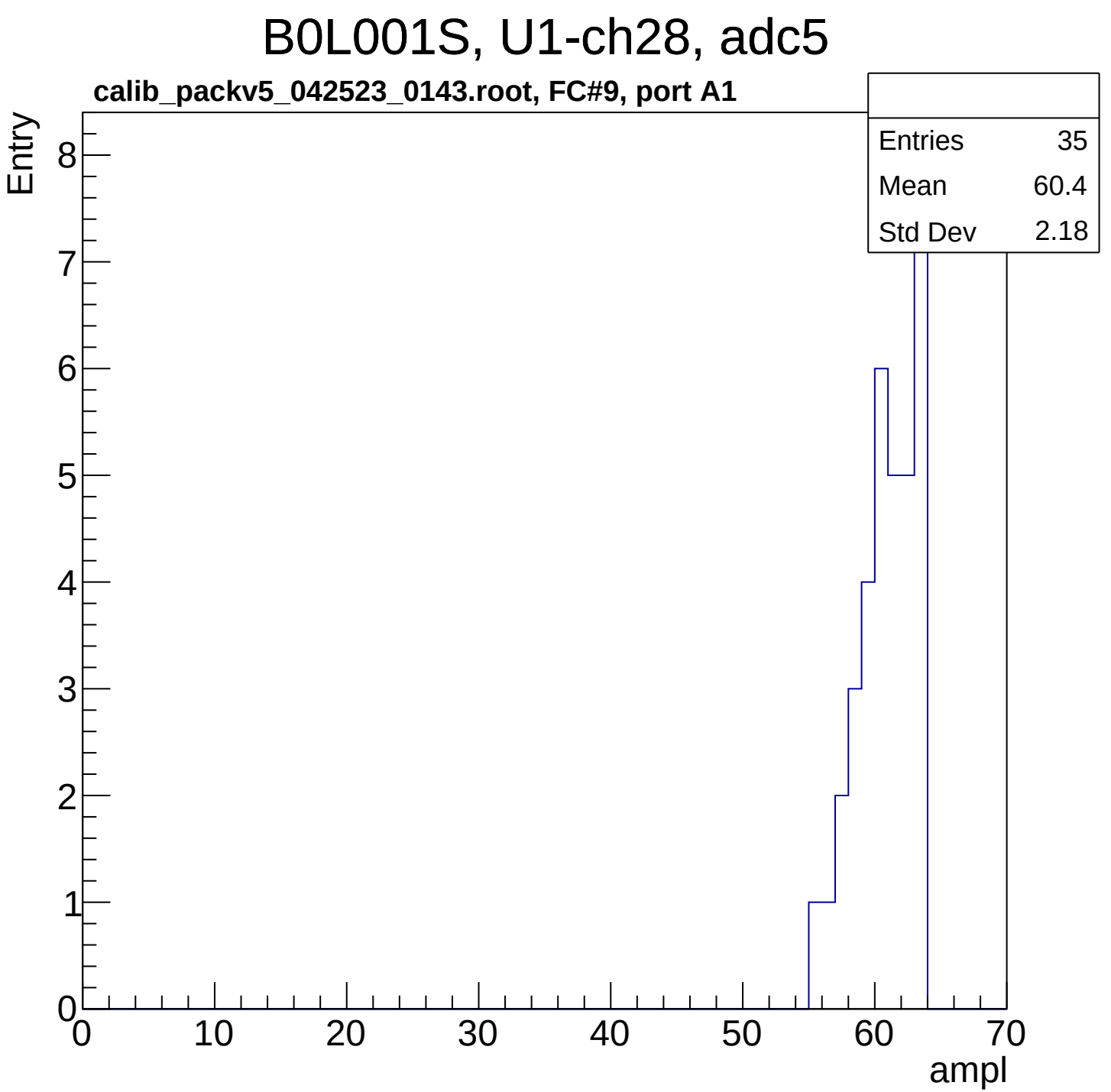
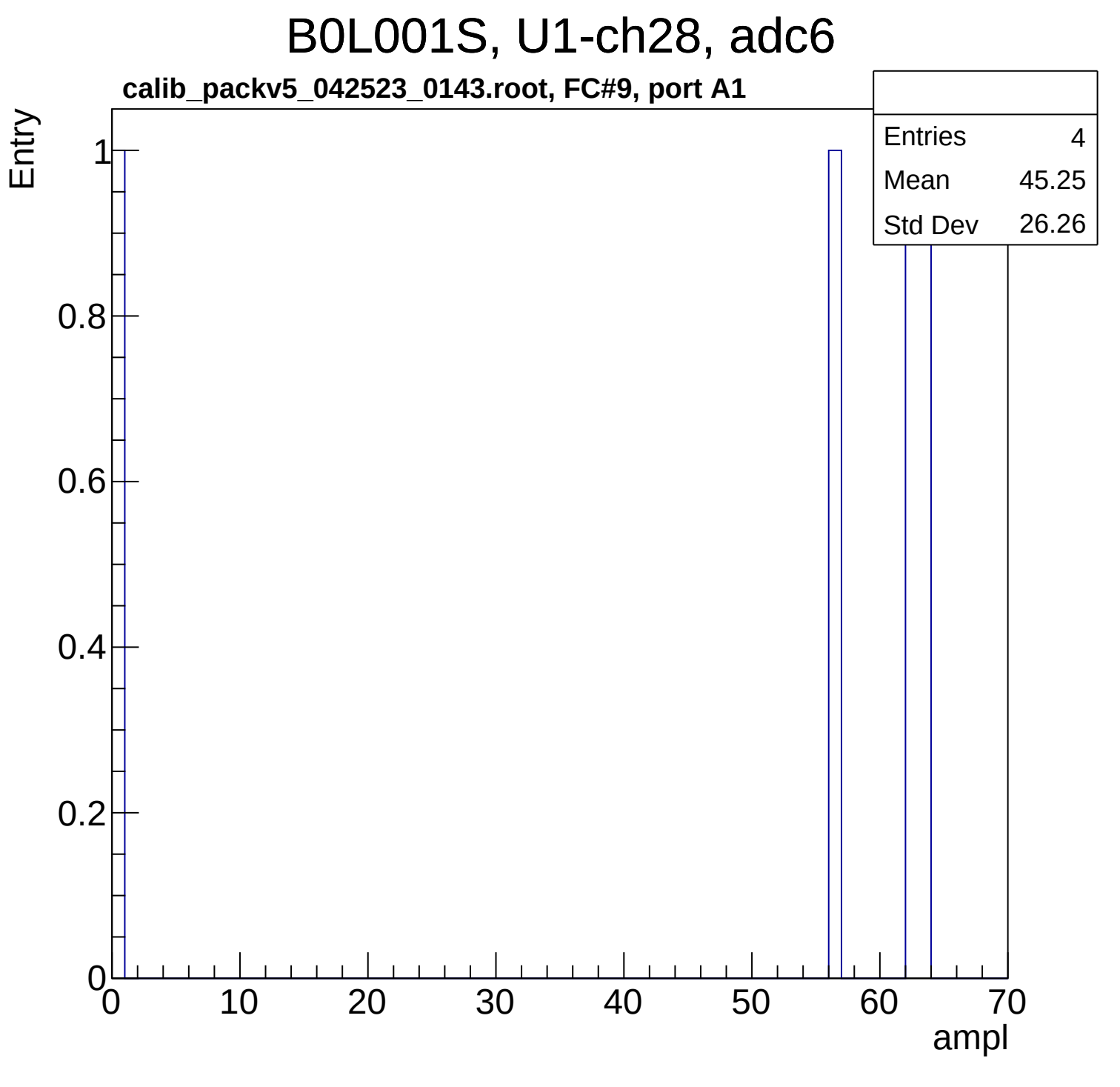
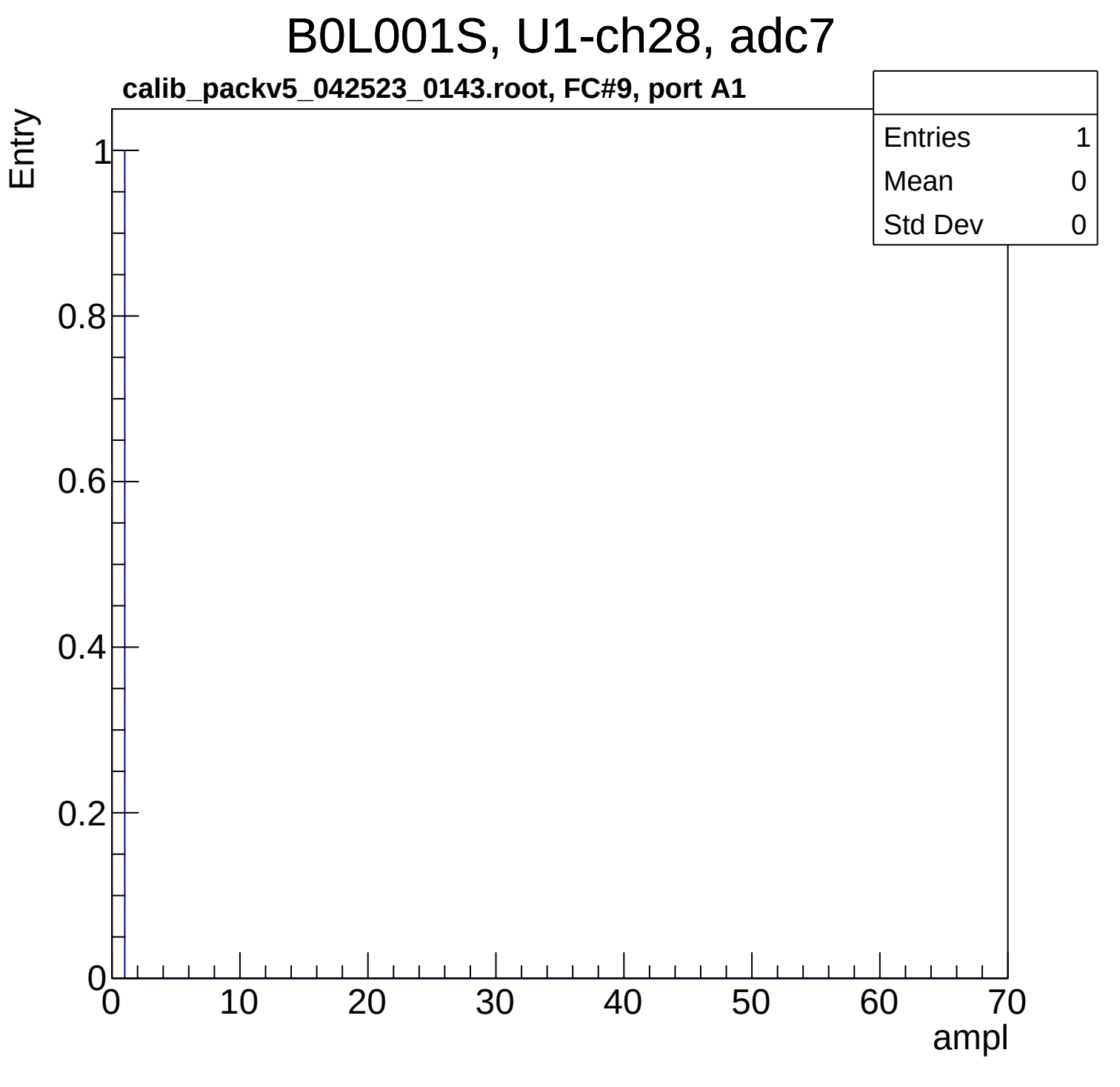

B0L001S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.28
Std Dev 3.651
Gaus mean : 44.4689
Gaus Width: 3.1612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

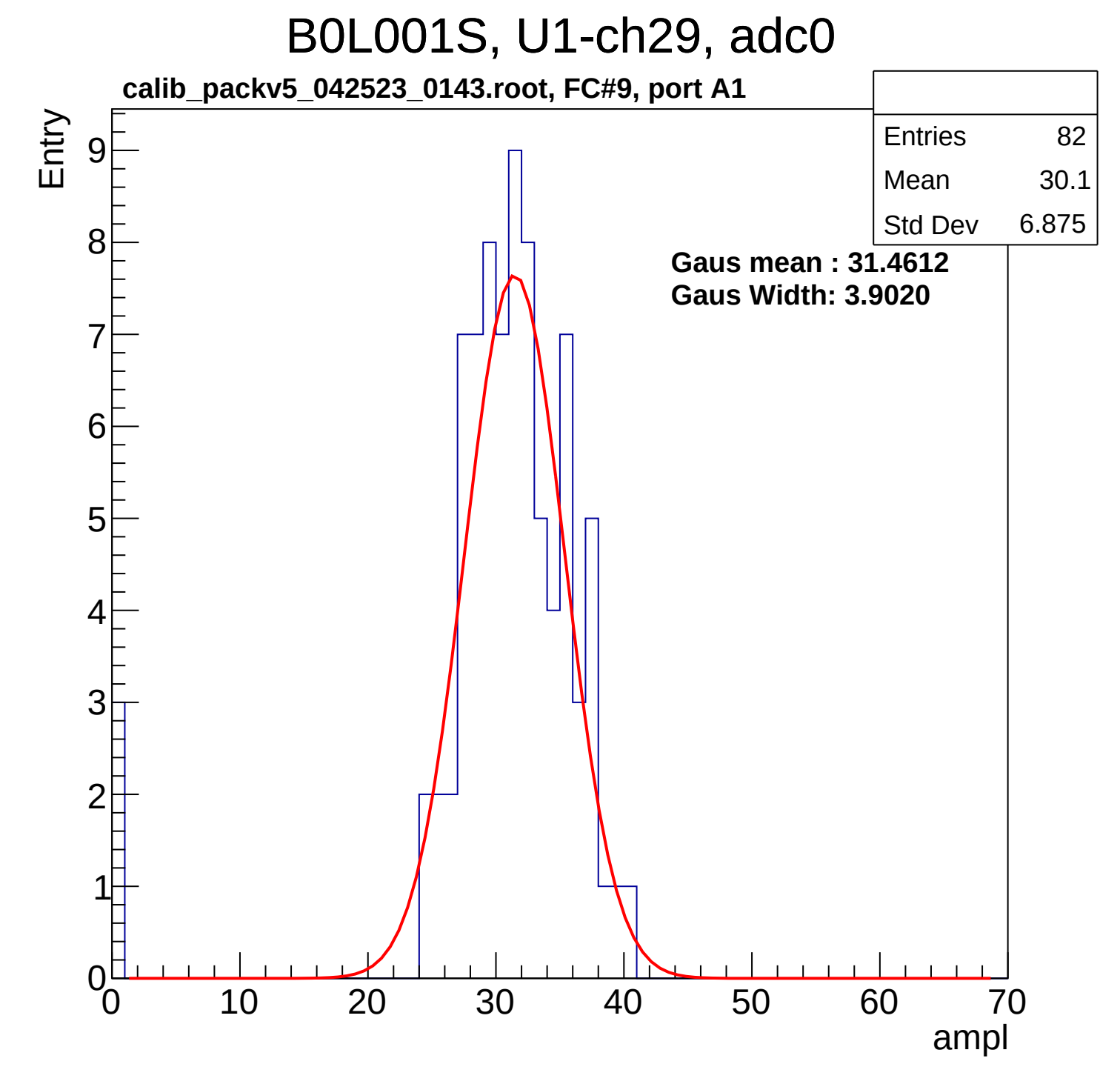

B0L001S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50
Std Dev 3.459
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.7
Std Dev 3.426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.4
Std Dev 2.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 45.25
Std Dev 26.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L001S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 30.1
Std Dev 6.875
Gaus mean : 31.4612
Gaus Width: 3.9020
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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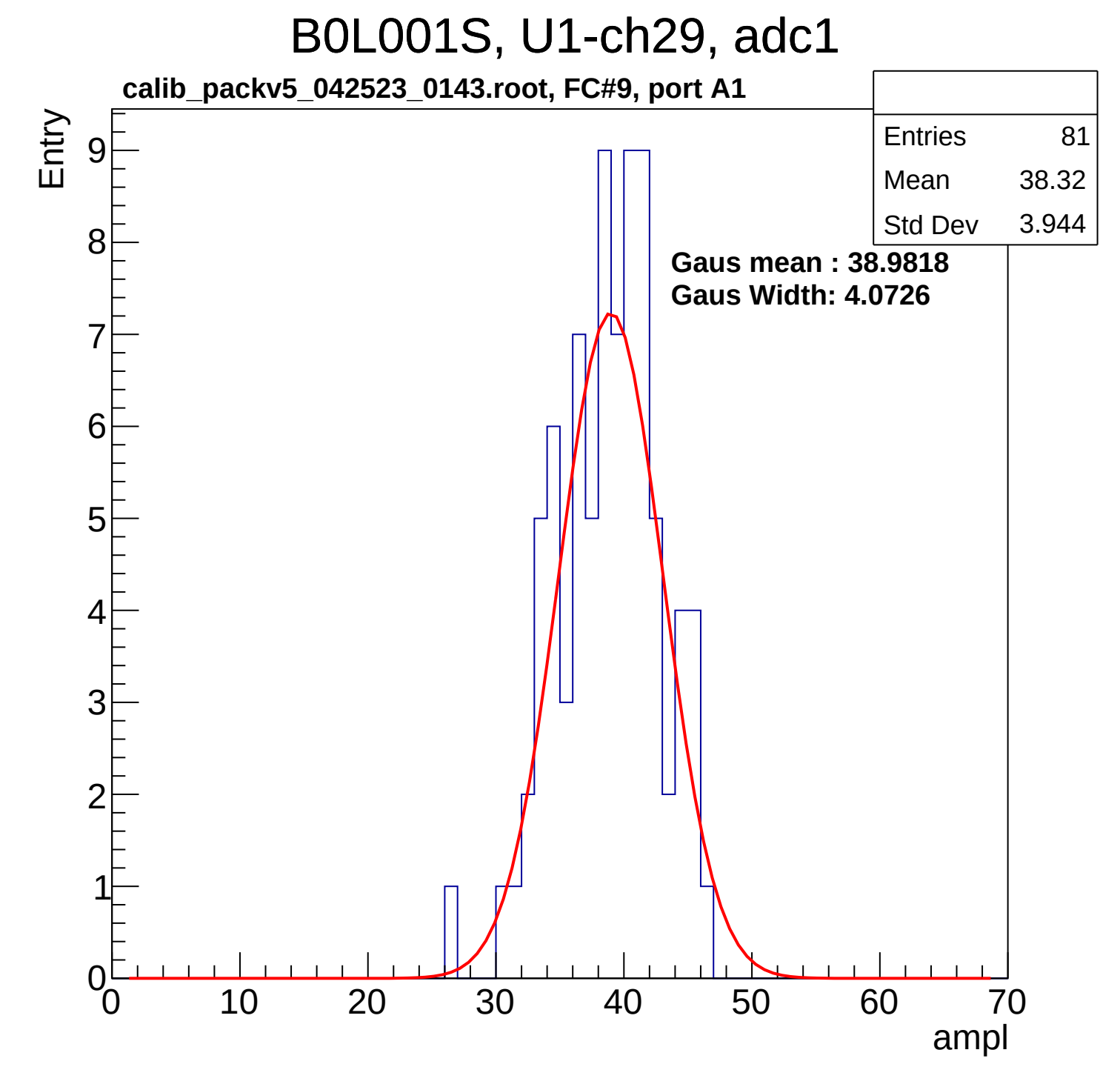
B0L001S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 38.32
Std Dev 3.944
Gaus mean : 38.9818
Gaus Width: 4.0726
Entry
ampl
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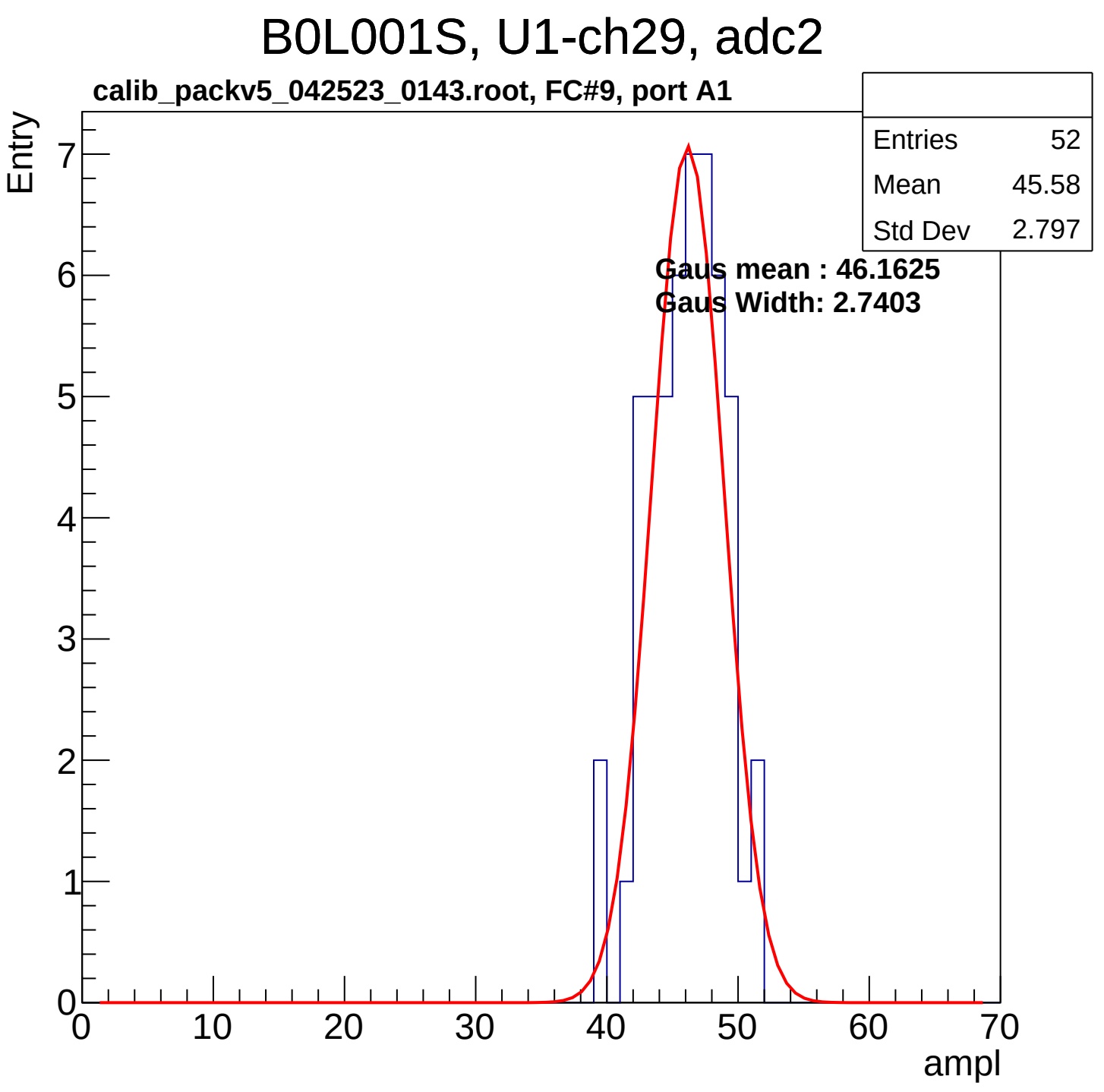
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 45.58
Std Dev 2.797
Gaus mean : 46.1625
Gaus Width: 2.7403
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
50
60
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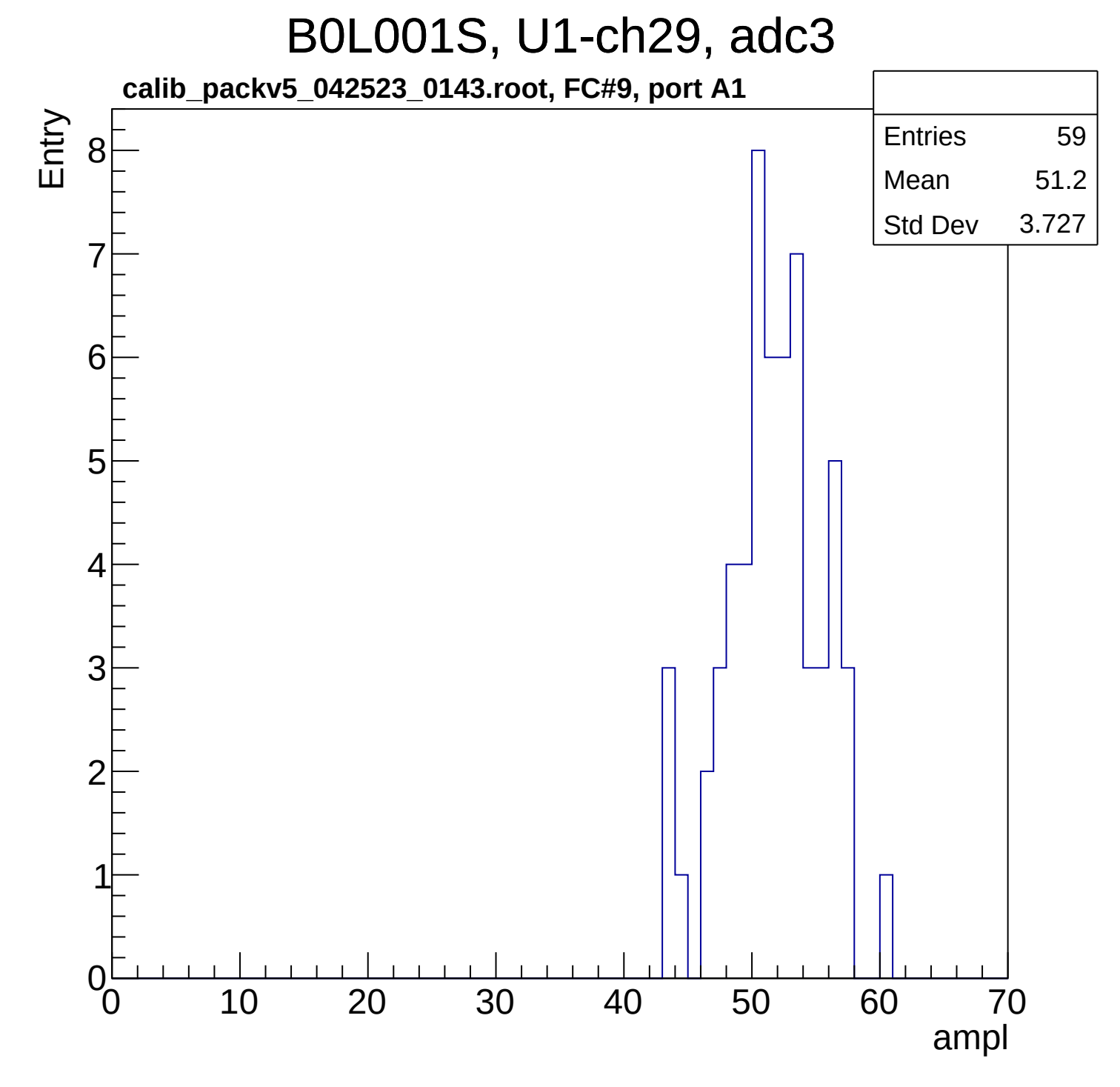
B0L001S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 51.2
Std Dev 3.727
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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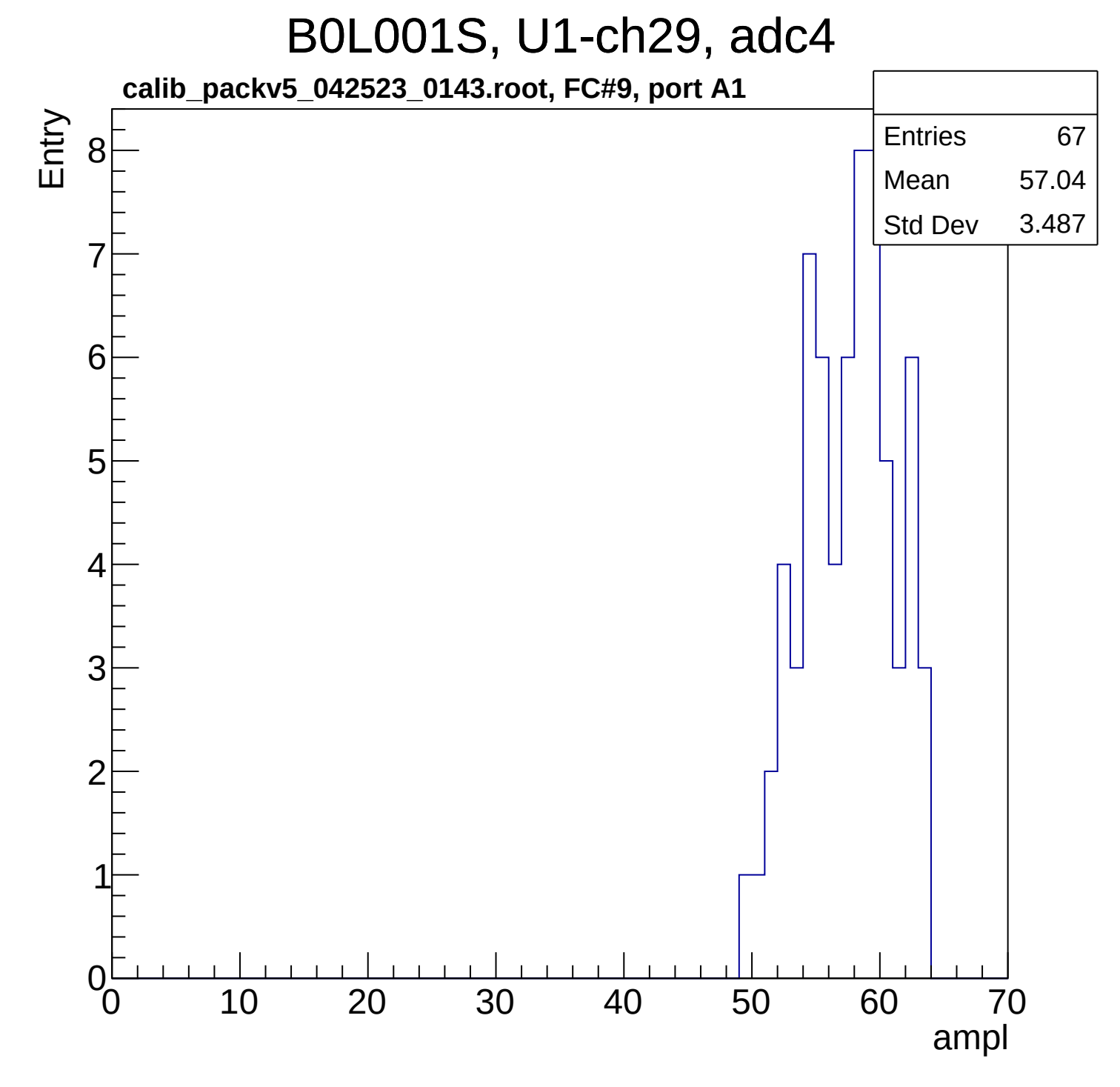
B0L001S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 57.04
Std Dev 3.487
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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60
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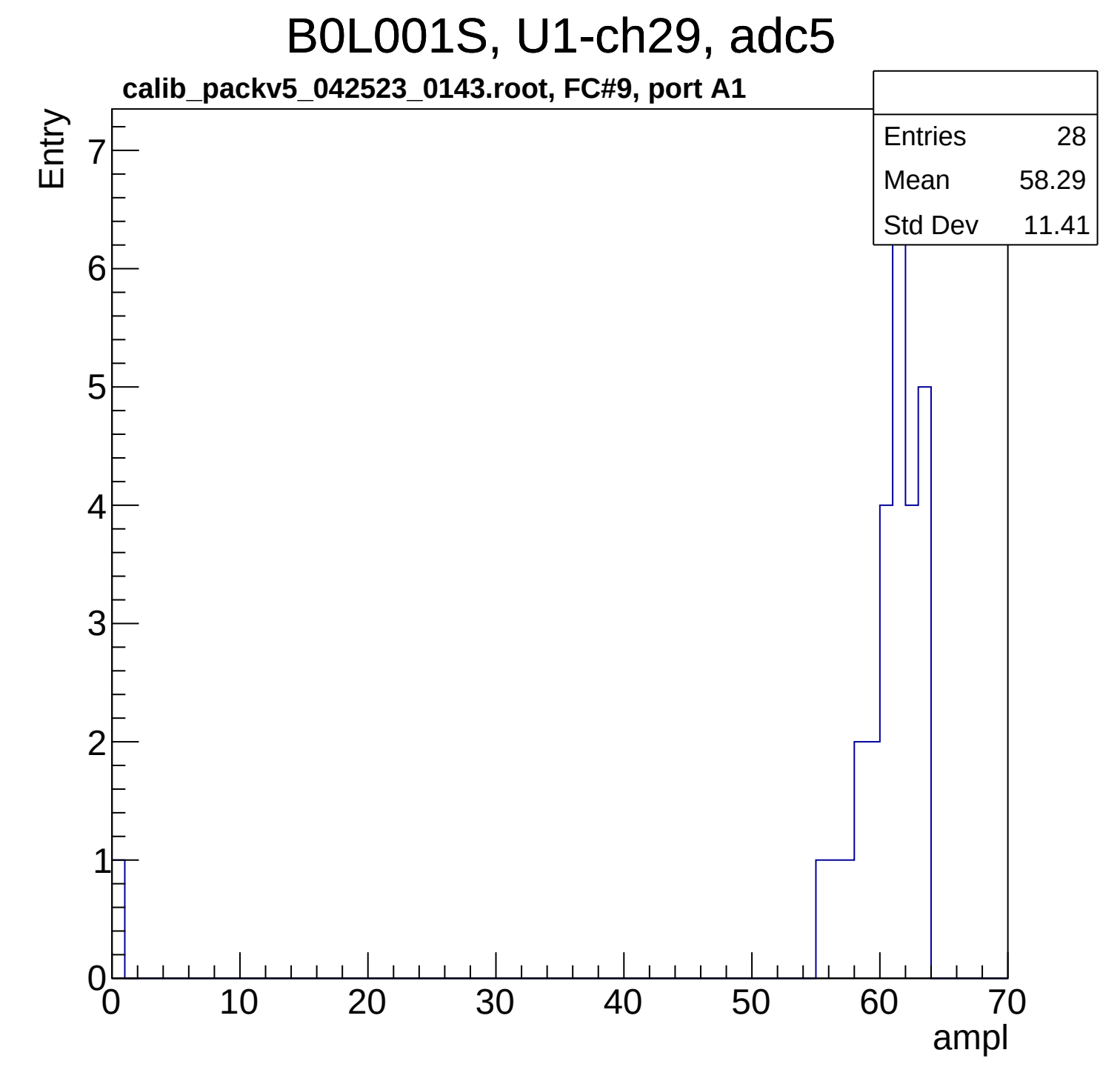
B0L001S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 58.29
Std Dev 11.41
Entry
ampl
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1
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4
5
6
7
0
10
20
30
40
50
60
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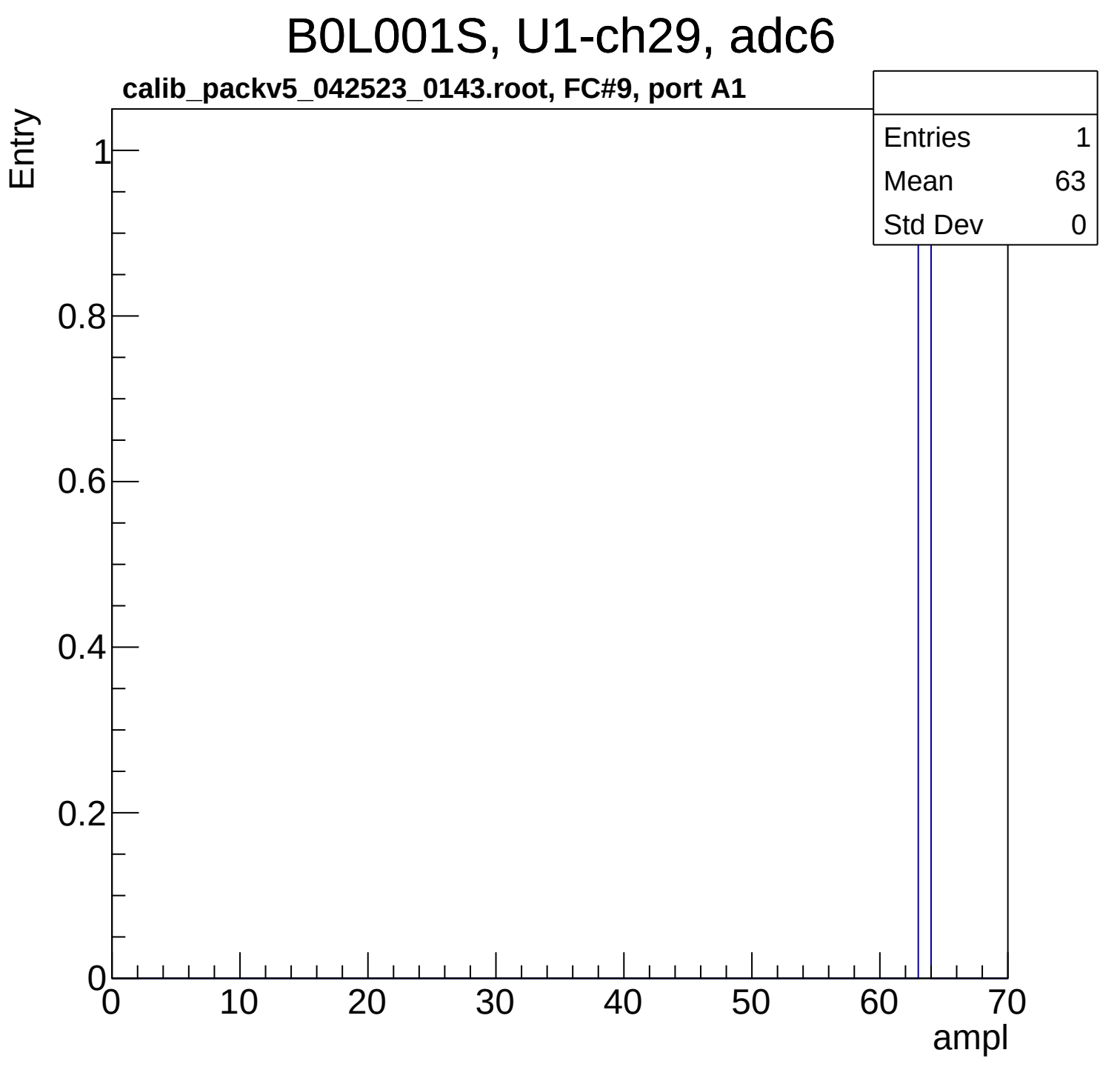
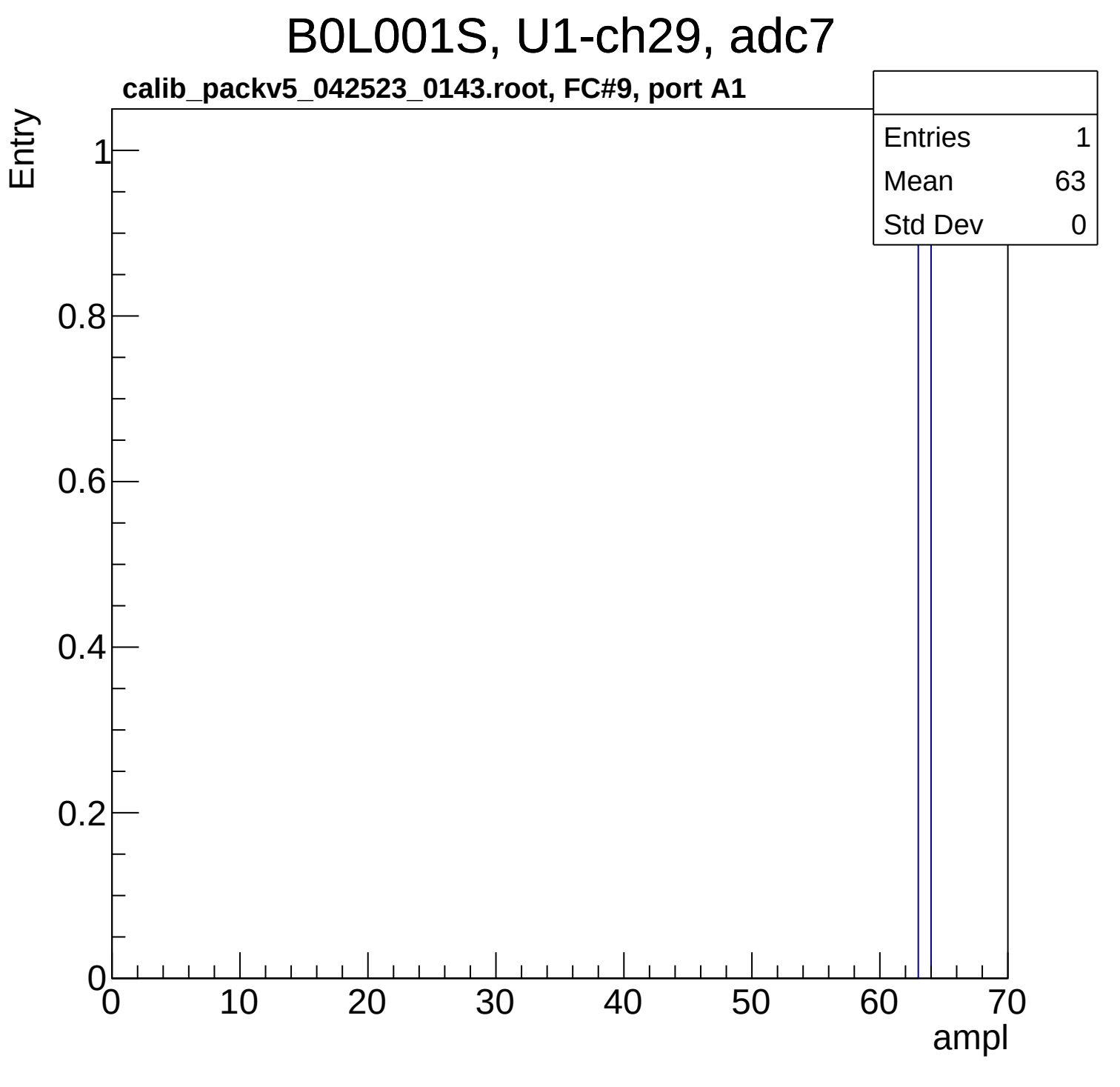

B0L001S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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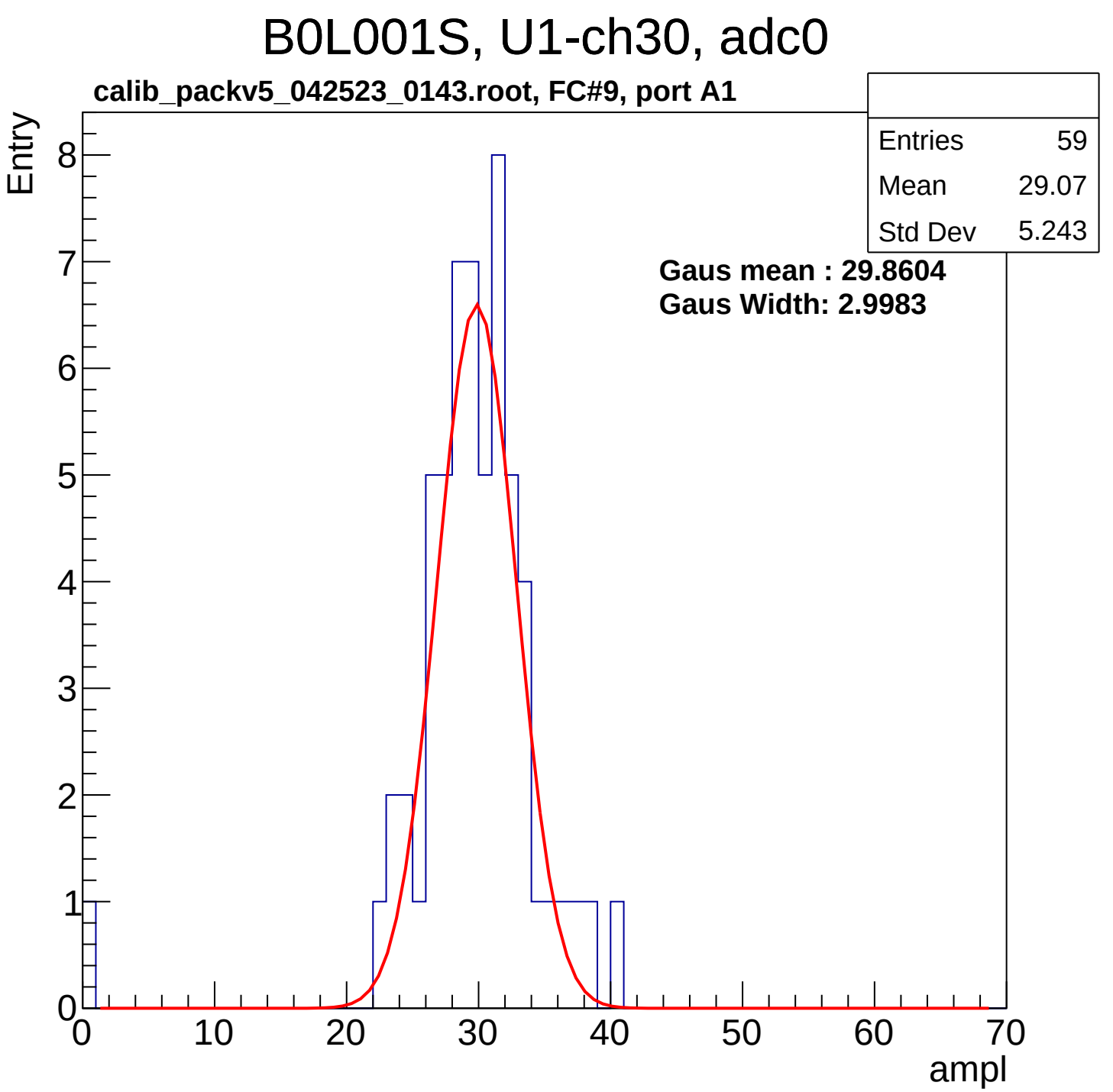
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.07
Std Dev 5.243
Gaus mean : 29.8604
Gaus Width: 2.9983
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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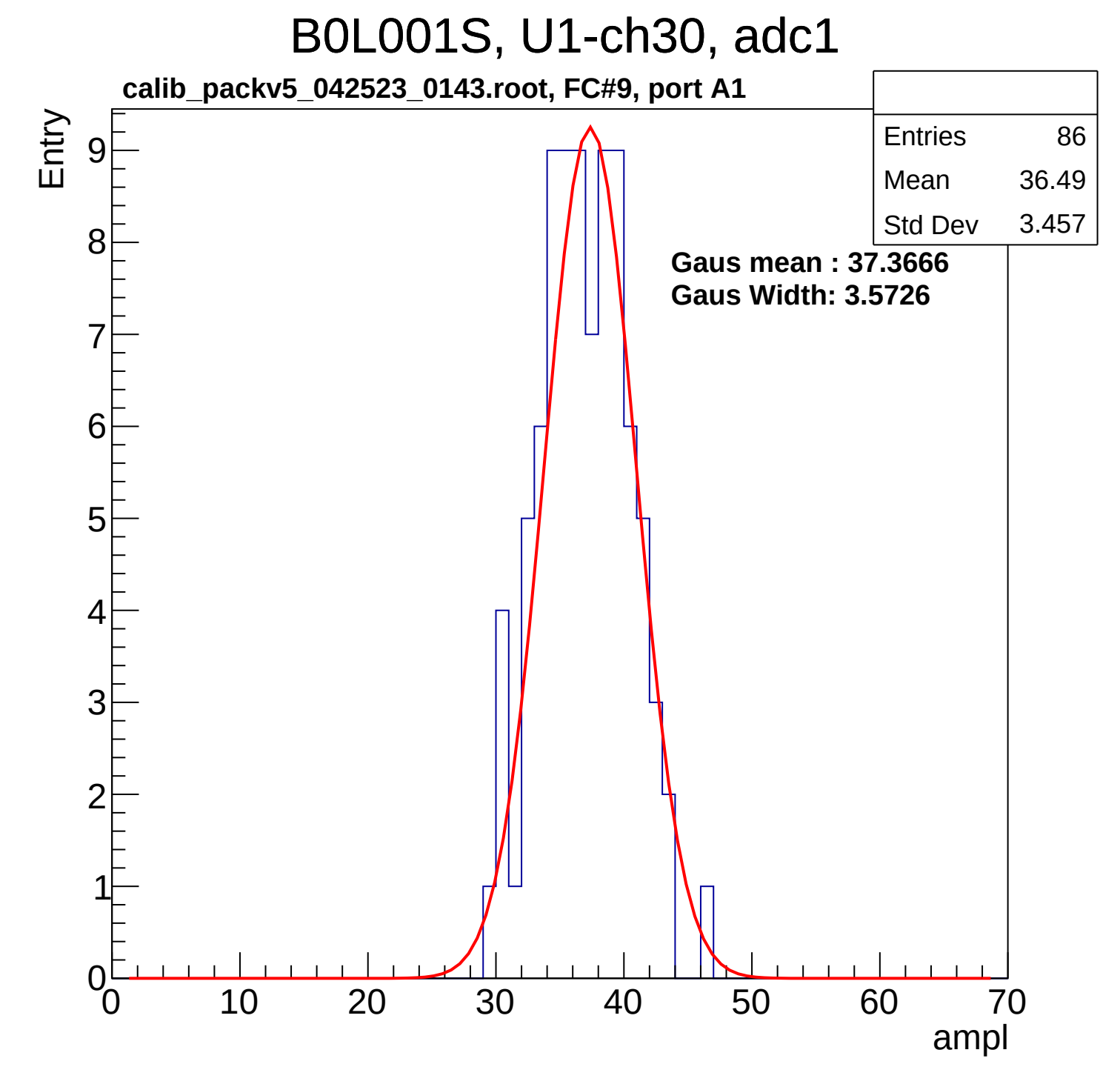
B0L001S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 36.49
Std Dev 3.457
Gaus mean : 37.3666
Gaus Width: 3.5726
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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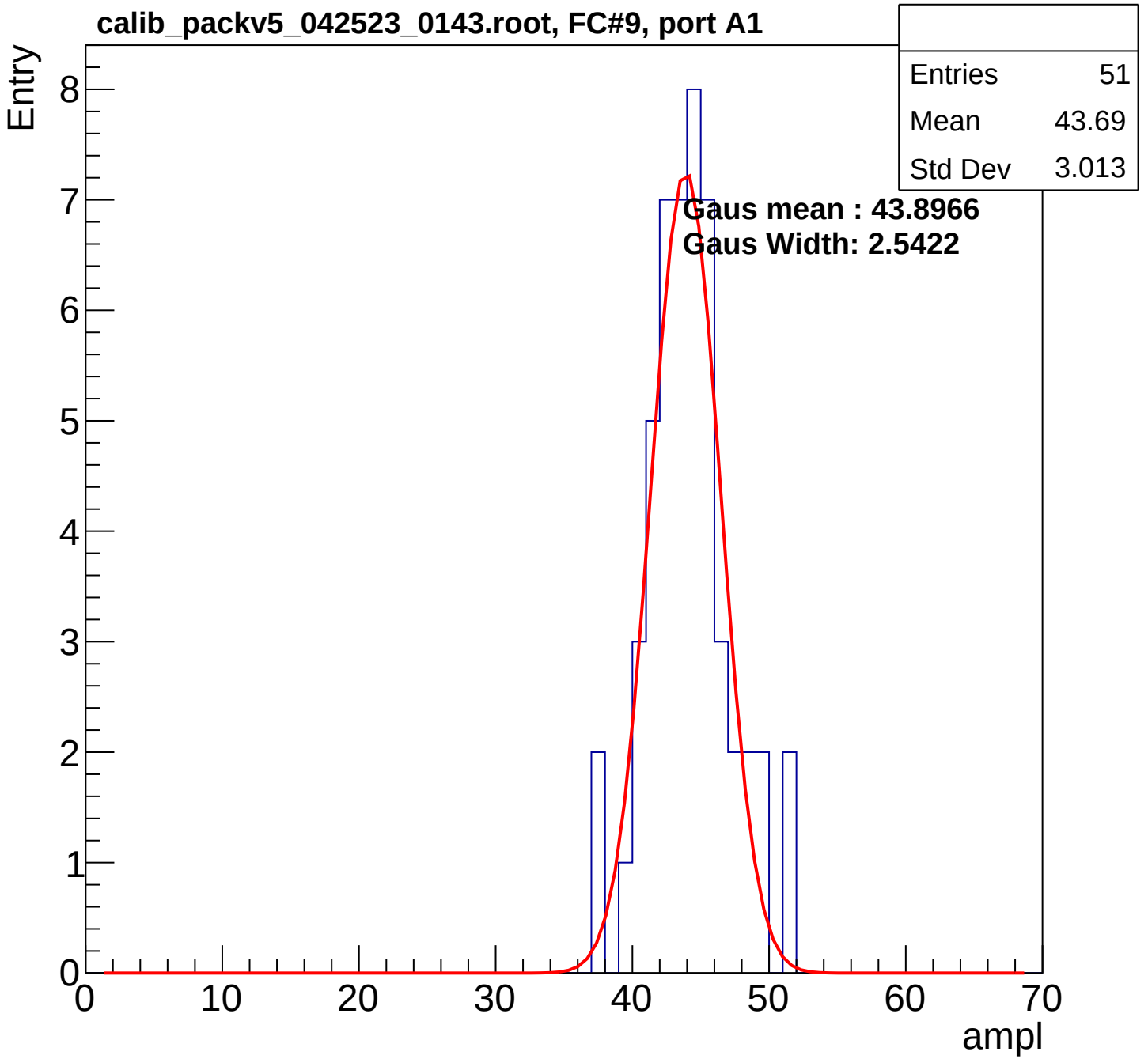
60
70


B0L001S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 43.69
Std Dev 3.013
Gaus mean : 43.8966
Gaus Width: 2.5422
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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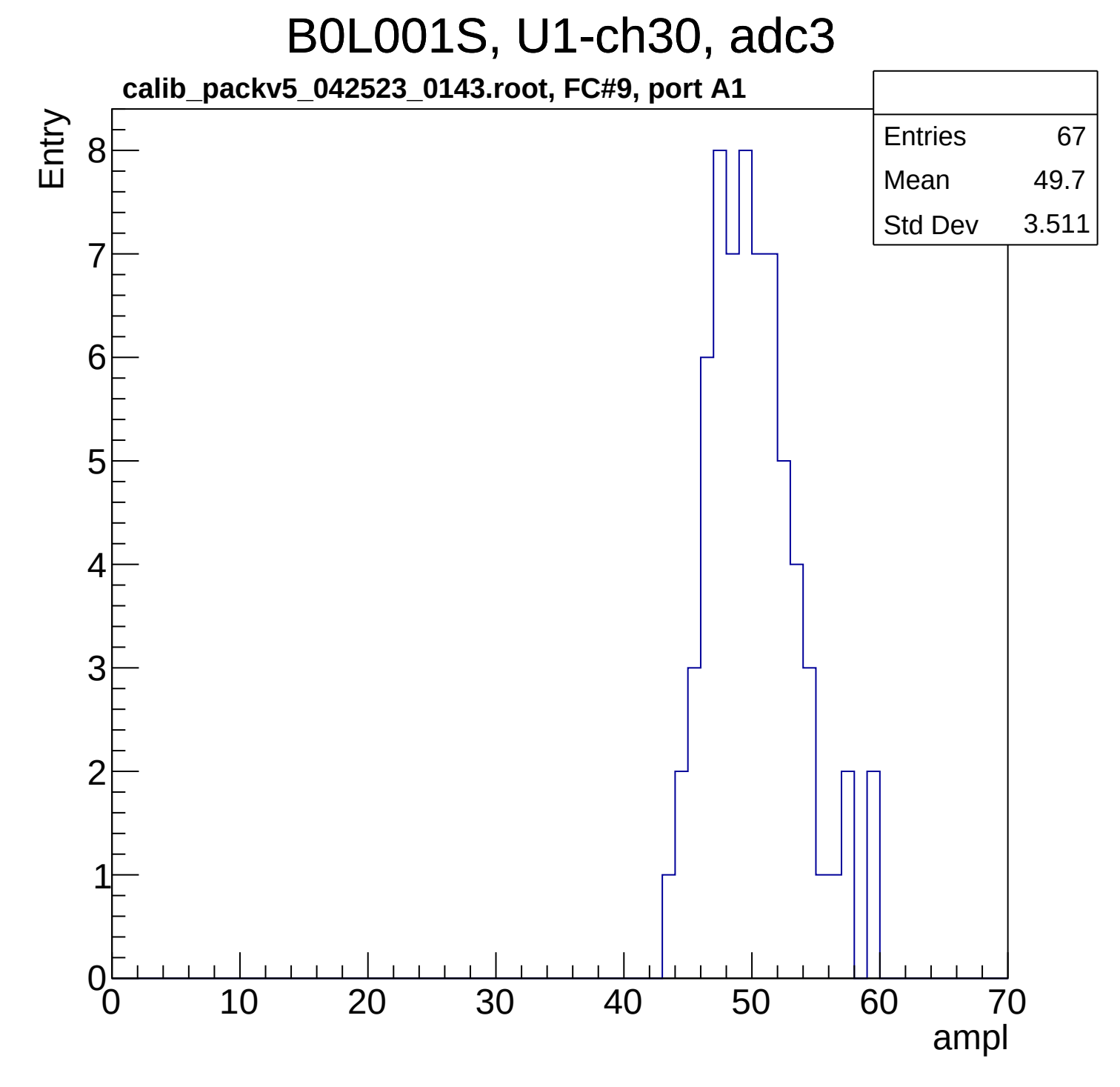
B0L001S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 49.7
Std Dev 3.511
Entry
ampl
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3
4
5
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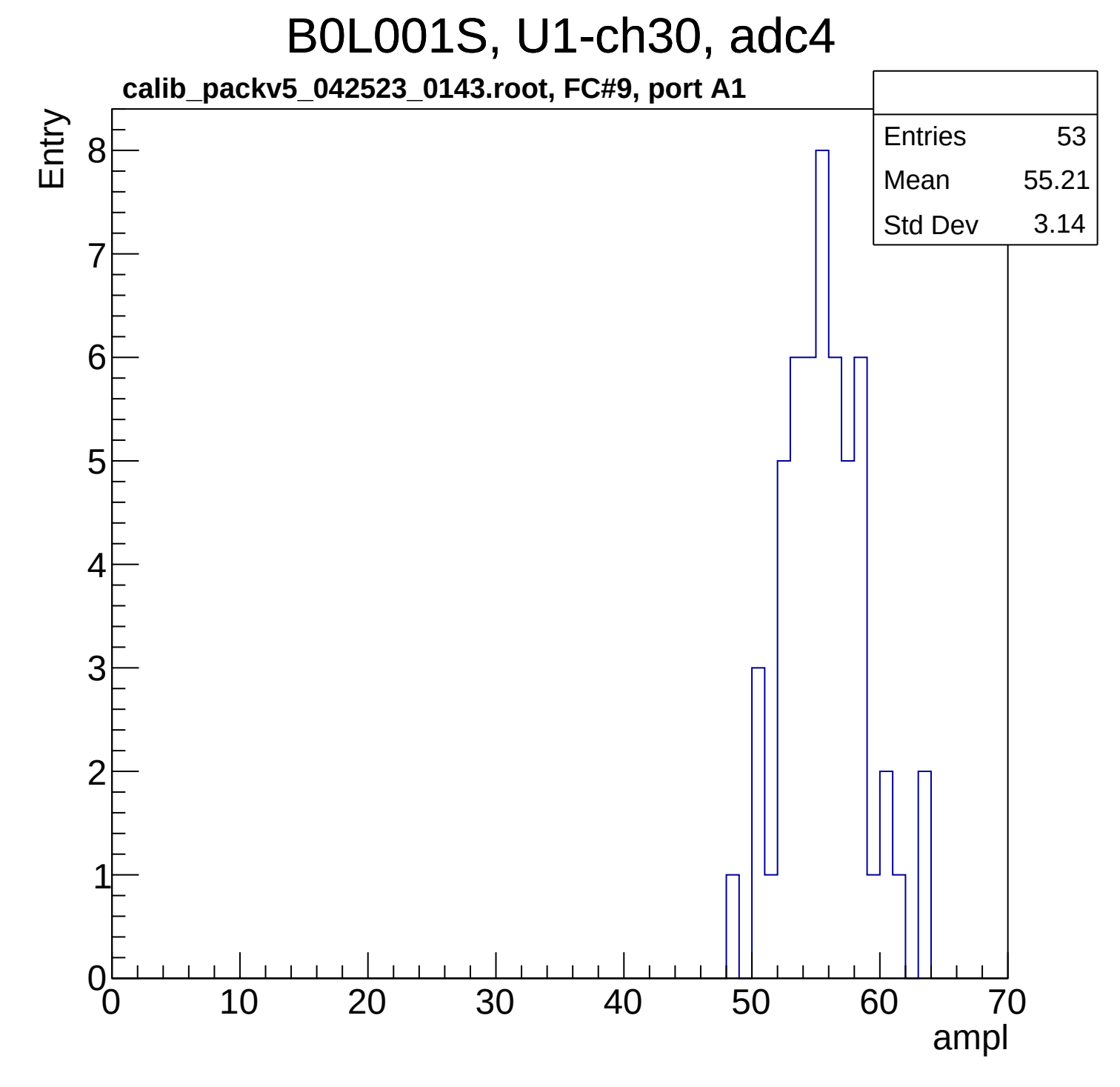
B0L001S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.21
Std Dev 3.14
Entry
ampl
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4
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6
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60
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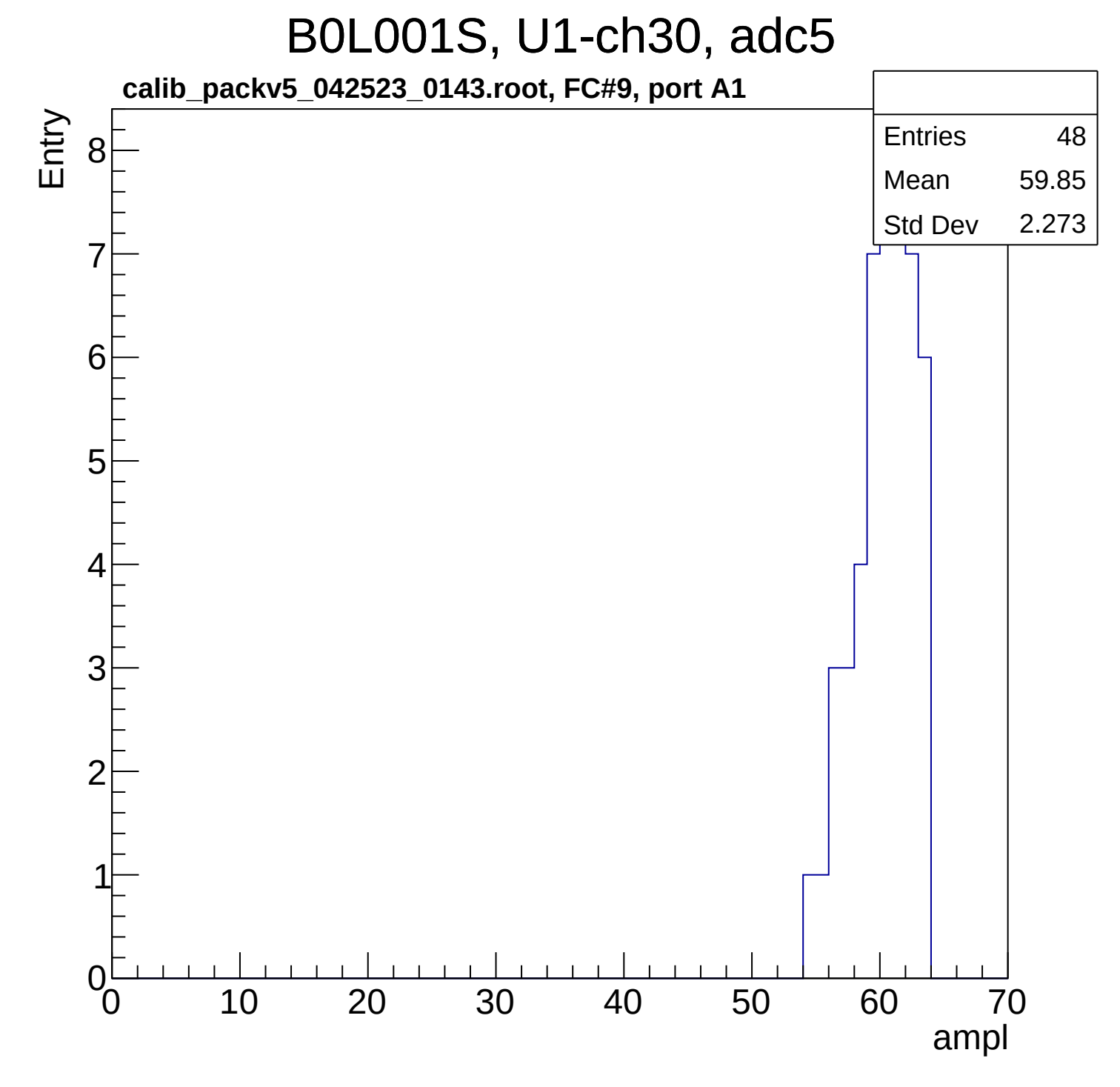
B0L001S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59.85
Std Dev 2.273
Entry
ampl
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4
5
6
7
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10
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40
50
60
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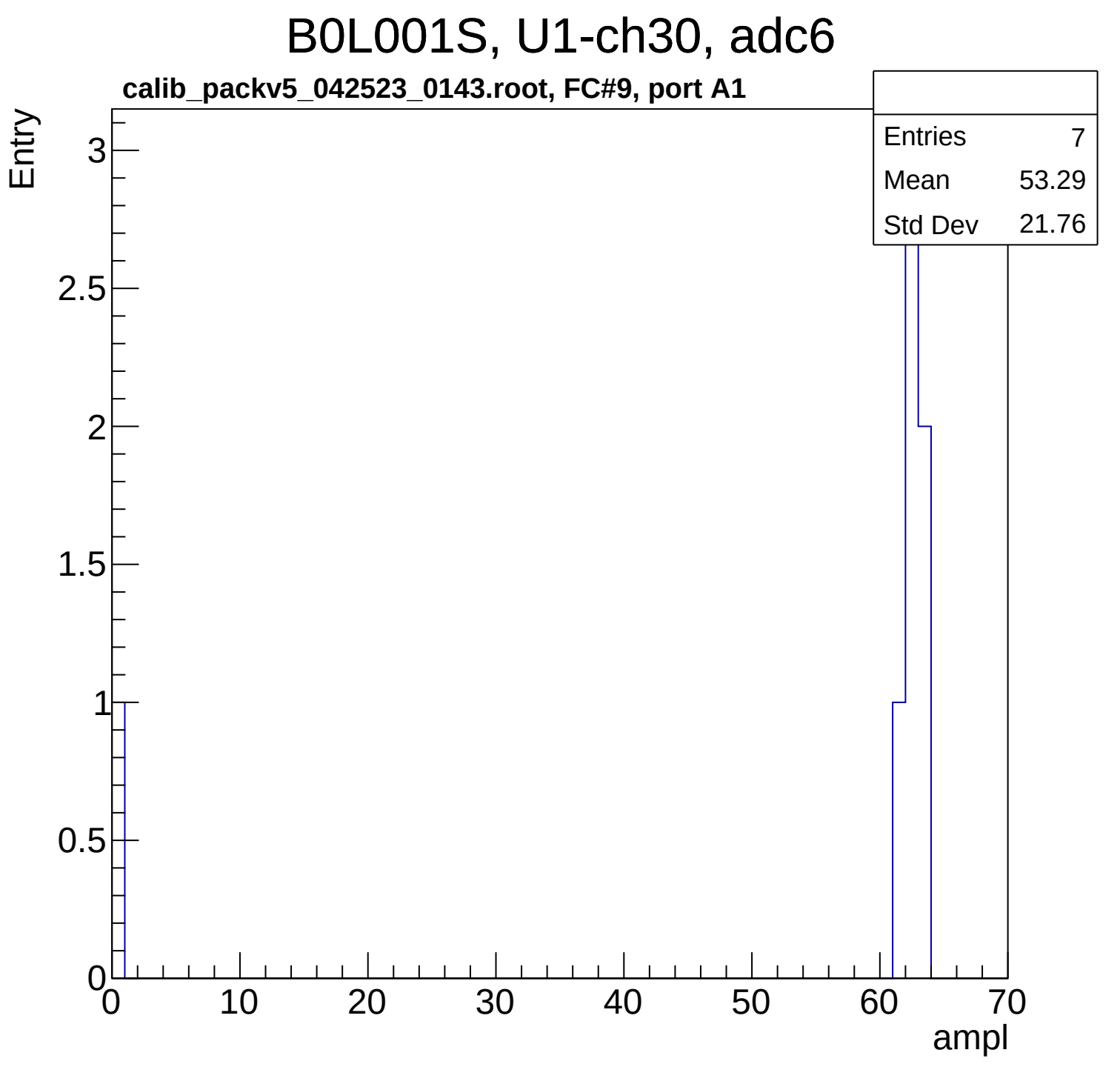
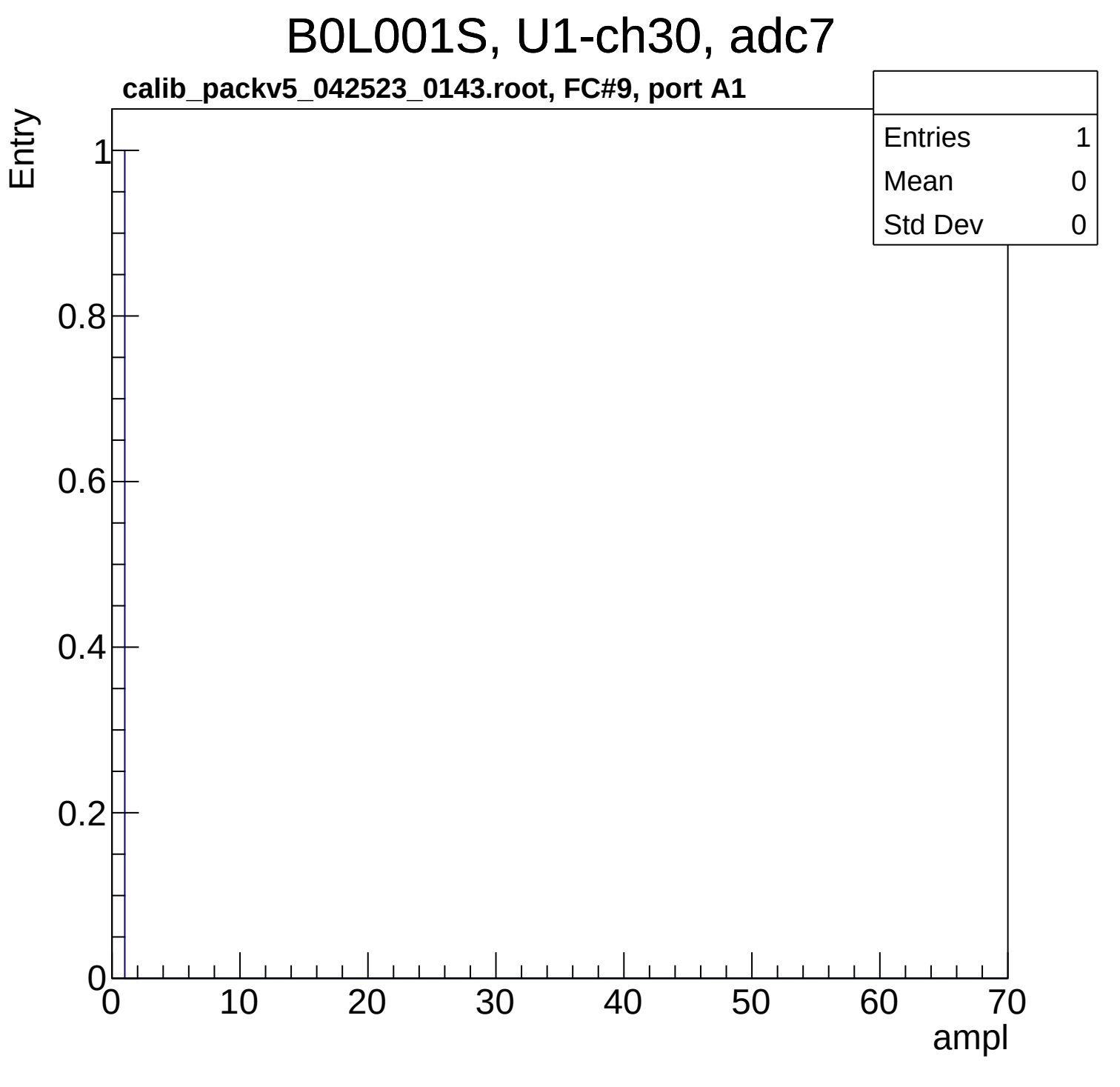

B0L001S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 53.29
Std Dev 21.76
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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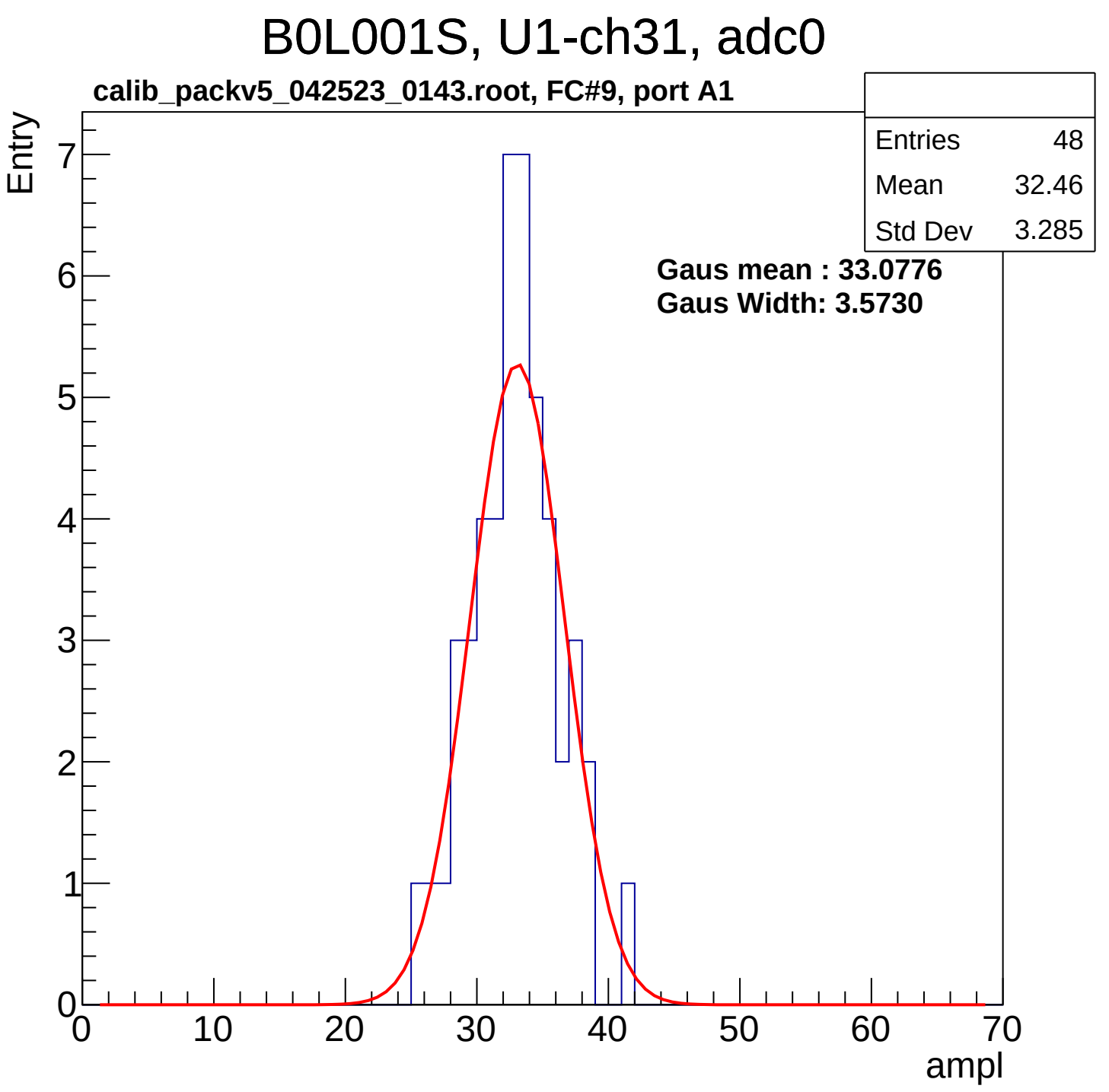
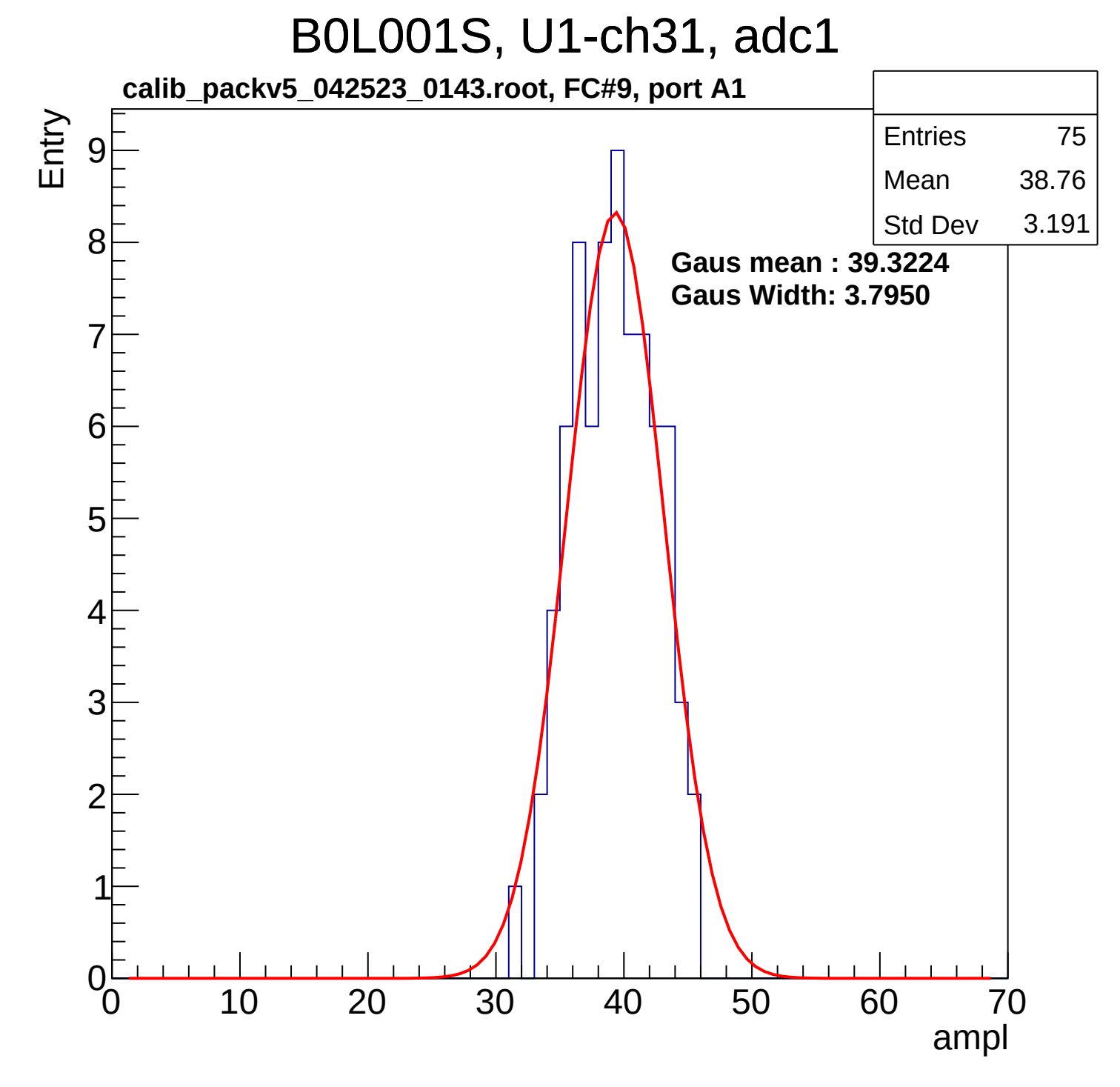
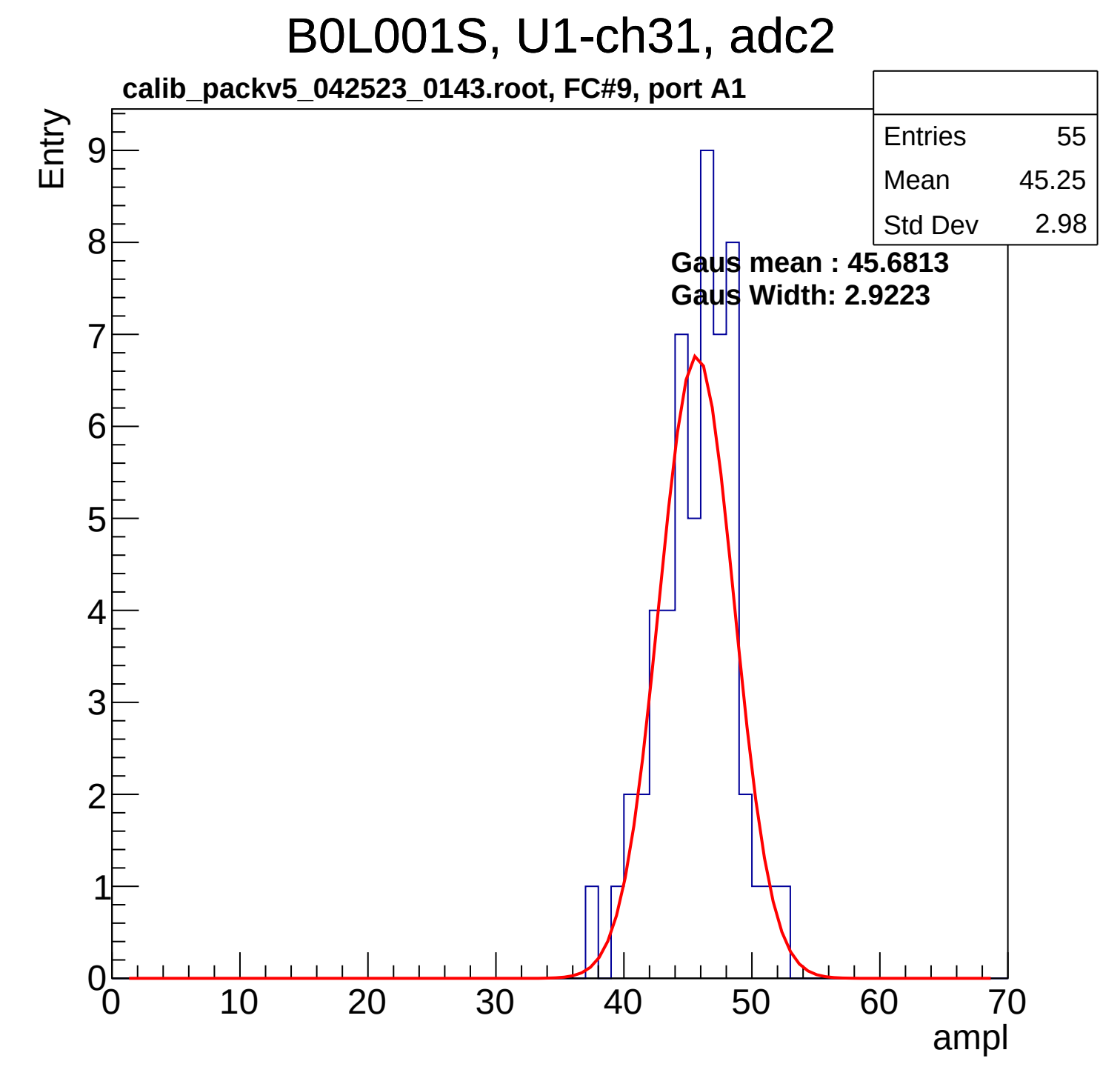
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 32.46
Std Dev 3.285
Gaus mean : 33.0776
Gaus Width: 3.5730
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.76
Std Dev 3.191
Gaus mean : 39.3224
Gaus Width: 3.7950
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 45.25
Std Dev 2.98
Gaus mean : 45.6813
Gaus Width: 2.9223
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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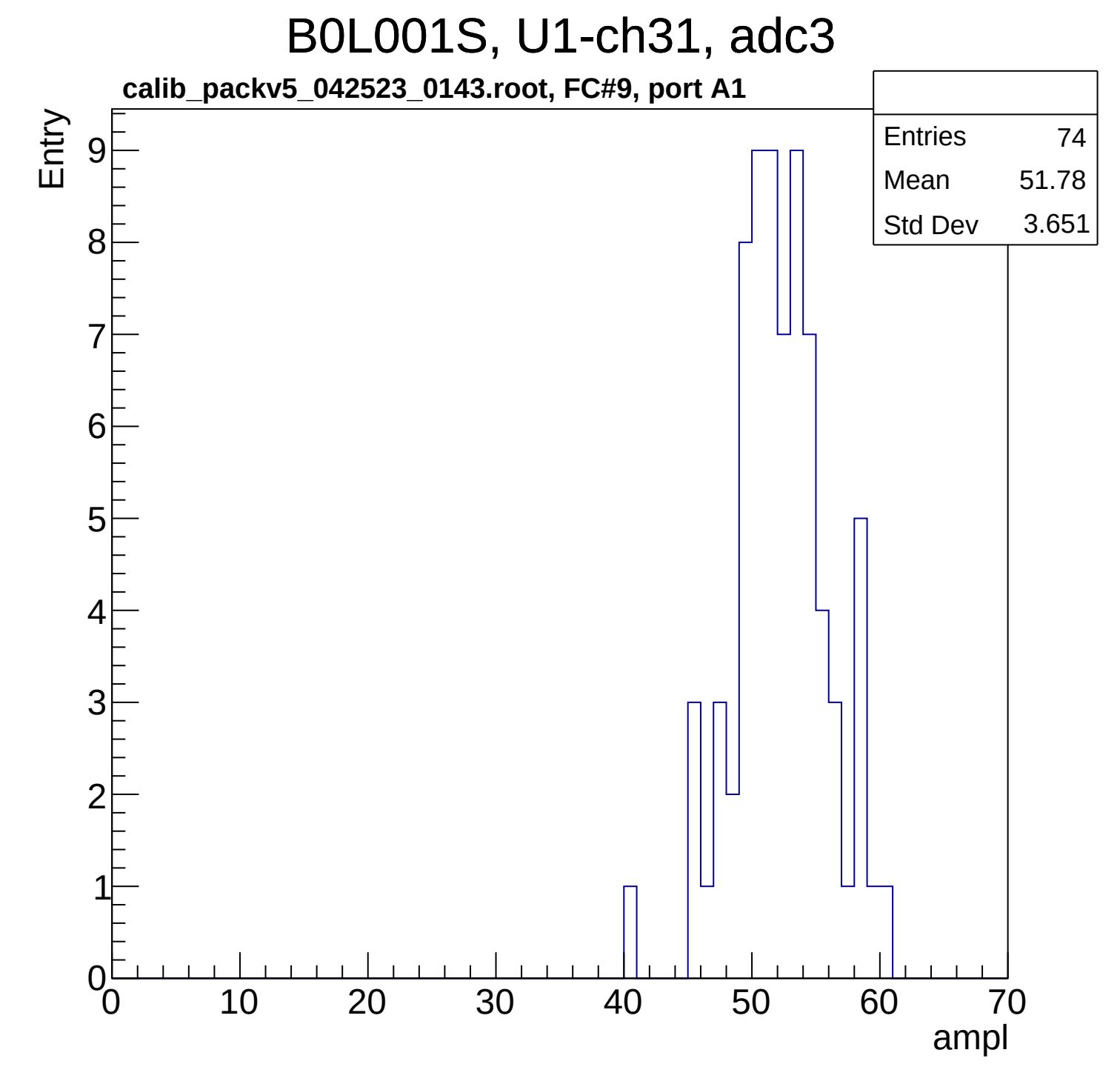
B0L001S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 51.78
Std Dev 3.651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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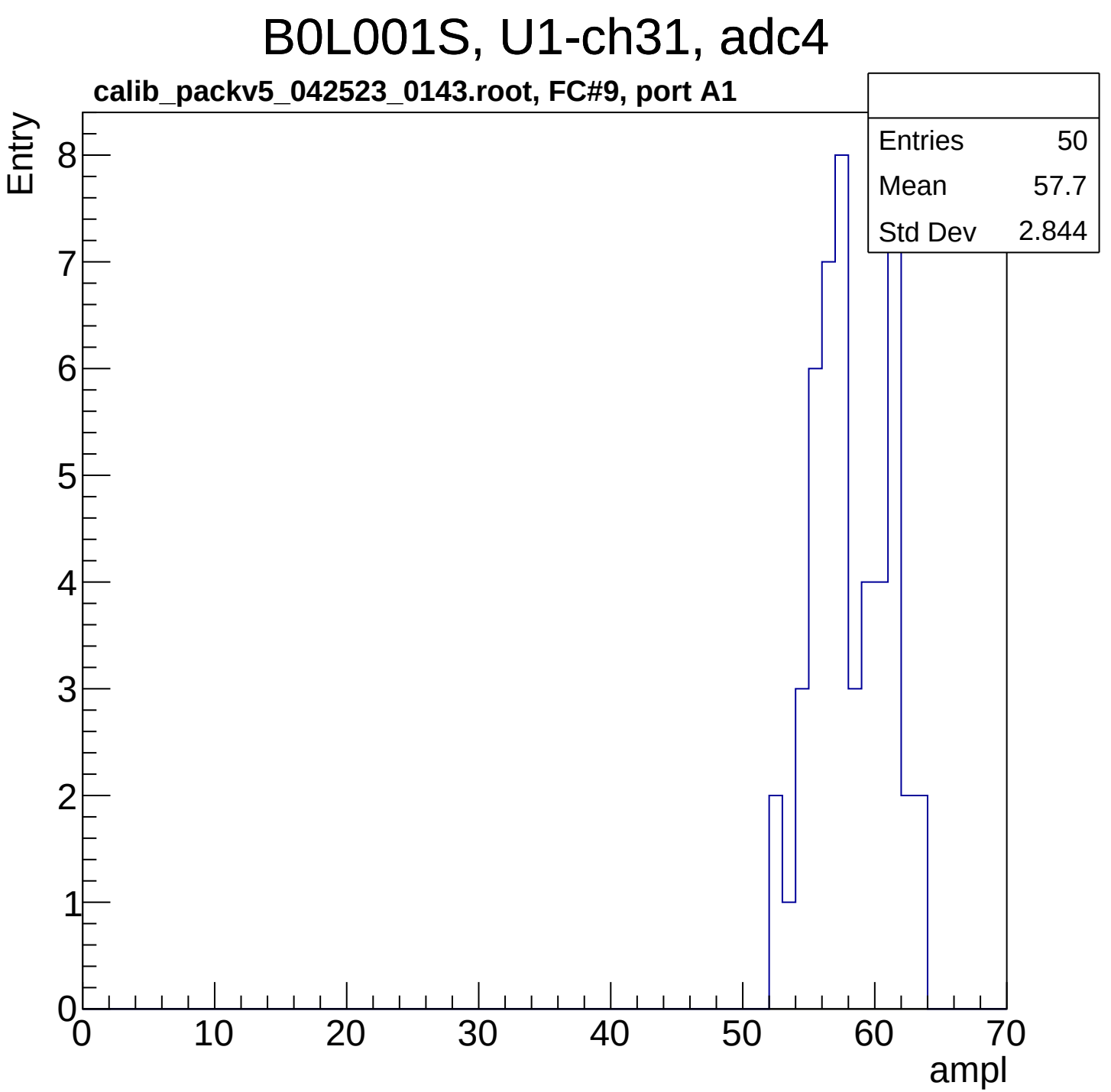
B0L001S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.7
Std Dev 2.844
Entry
ampl
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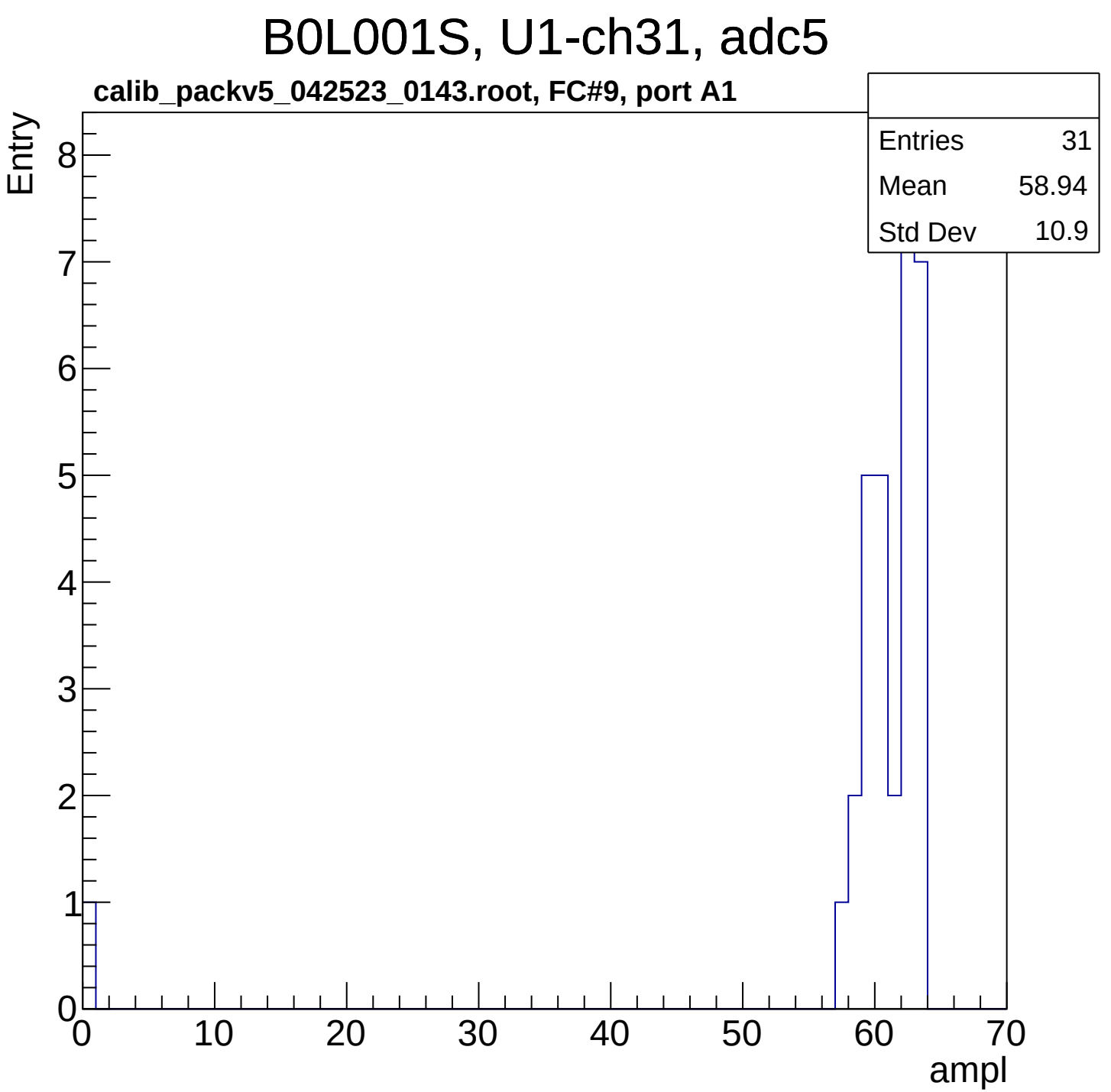
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58.94
Std Dev 10.9
Entry
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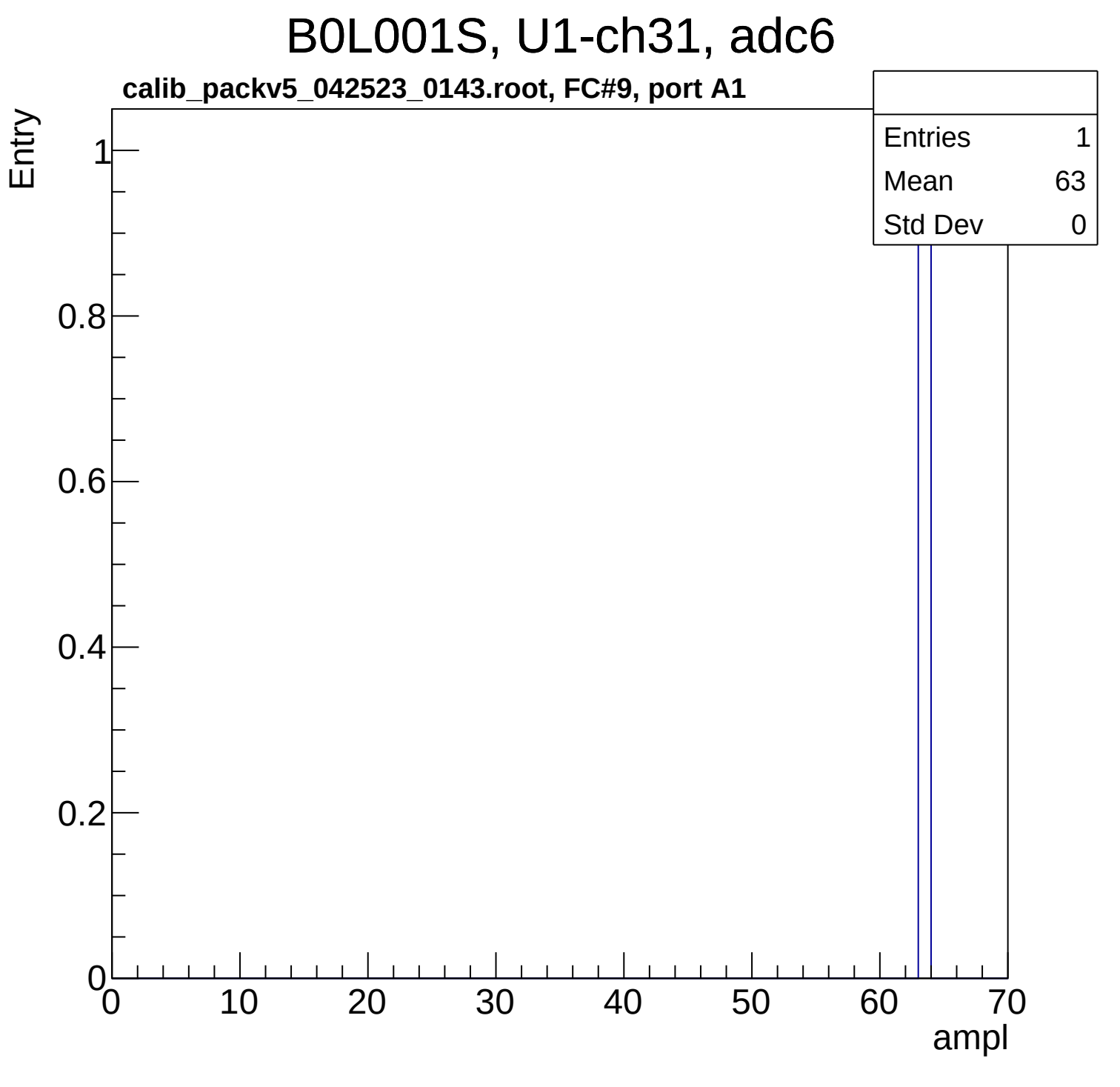
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
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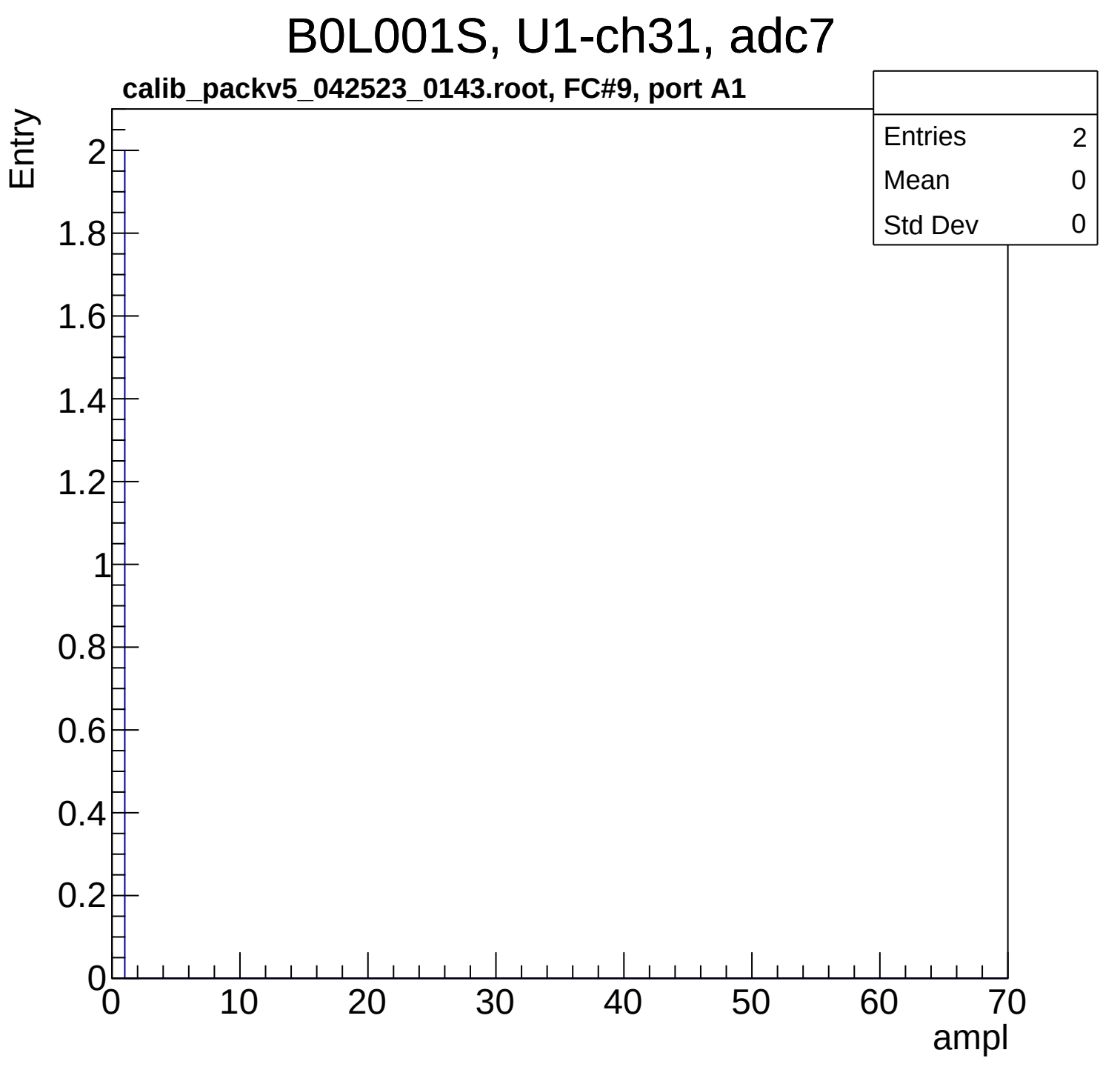
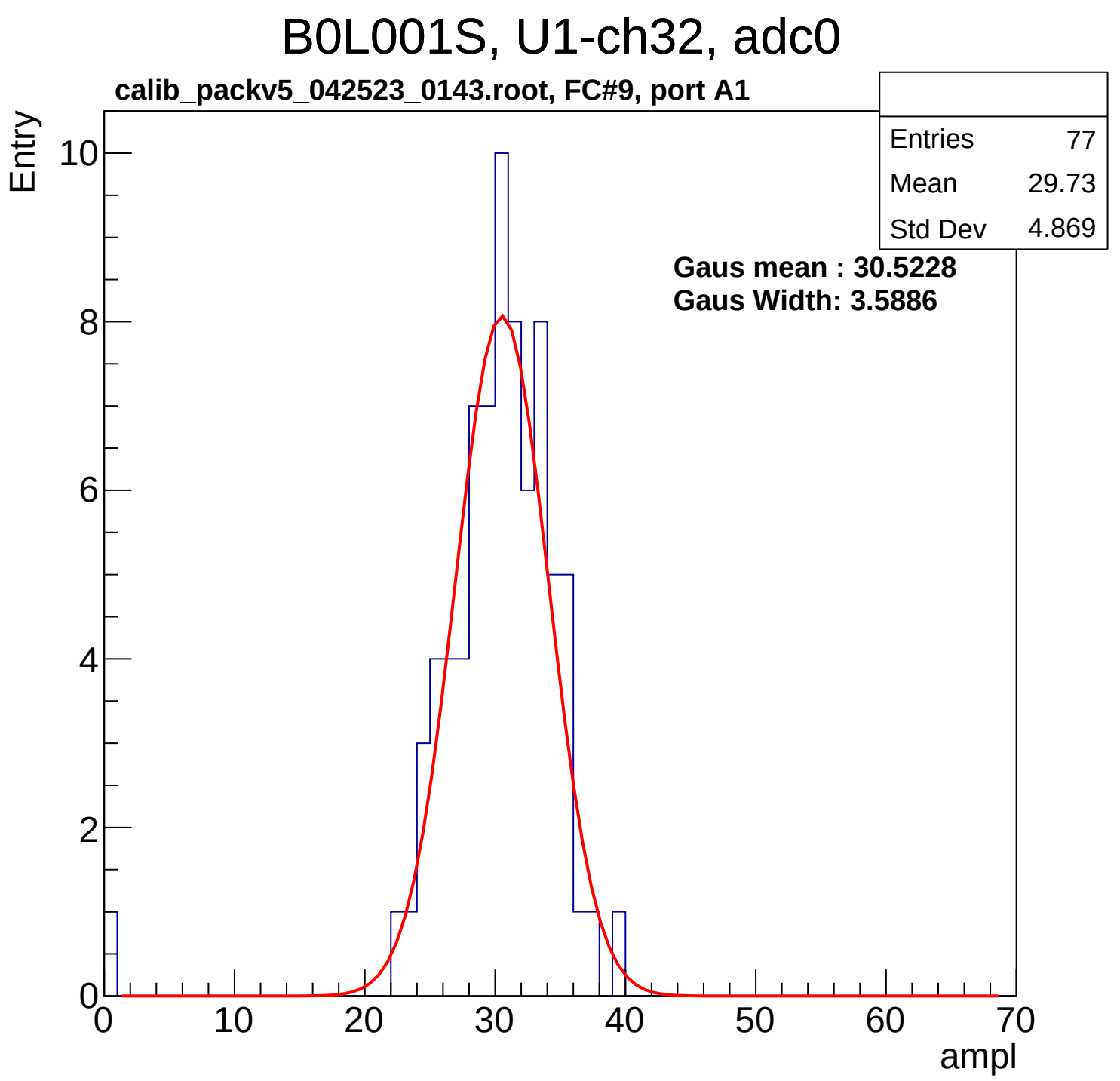
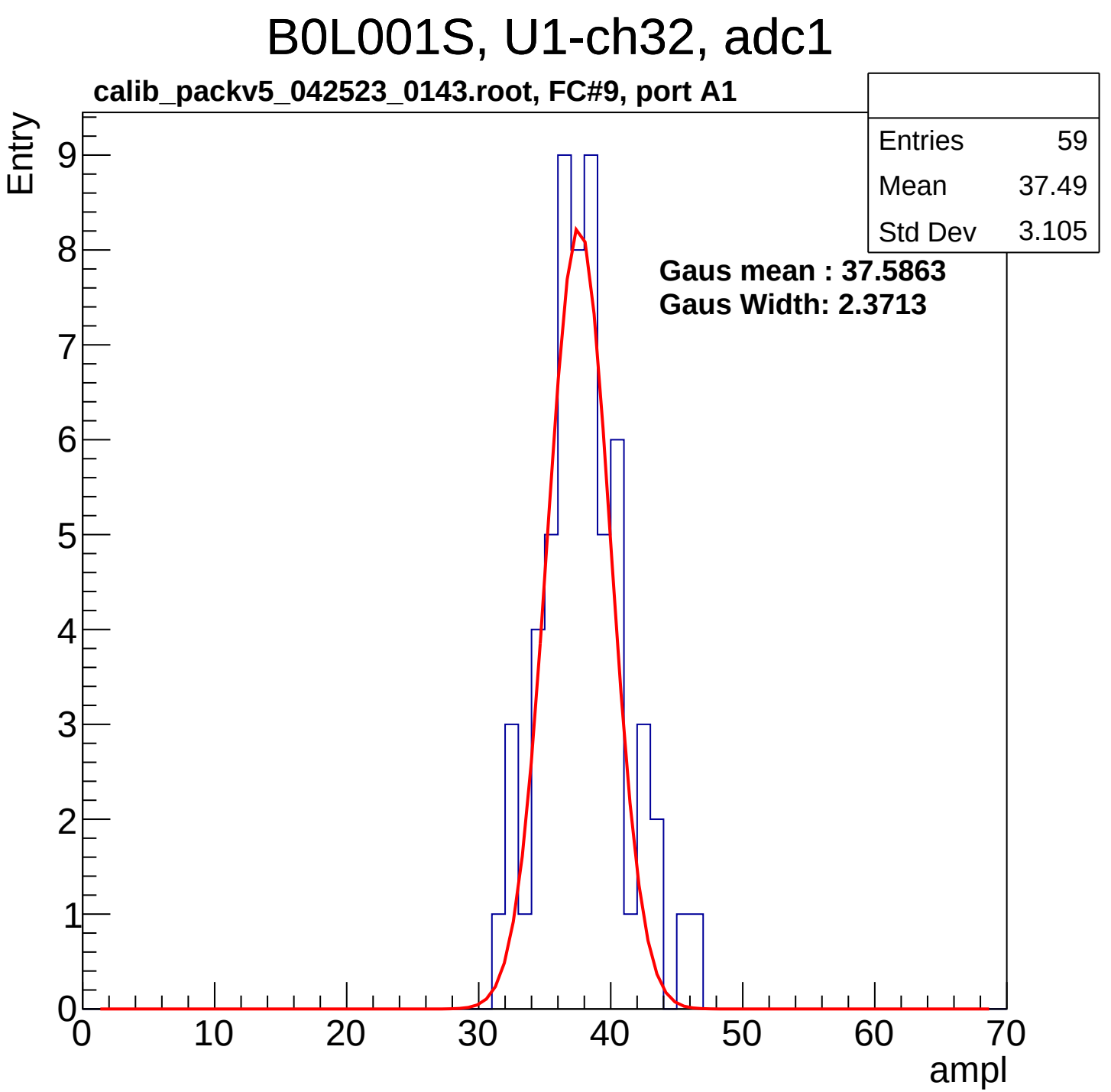
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.73
Std Dev 4.869
Gaus mean : 30.5228
Gaus Width: 3.5886
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.49
Std Dev 3.105
Gaus mean : 37.5863
Gaus Width: 2.3713
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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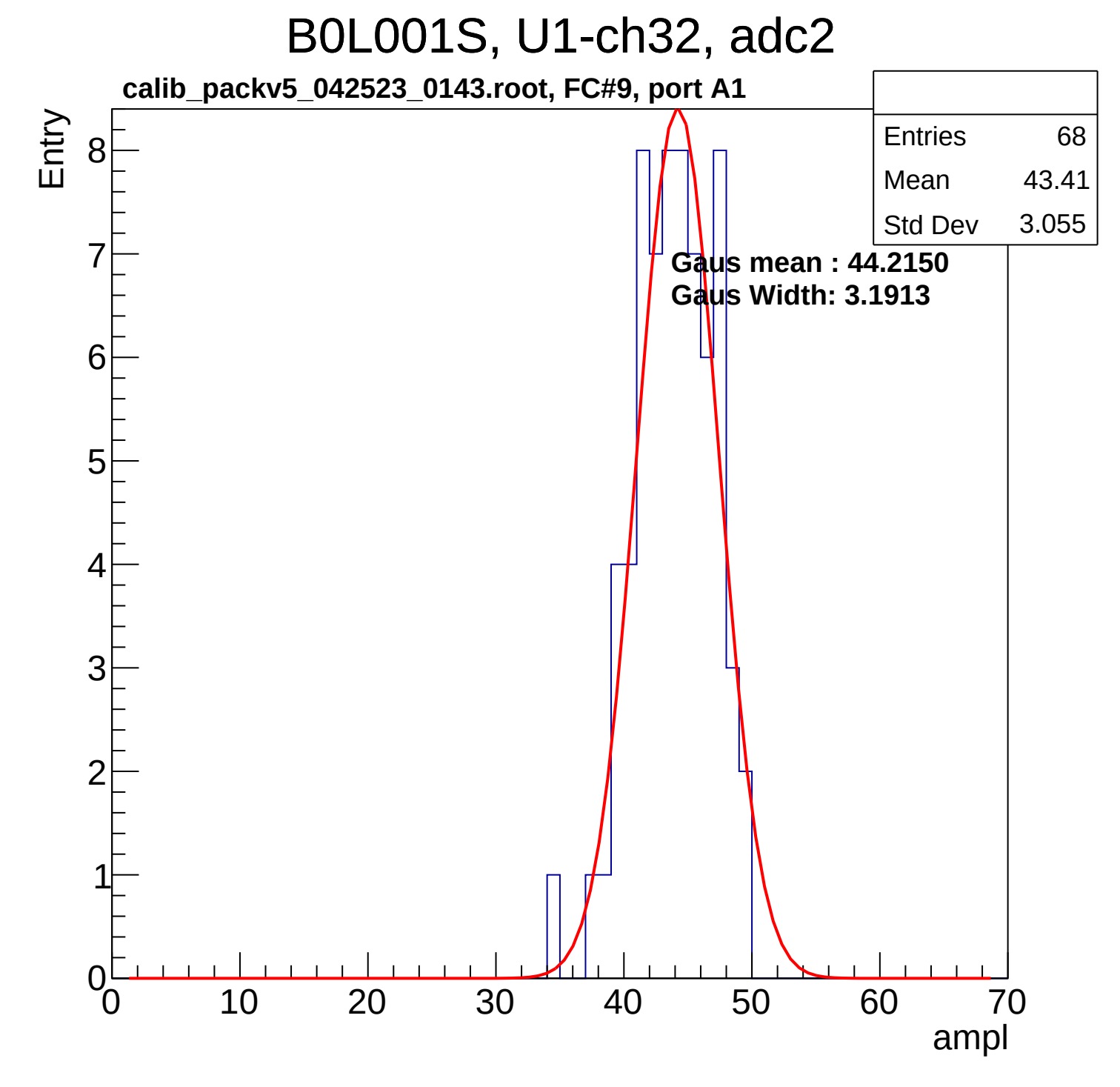
B0L001S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.41
Std Dev 3.055
Gaus mean : 44.2150
Gaus Width: 3.1913
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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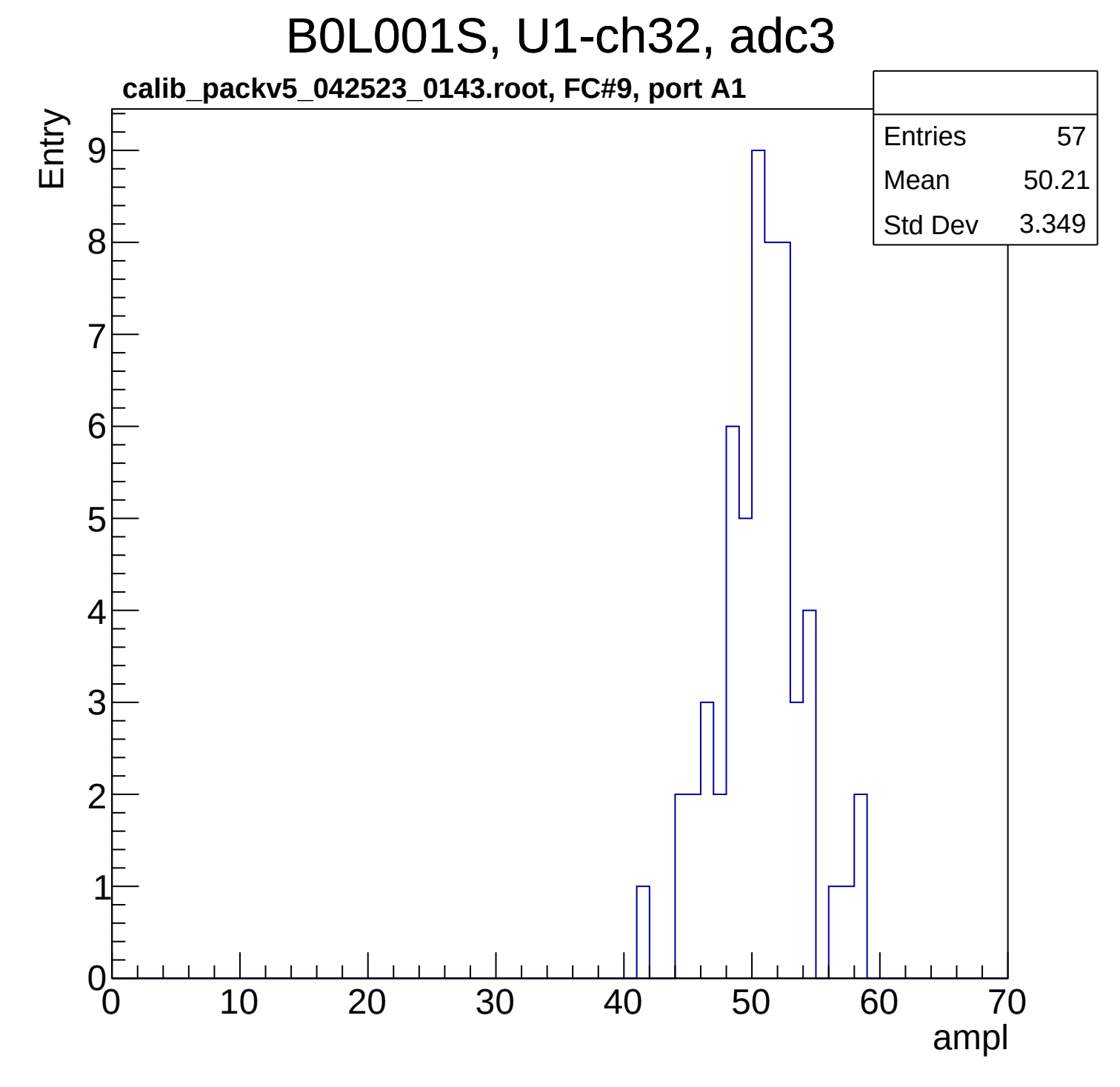
B0L001S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.21
Std Dev 3.349
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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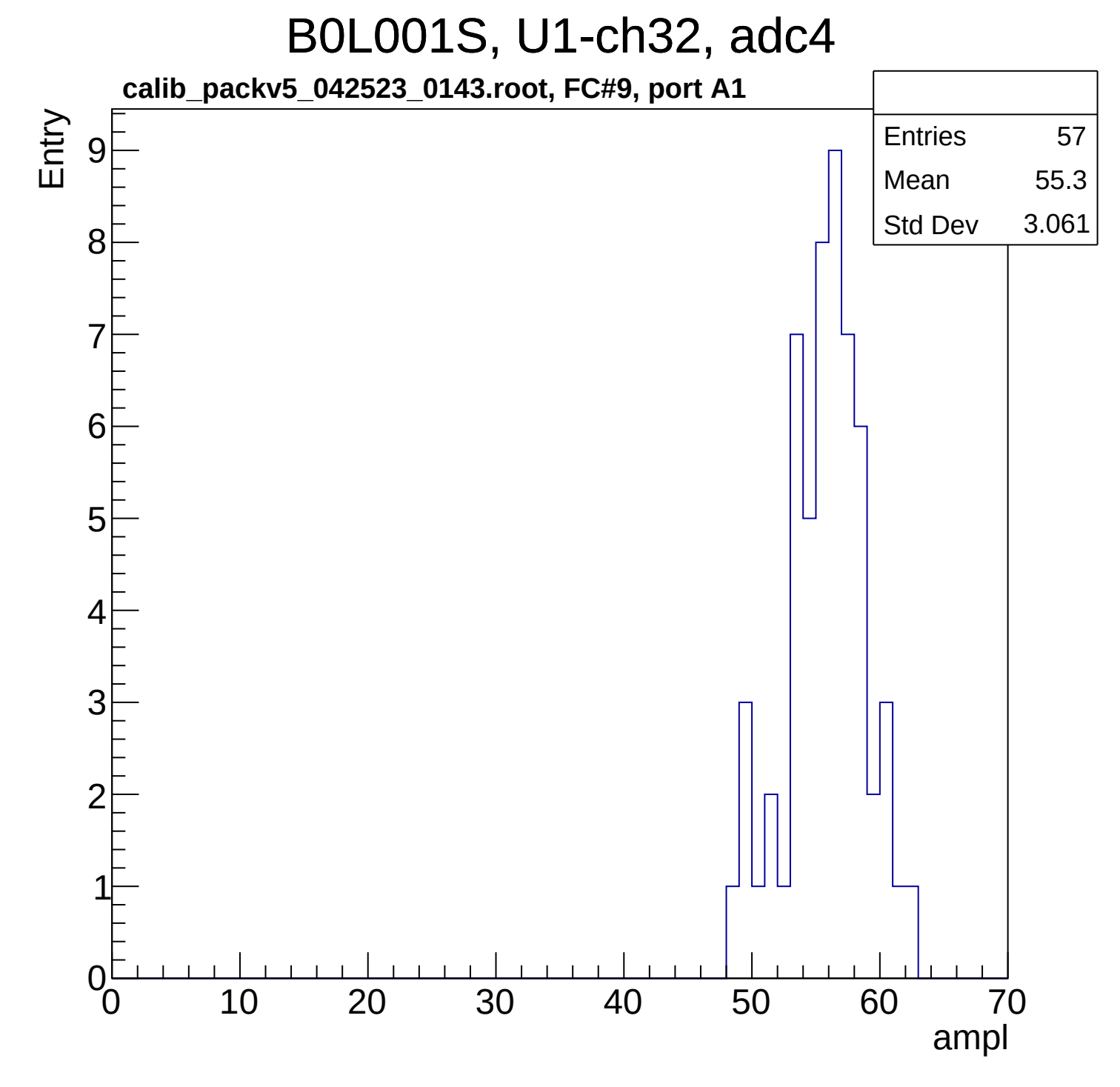
B0L001S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 55.3
Std Dev 3.061
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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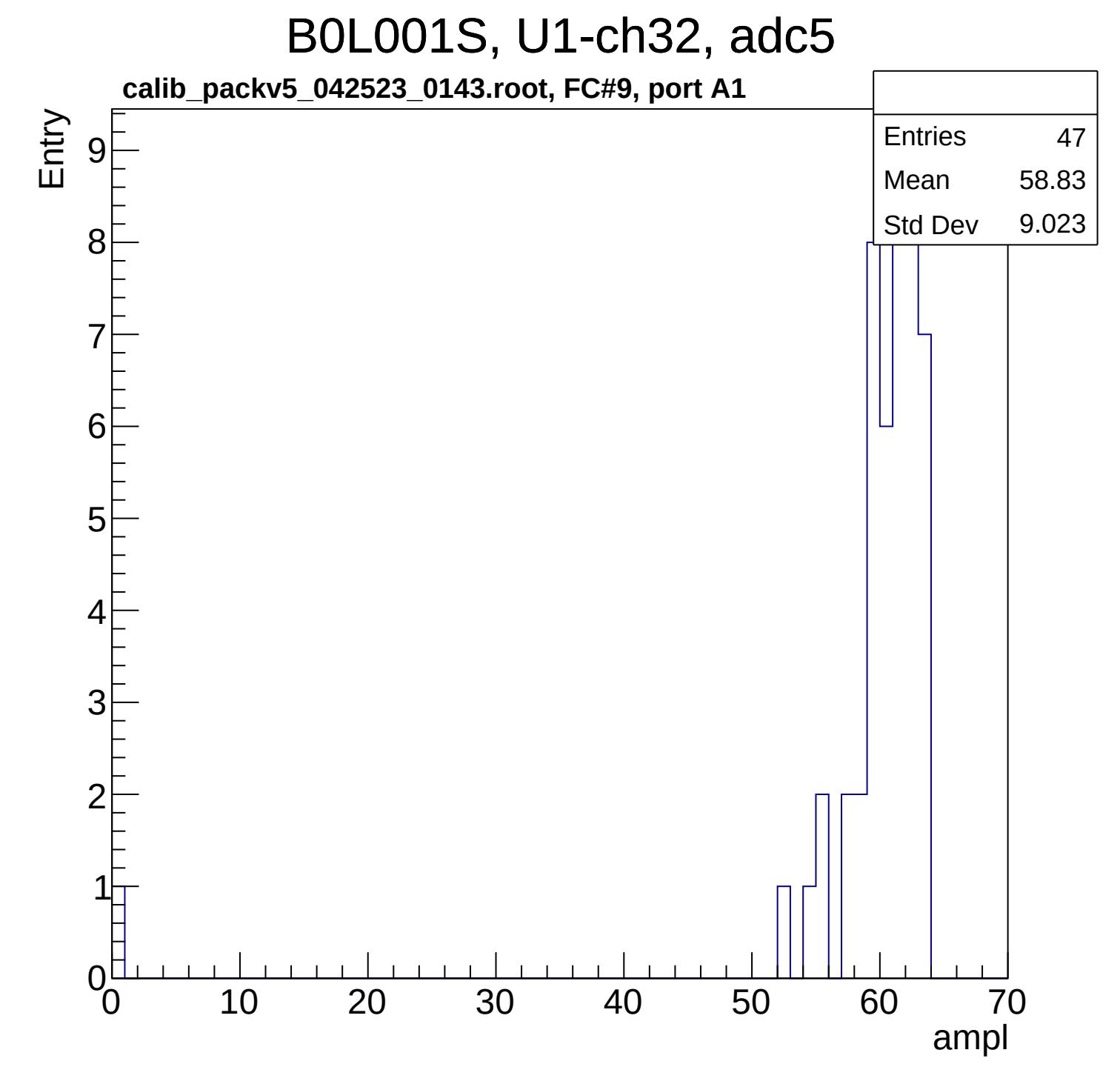
B0L001S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.83
Std Dev 9.023
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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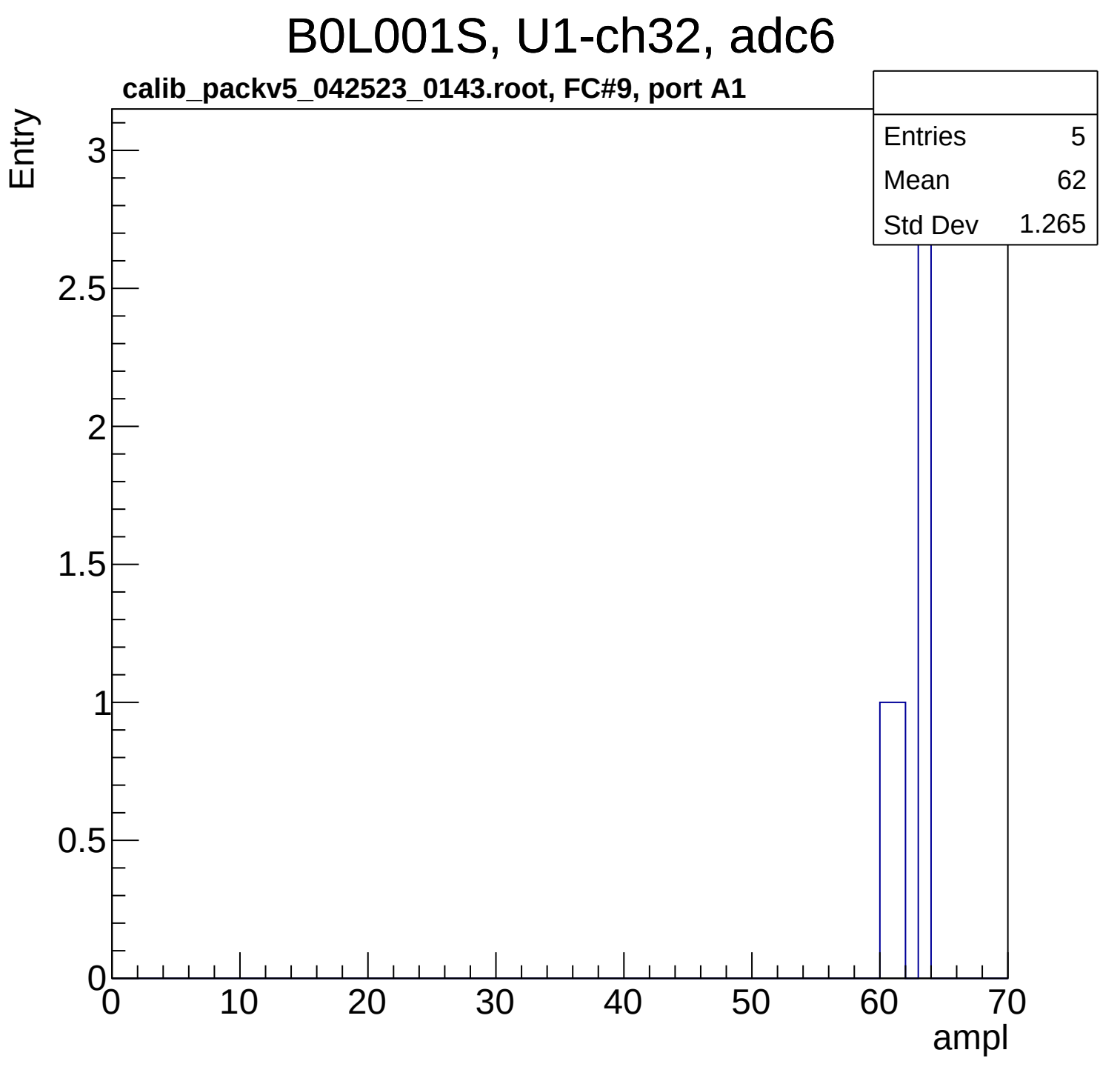
B0L001S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 1.265
Entry
ampl
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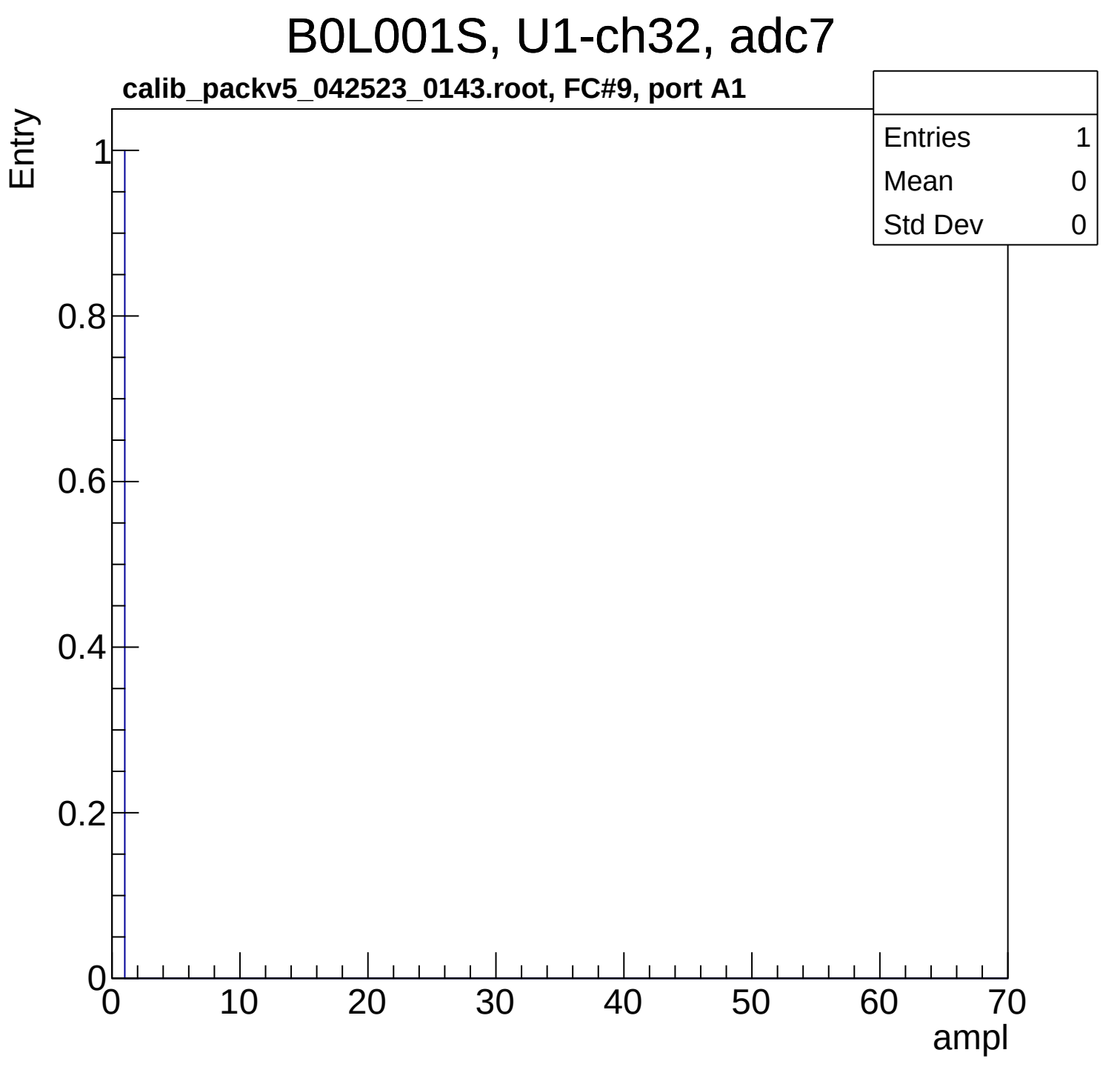
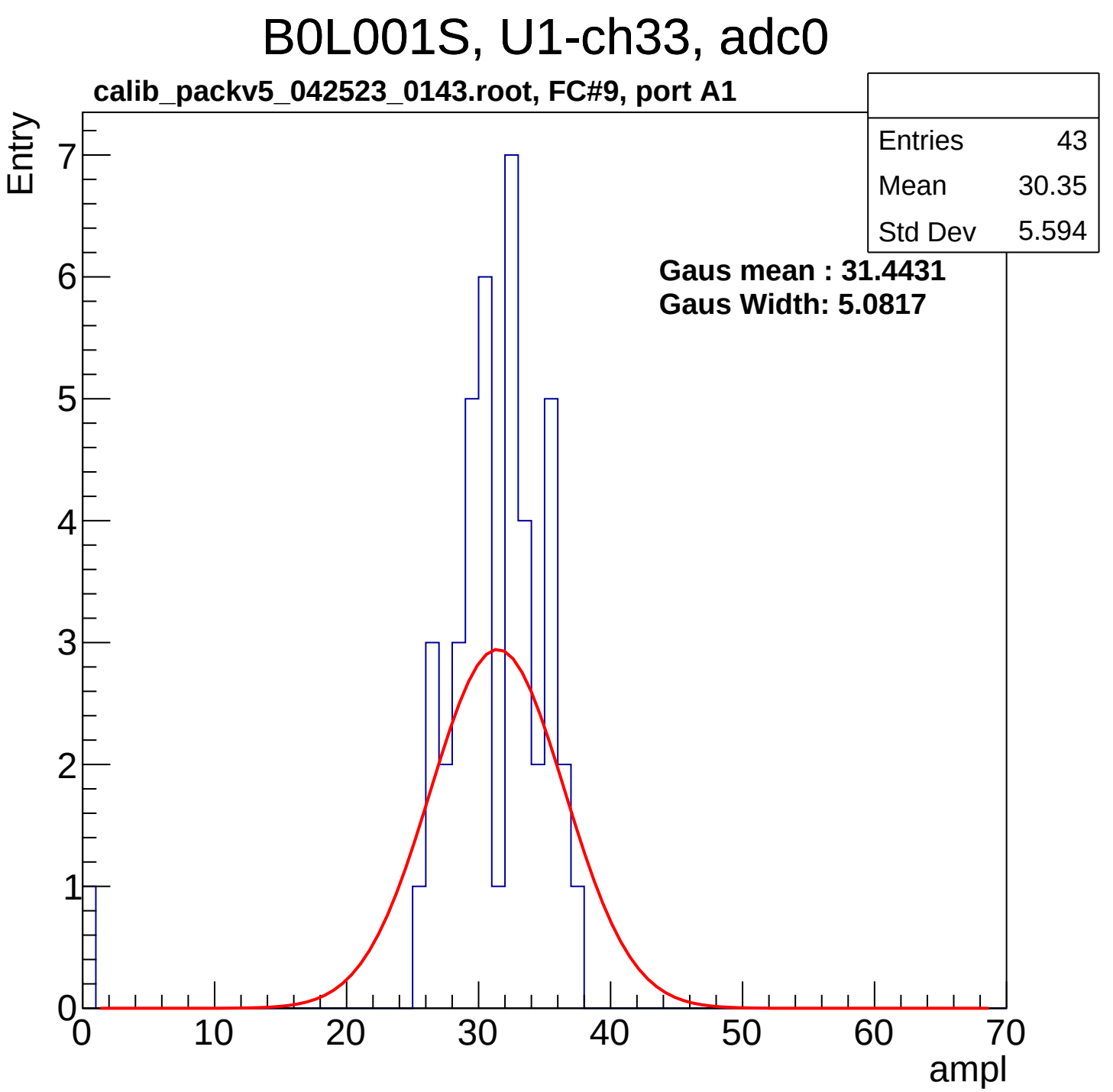
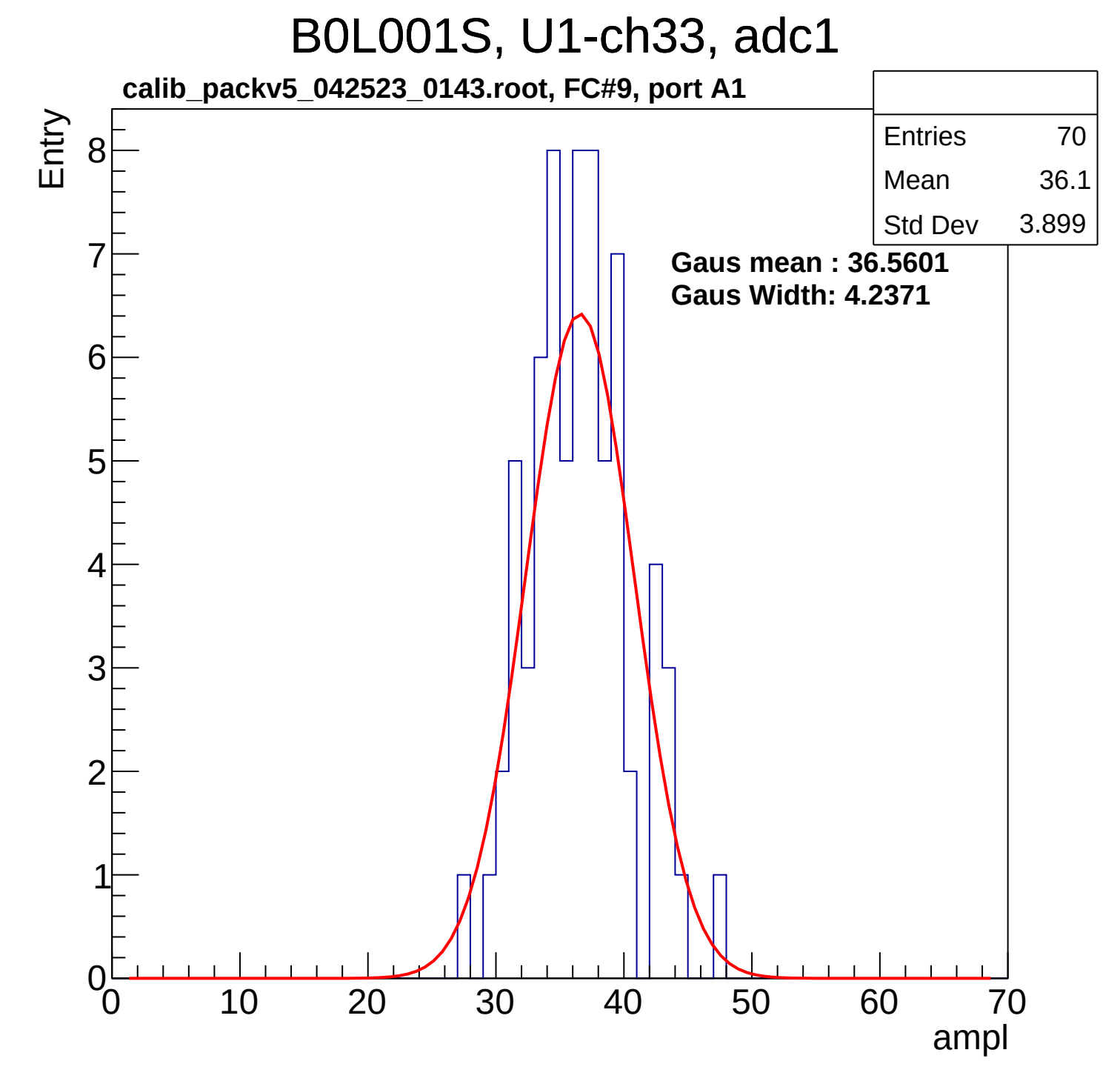
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 30.35
Std Dev 5.594
Gaus mean : 31.4431
Gaus Width: 5.0817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.1
Std Dev 3.899
Gaus mean : 36.5601
Gaus Width: 4.2371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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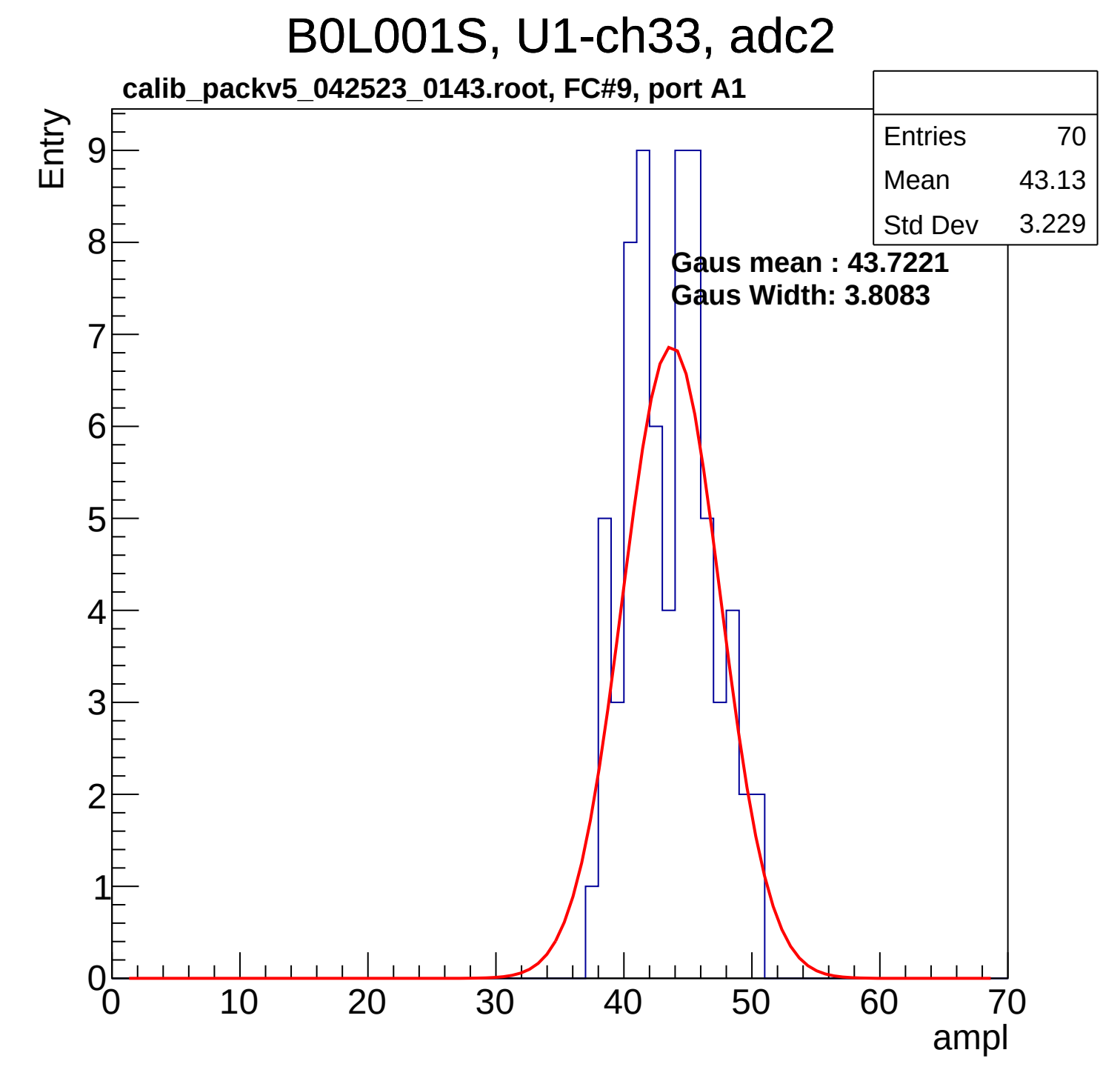
40
50
60
70


B0L001S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 43.13
Std Dev 3.229
Gaus mean : 43.7221
Gaus Width: 3.8083
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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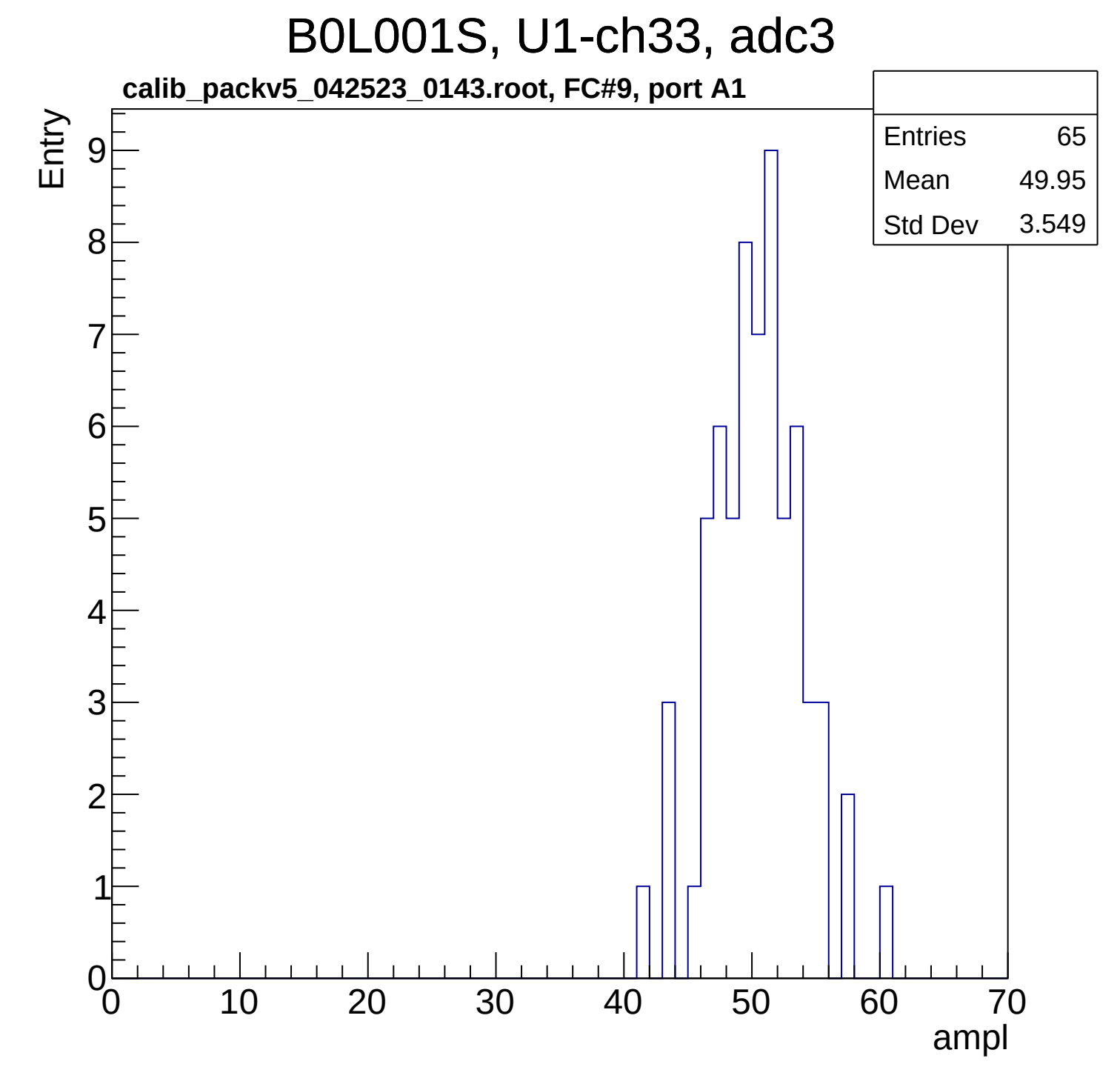
60
70


B0L001S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.95
Std Dev 3.549
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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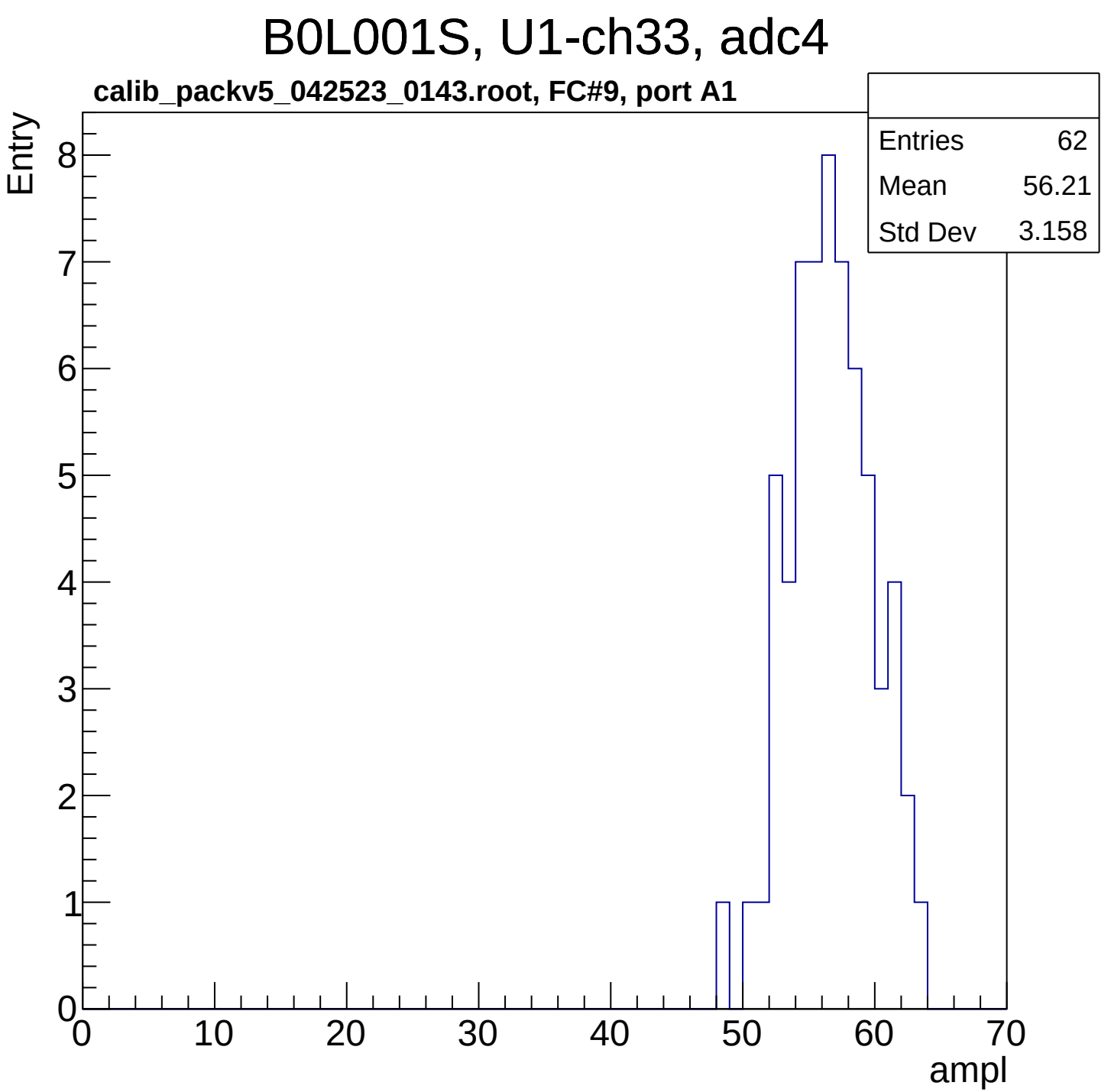
B0L001S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.21
Std Dev 3.158
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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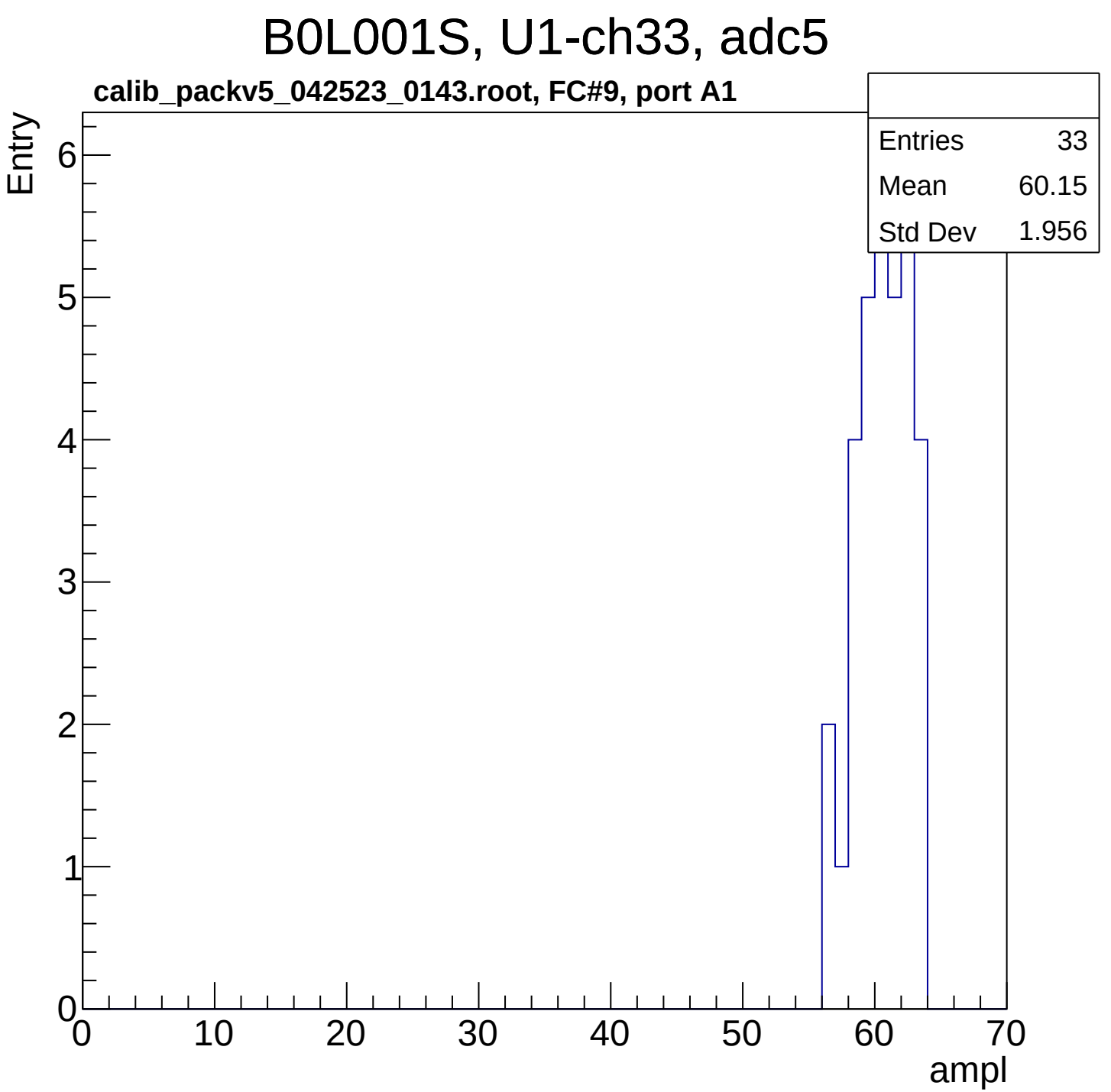
60
70


B0L001S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 33
Mean 60.15
Std Dev 1.956
Entry
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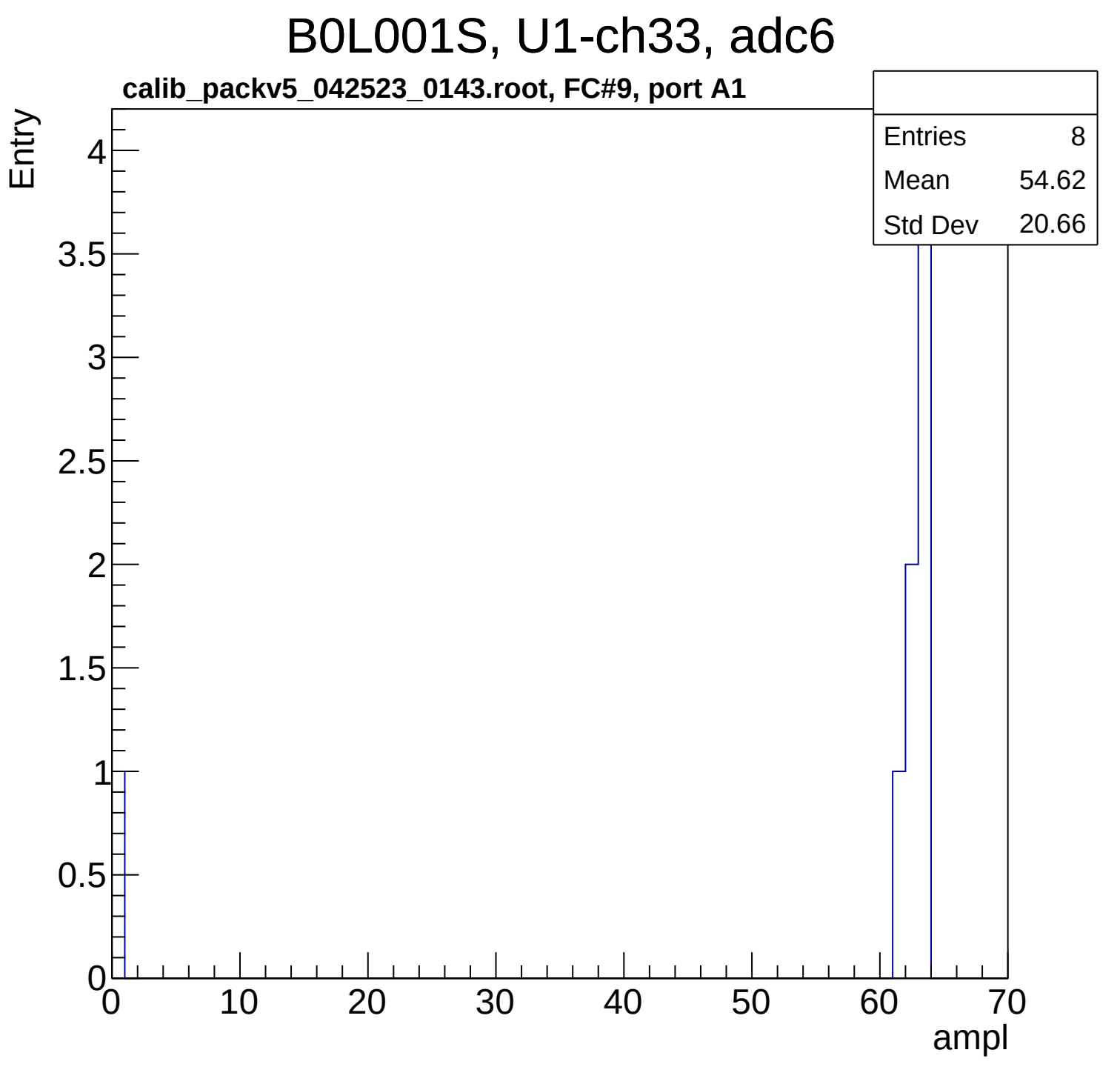
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 54.62
Std Dev 20.66
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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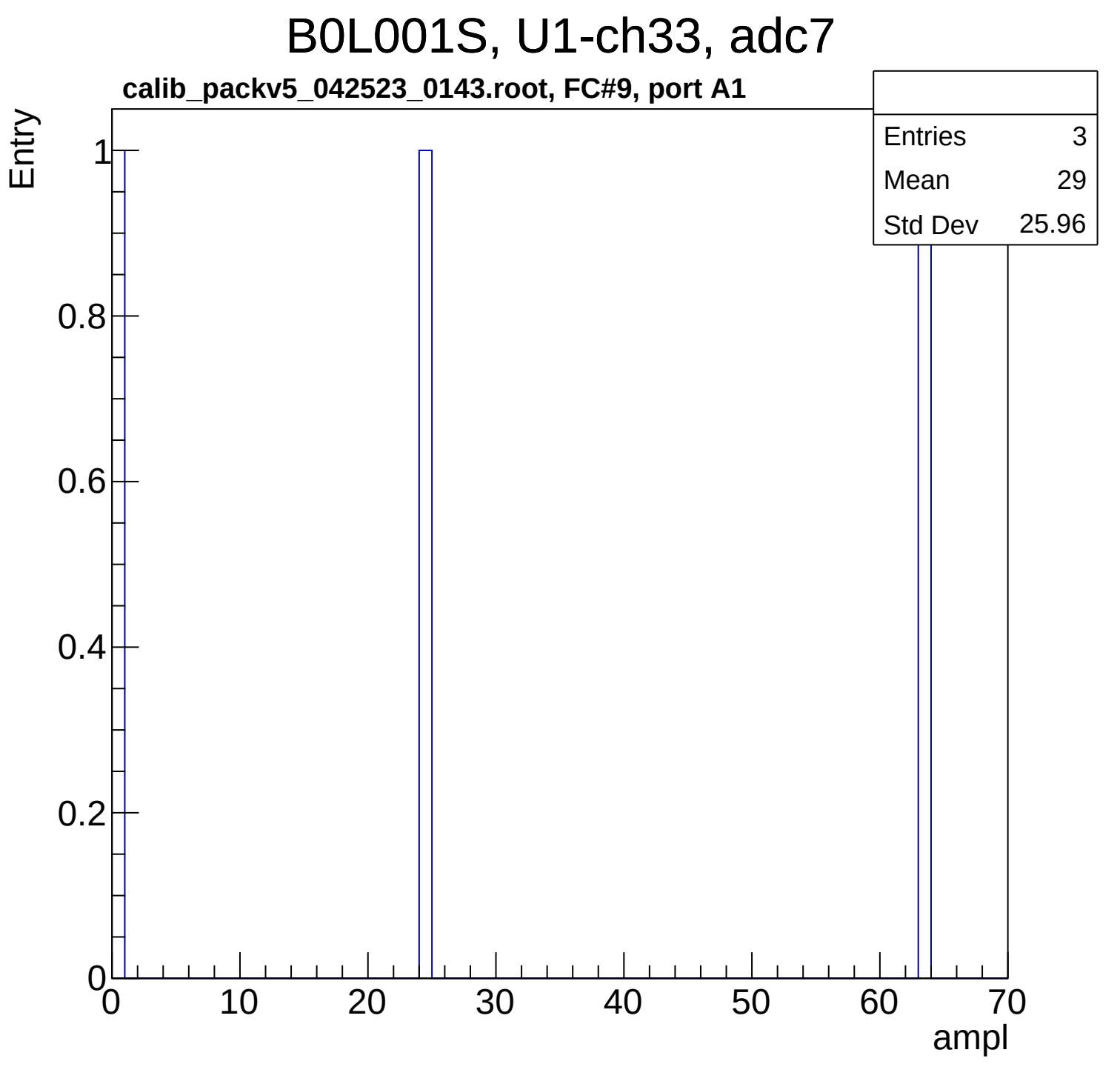
B0L001S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 29
Std Dev 25.96
Entry
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0.6
0.4
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0
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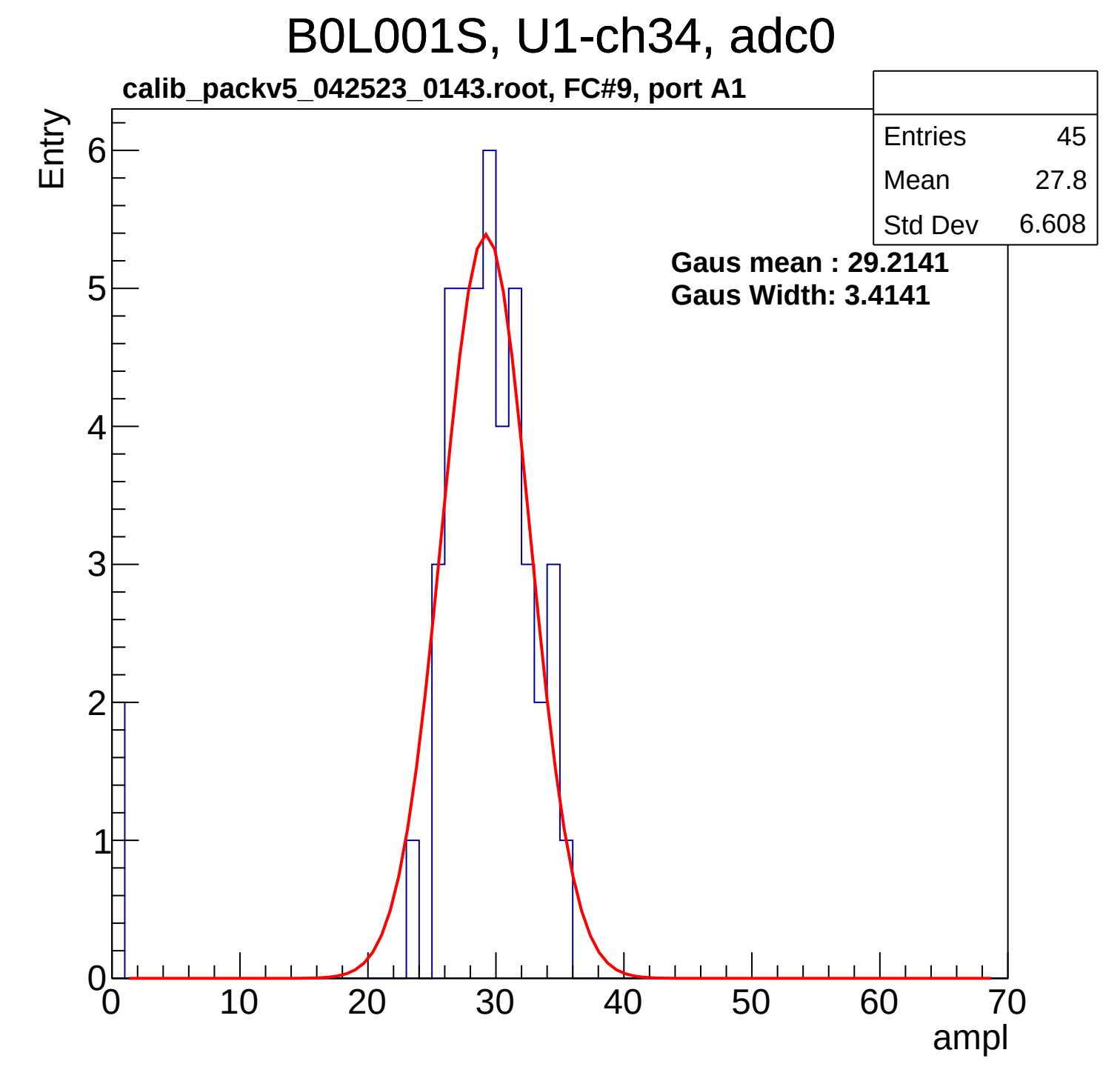
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 27.8
Std Dev 6.608
Gaus mean : 29.2141
Gaus Width: 3.4141
Entry
ampl
0
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2
3
4
5
6
0
10
20
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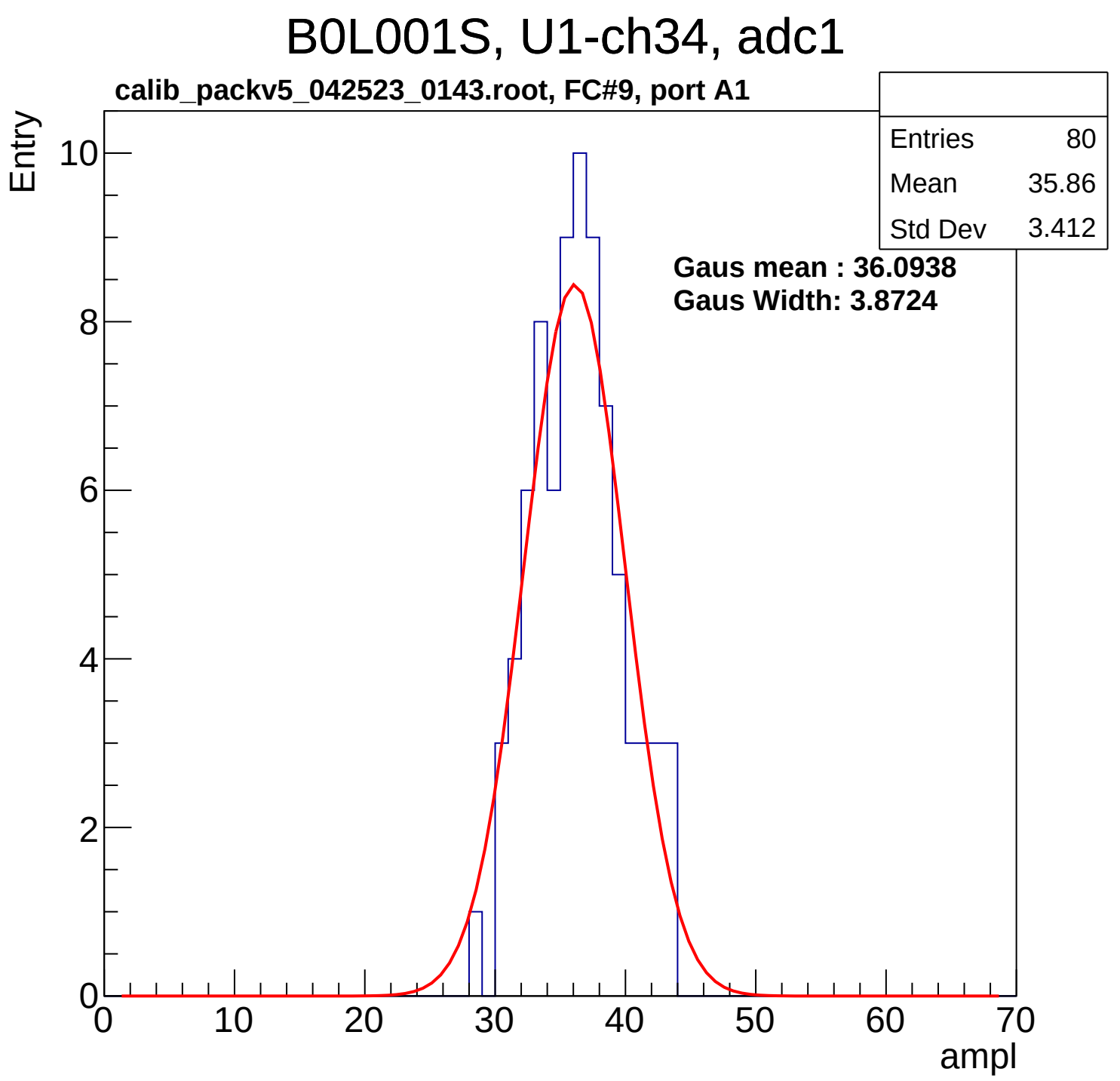
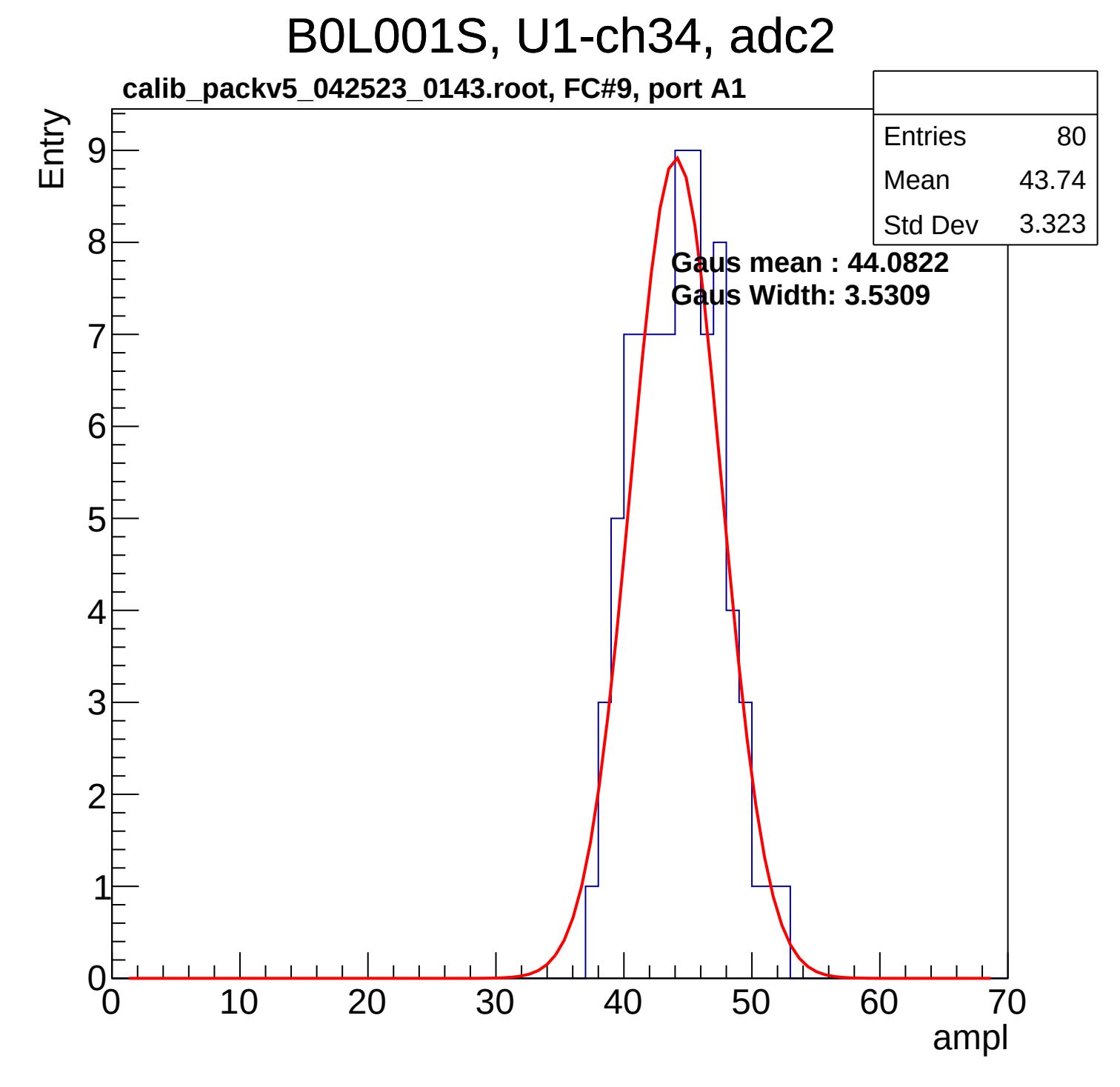
40
50
60
70


B0L001S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 35.86
Std Dev 3.412
Gaus mean : 36.0938
Gaus Width: 3.8724
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 43.74
Std Dev 3.323
Gaus mean : 44.0822
Gaus Width: 3.5309
Entry
ampl
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2
3
4
5
6
7
8
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0
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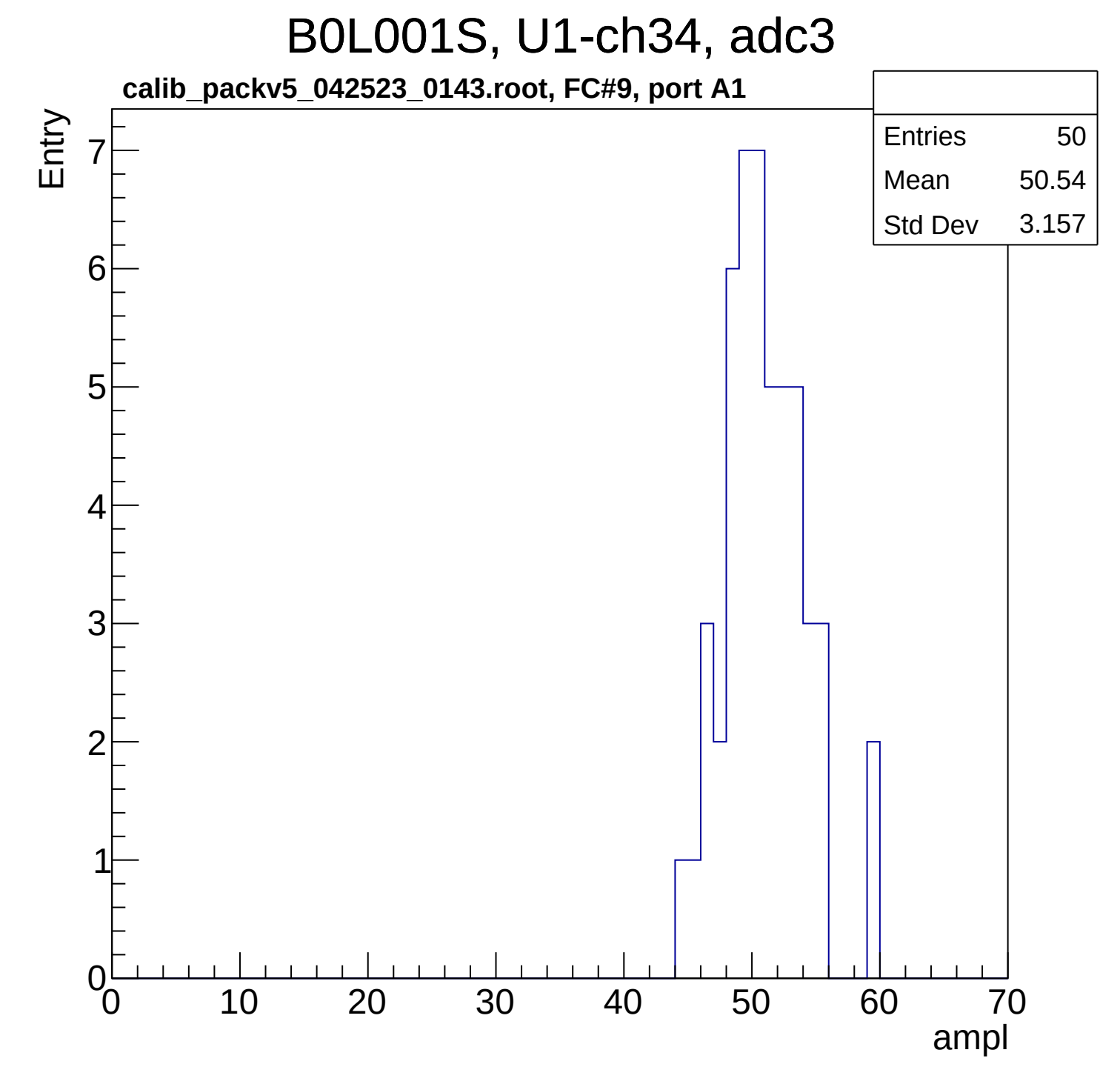
B0L001S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 50.54
Std Dev 3.157
Entry
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2
3
4
5
6
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10
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30
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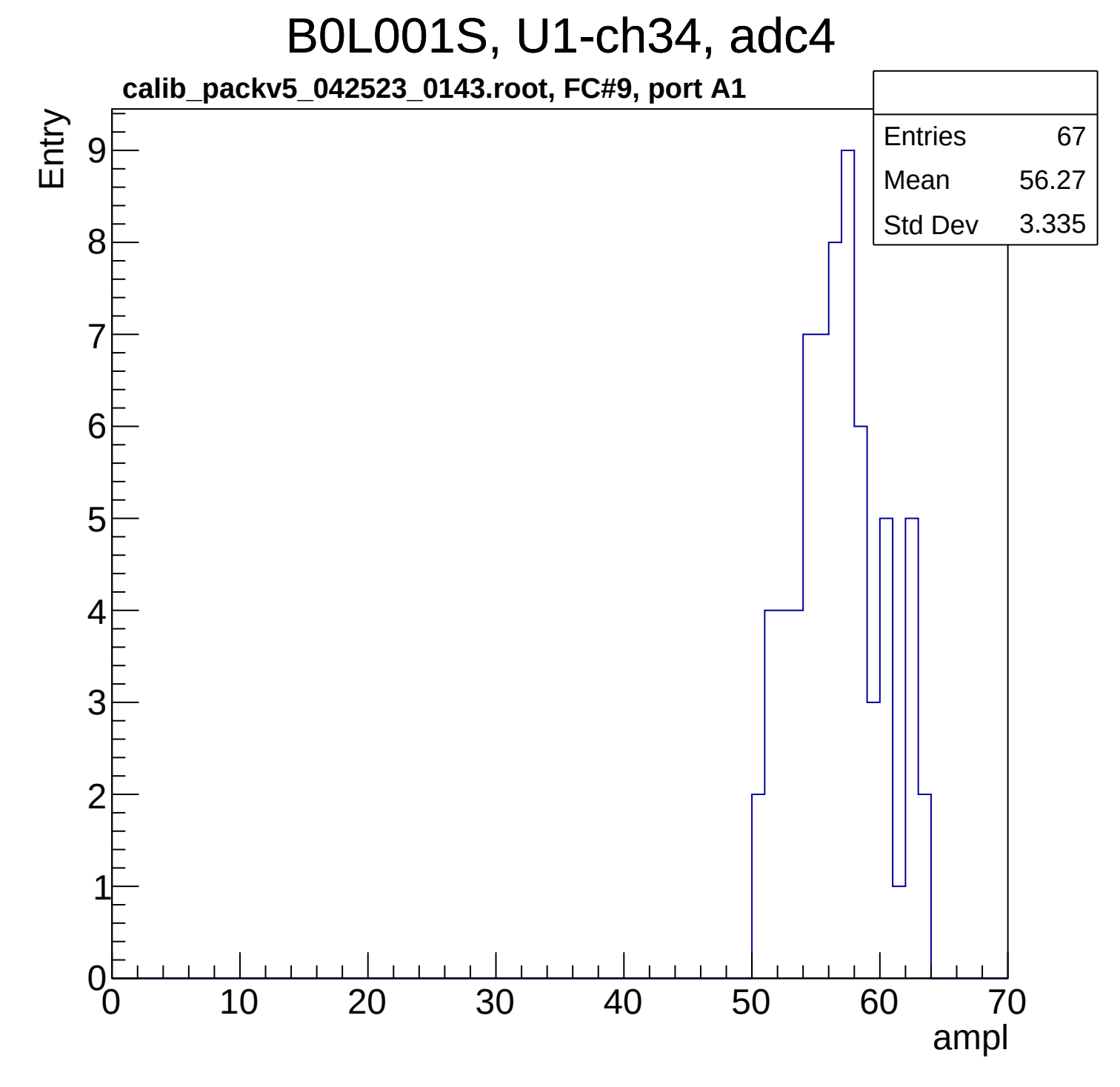
50
60
70
ampl


B0L001S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.27
Std Dev 3.335
Entry
ampl
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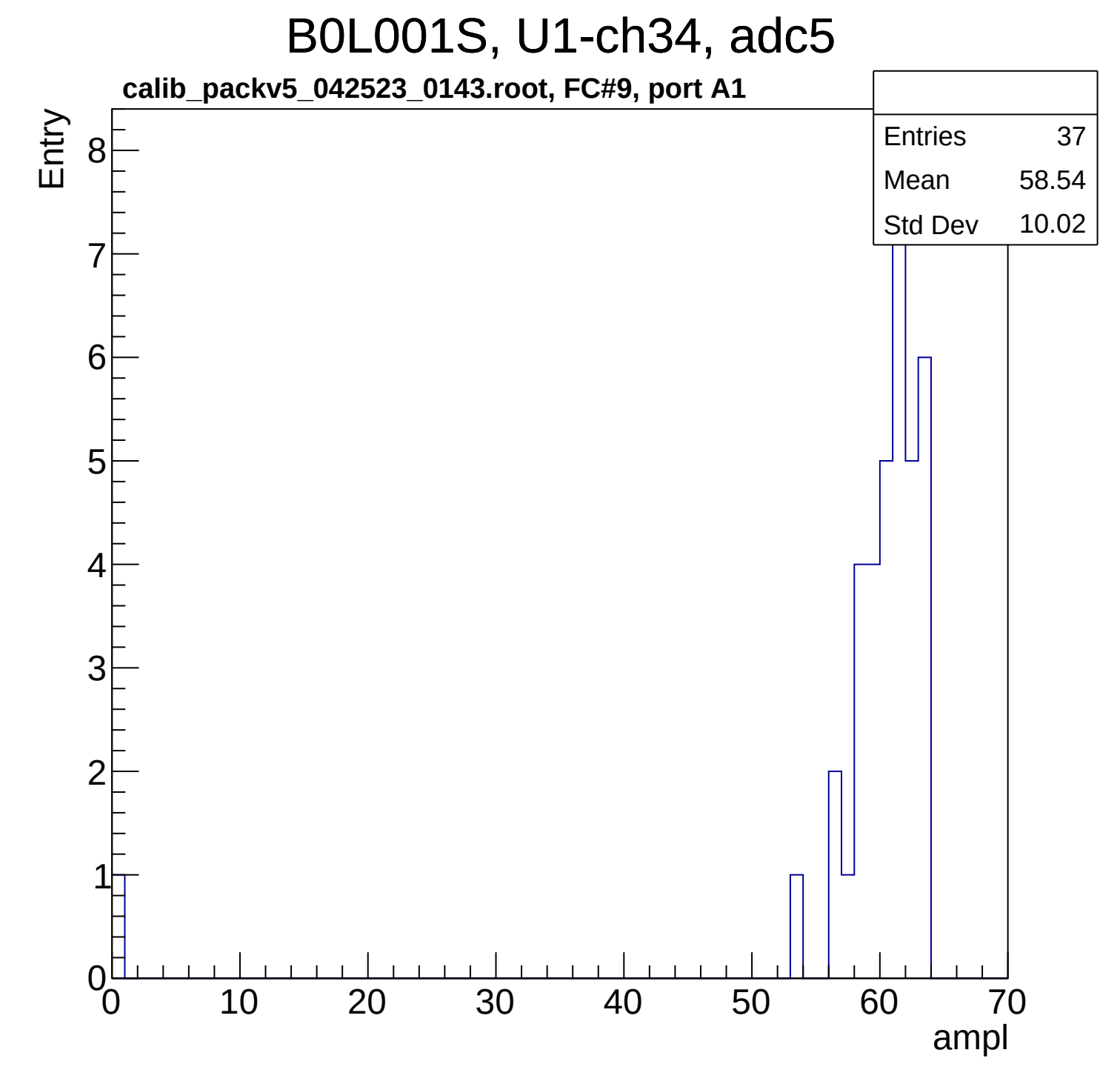
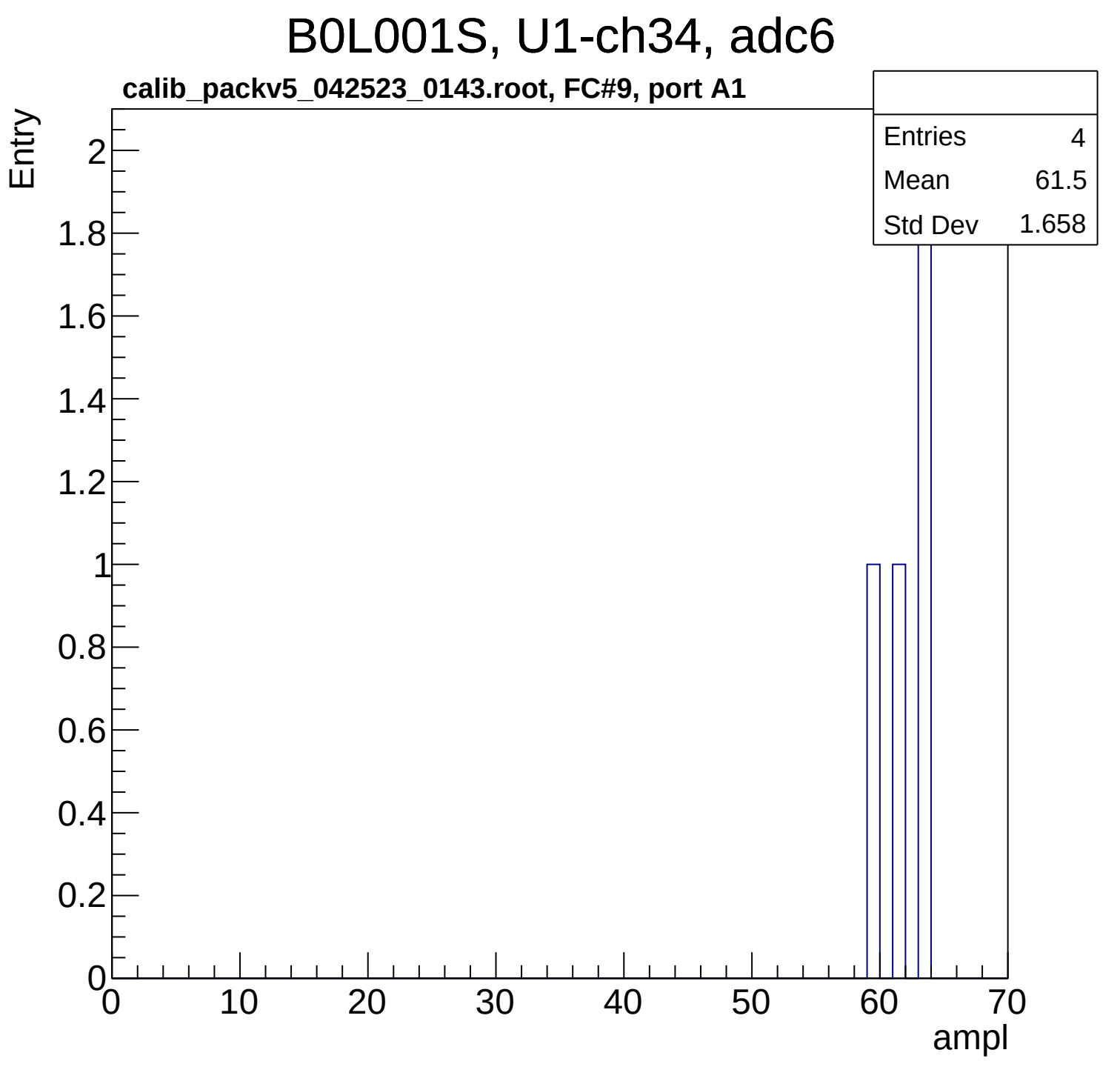

B0L001S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.54
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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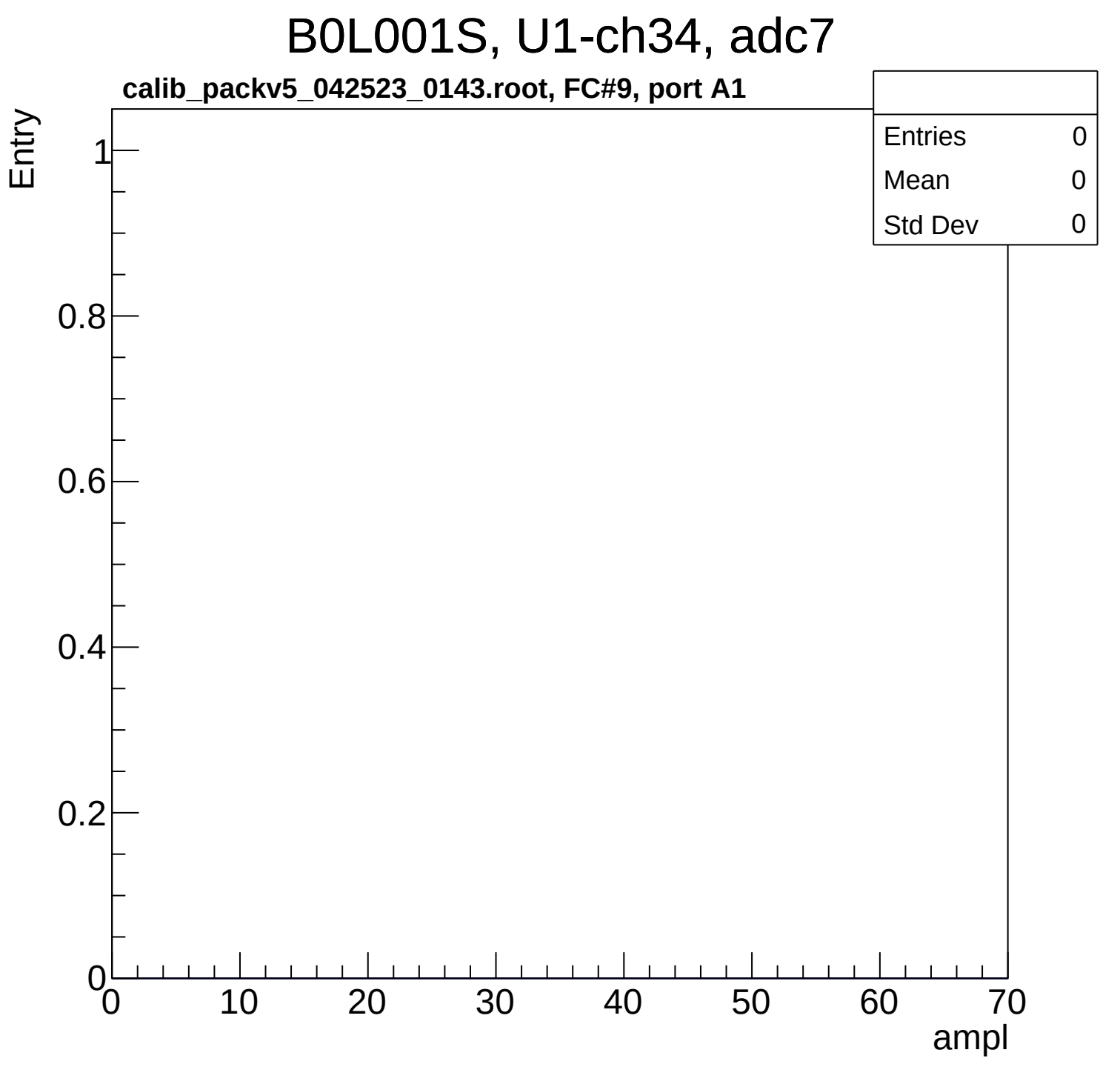
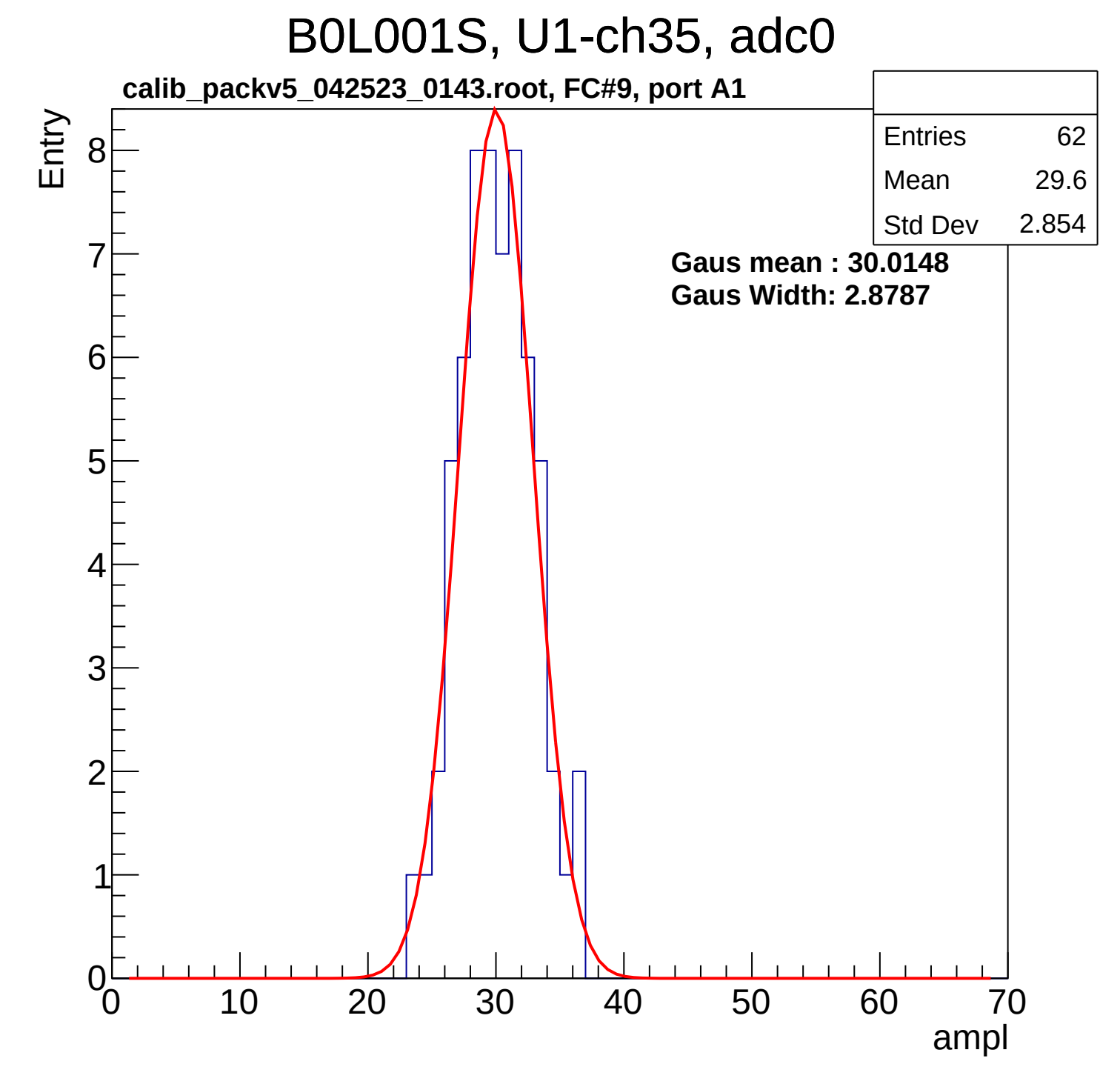
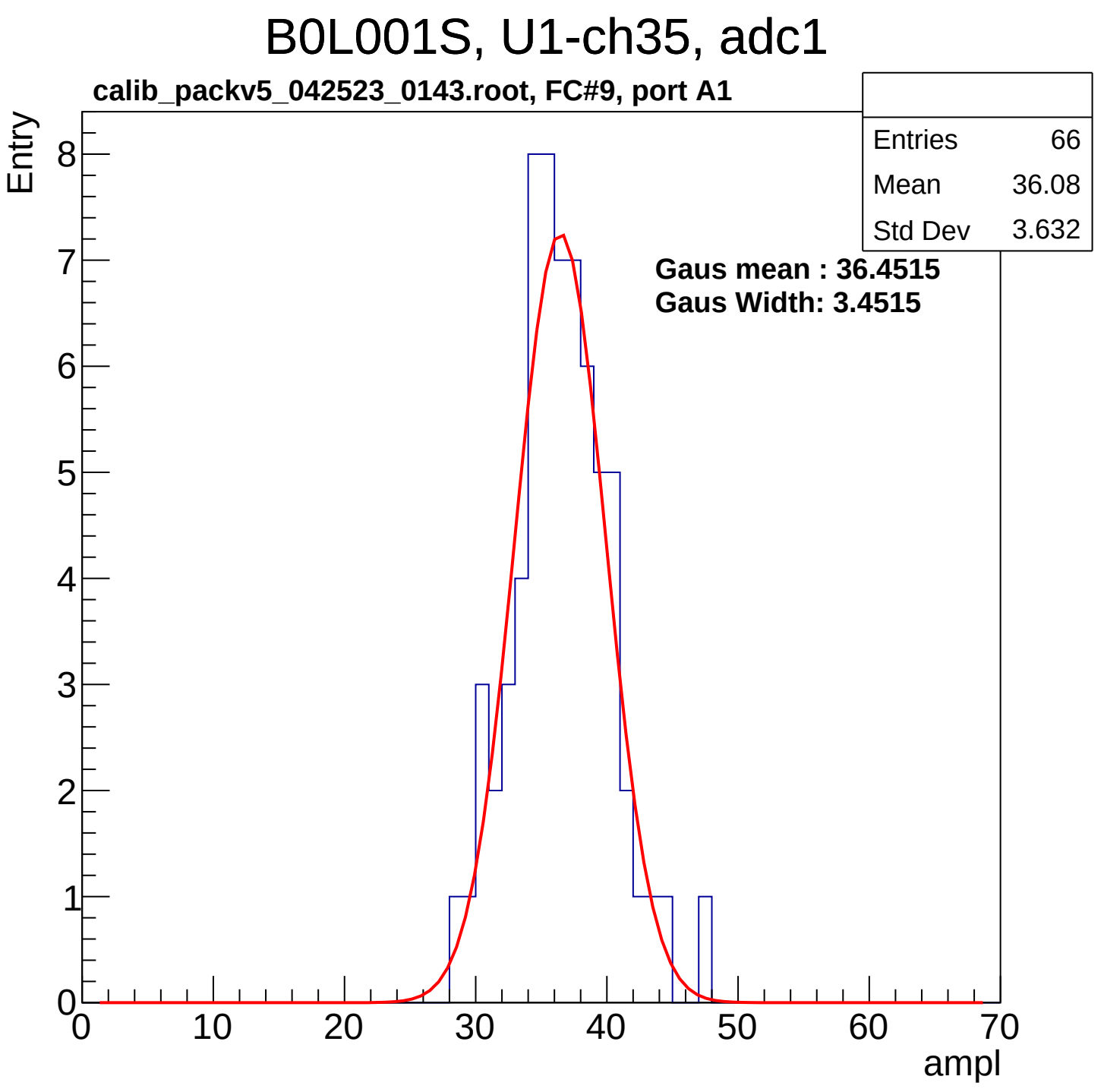
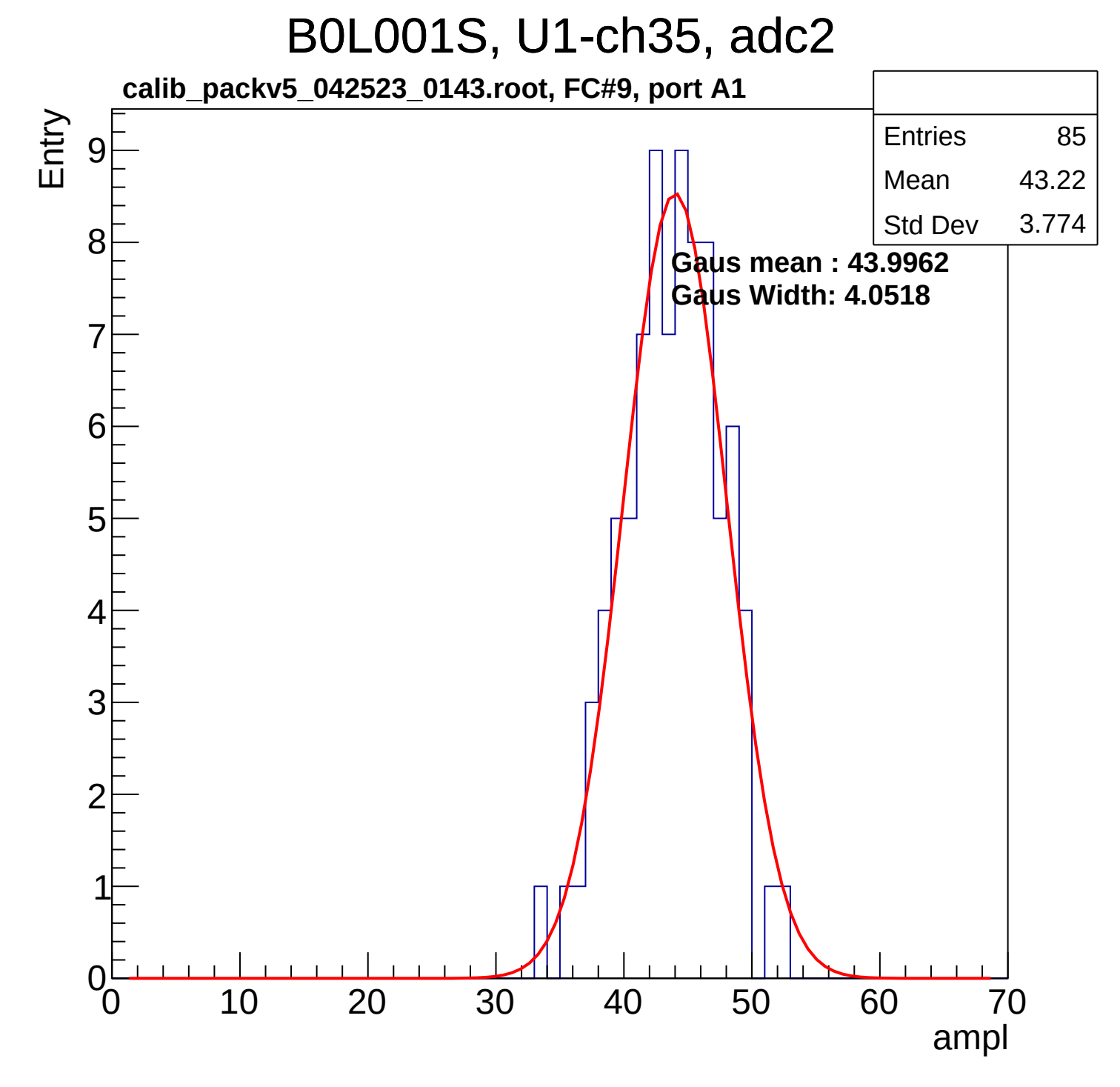

B0L001S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 29.6
Std Dev 2.854
Gaus mean : 30.0148
Gaus Width: 2.8787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.08
Std Dev 3.632
Gaus mean : 36.4515
Gaus Width: 3.4515
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 43.22
Std Dev 3.774
Gaus mean : 43.9962
Gaus Width: 4.0518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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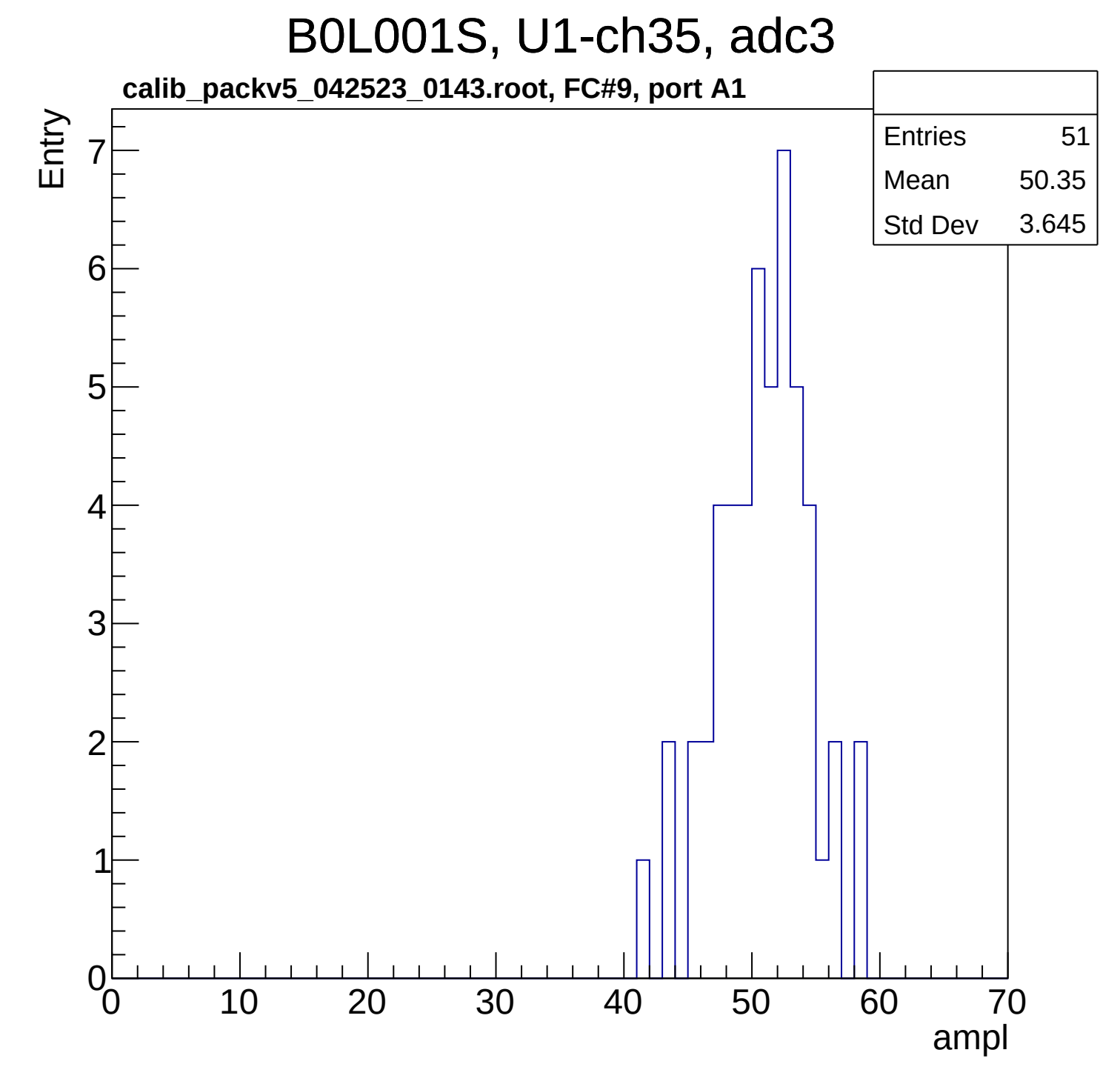
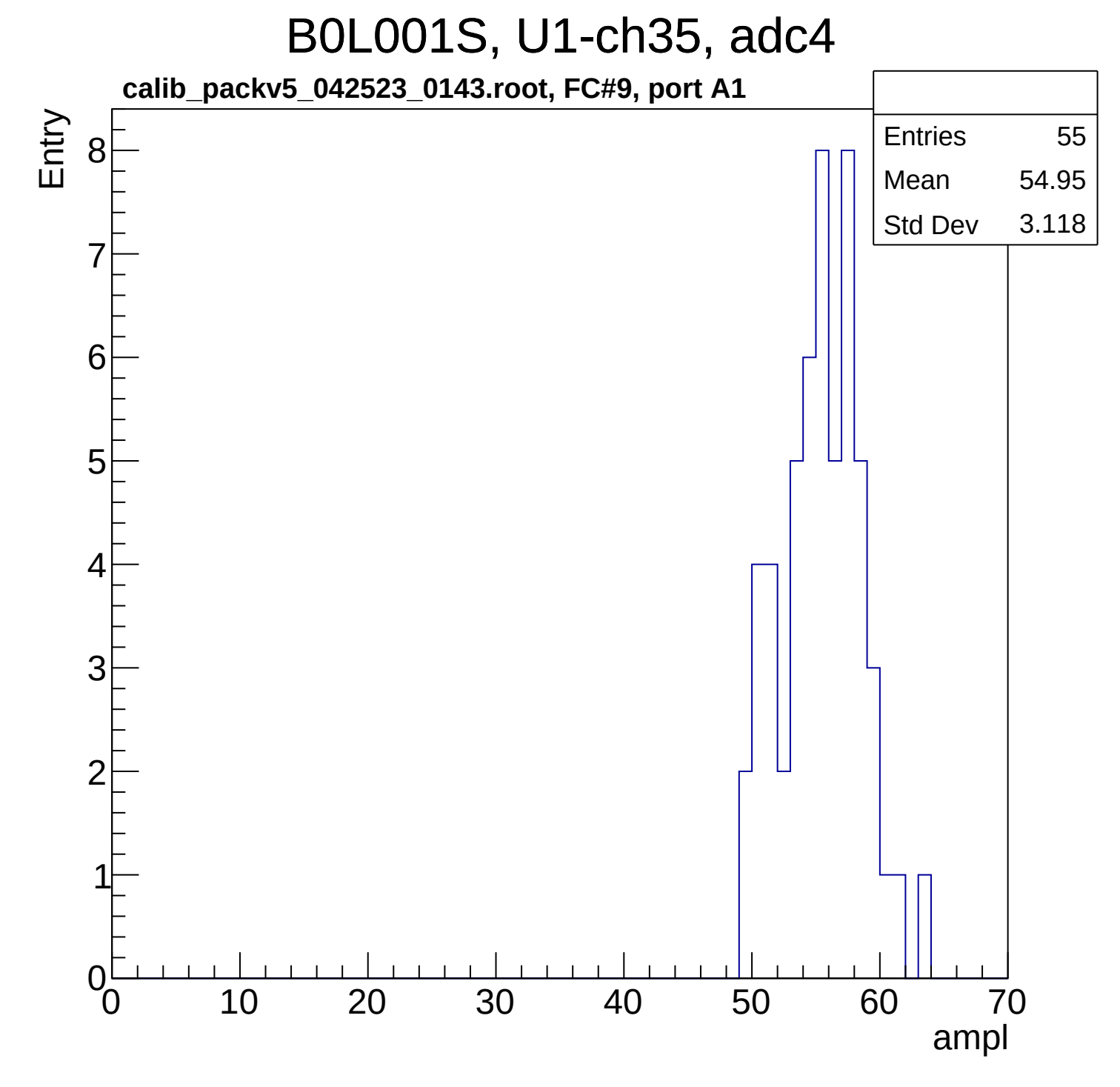
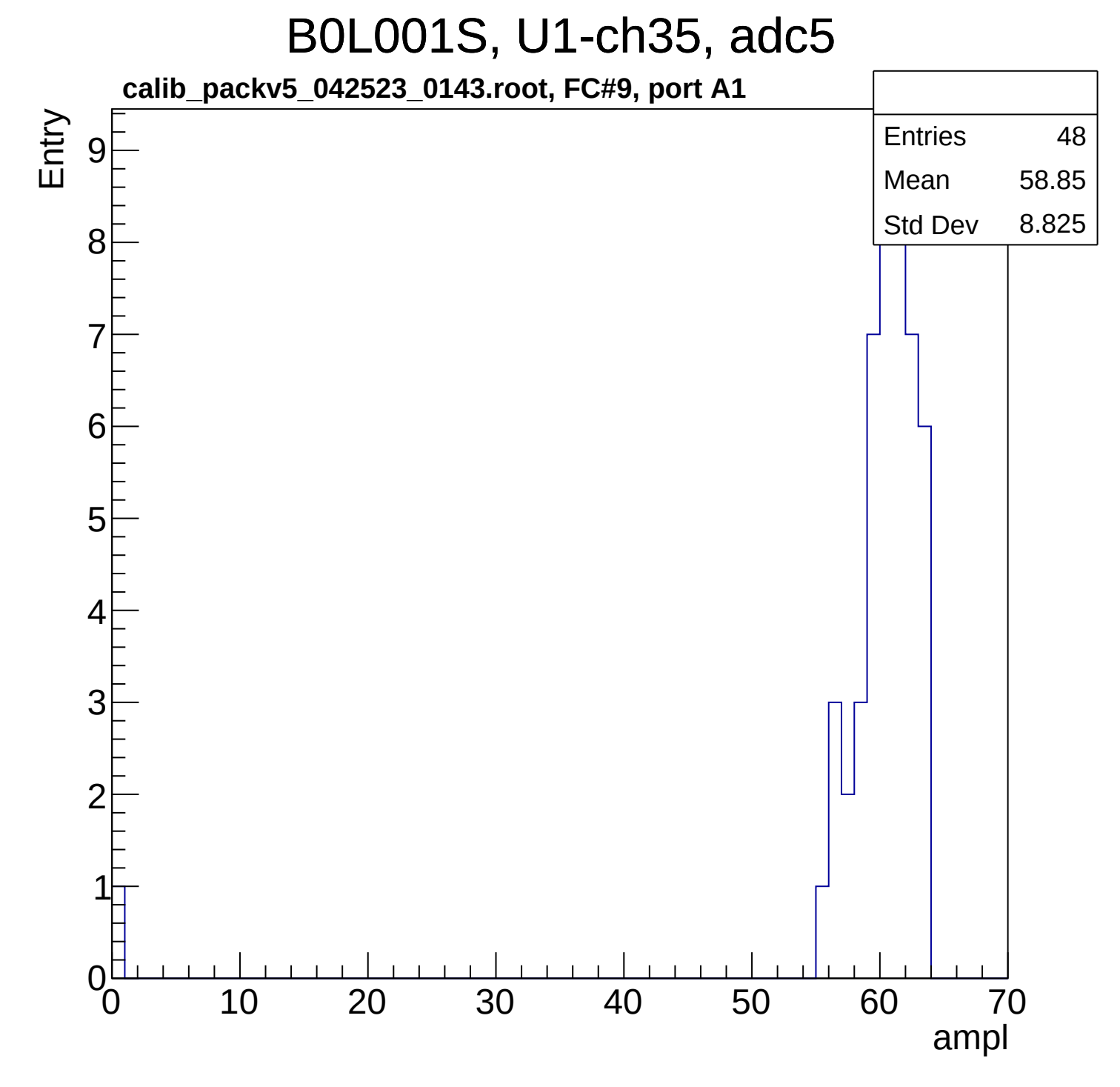
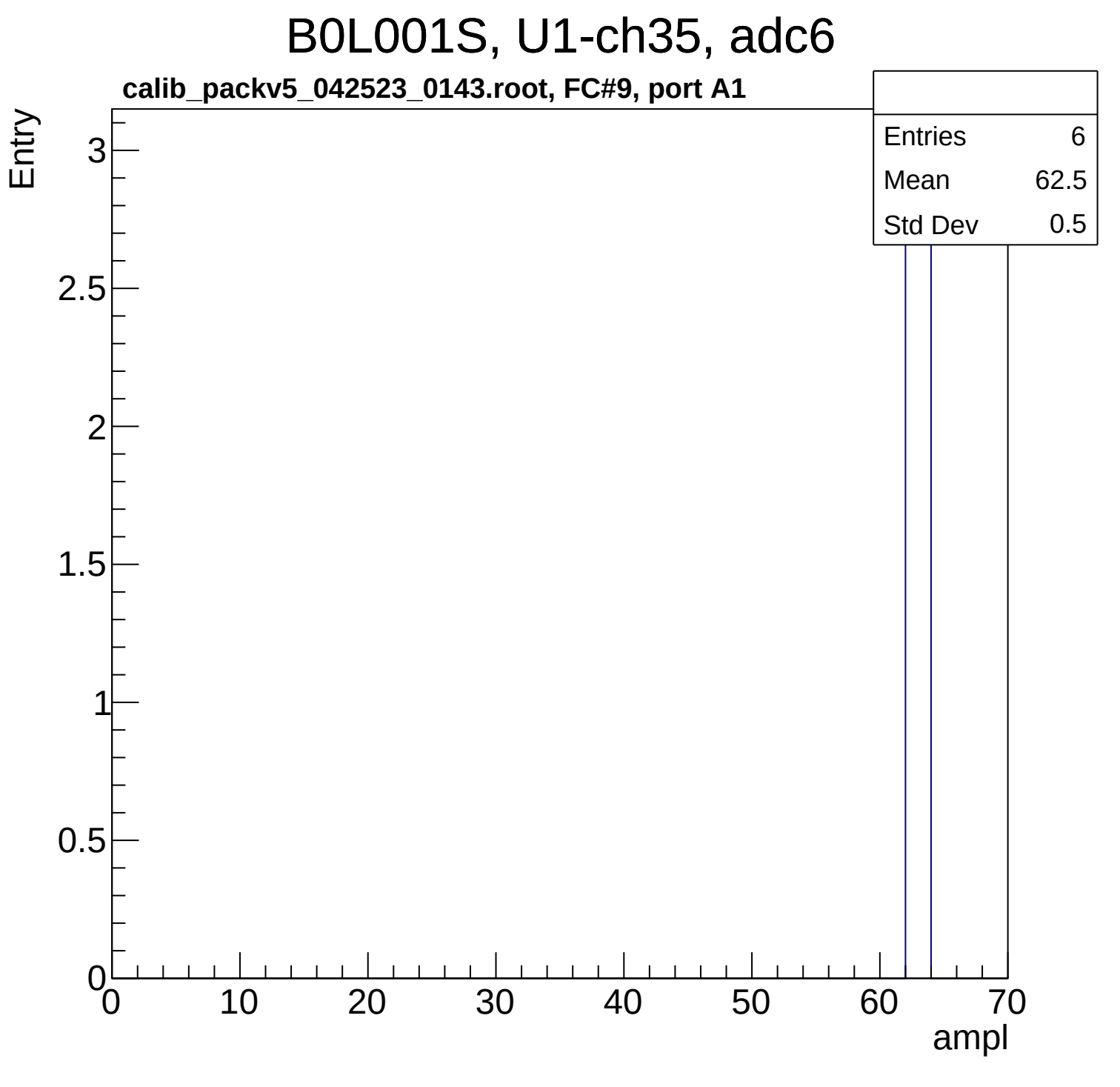
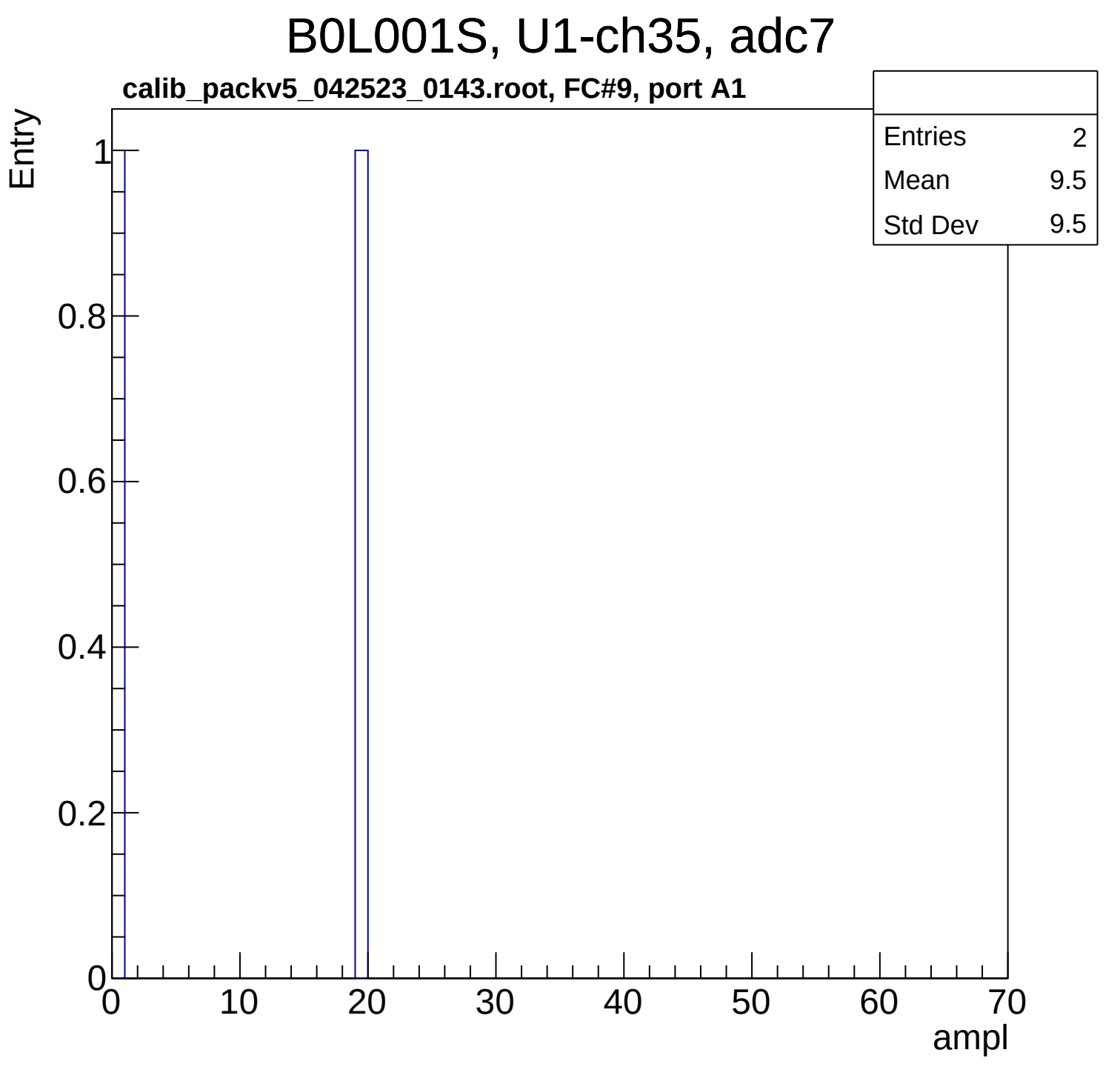
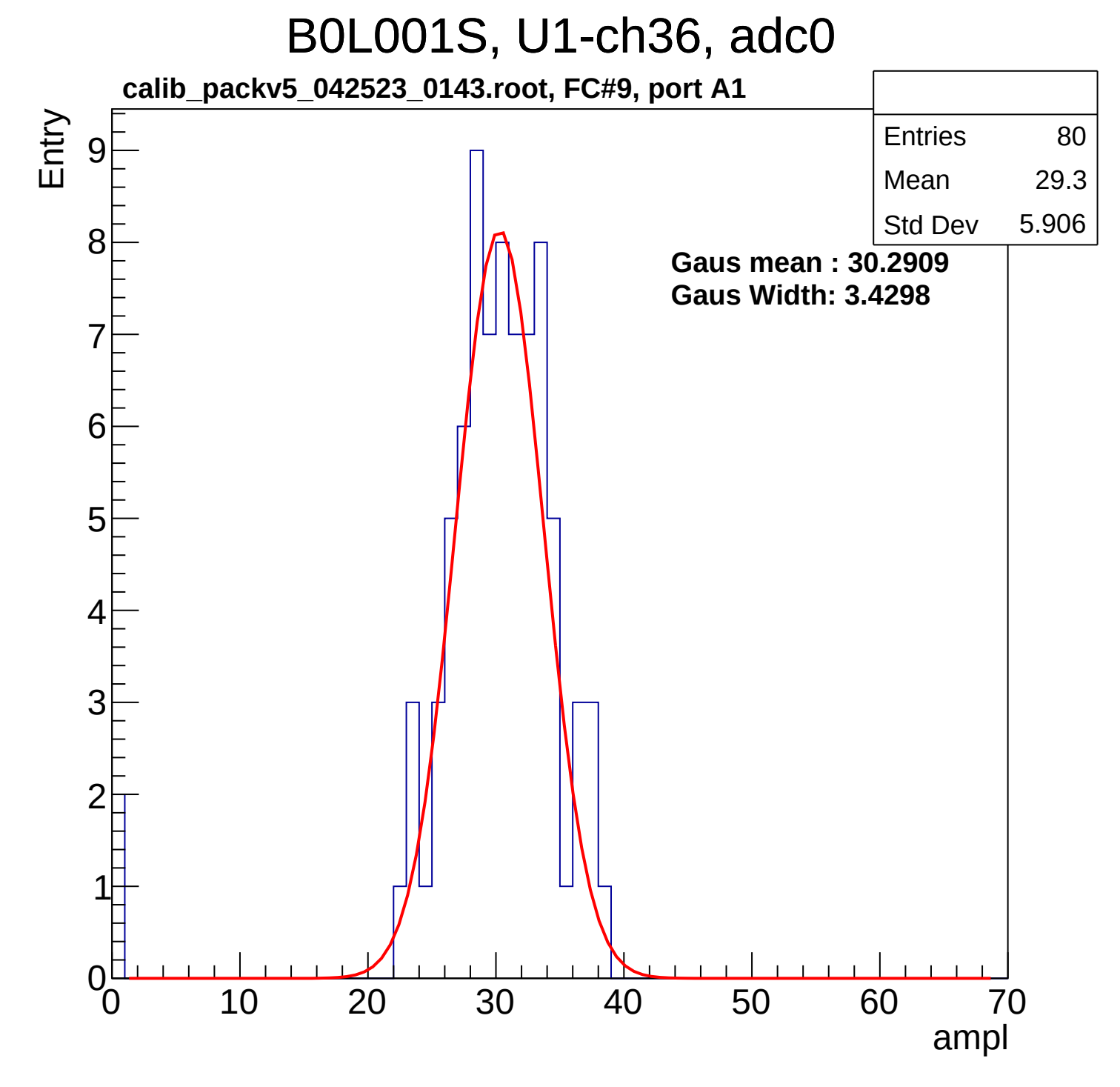

B0L001S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 50.35
Std Dev 3.645
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

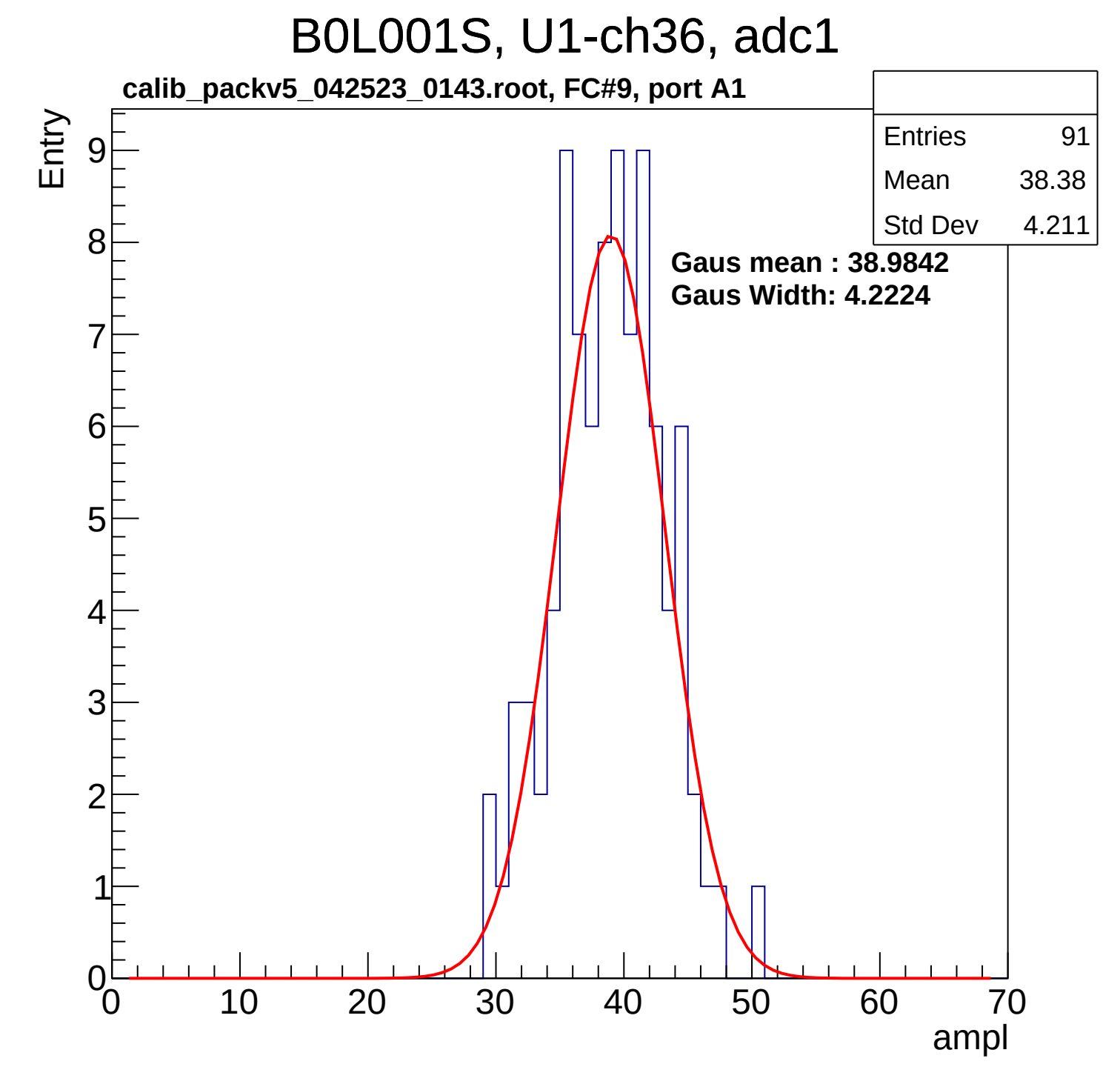

B0L001S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 54.95
Std Dev 3.118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

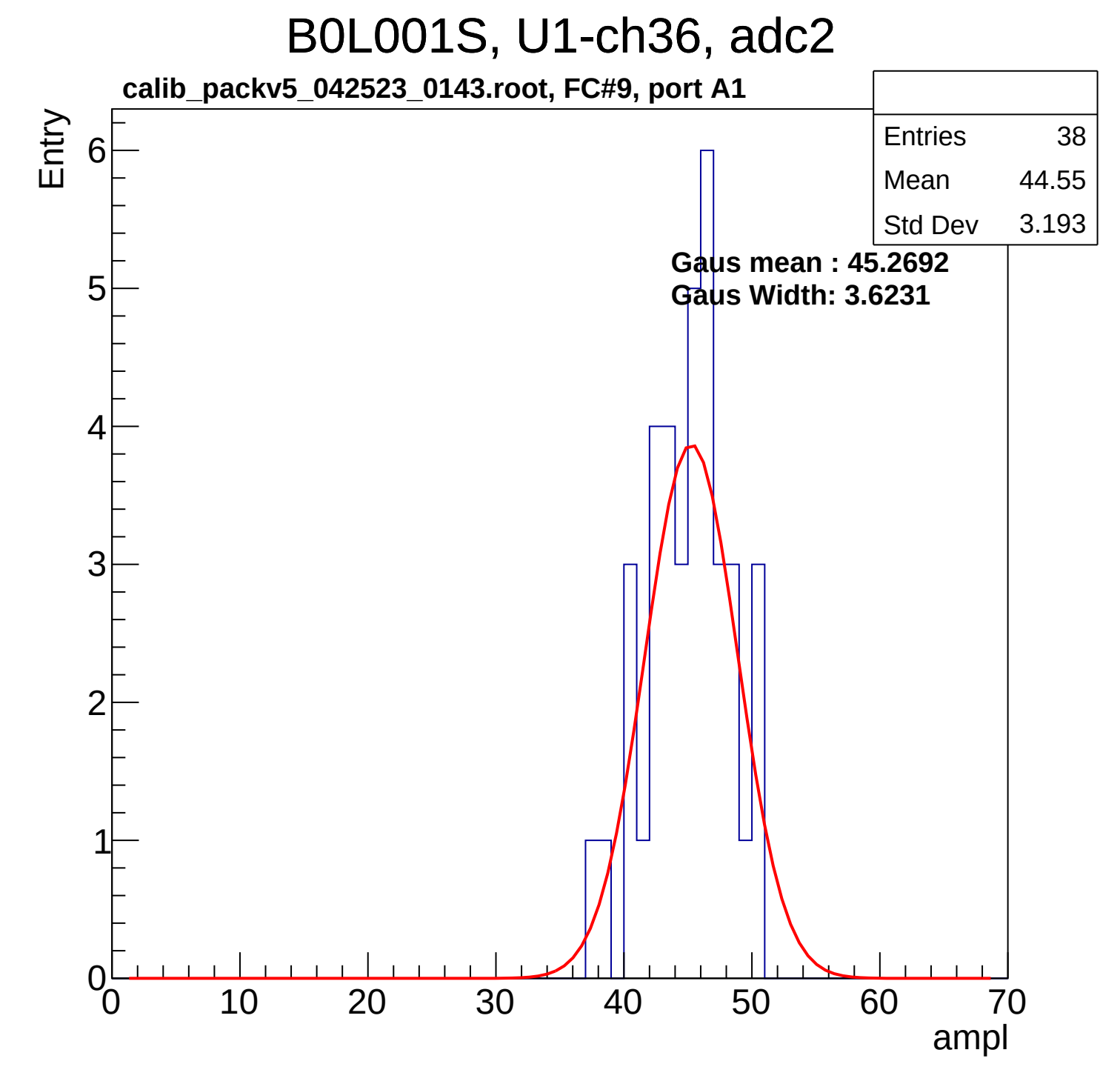

B0L001S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.85
Std Dev 8.825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

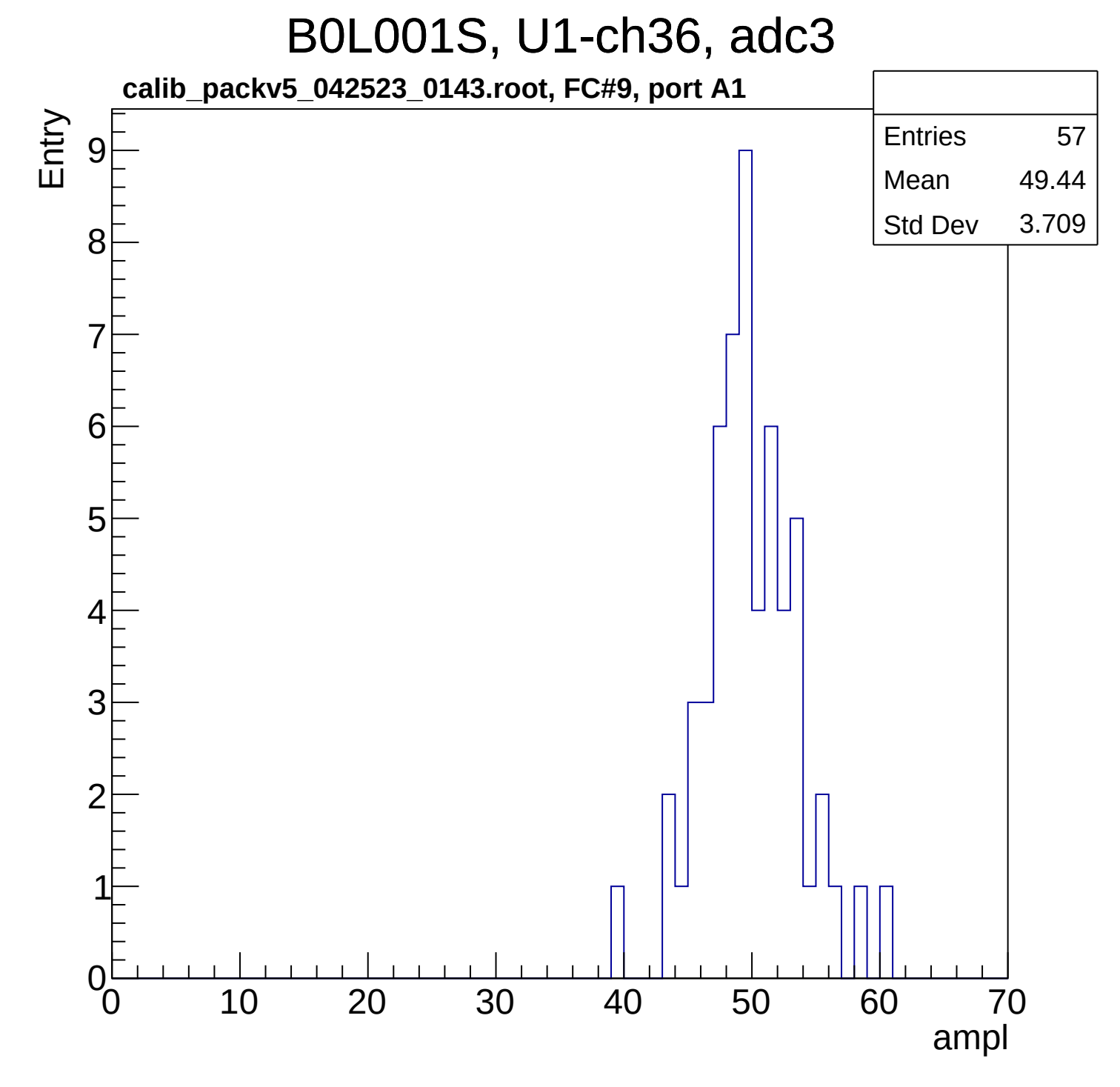

B0L001S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

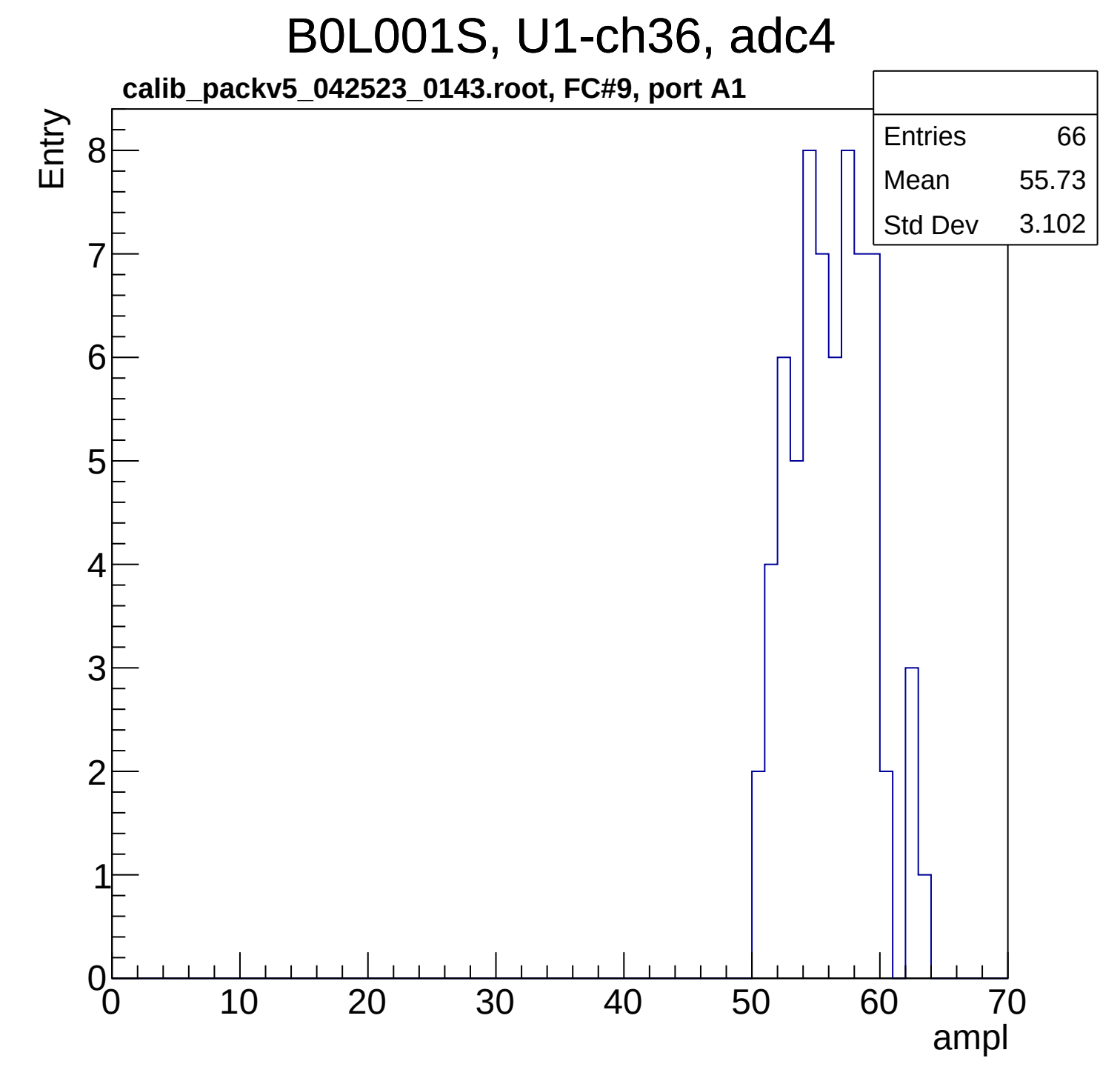

B0L001S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

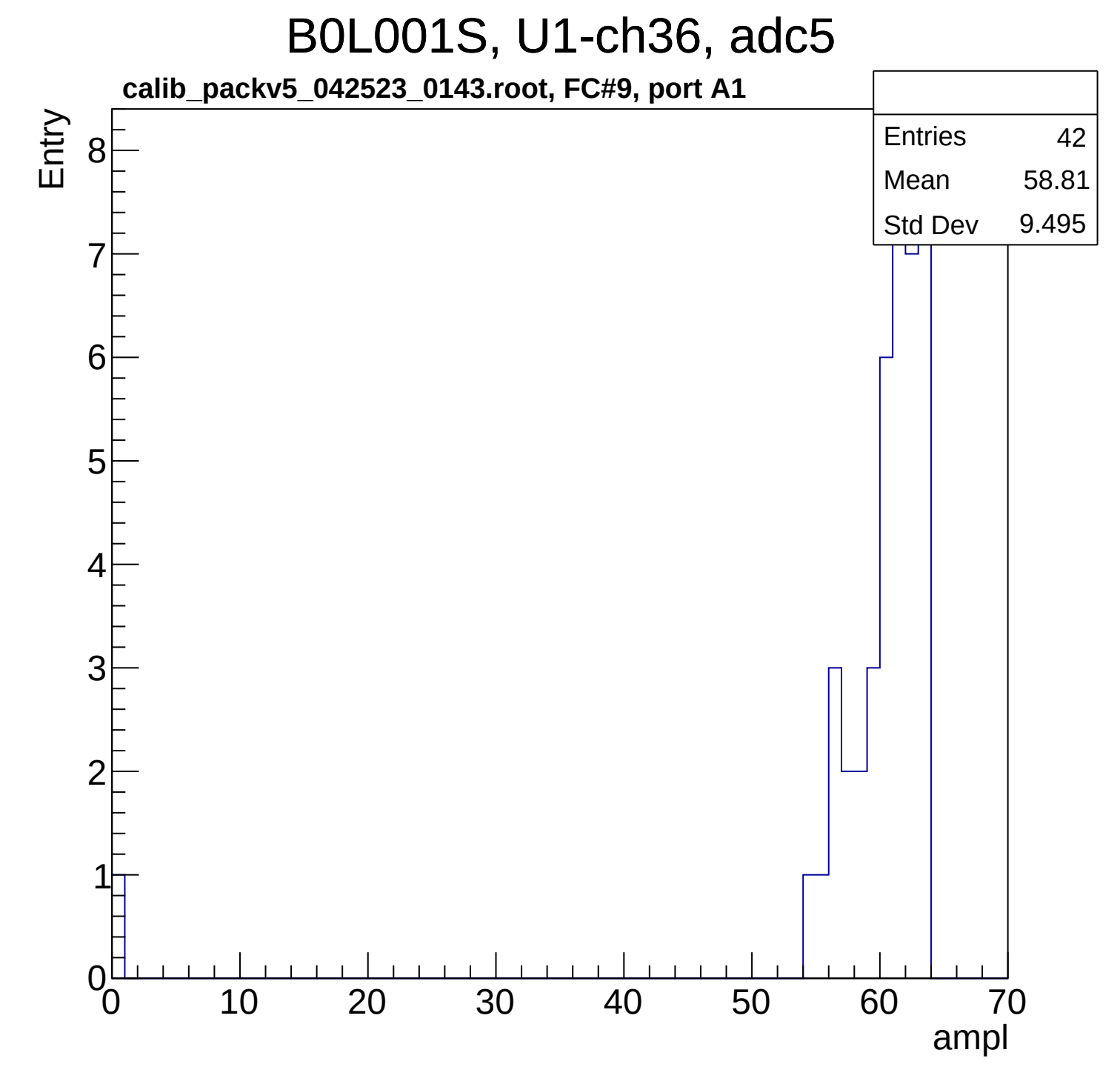

B0L001S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.3
Std Dev 5.906
Gaus mean : 30.2909
Gaus Width: 3.4298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 38.38
Std Dev 4.211
Gaus mean : 38.9842
Gaus Width: 4.2224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 44.55
Std Dev 3.193
Gaus mean : 45.2692
Gaus Width: 3.6231
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.44
Std Dev 3.709
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.73
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.81
Std Dev 9.495
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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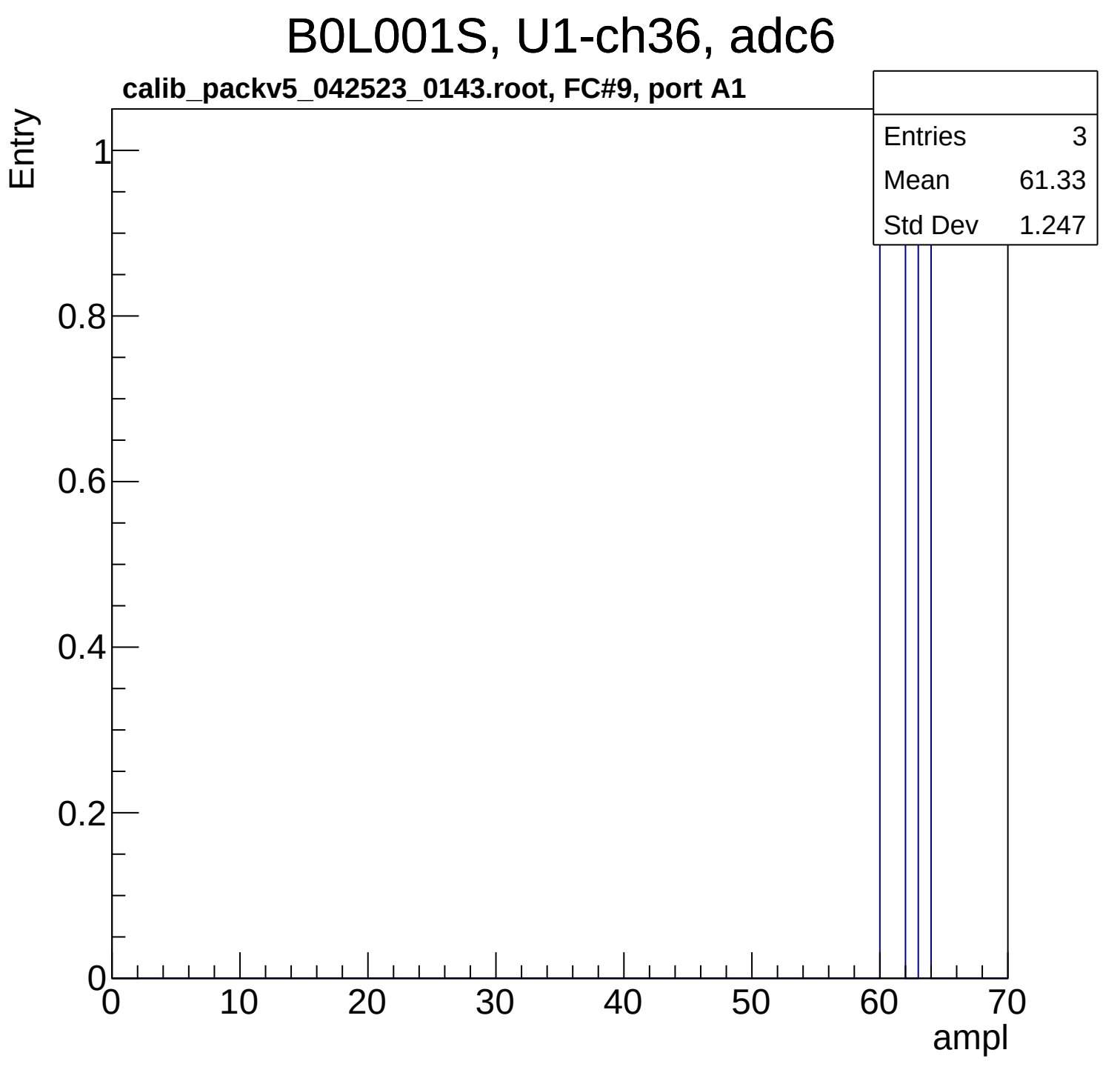
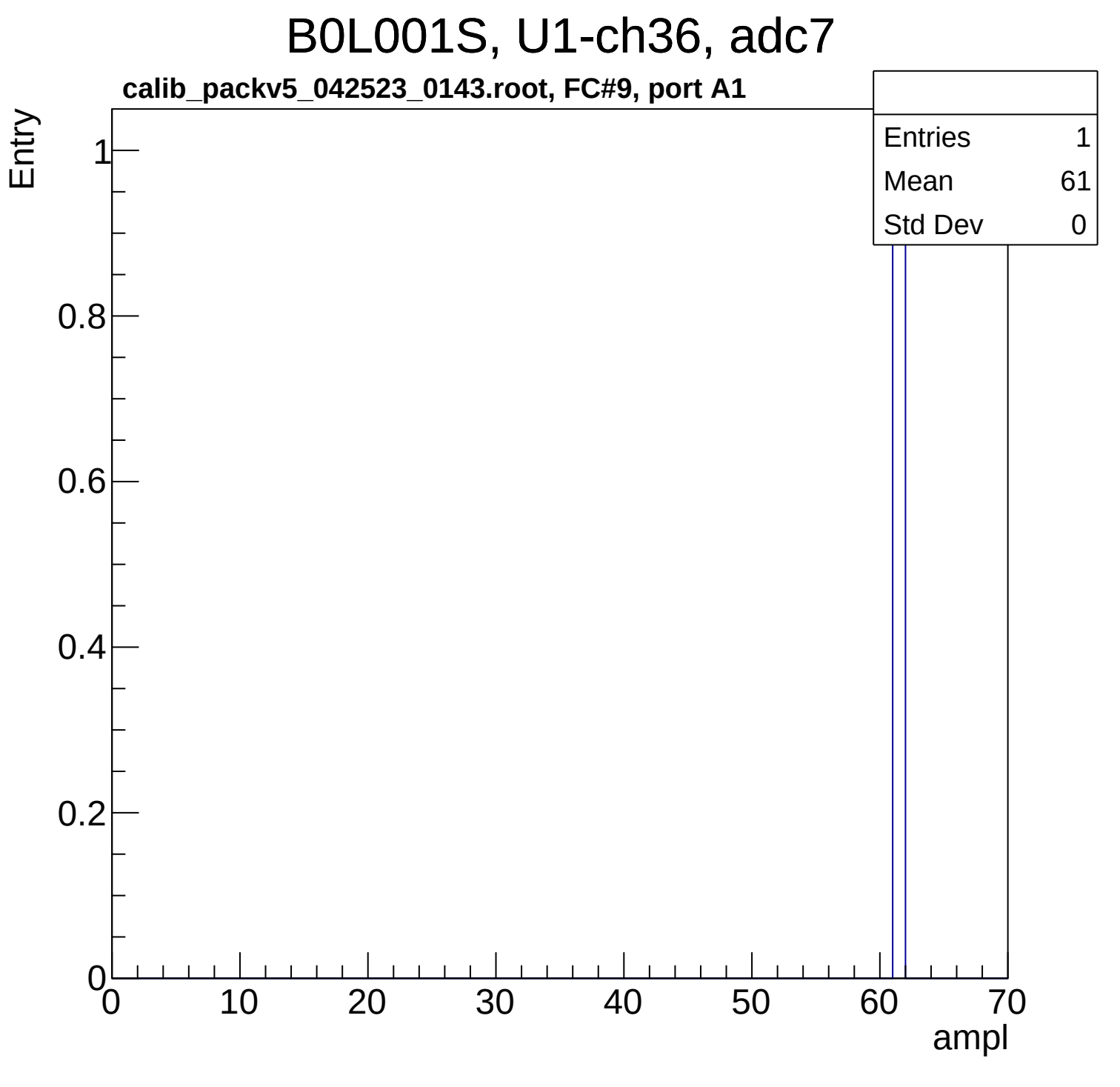
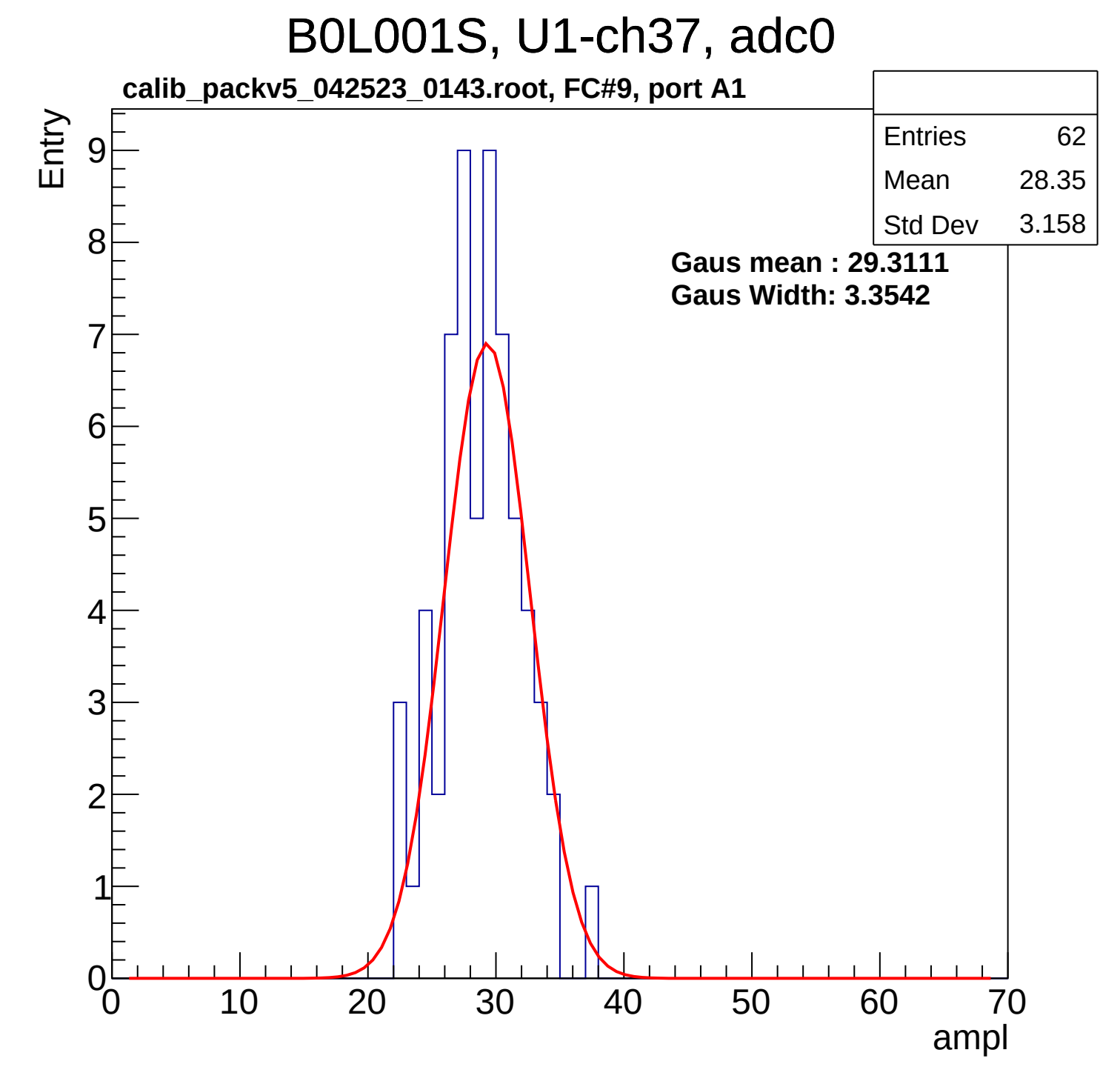
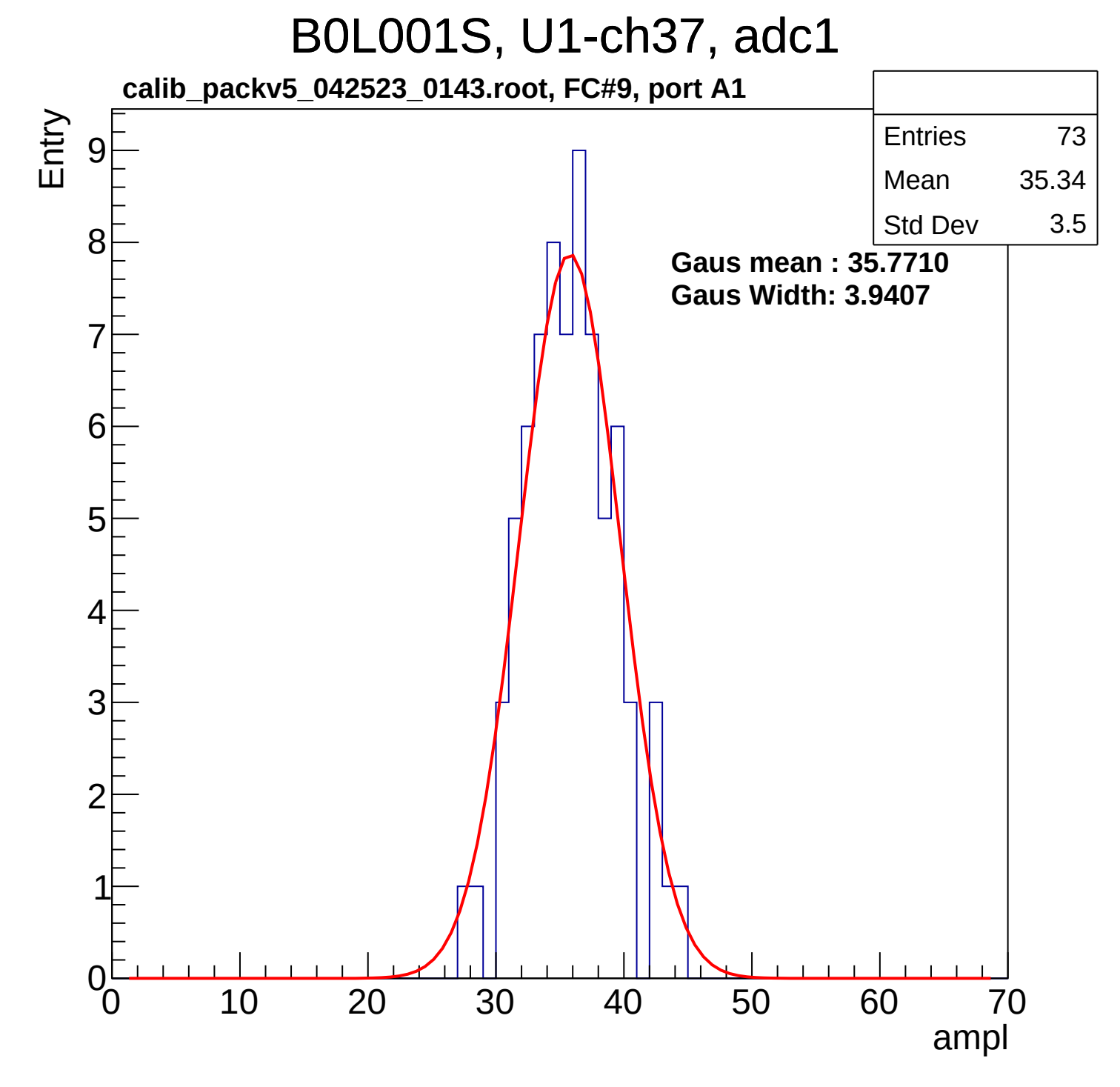
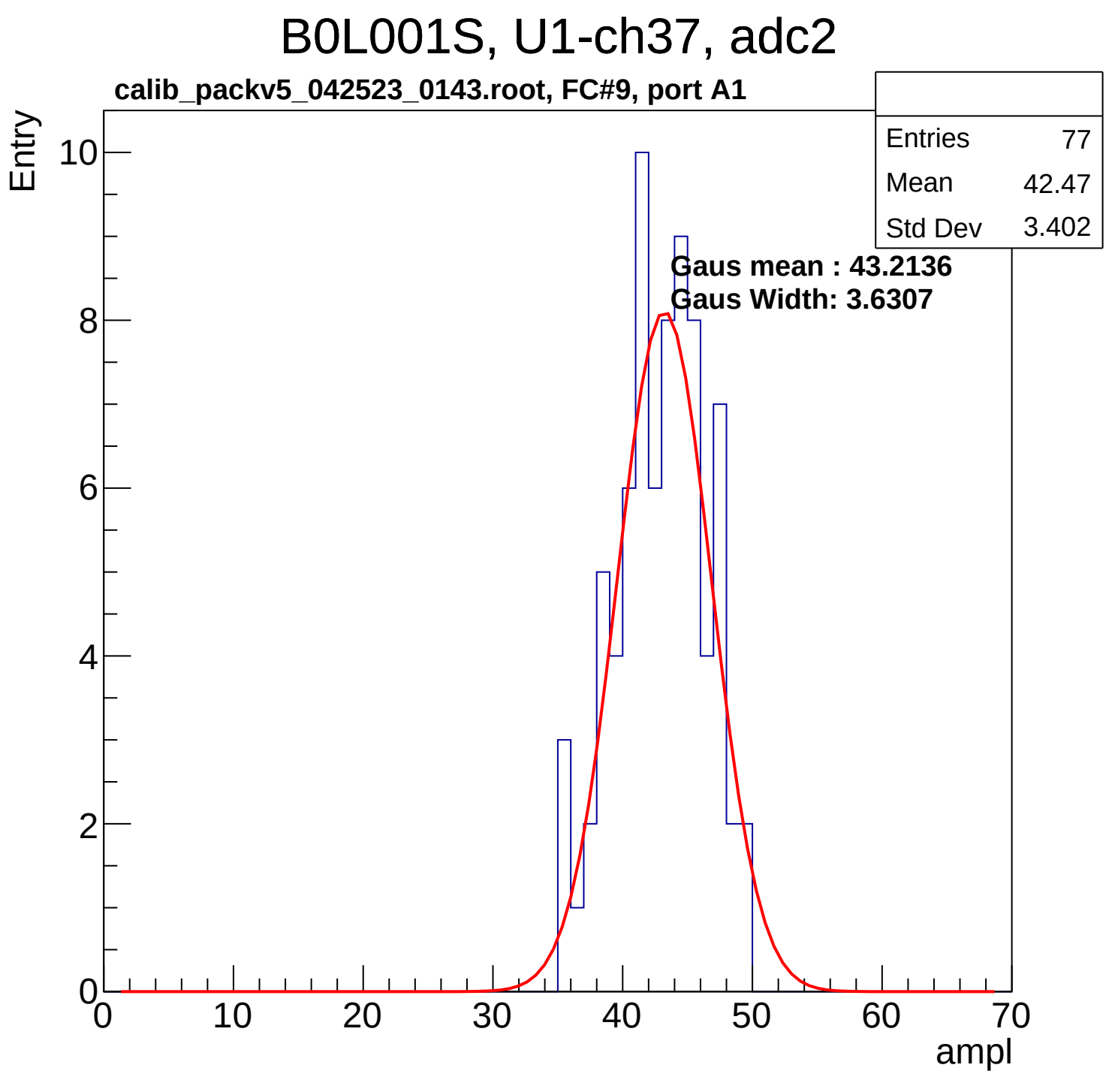
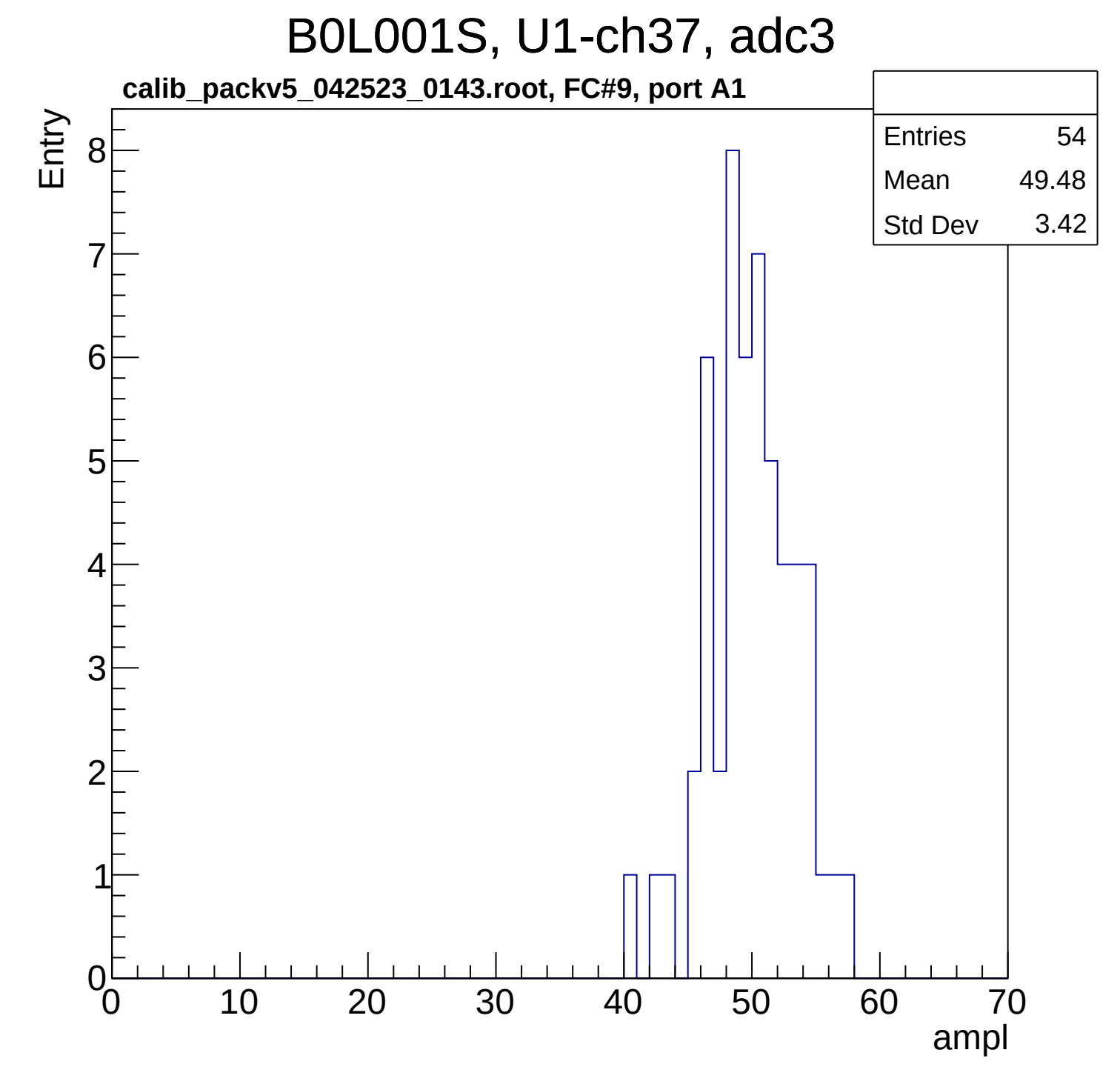

B0L001S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 28.35
Std Dev 3.158
Gaus mean : 29.3111
Gaus Width: 3.3542
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 35.34
Std Dev 3.5
Gaus mean : 35.7710
Gaus Width: 3.9407
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 42.47
Std Dev 3.402
Gaus mean : 43.2136
Gaus Width: 3.6307
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 49.48
Std Dev 3.42
Entry
ampl
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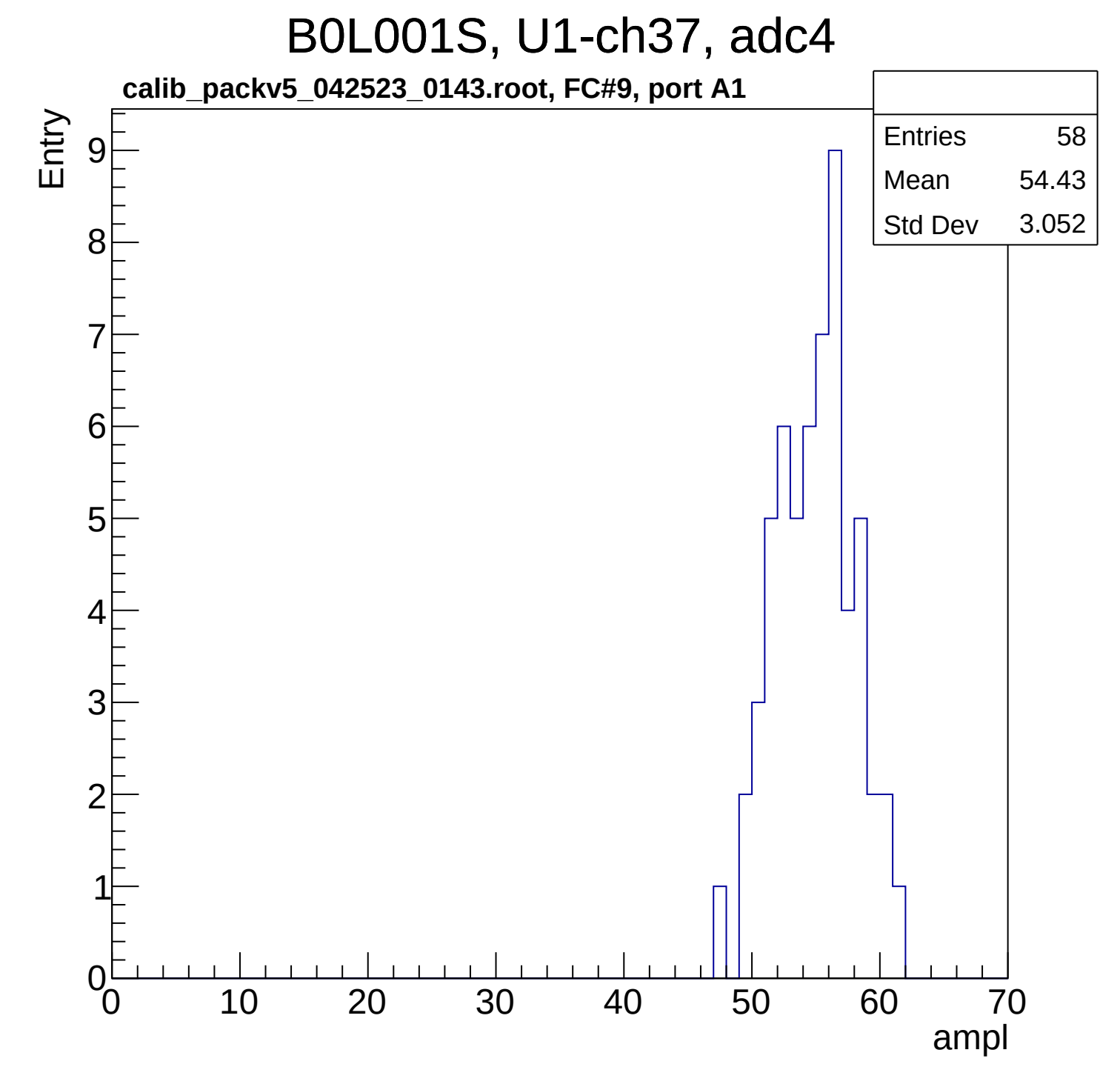
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 54.43
Std Dev 3.052
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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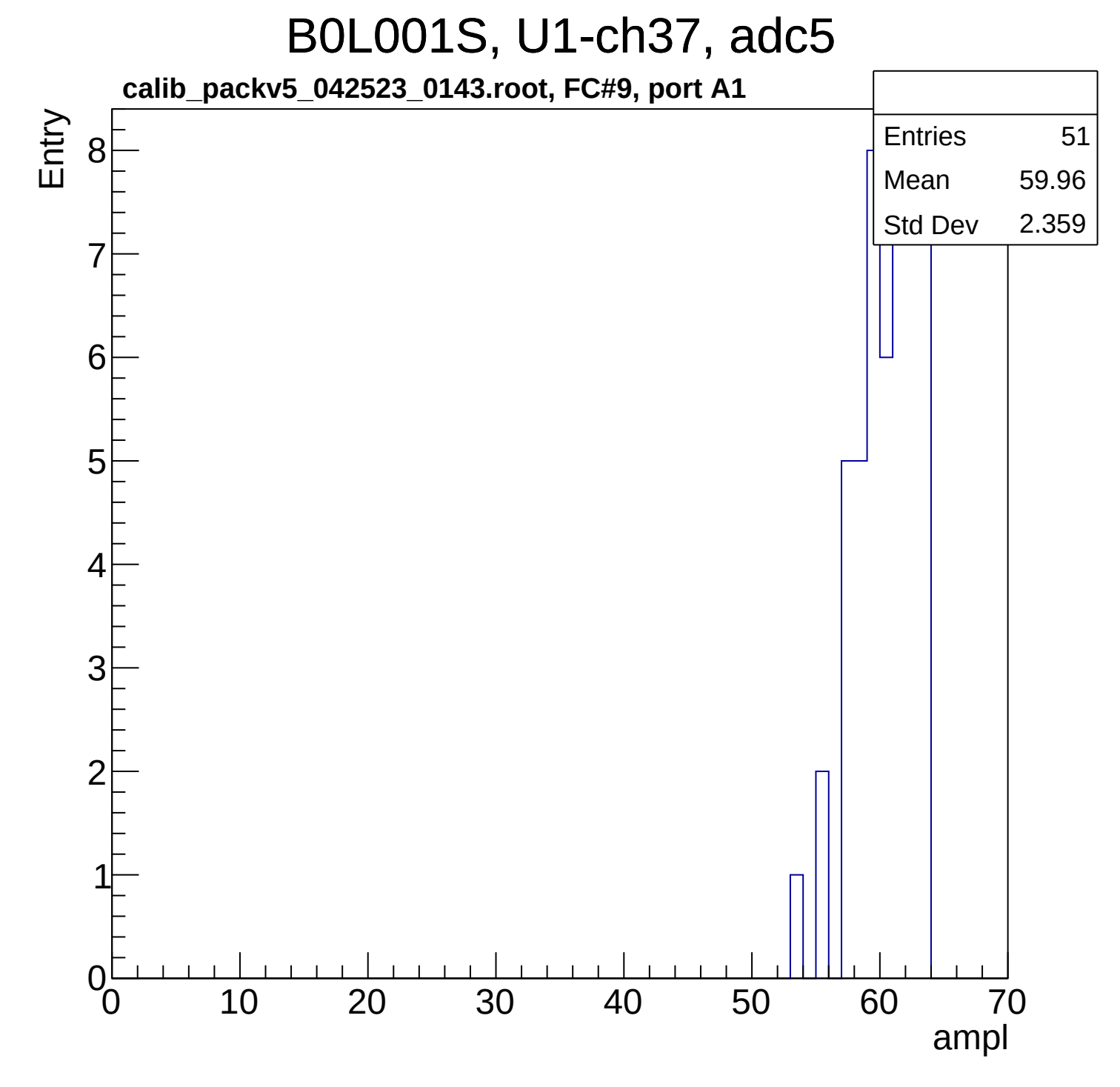
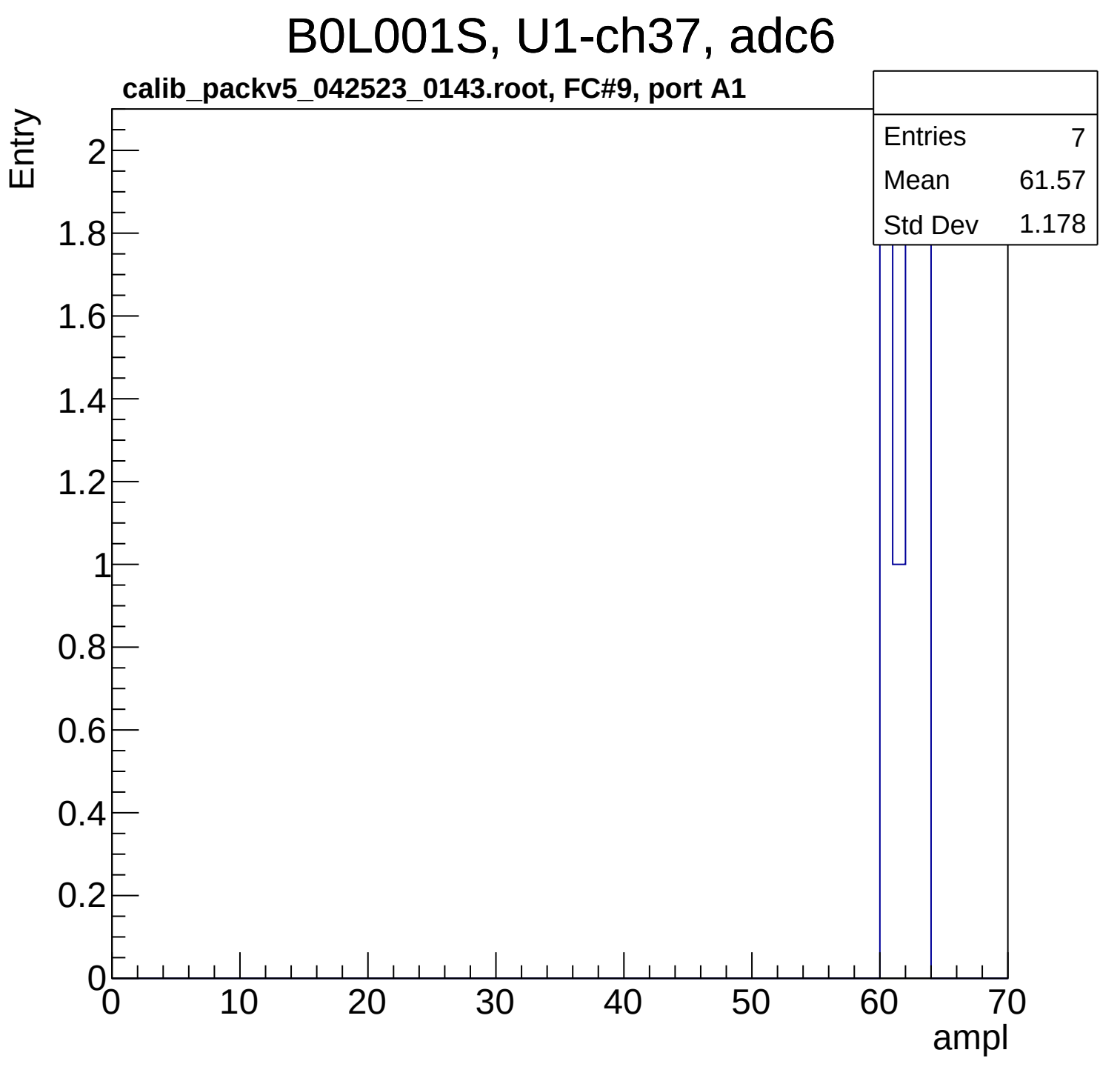
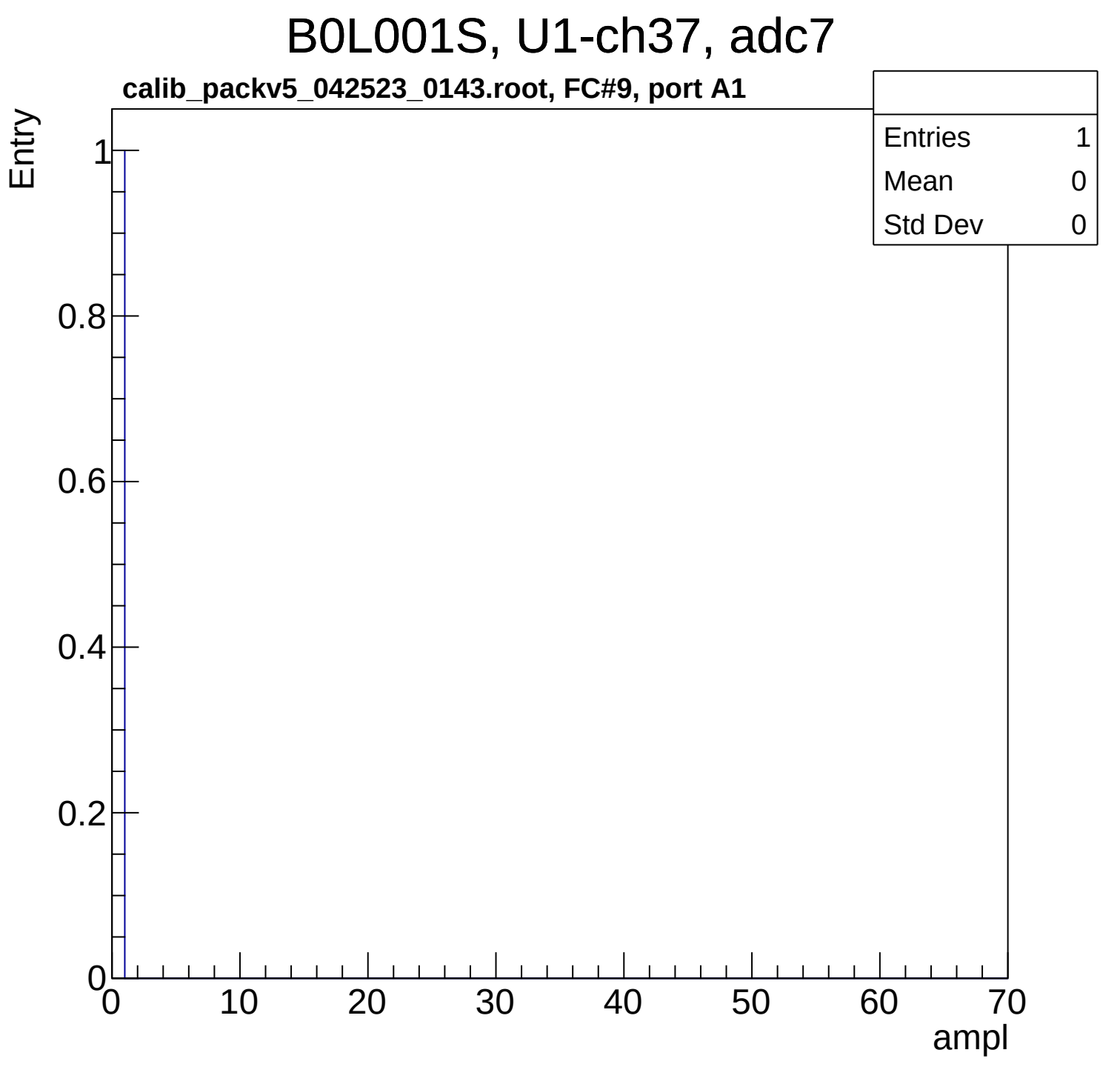
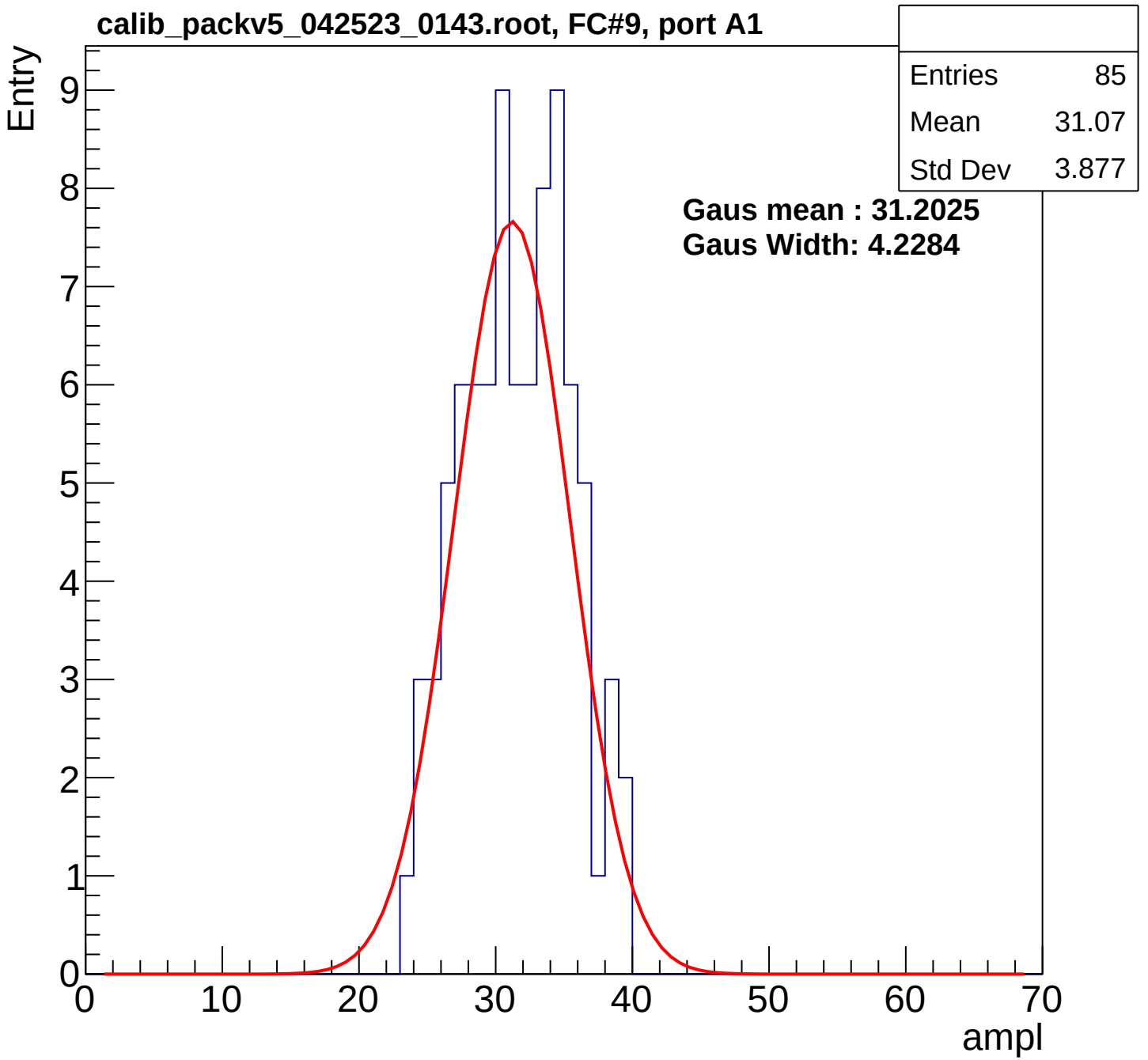
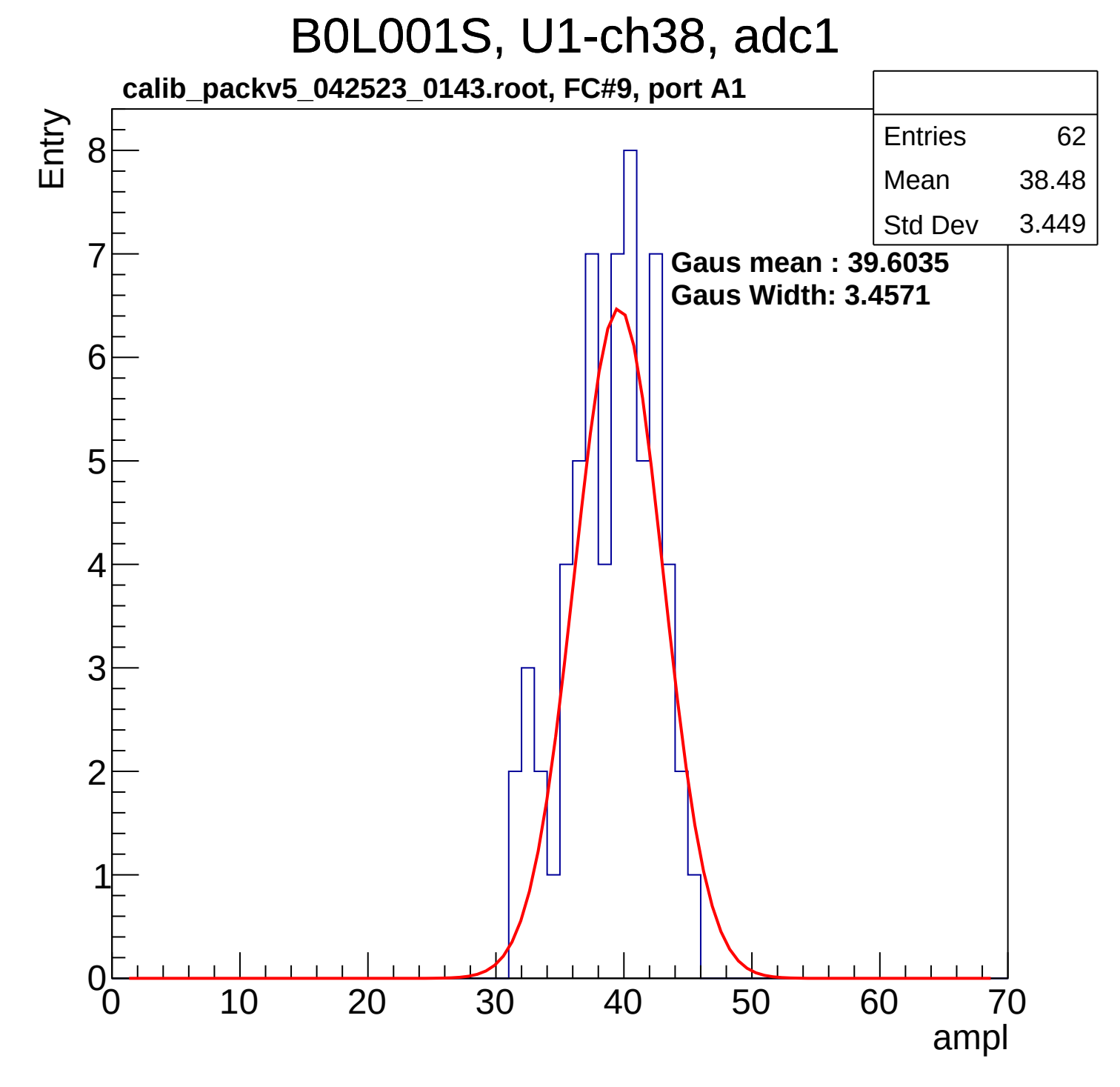

B0L001S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 59.96
Std Dev 2.359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 61.57
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 31.07
Std Dev 3.877
Gaus mean : 31.2025
Gaus Width: 4.2284
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 38.48
Std Dev 3.449
Gaus mean : 39.6035
Gaus Width: 3.4571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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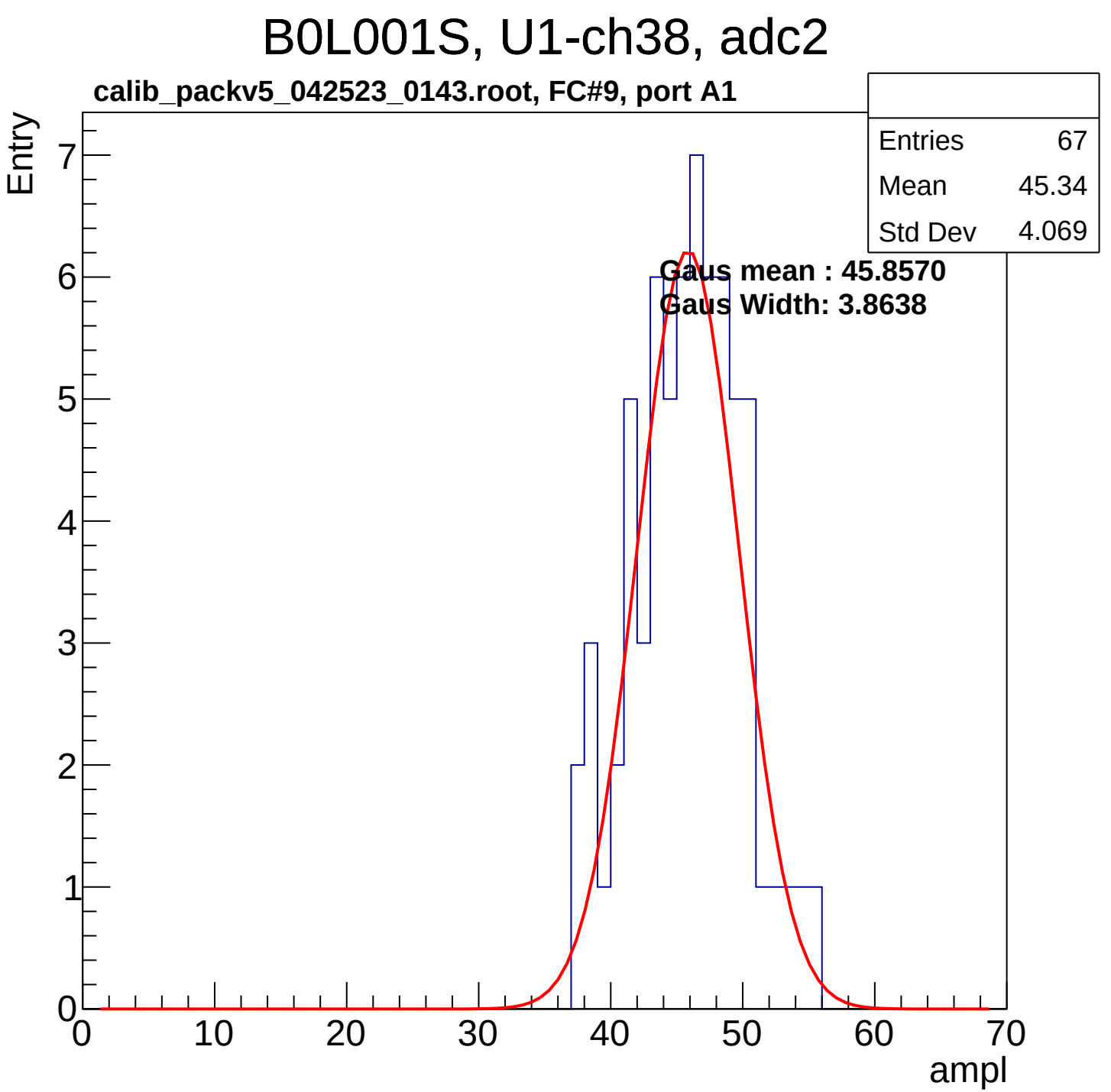
B0L001S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 45.34
Std Dev 4.069
Gaus mean : 45.8570
Gaus Width: 3.8638
Entry
ampl
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1
2
3
4
5
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0
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40
50
60
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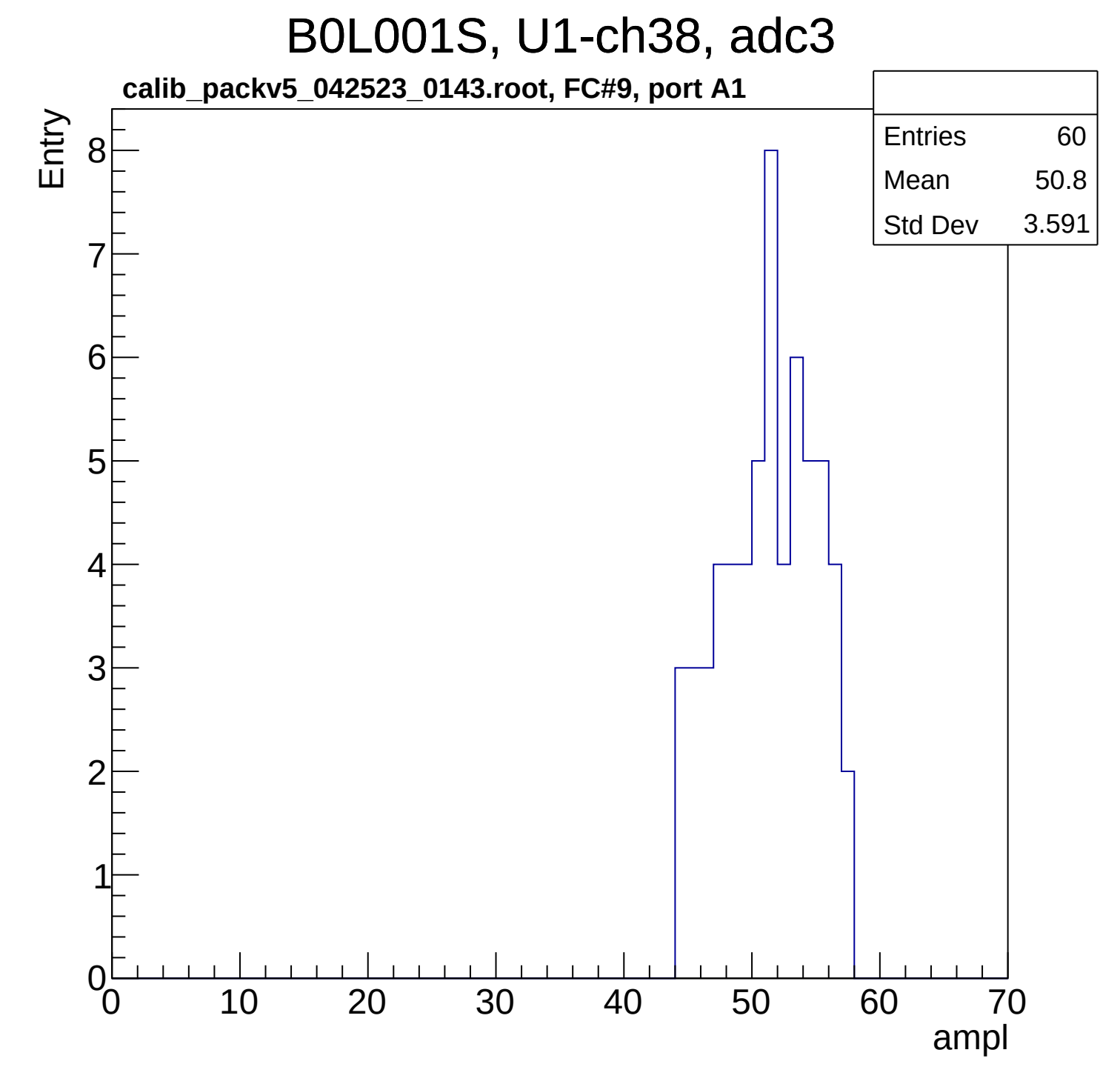
B0L001S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.8
Std Dev 3.591
Entry
ampl
0
1
2
3
4
5
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7
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0
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50
60
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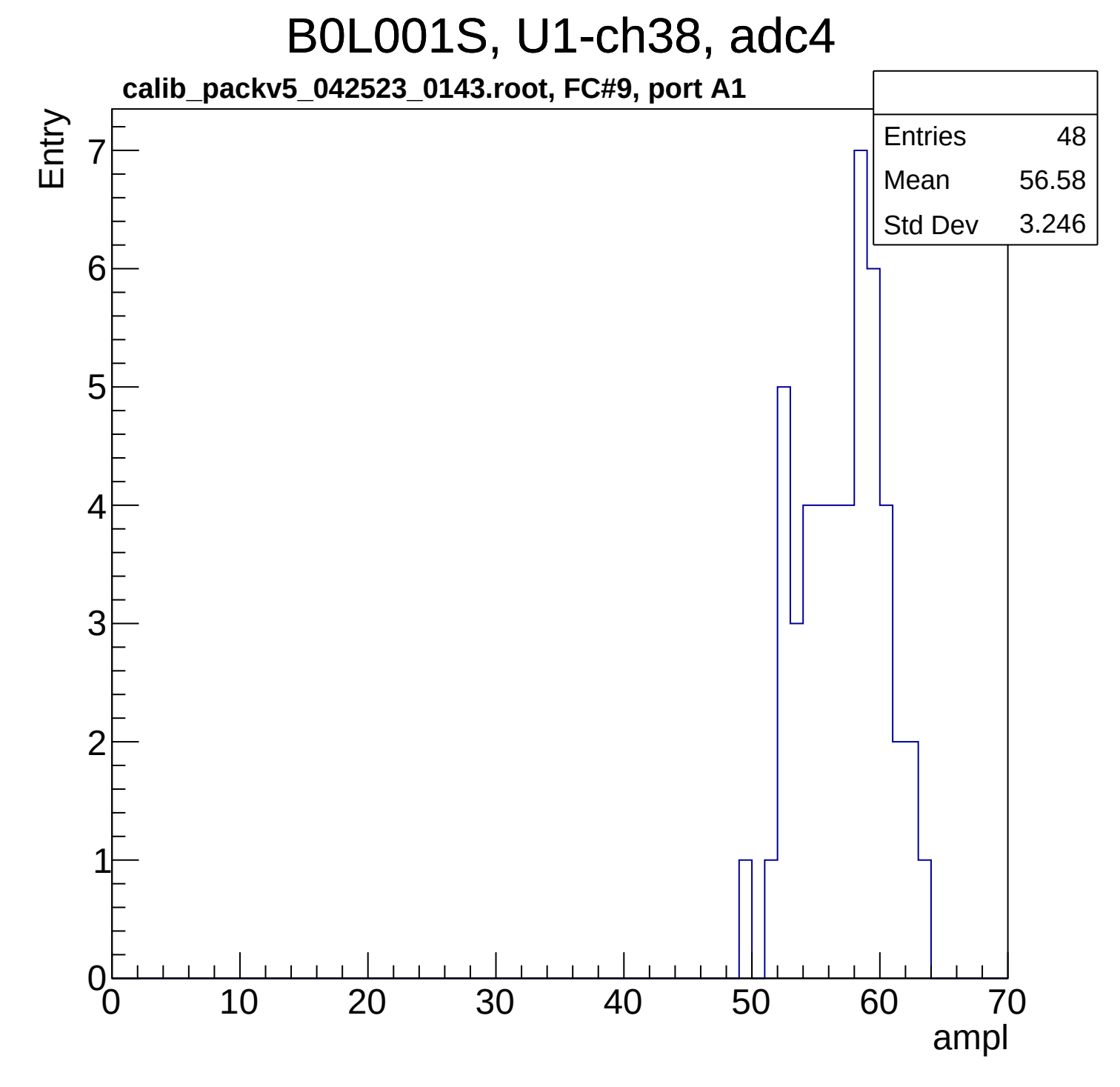
B0L001S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 56.58
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
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0
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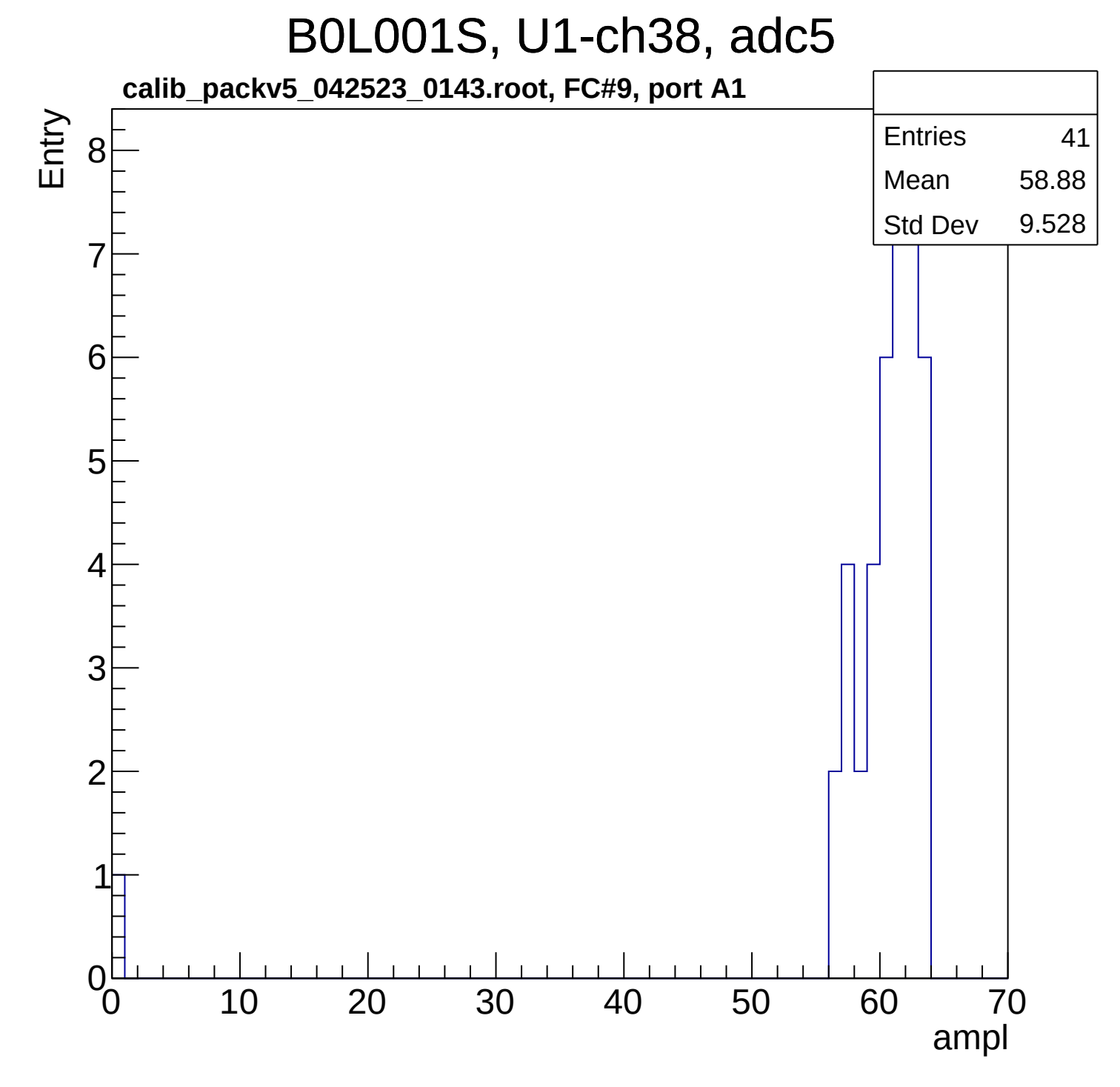
B0L001S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.88
Std Dev 9.528
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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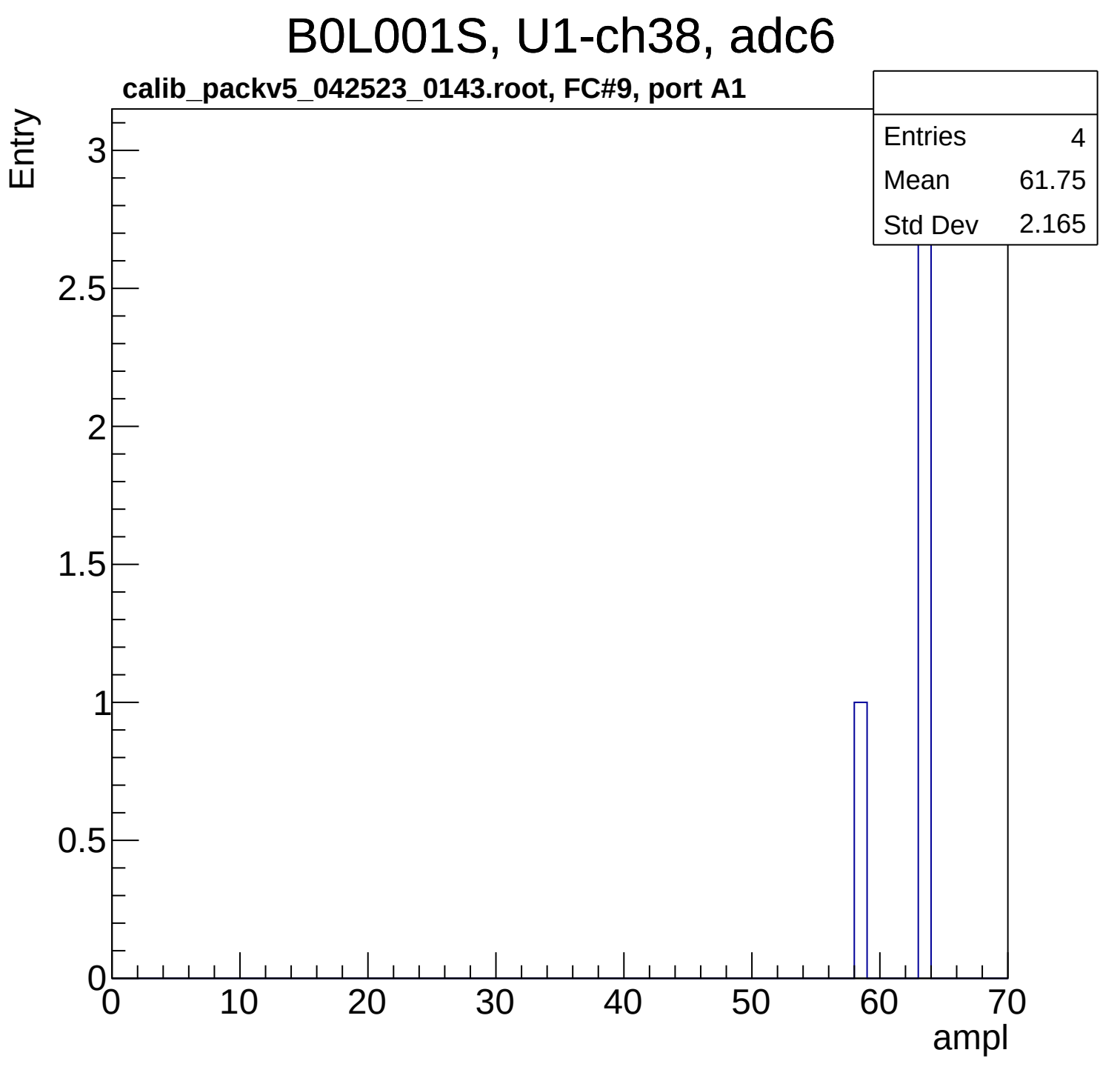
B0L001S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 2.165
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
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60
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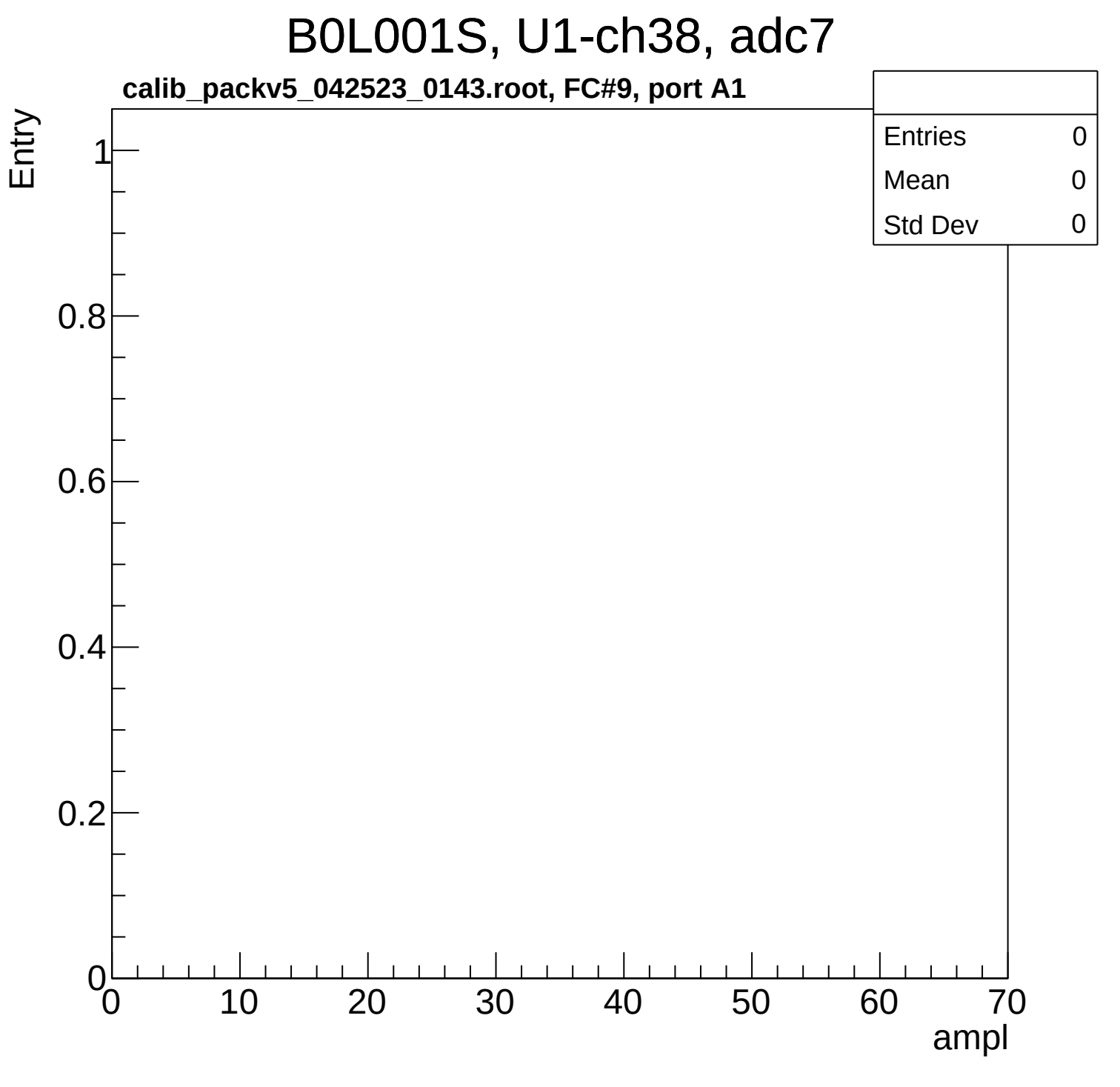
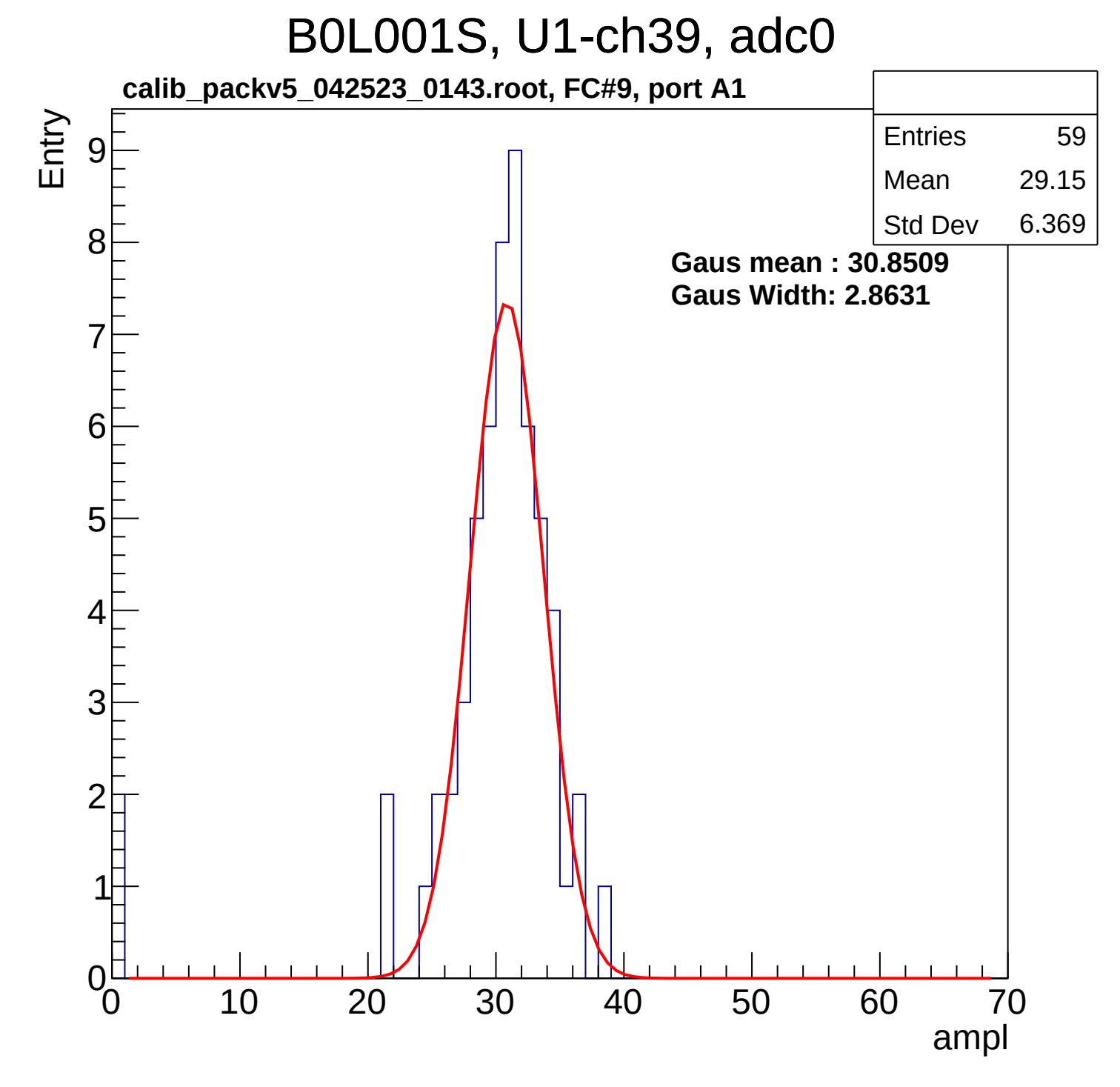

B0L001S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.15
Std Dev 6.369
Gaus mean : 30.8509
Gaus Width: 2.8631
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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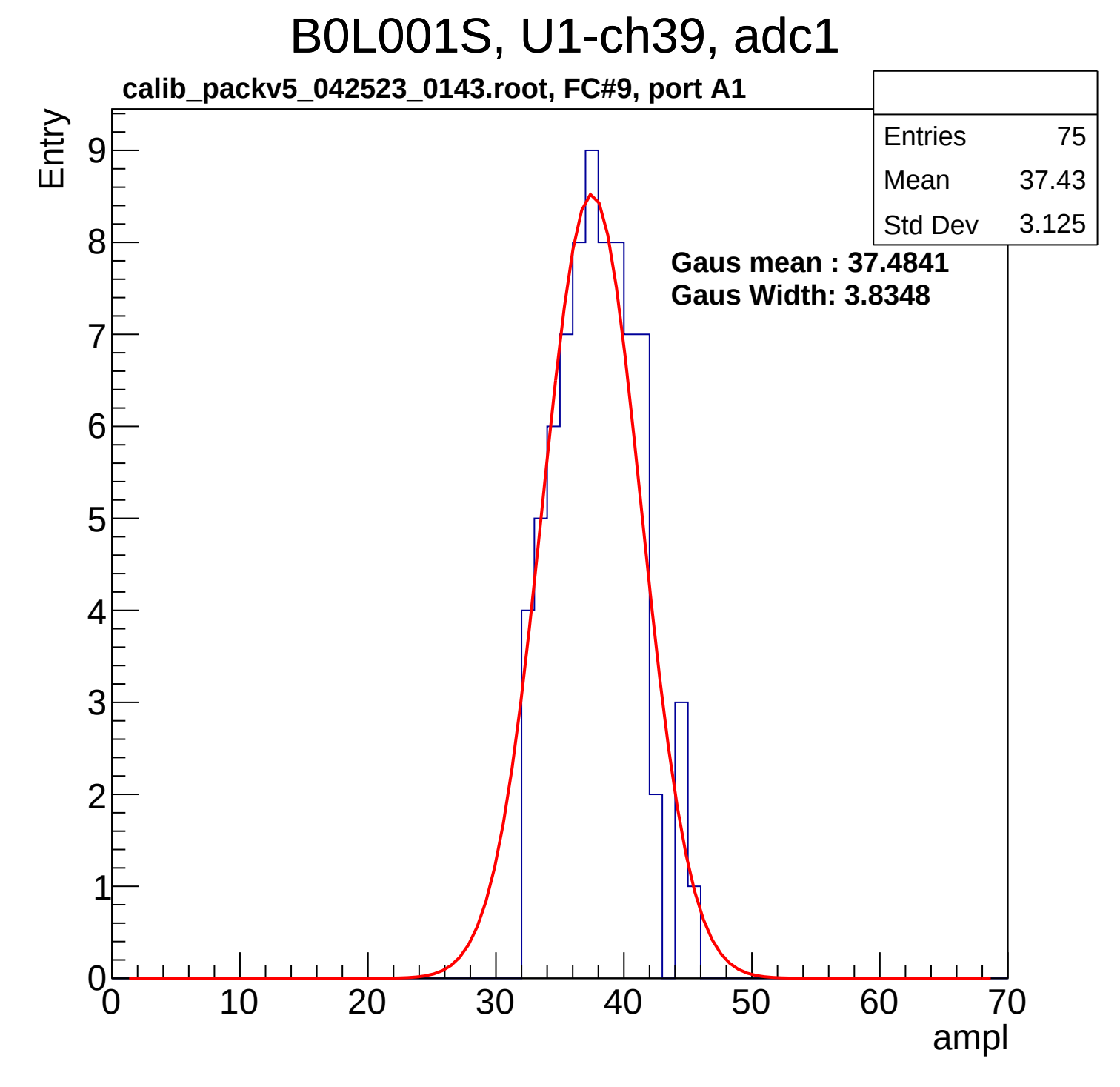
B0L001S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.43
Std Dev 3.125
Gaus mean : 37.4841
Gaus Width: 3.8348
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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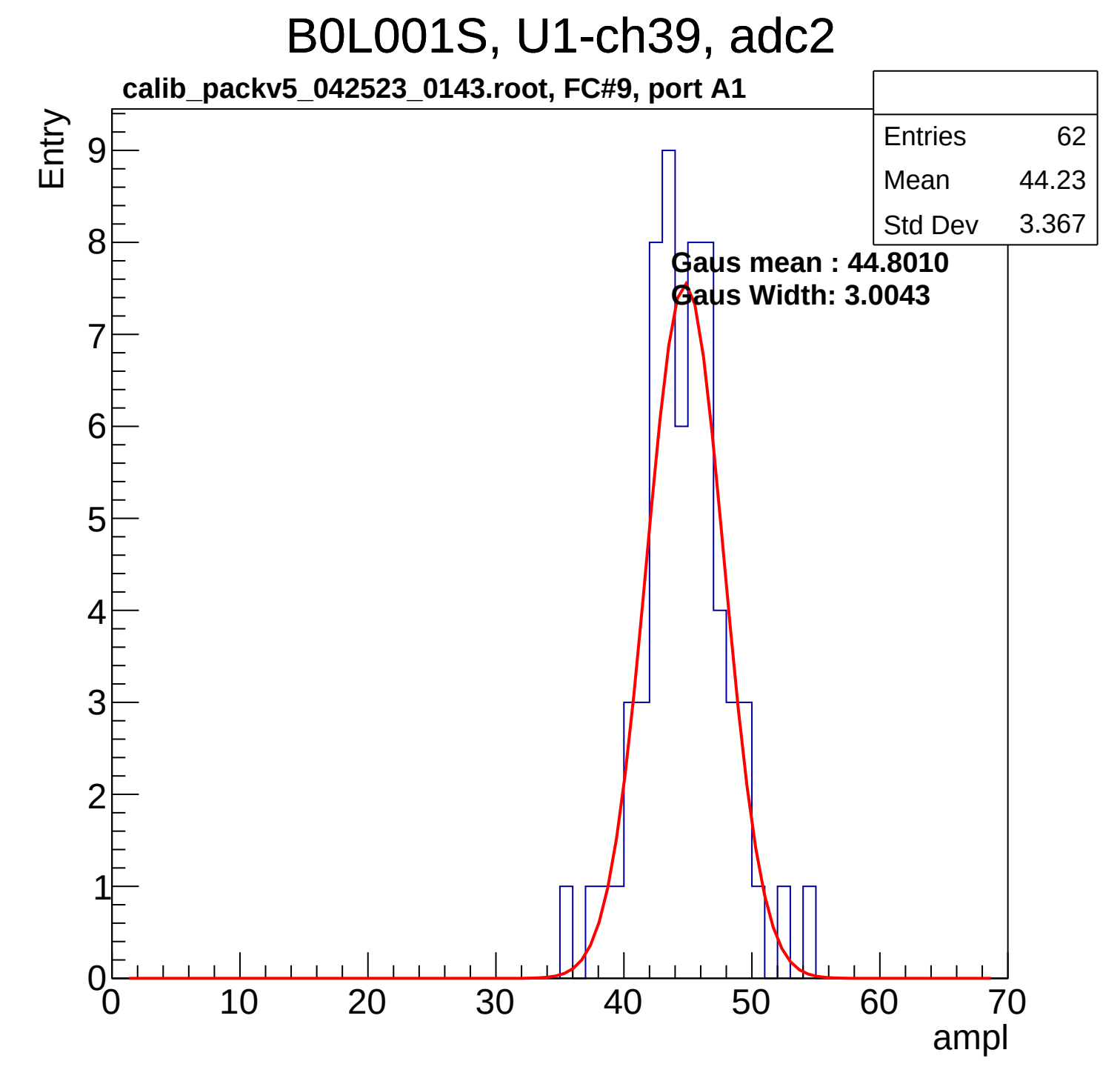
B0L001S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.23
Std Dev 3.367
Gaus mean : 44.8010
Gaus Width: 3.0043
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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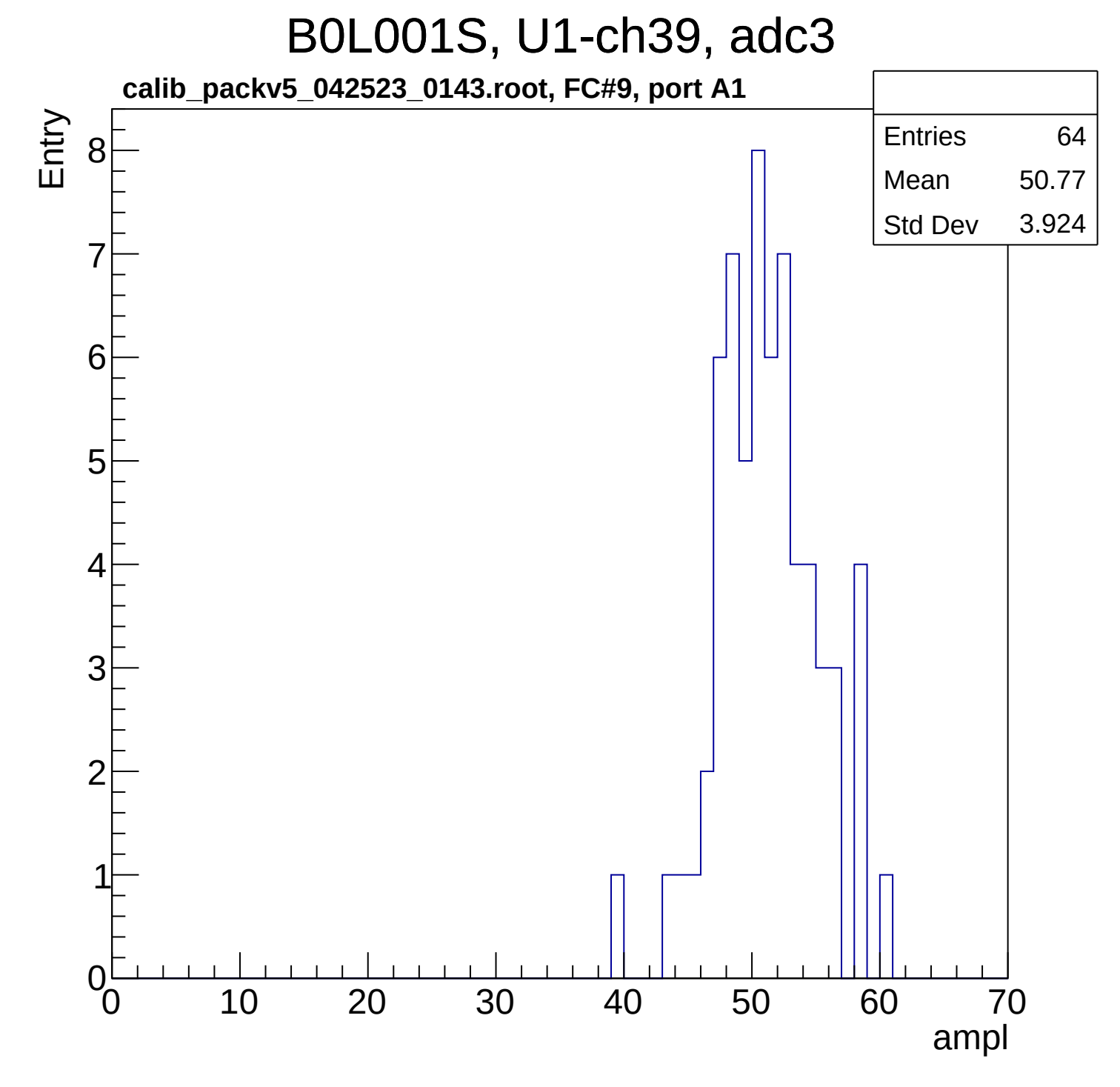
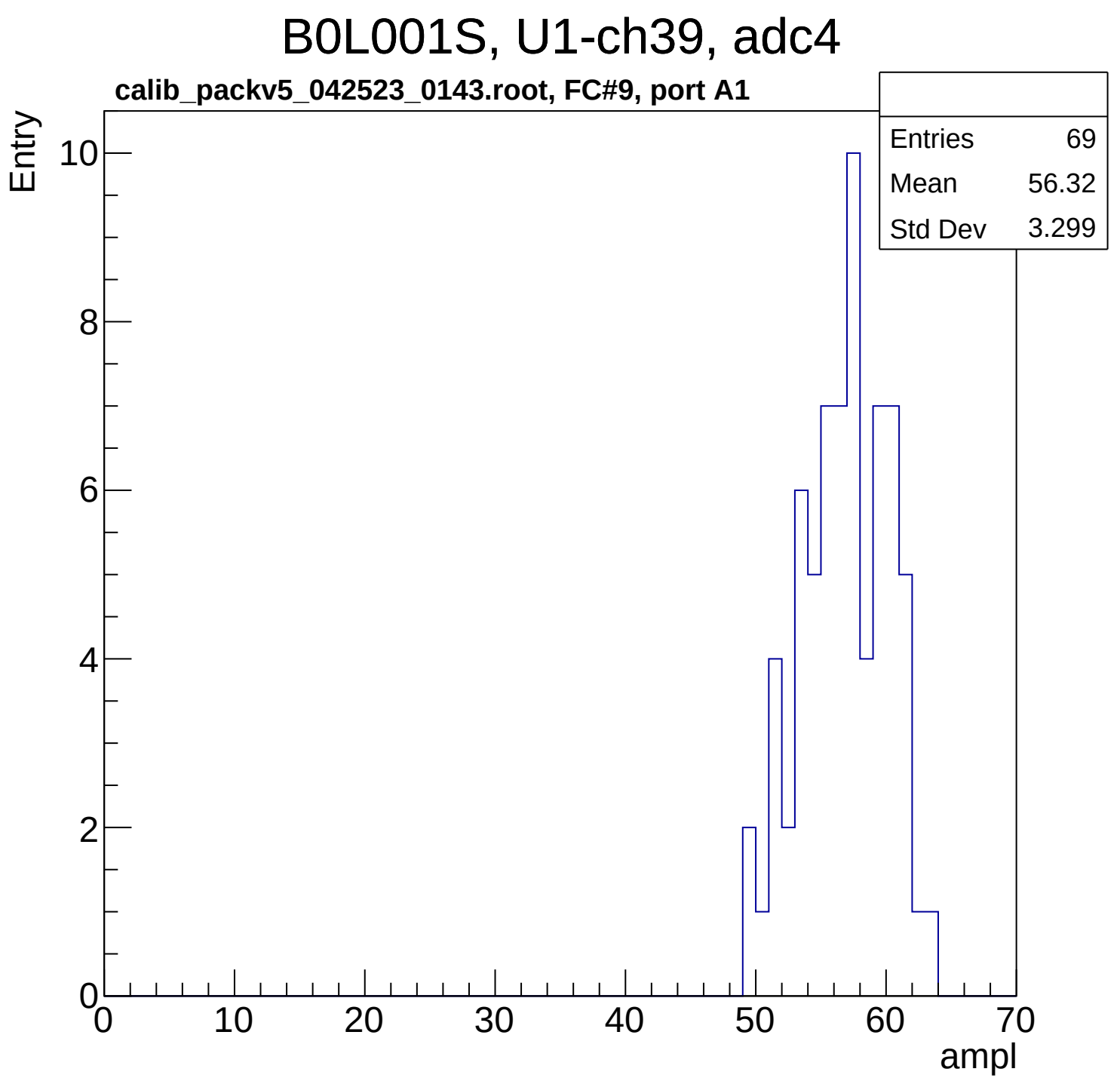
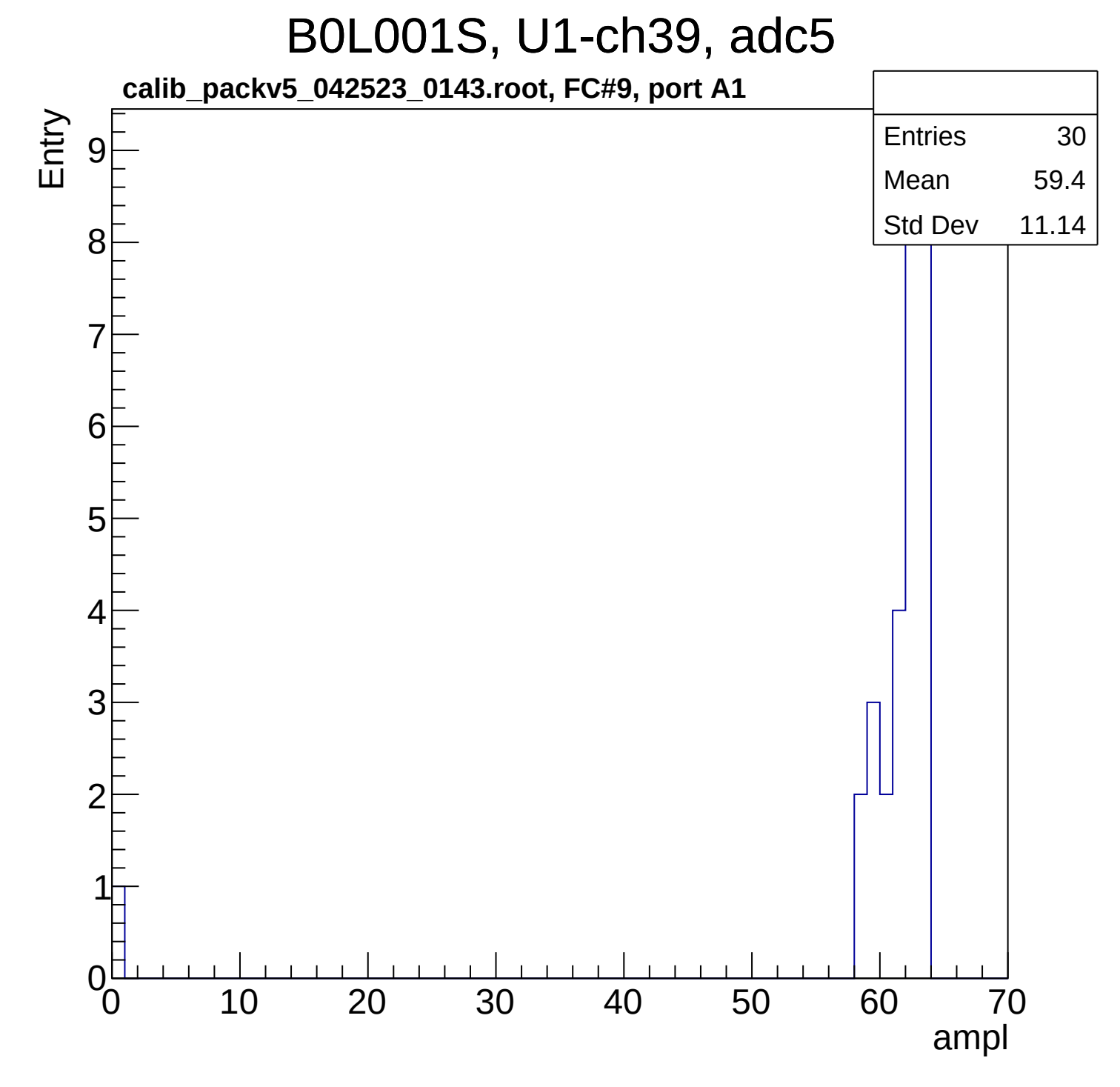
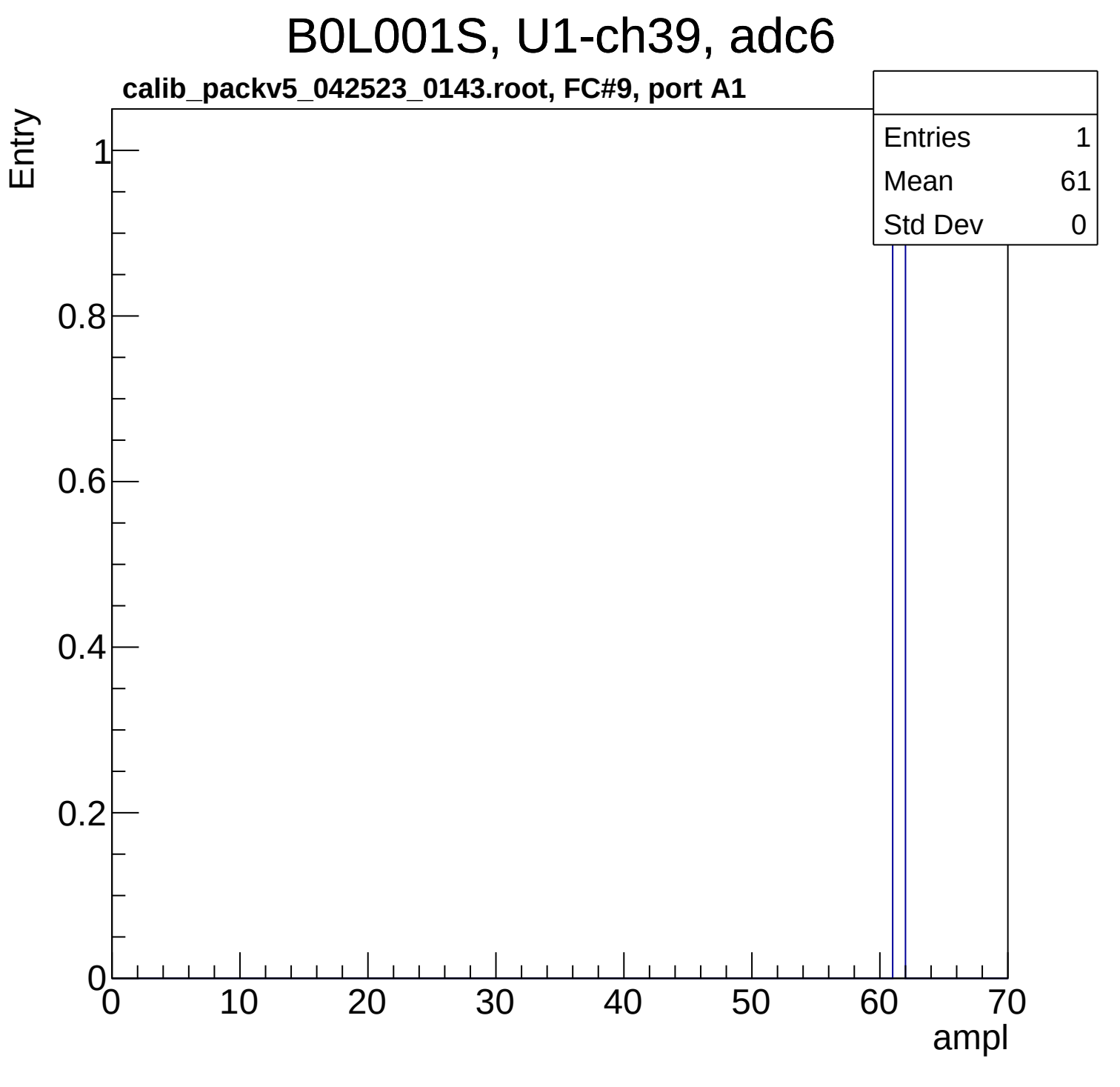
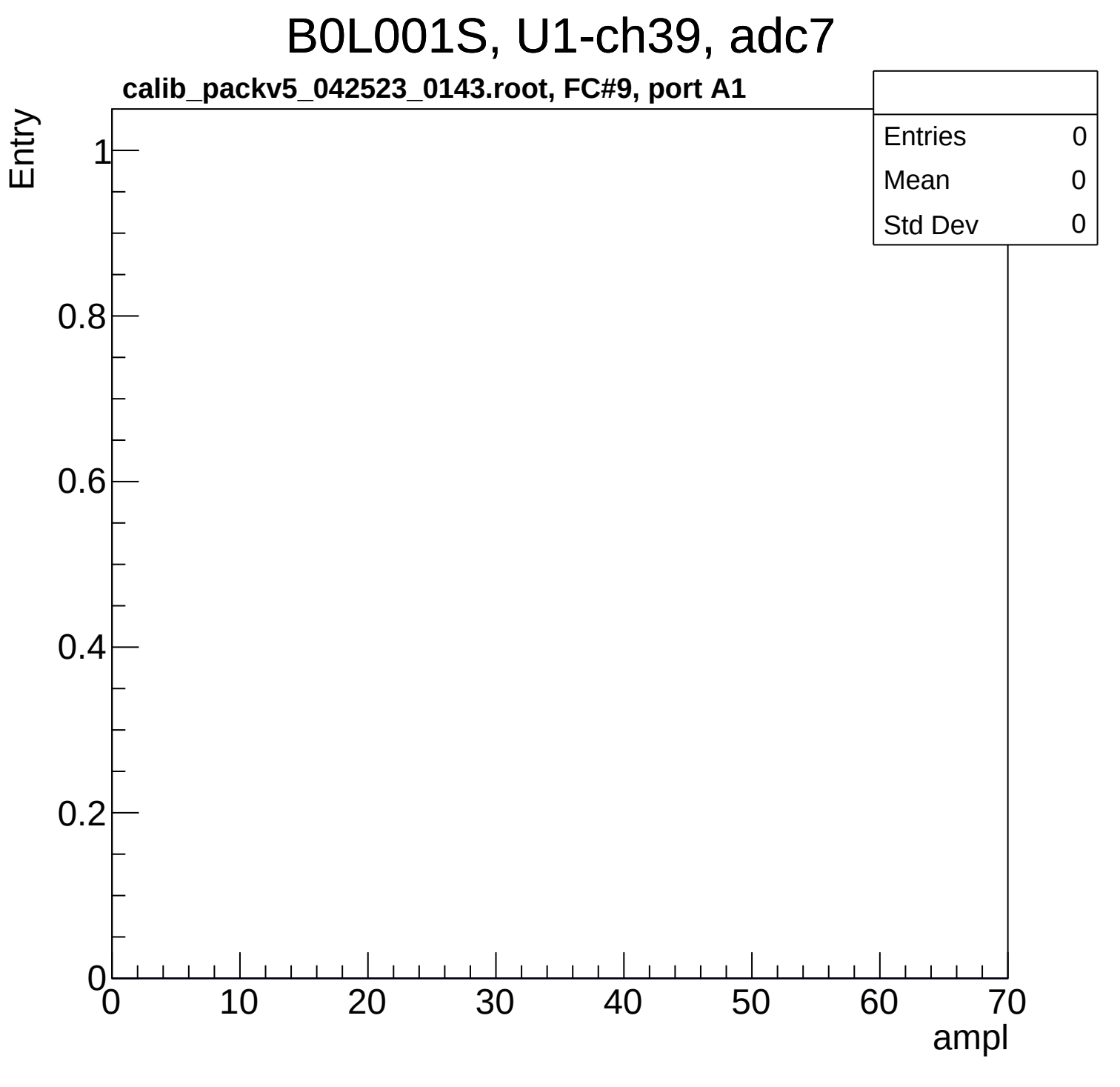
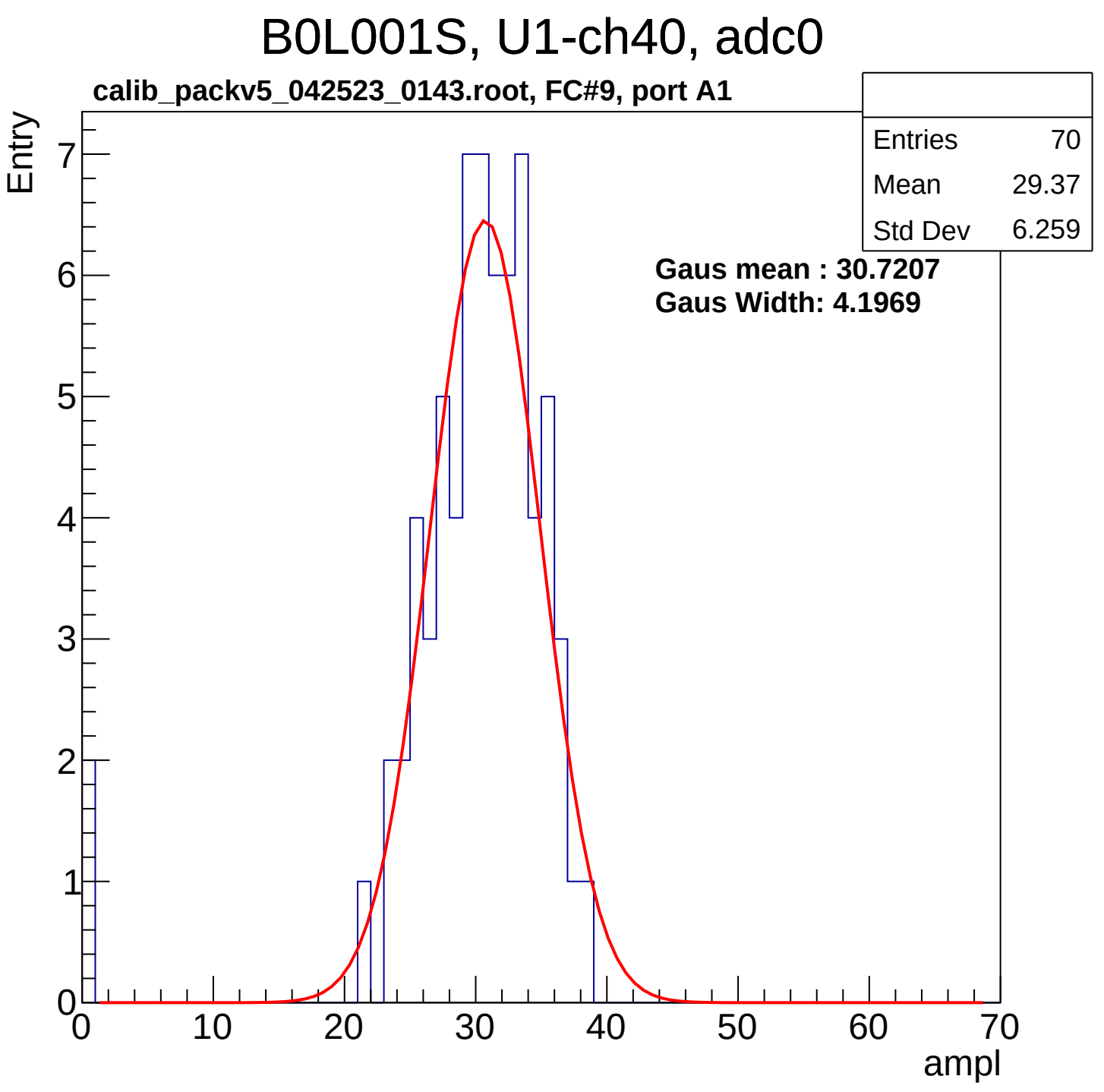

B0L001S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 50.77
Std Dev 3.924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

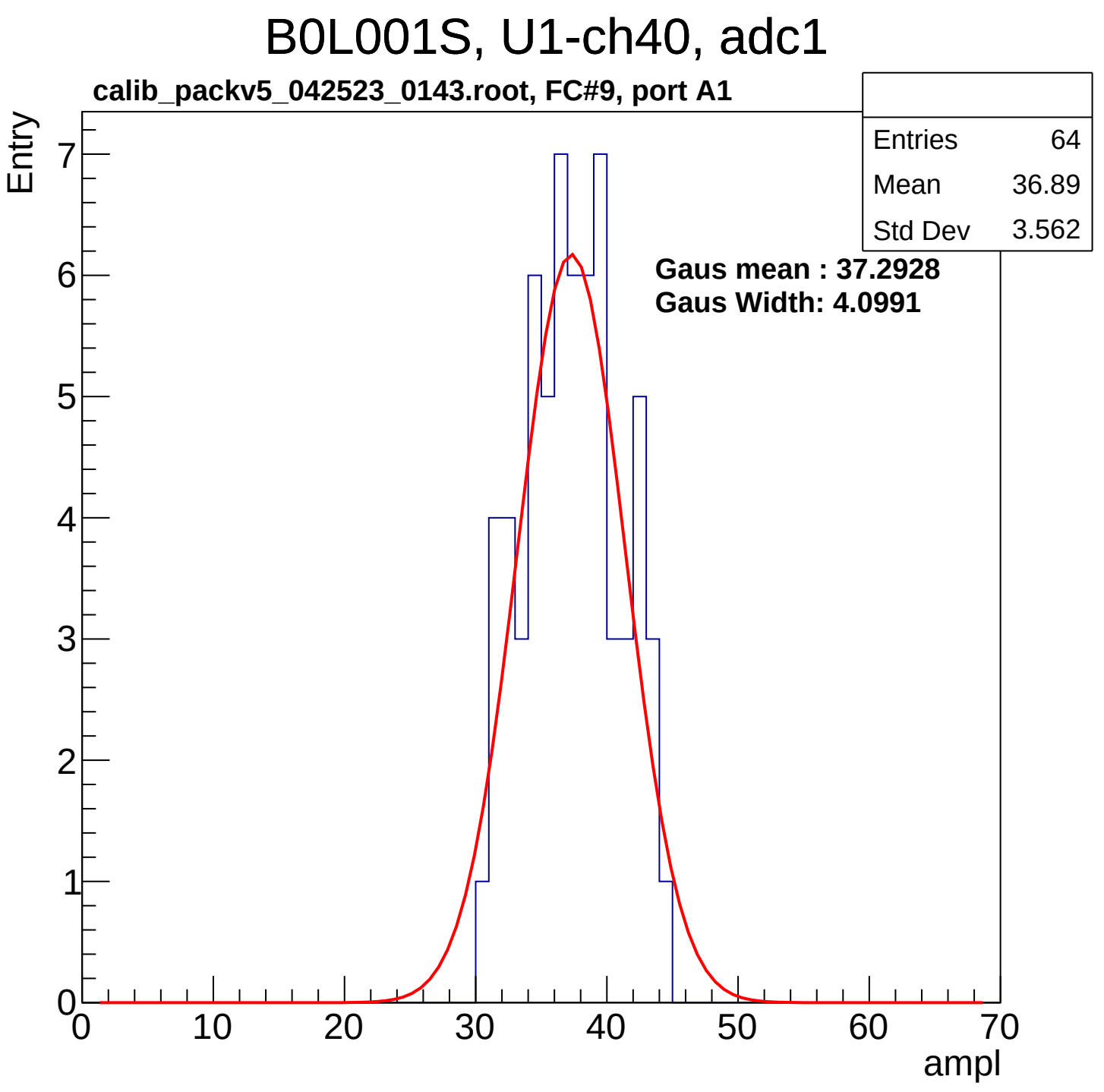

B0L001S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 56.32
Std Dev 3.299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

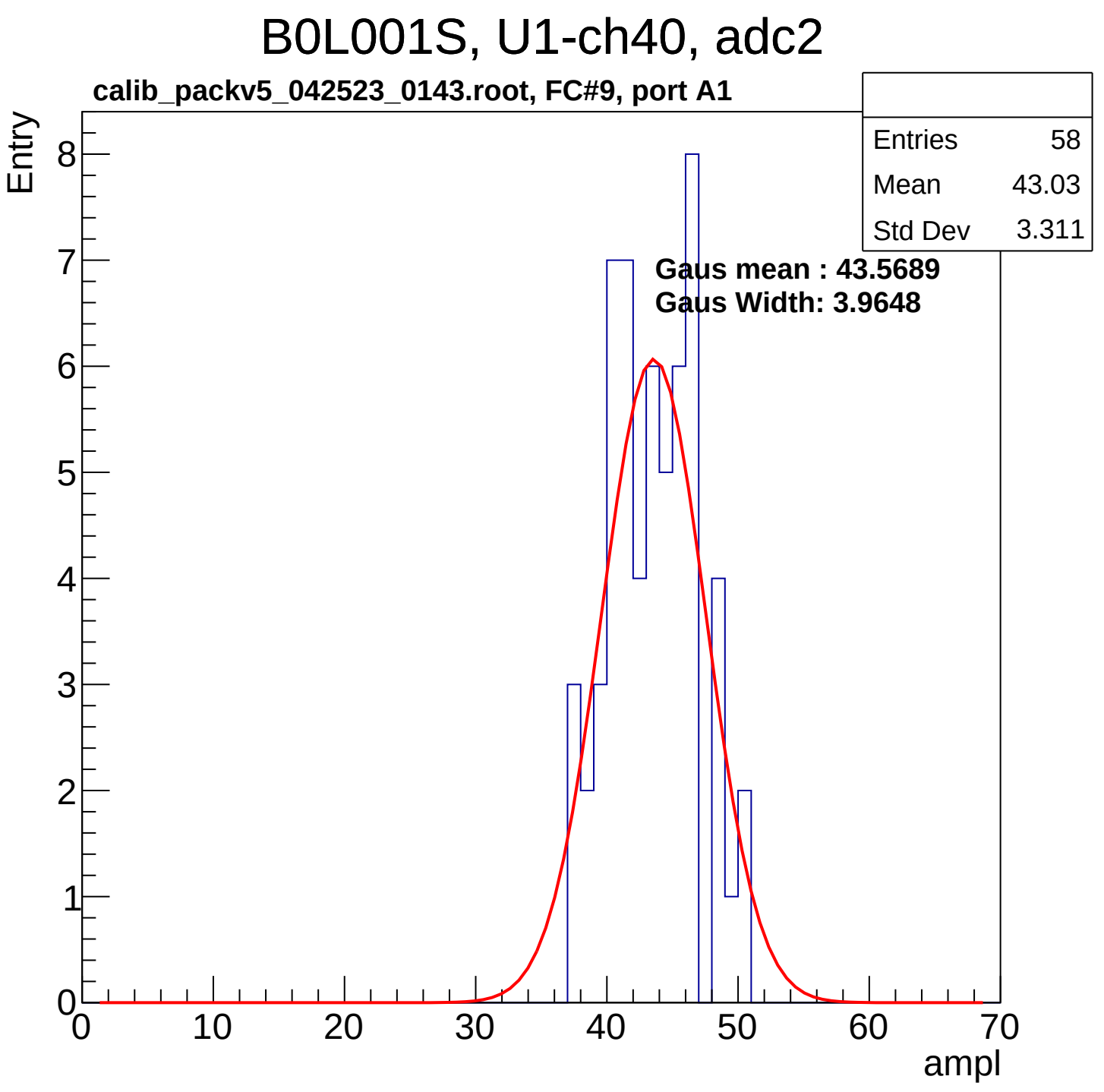

B0L001S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 30
Mean 59.4
Std Dev 11.14
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.37
Std Dev 6.259
Gaus mean : 30.7207
Gaus Width: 4.1969
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.89
Std Dev 3.562
Gaus mean : 37.2928
Gaus Width: 4.0991
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.03
Std Dev 3.311
Gaus mean : 43.5689
Gaus Width: 3.9648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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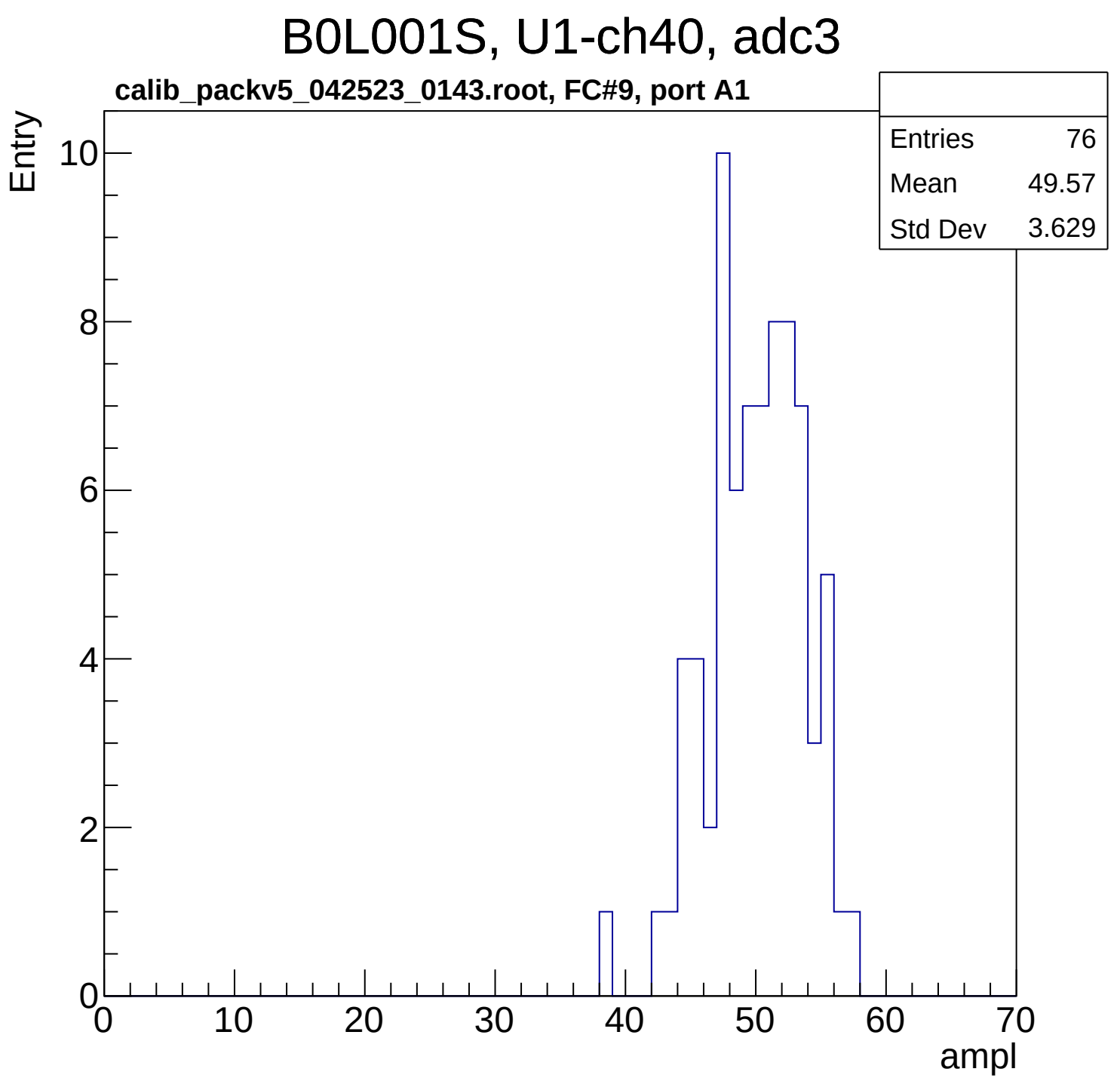
B0L001S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 49.57
Std Dev 3.629
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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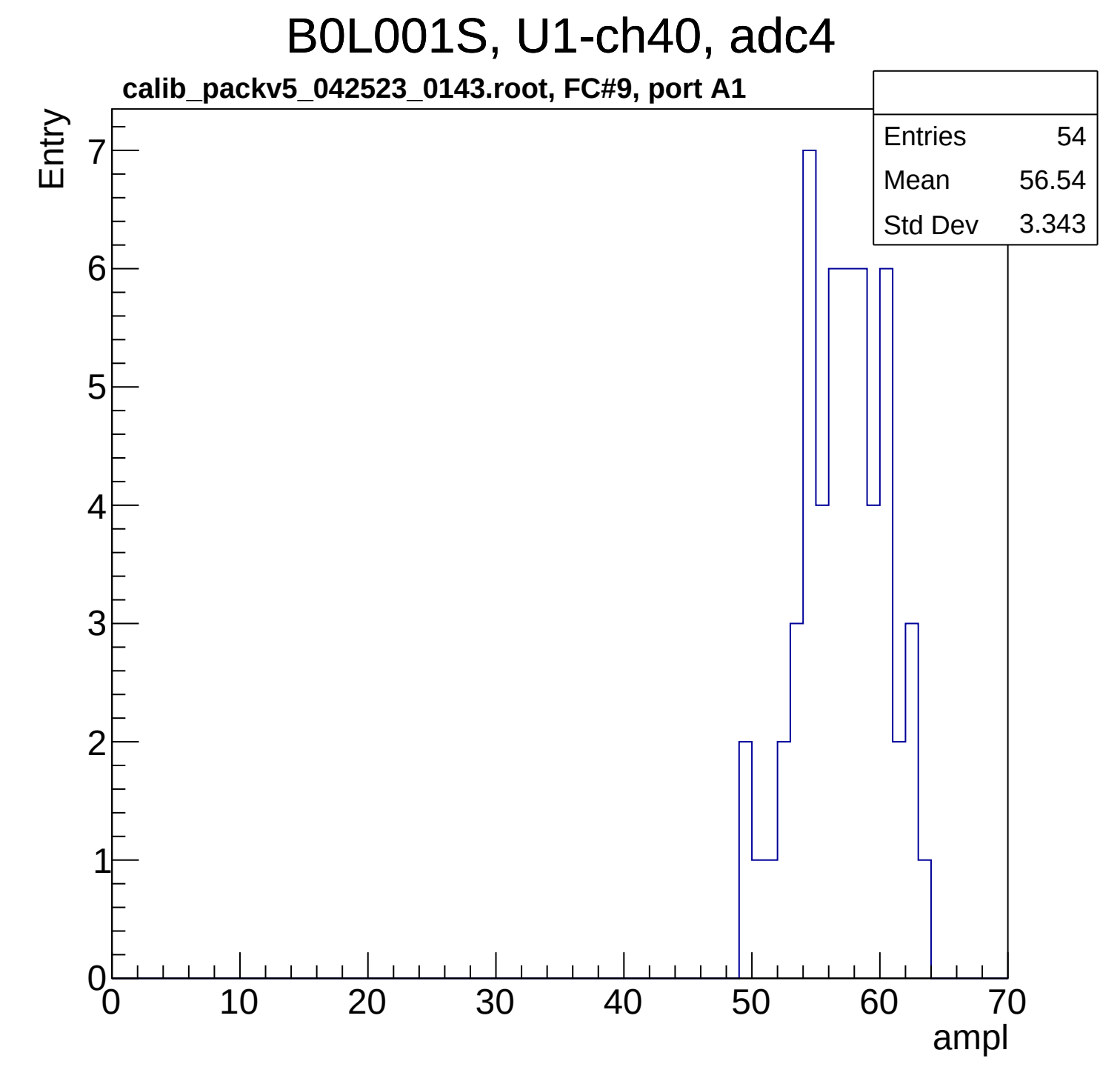
B0L001S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.54
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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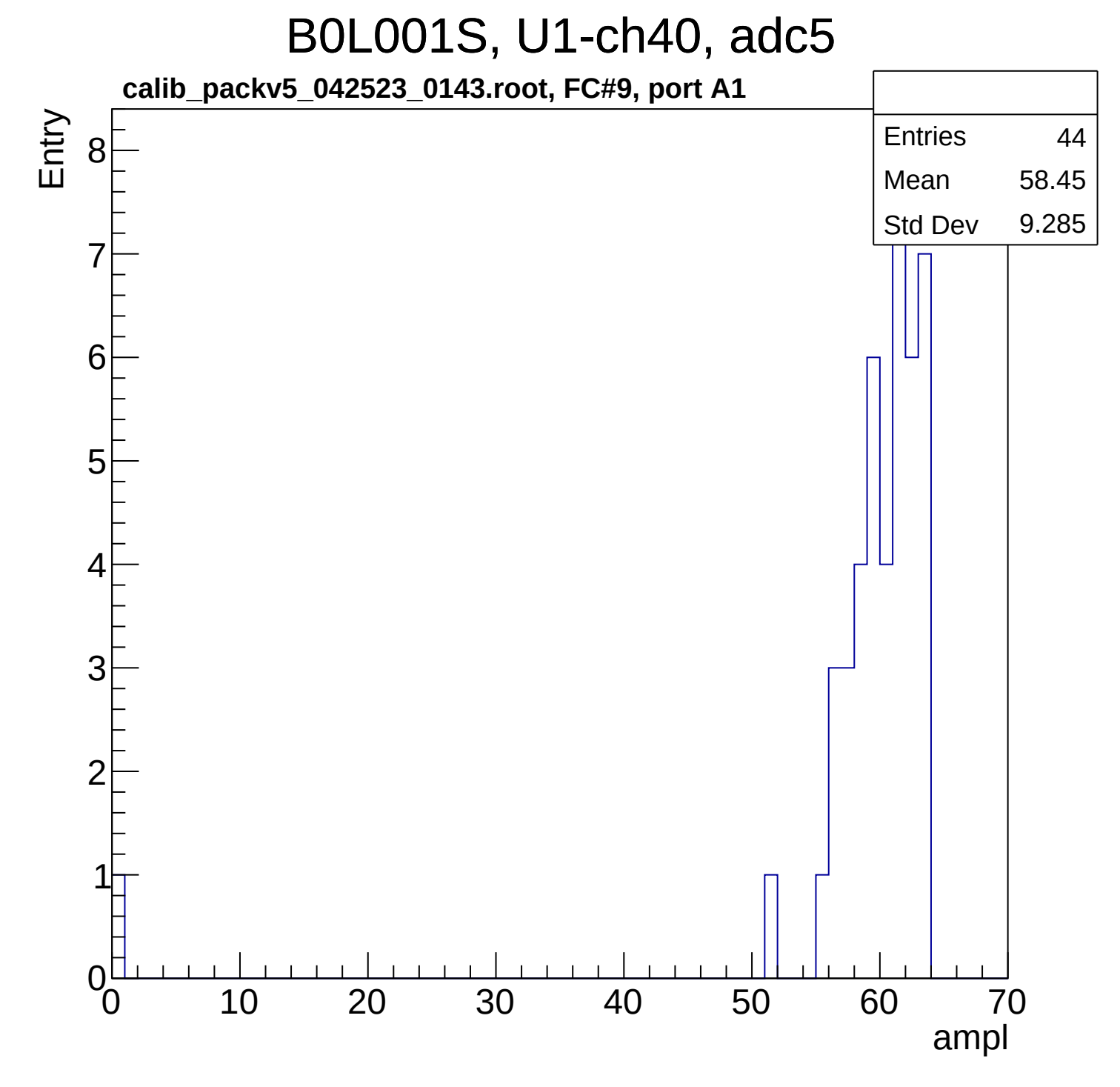
B0L001S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.45
Std Dev 9.285
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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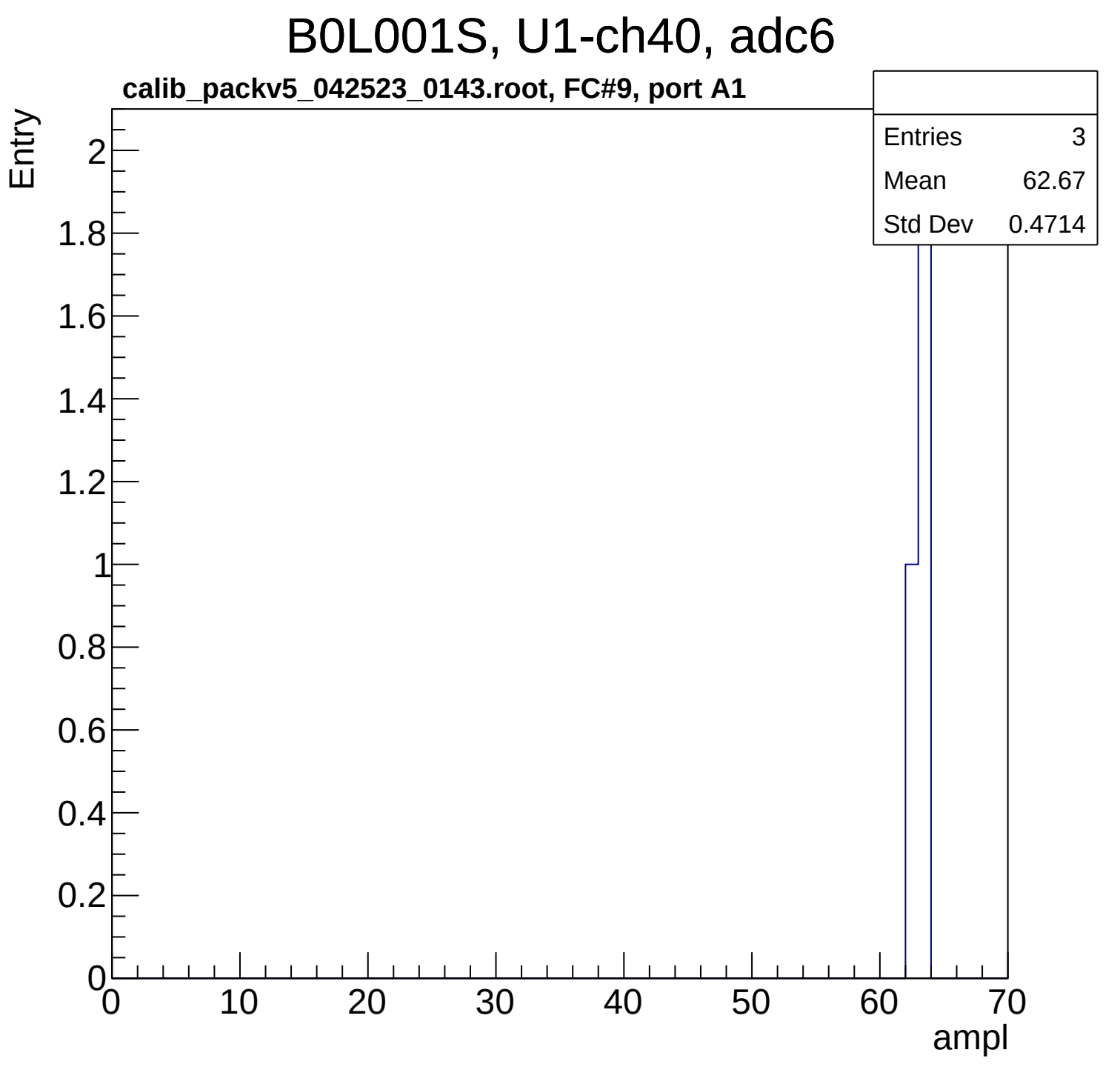
B0L001S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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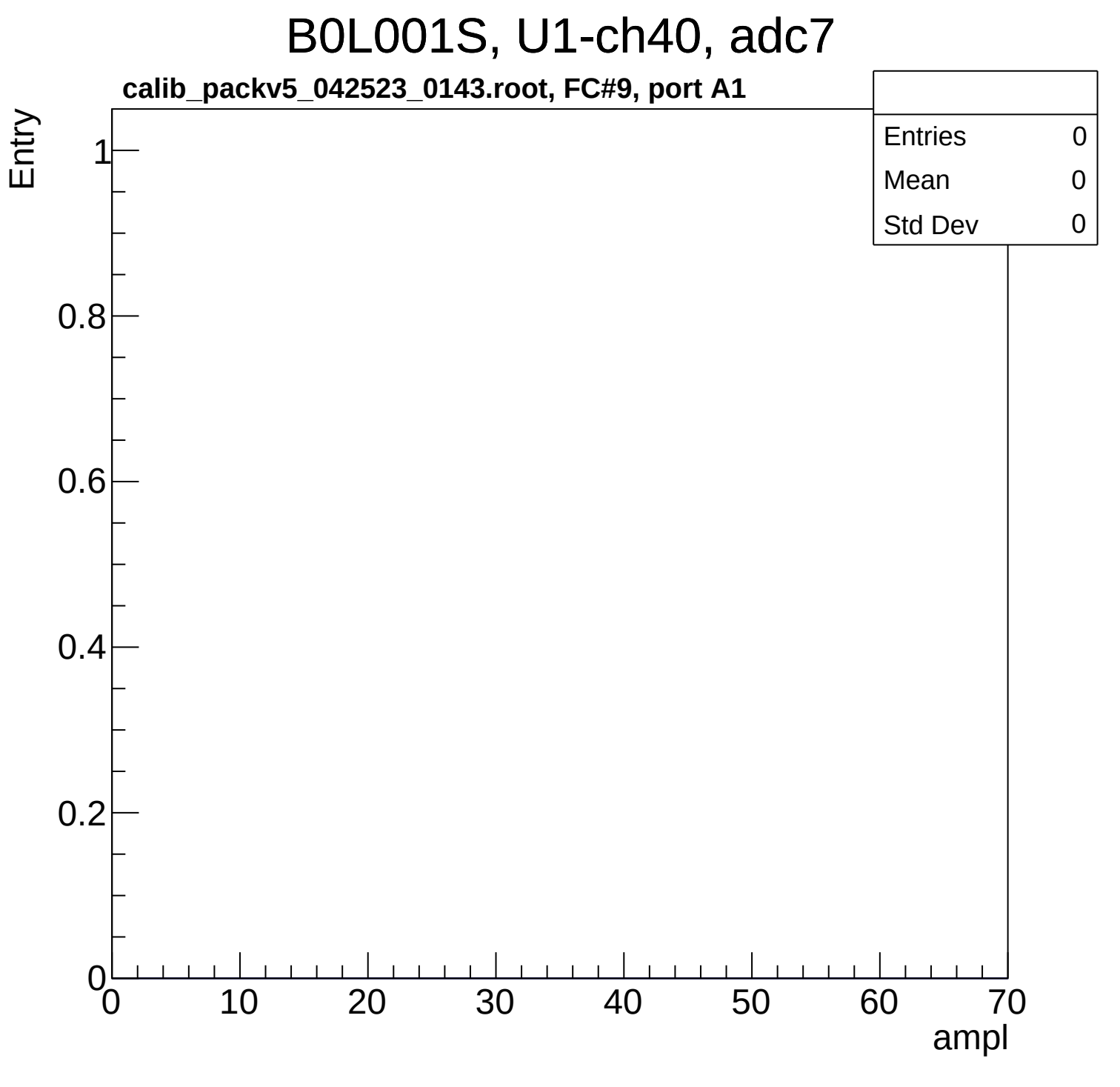
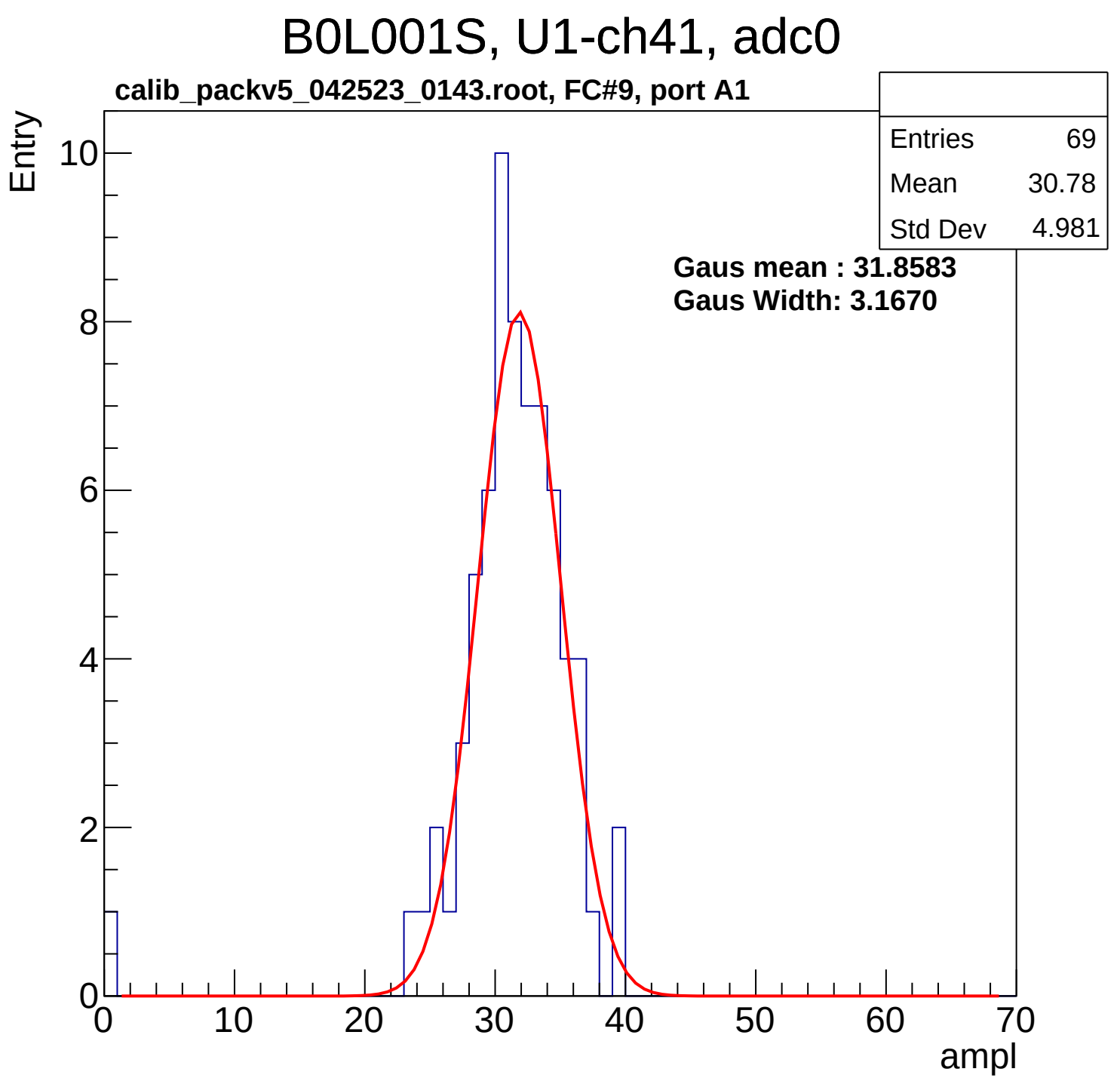
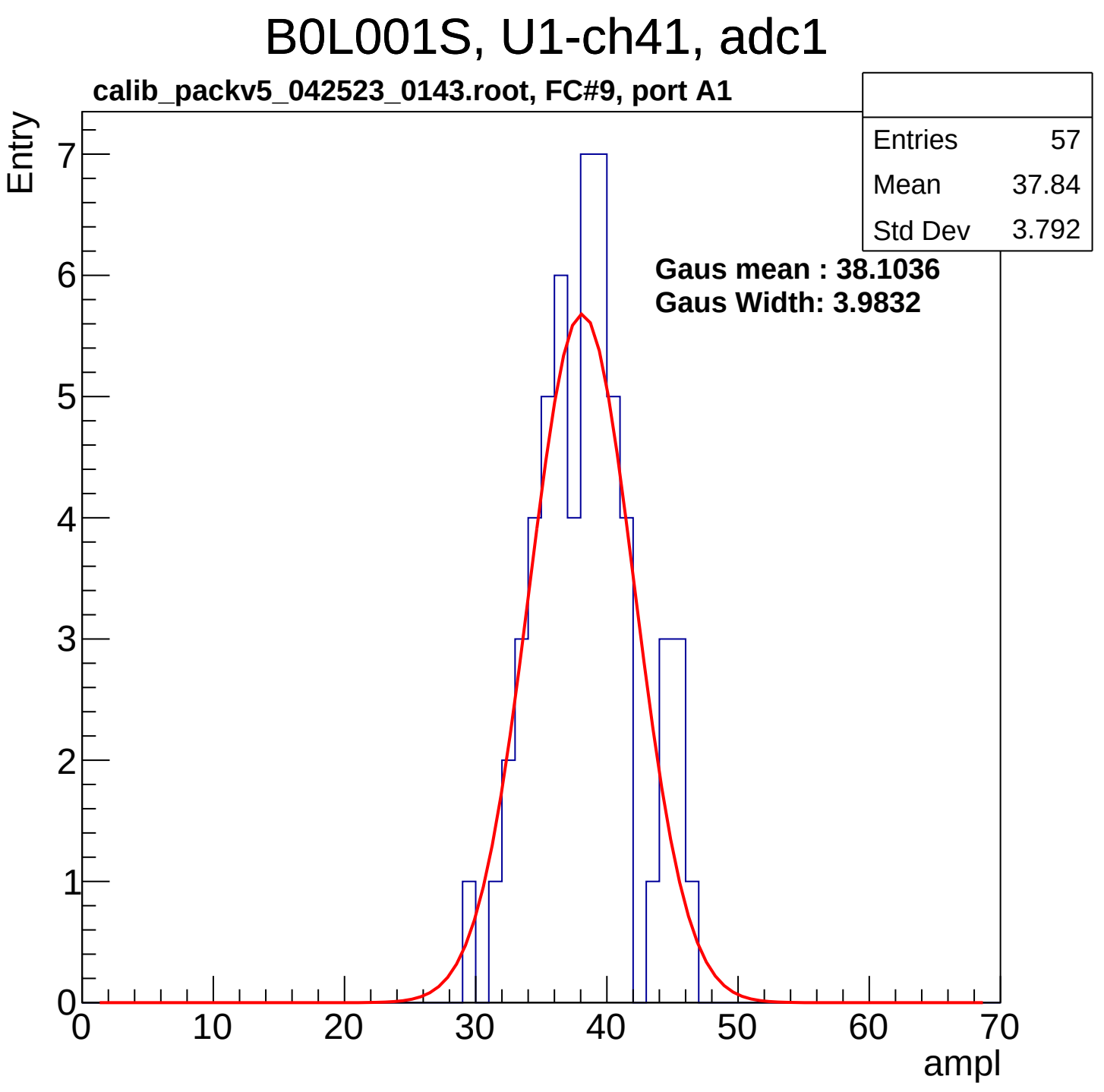
40
50
60
70


B0L001S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 30.78
Std Dev 4.981
Gaus mean : 31.8583
Gaus Width: 3.1670
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.84
Std Dev 3.792
Gaus mean : 38.1036
Gaus Width: 3.9832
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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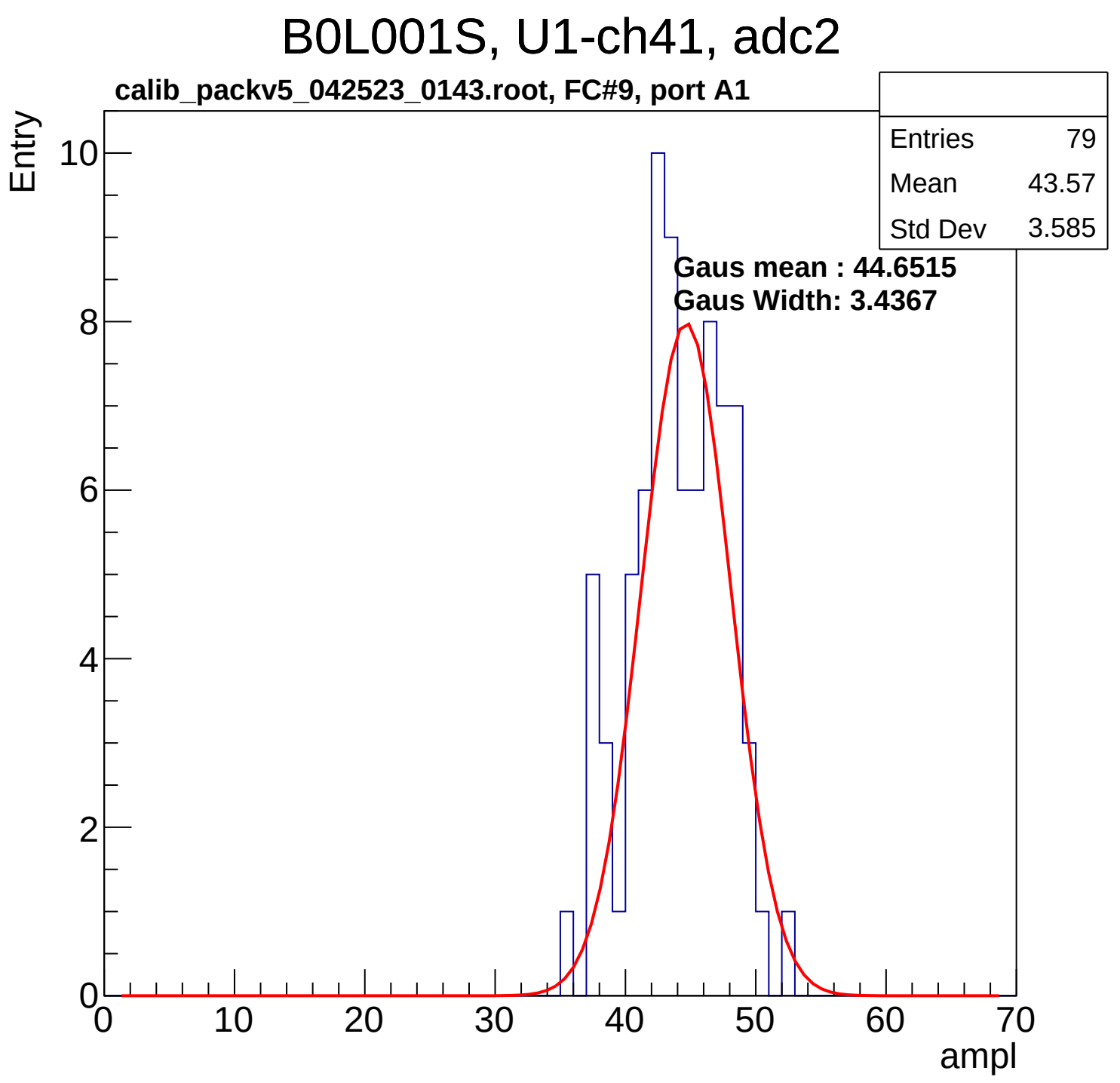
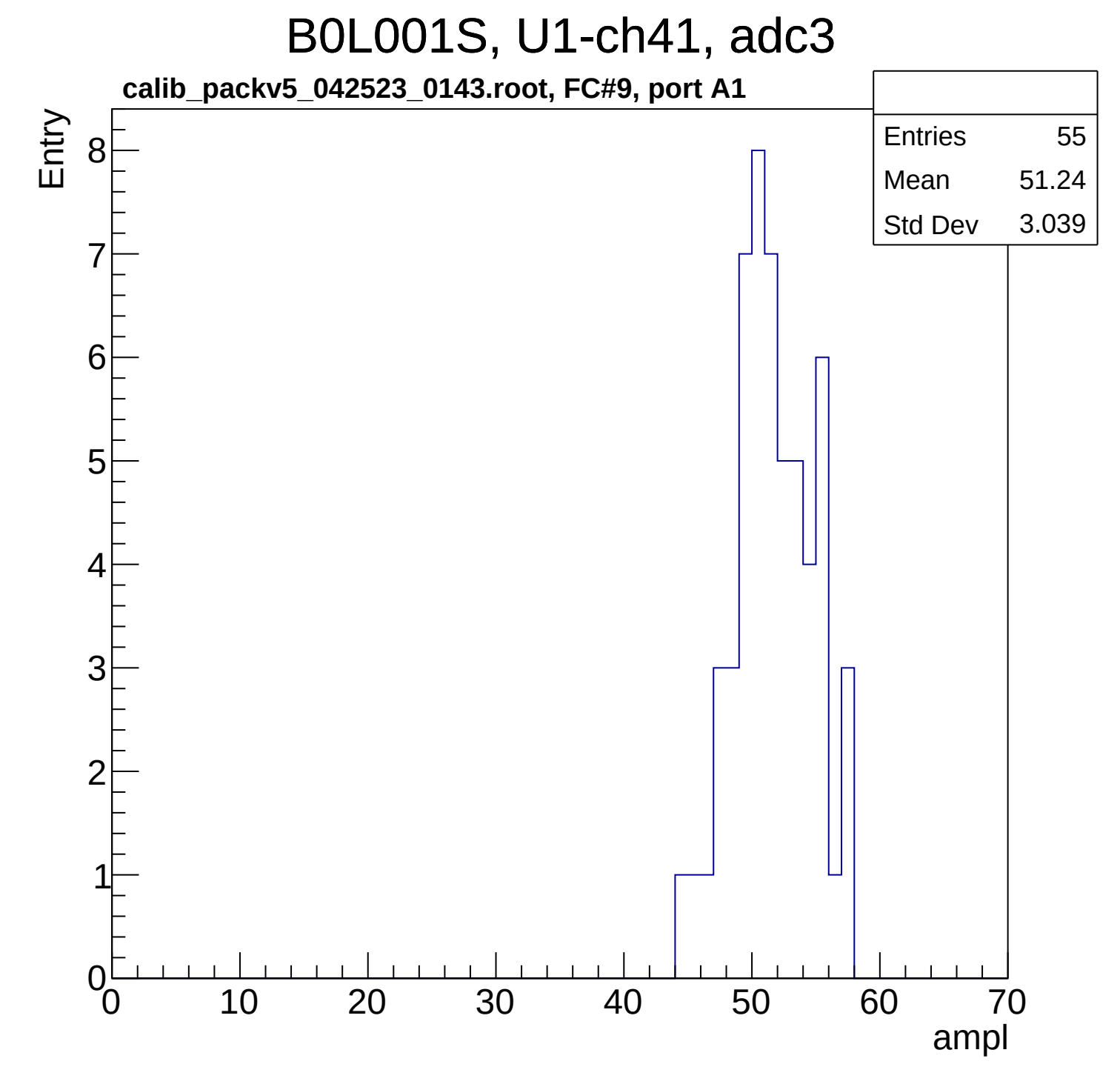

B0L001S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 43.57
Std Dev 3.585
Gaus mean : 44.6515
Gaus Width: 3.4367
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 51.24
Std Dev 3.039
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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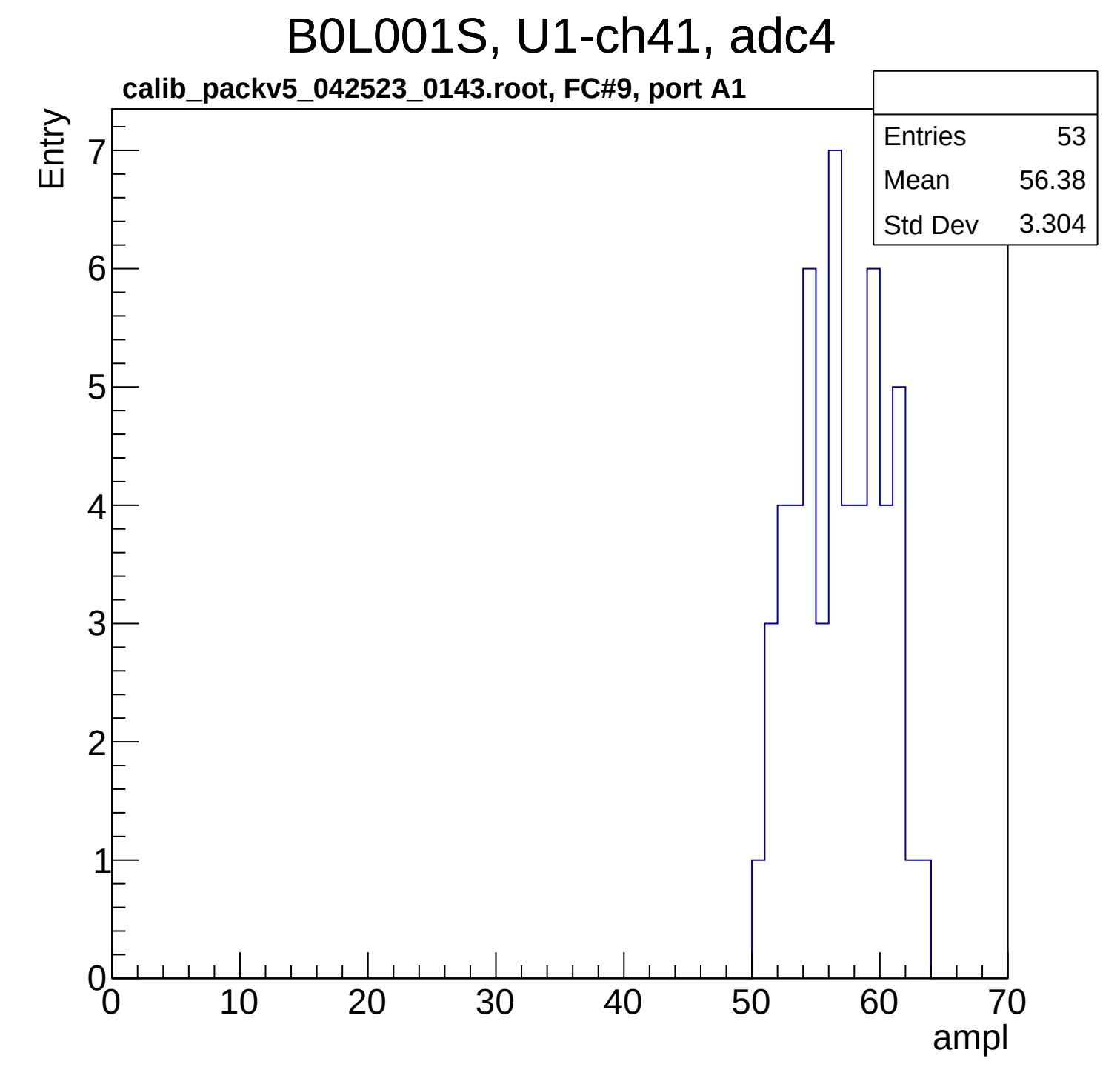
B0L001S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.38
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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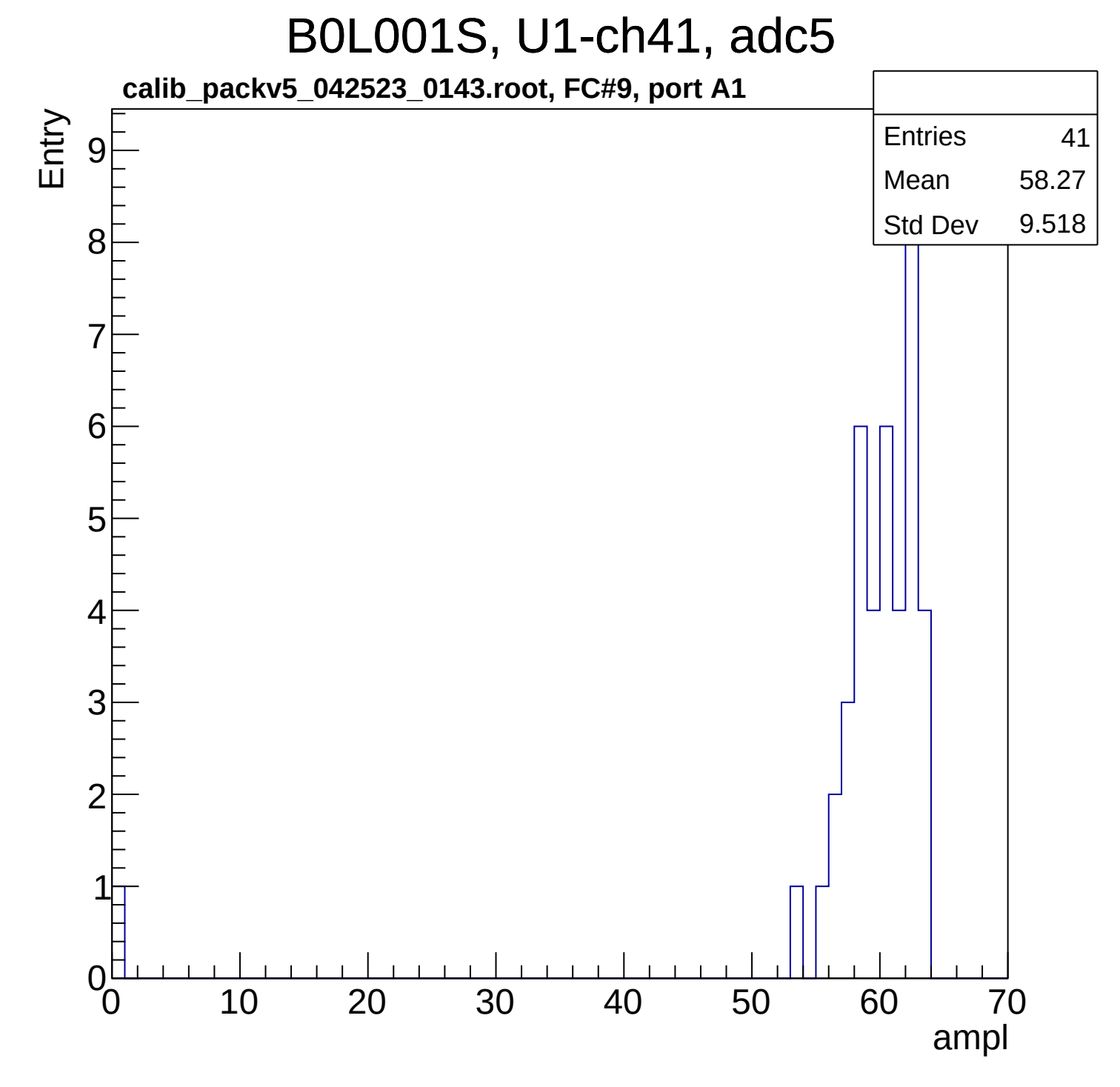
B0L001S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.27
Std Dev 9.518
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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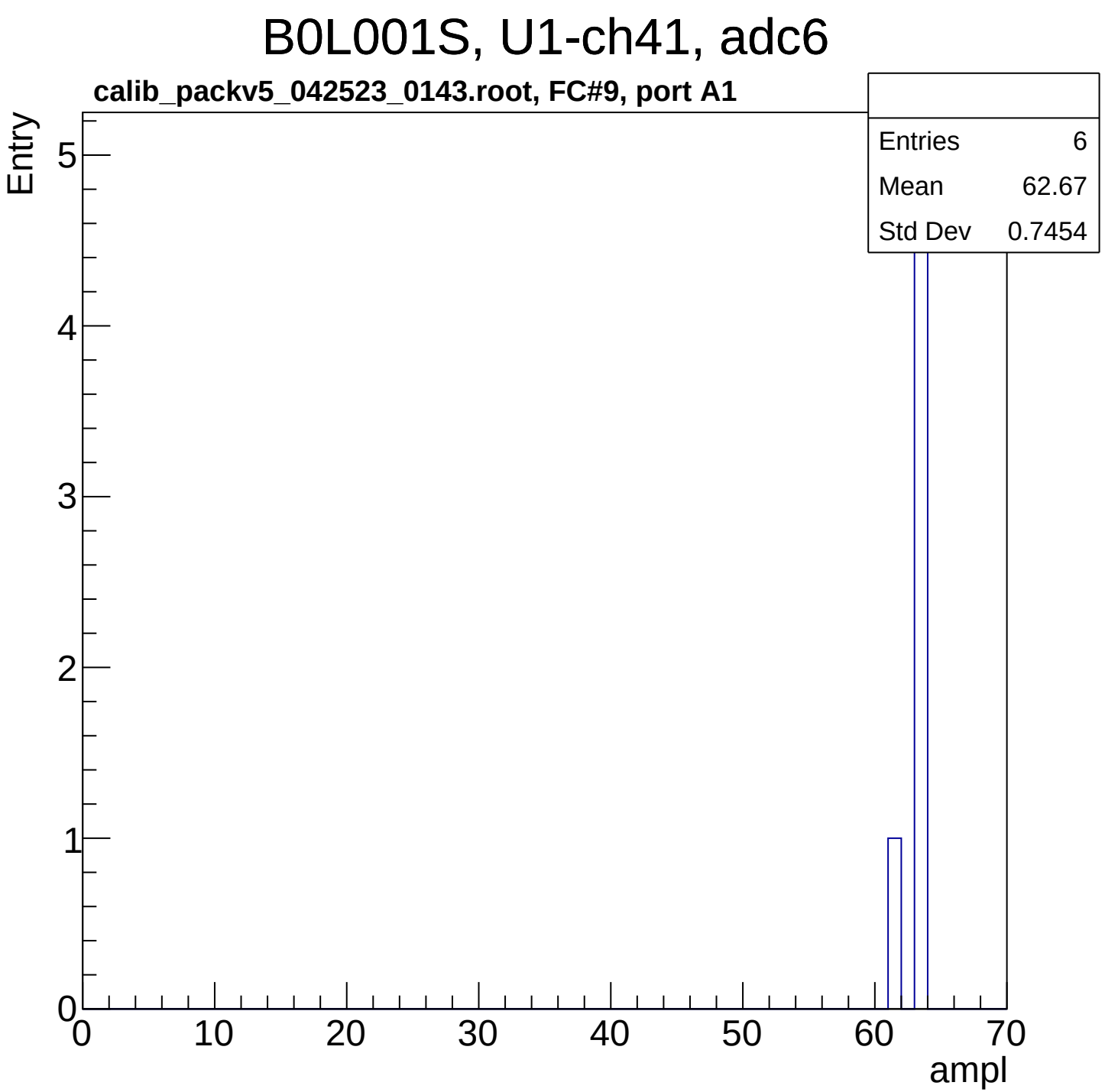
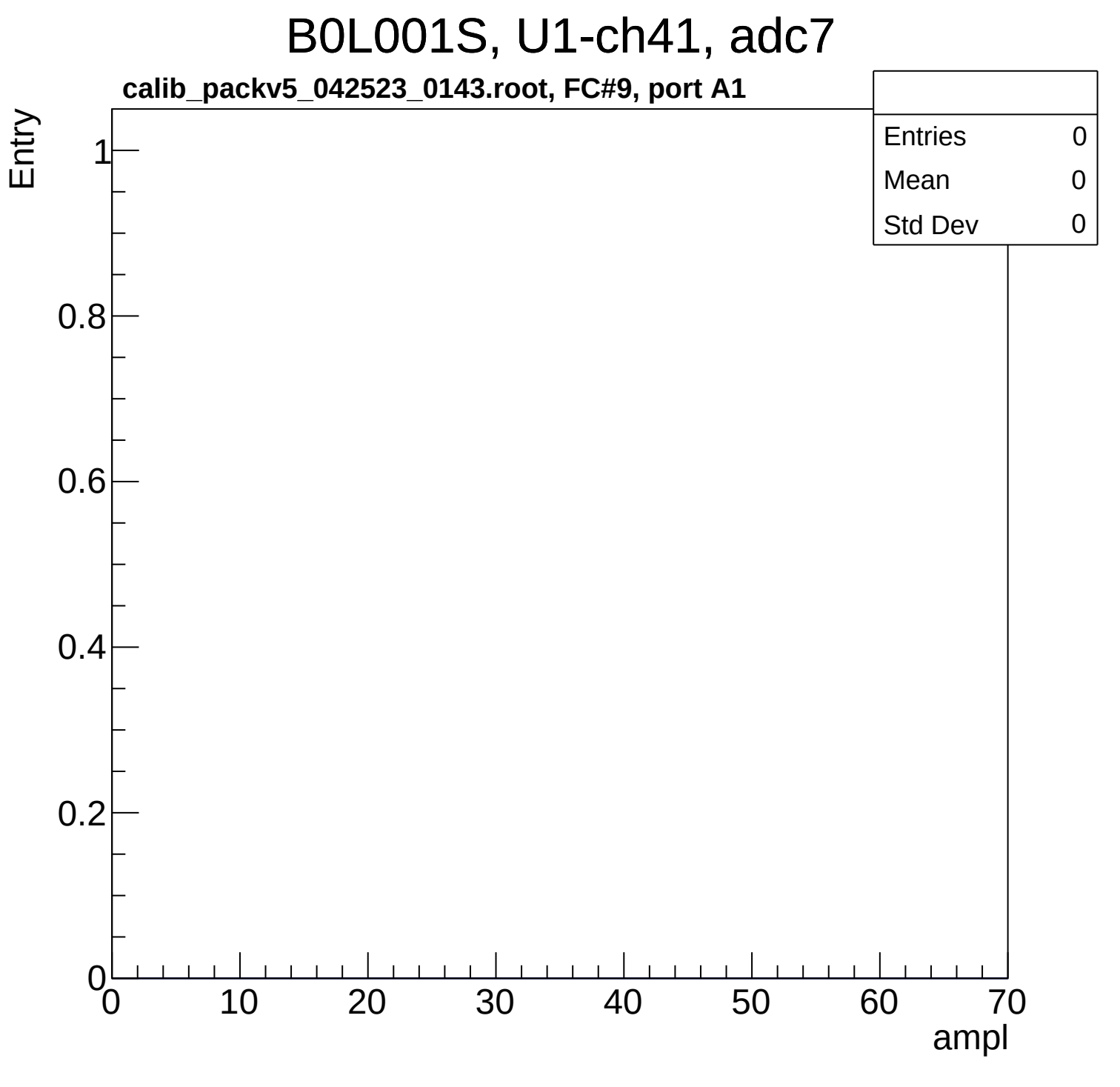

B0L001S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.67
Std Dev 0.7454
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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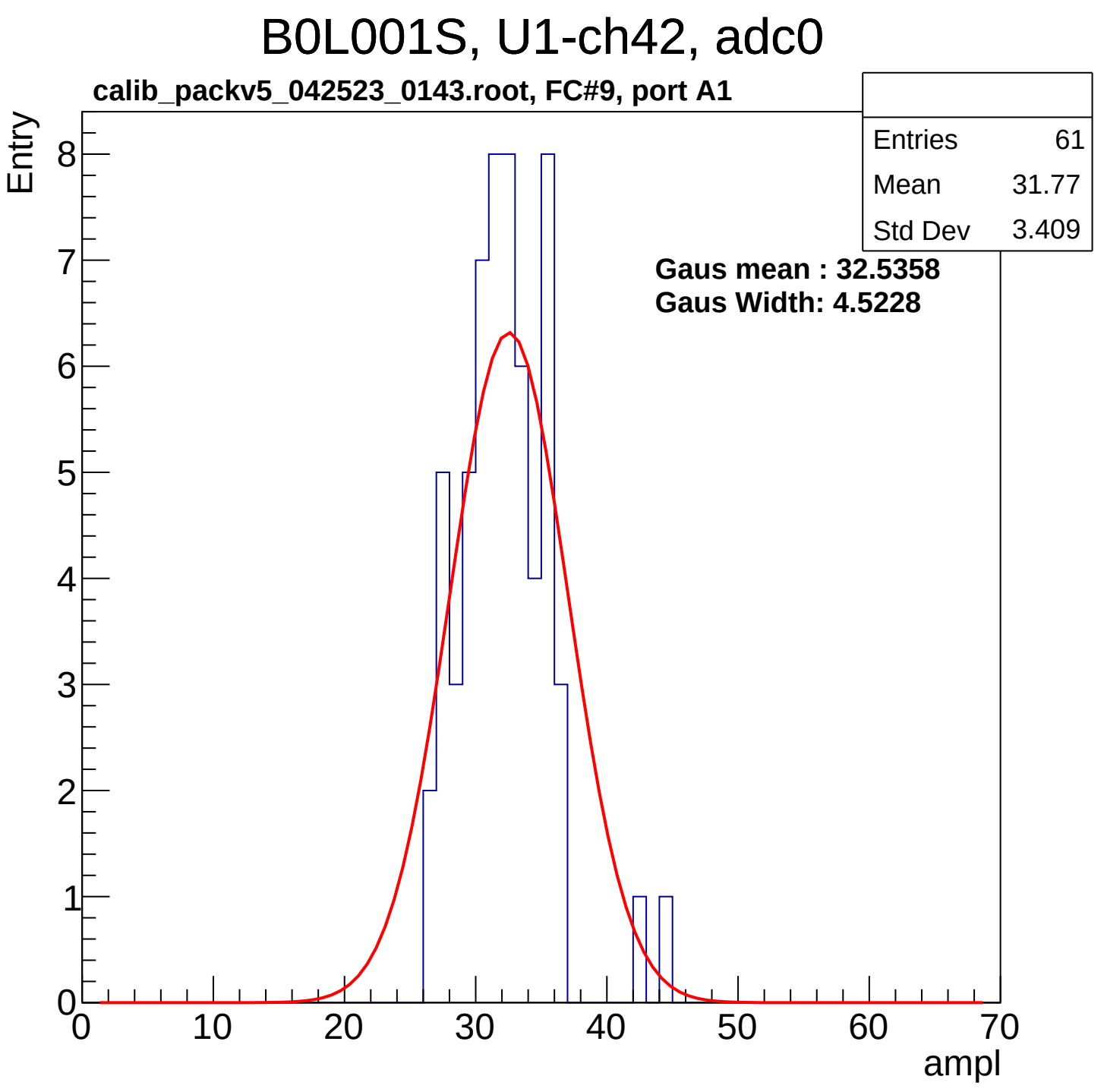
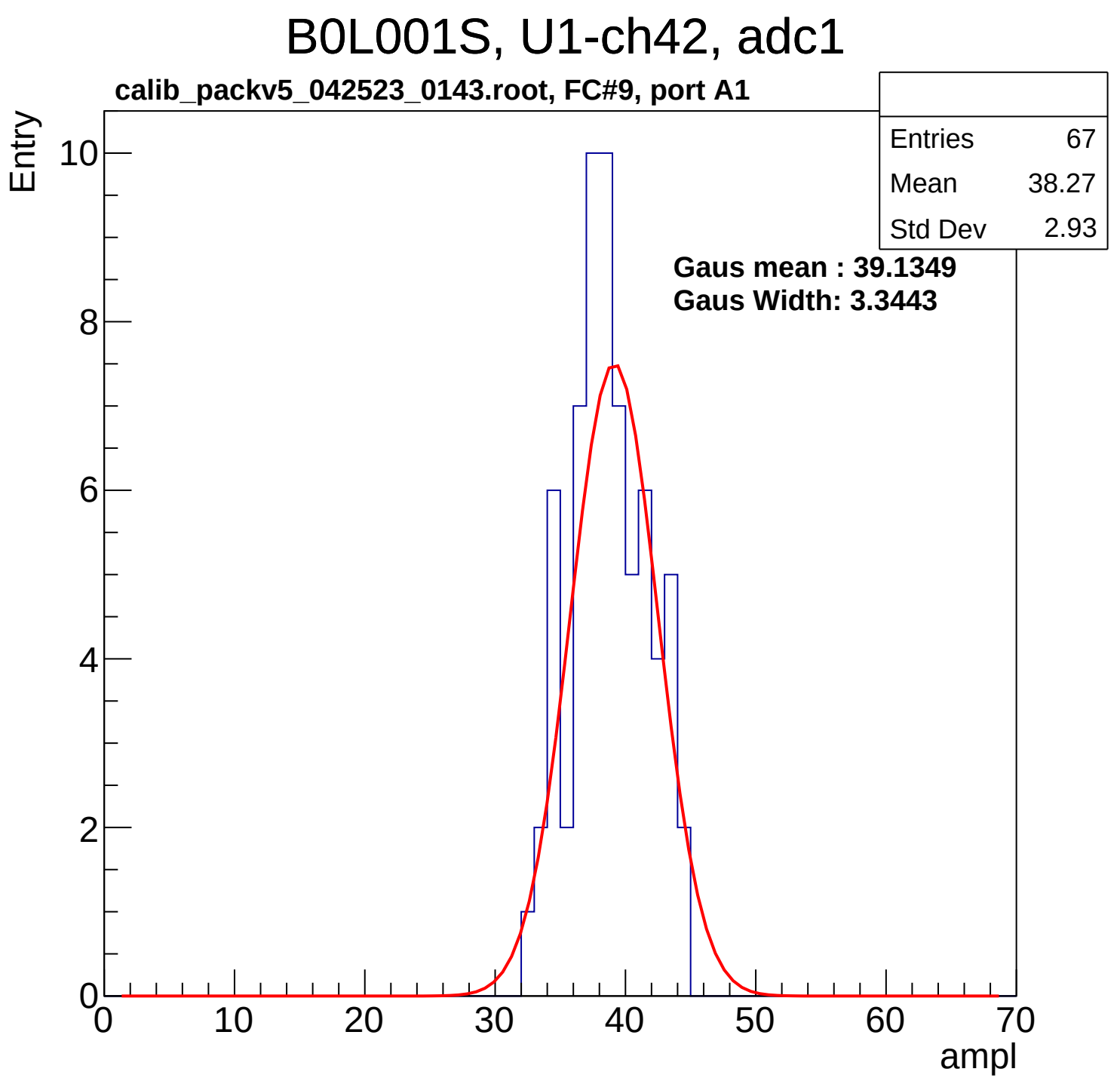
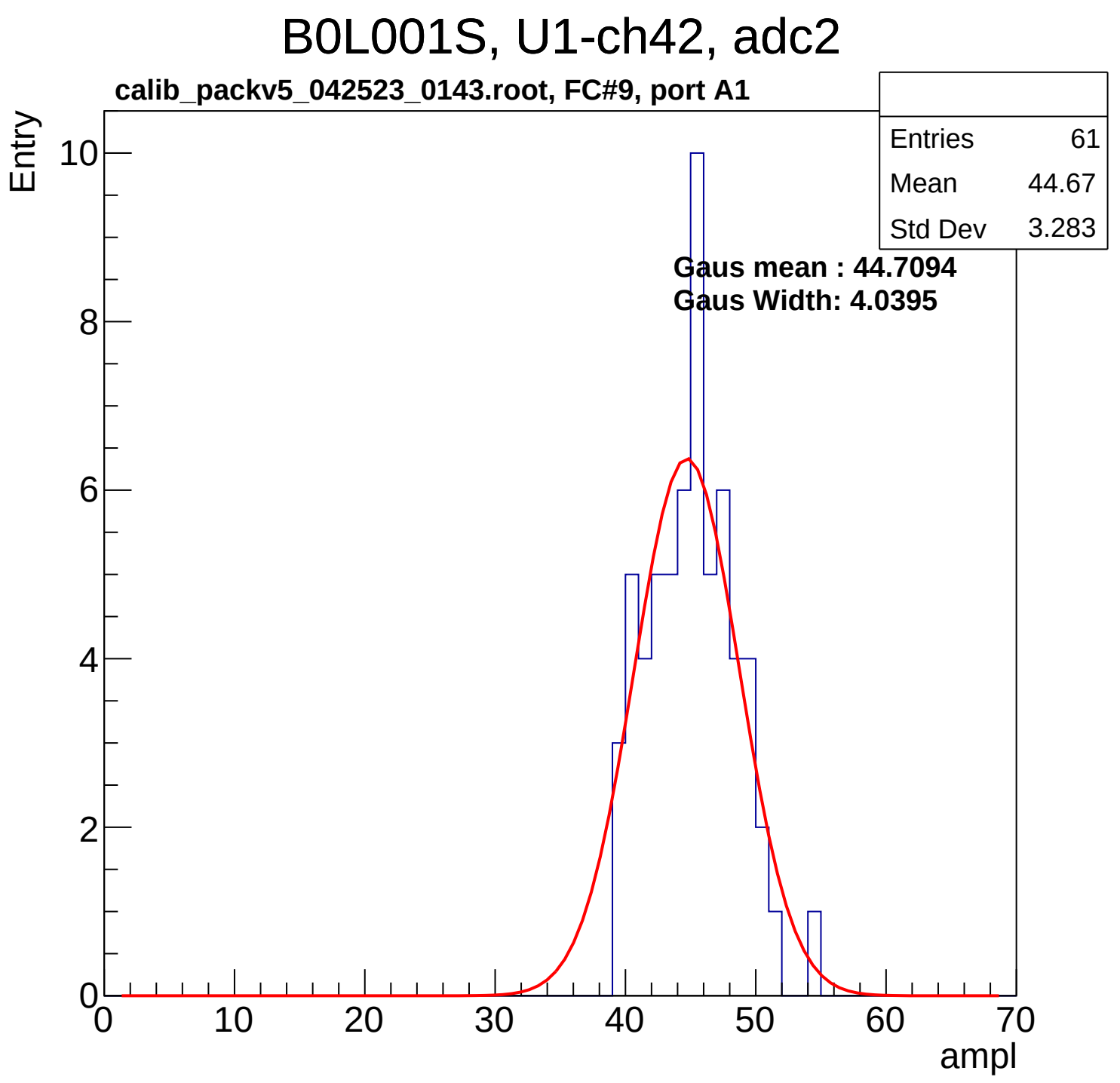
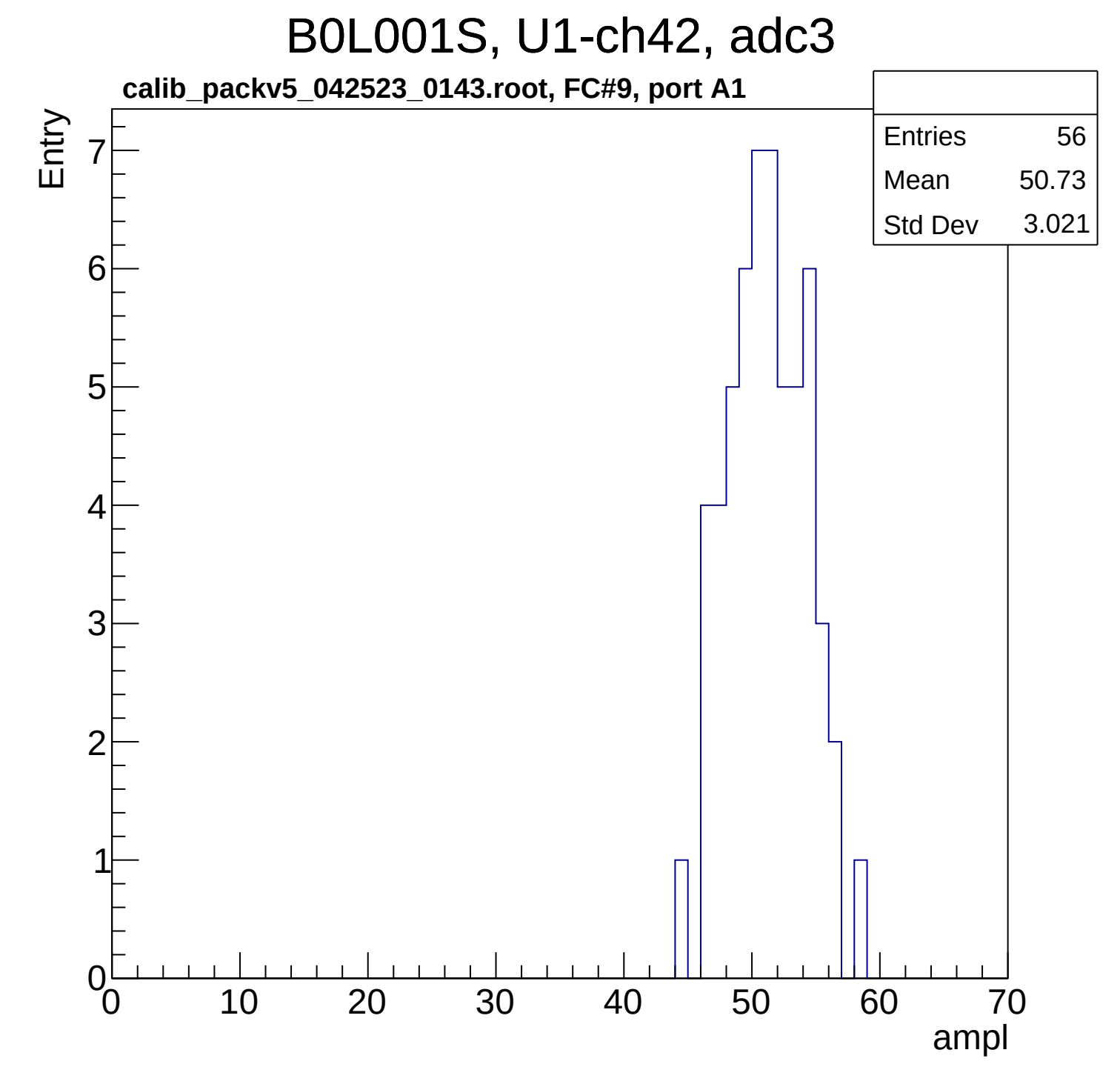
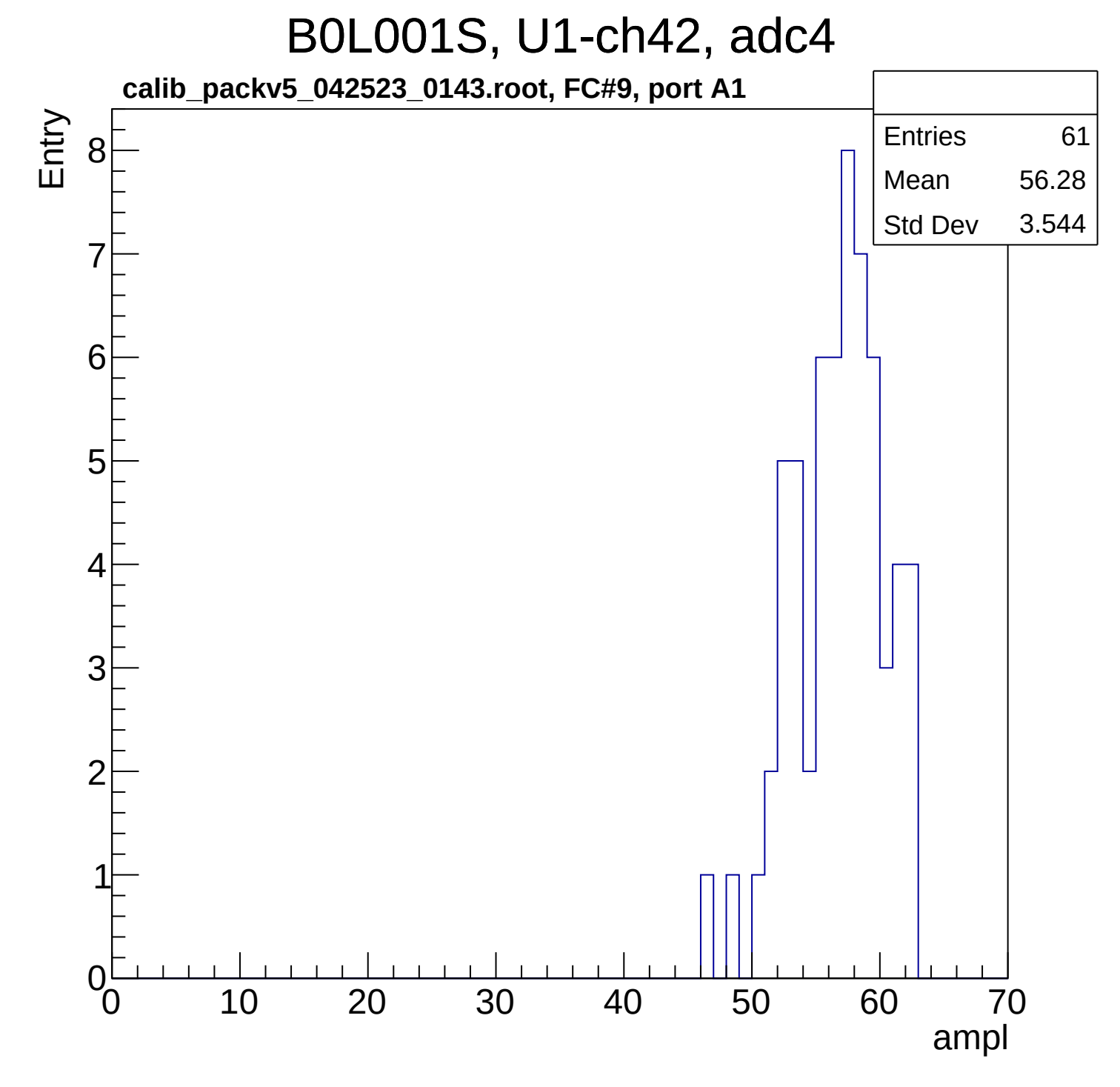
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

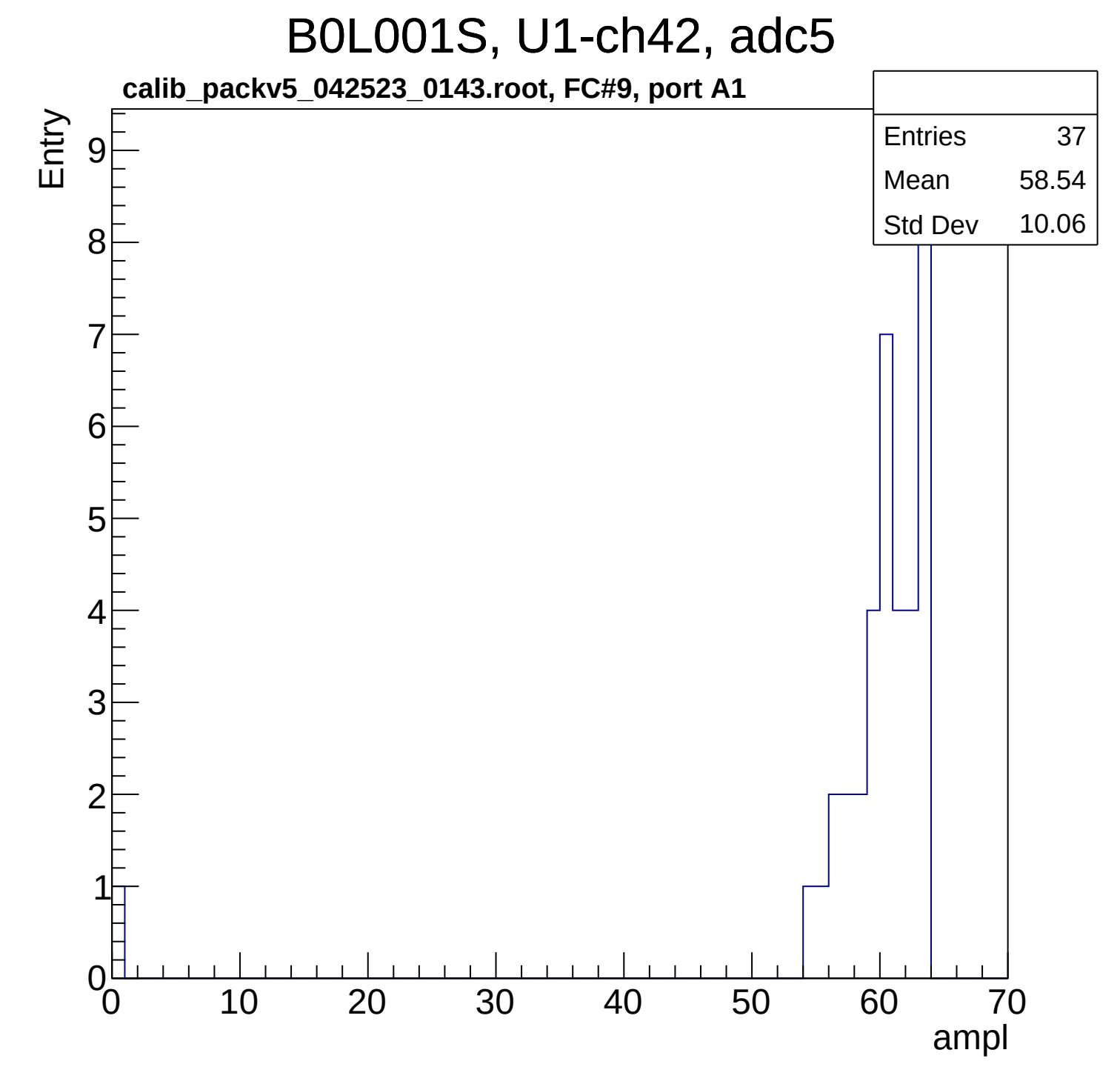

B0L001S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 31.77
Std Dev 3.409
Gaus mean : 32.5358
Gaus Width: 4.5228
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.27
Std Dev 2.93
Gaus mean : 39.1349
Gaus Width: 3.3443
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 44.67
Std Dev 3.283
Gaus mean : 44.7094
Gaus Width: 4.0395
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.73
Std Dev 3.021
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56.28
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 58.54
Std Dev 10.06
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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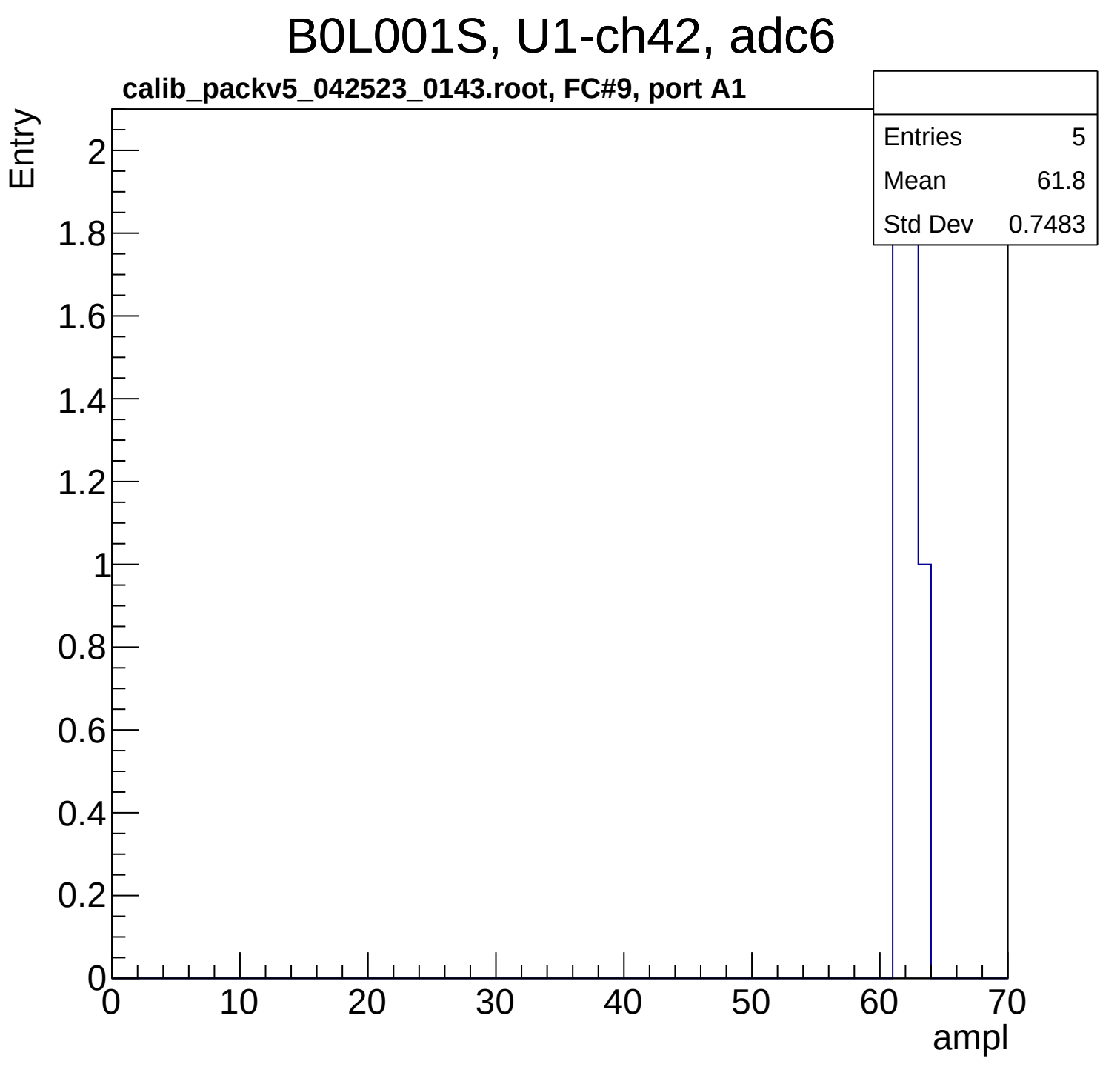
B0L001S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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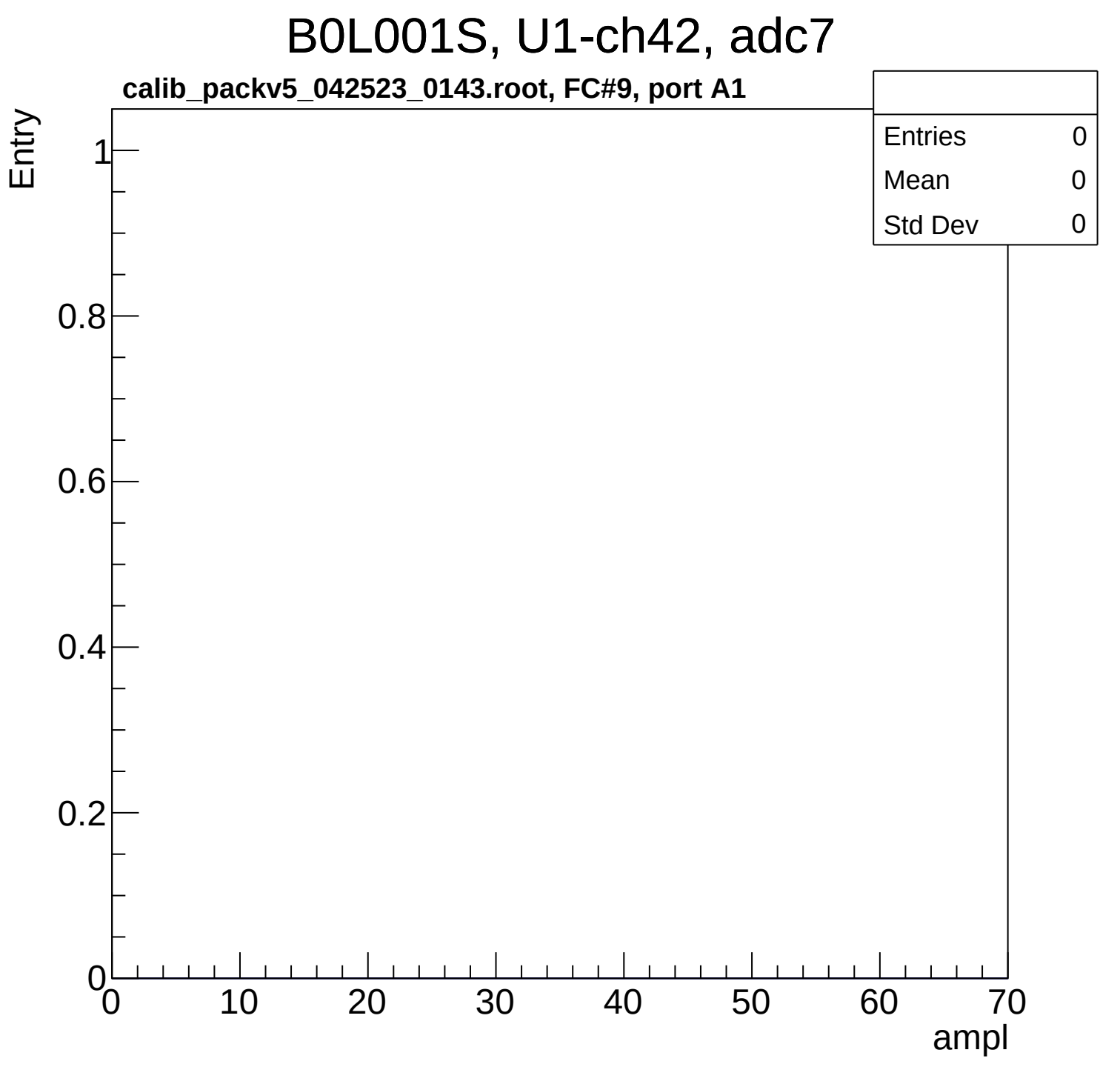
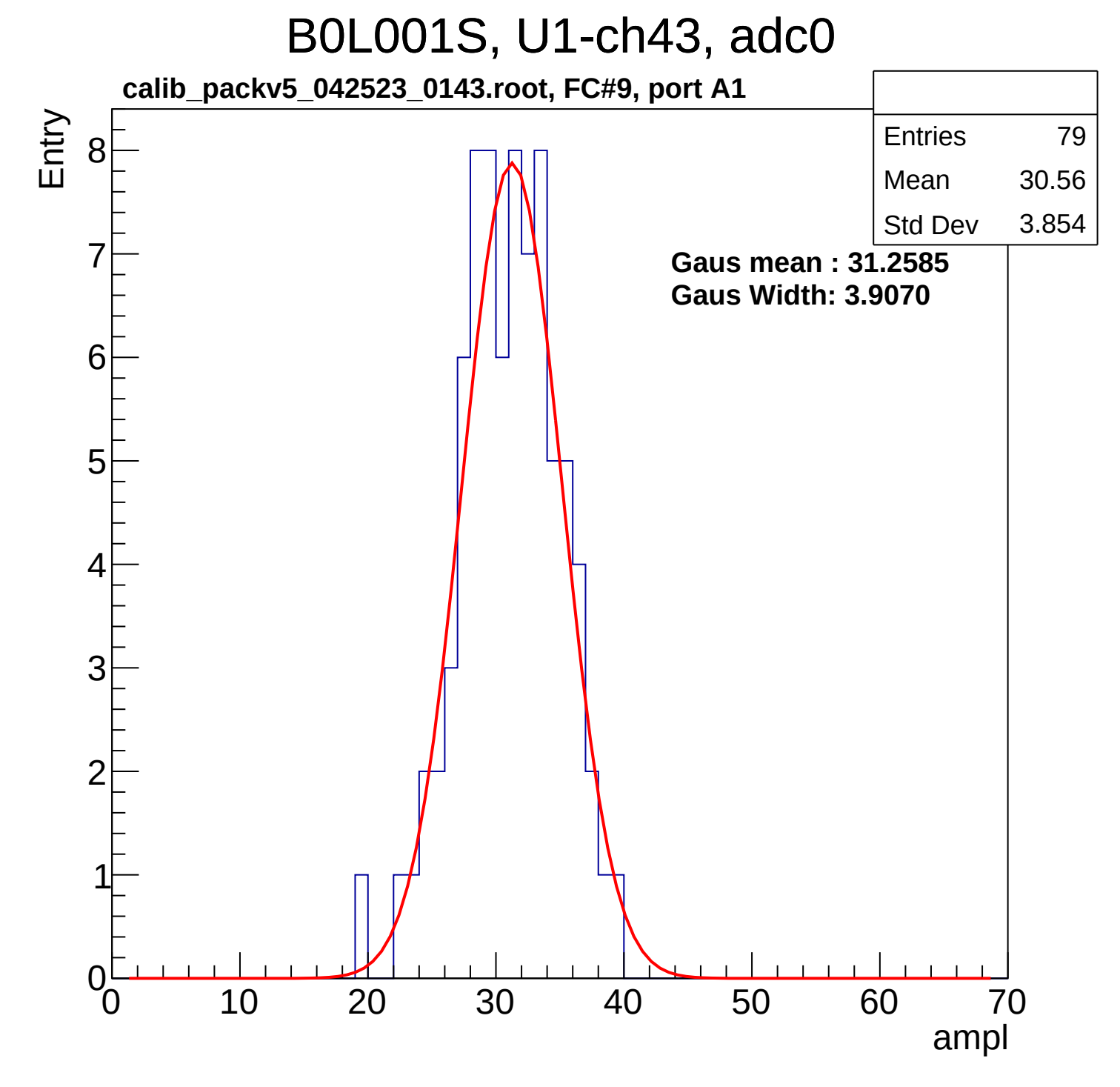
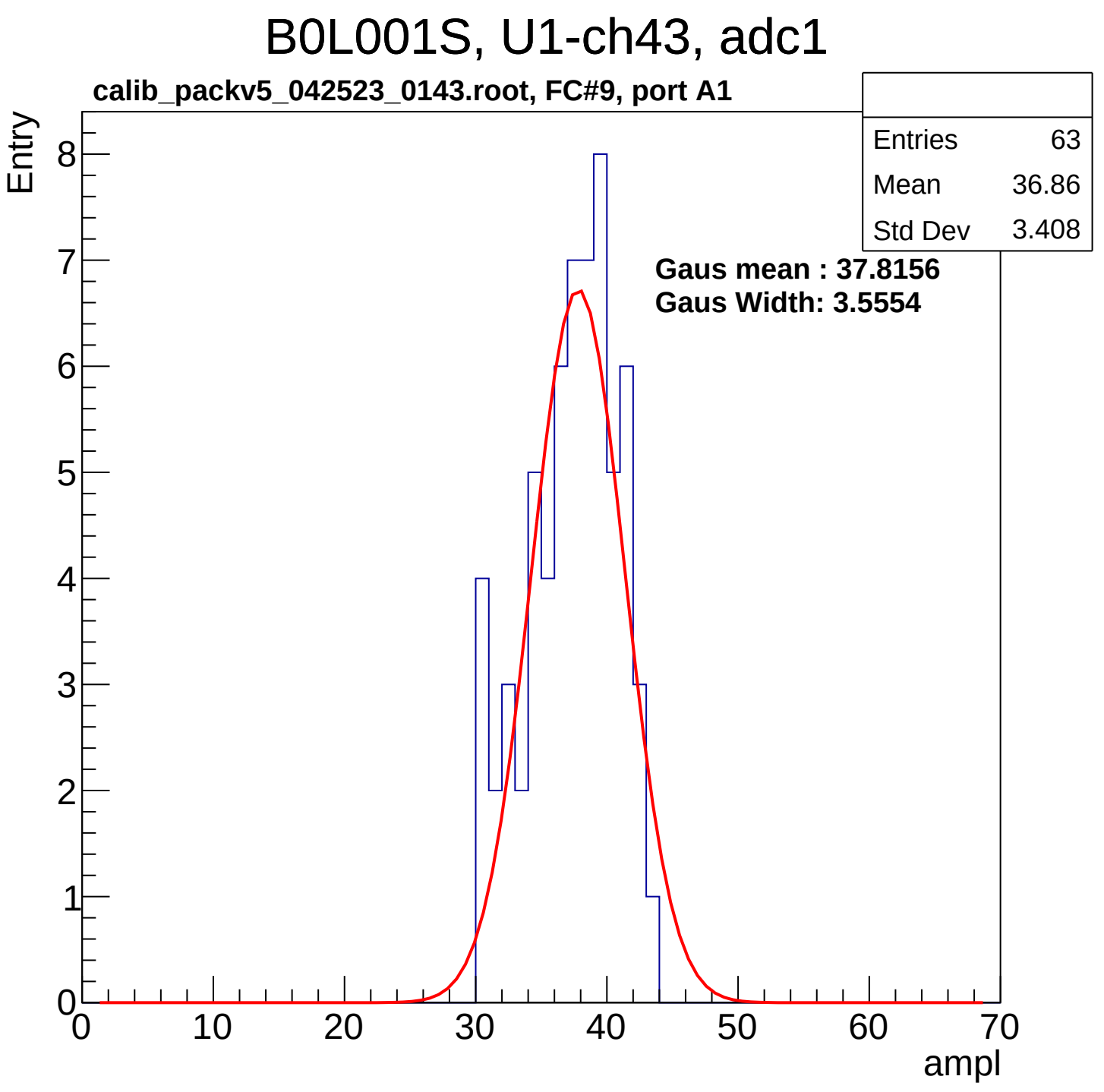
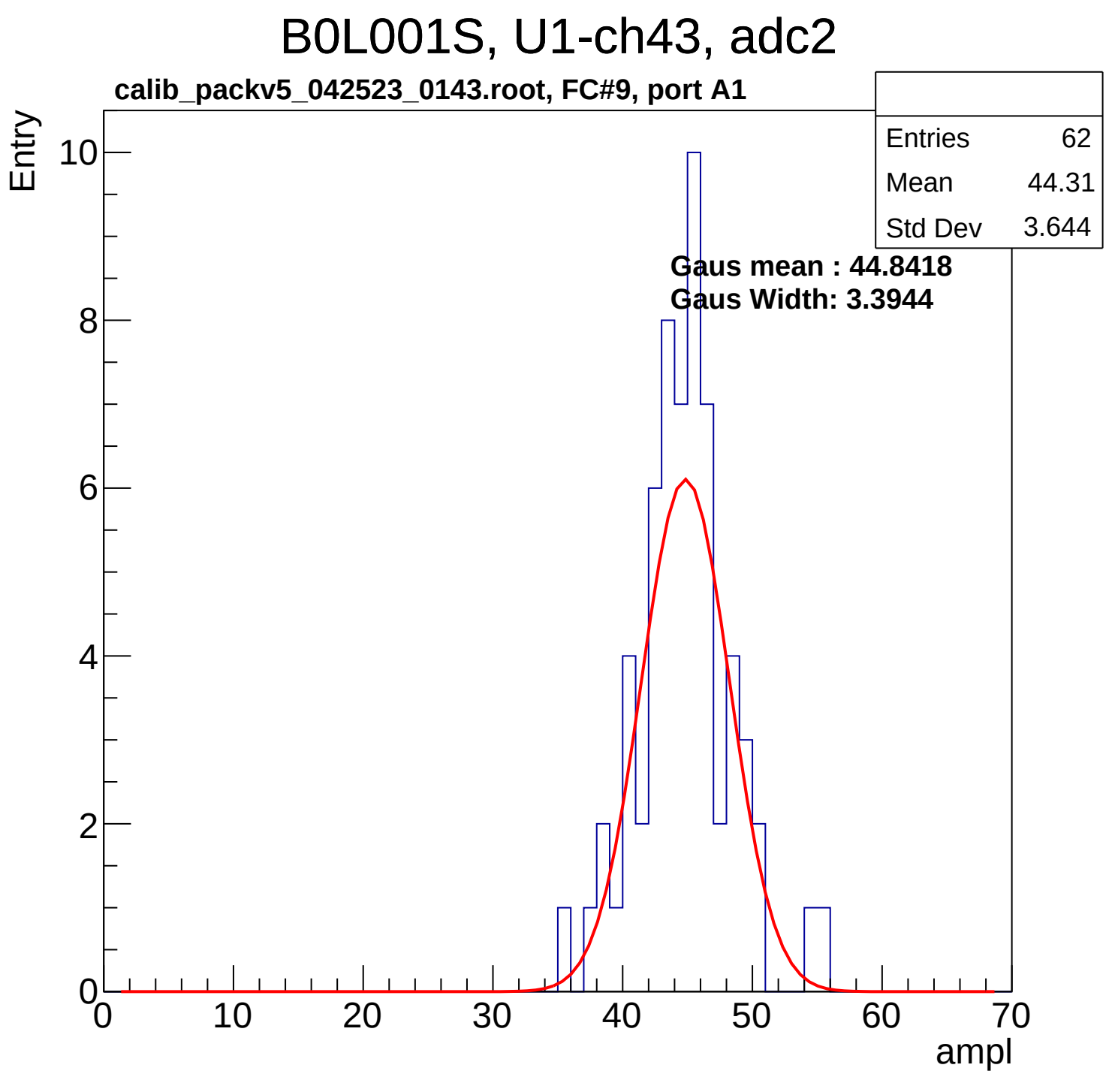
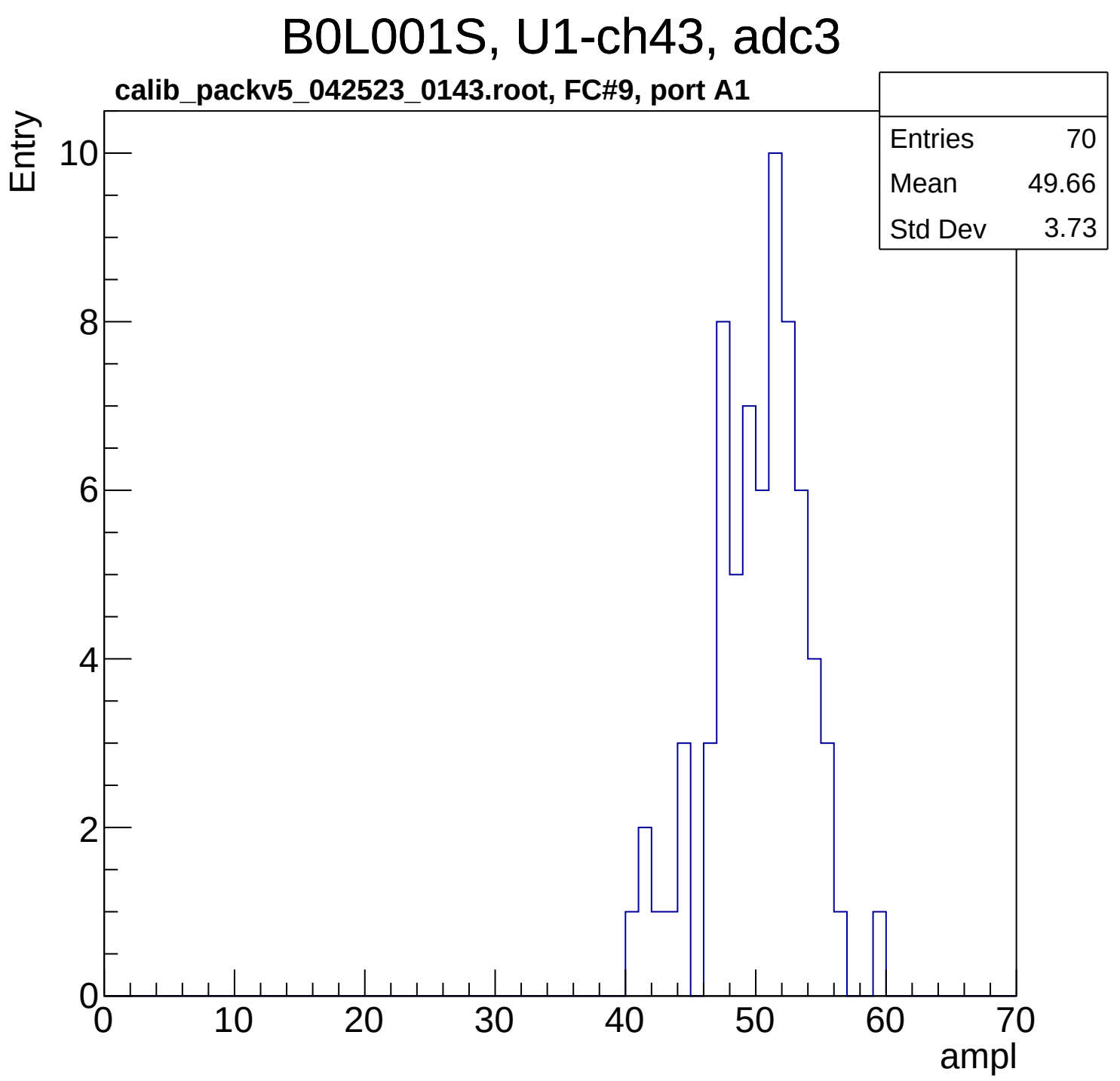
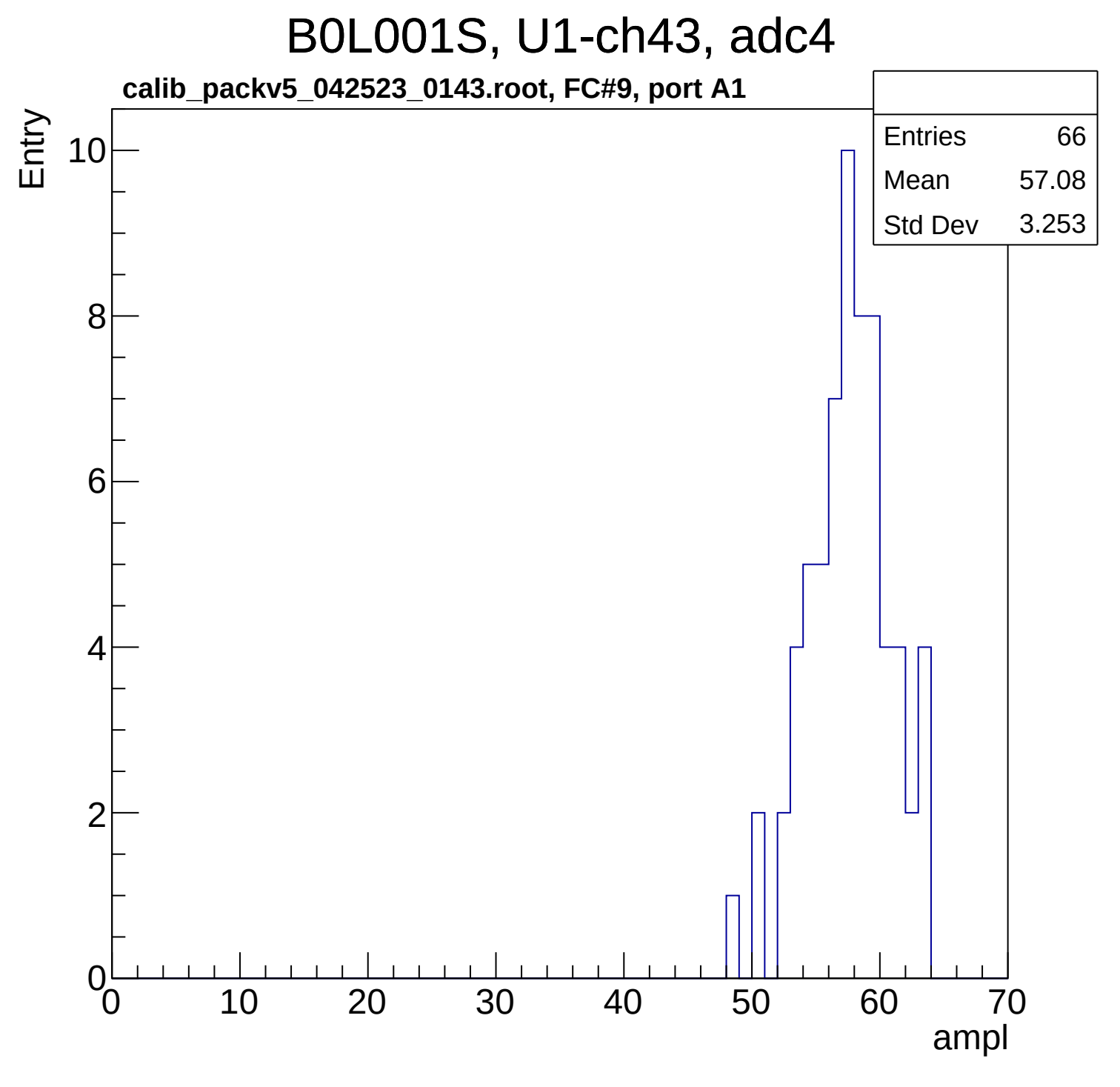

B0L001S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

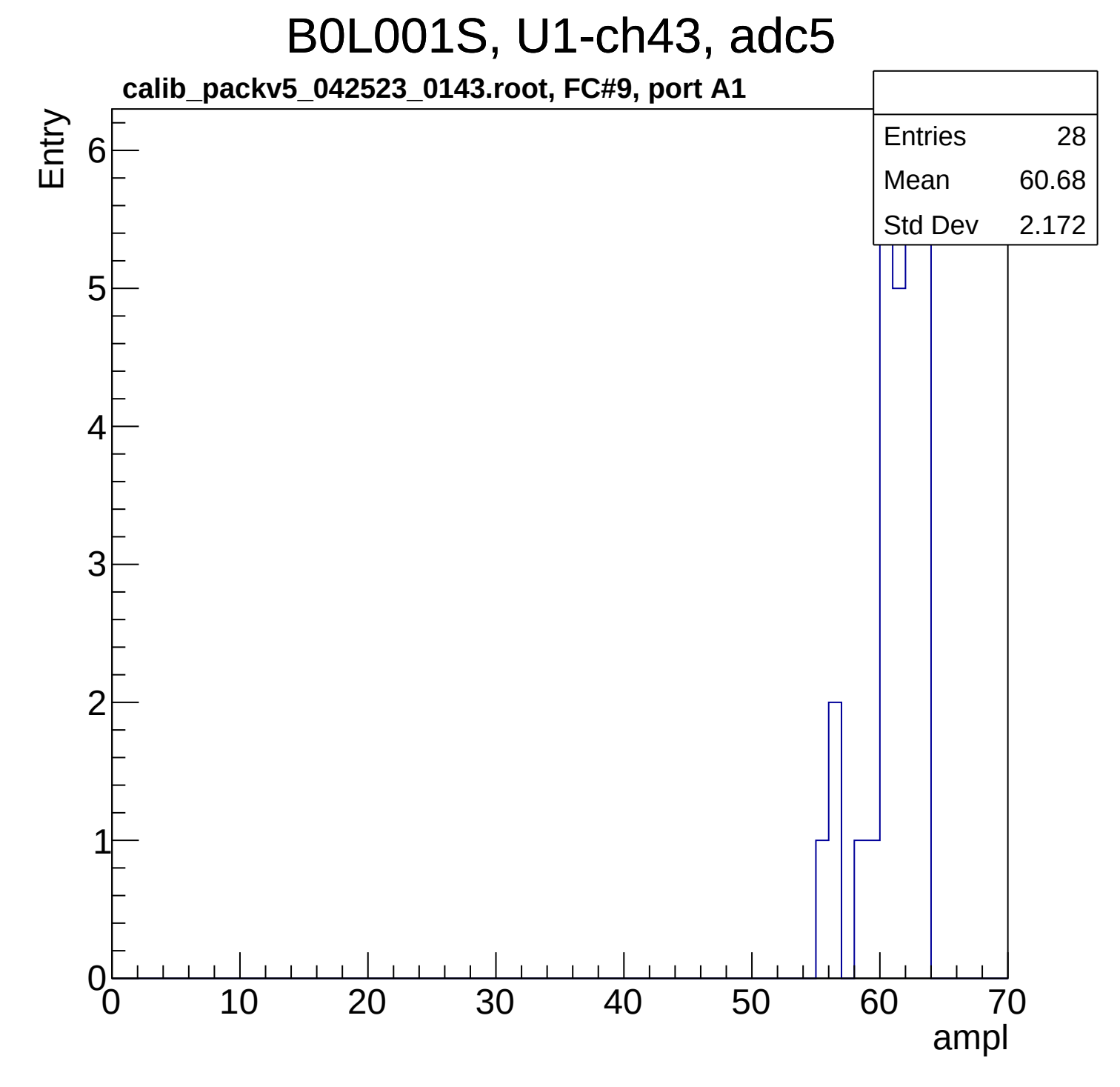

B0L001S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.56
Std Dev 3.854
Gaus mean : 31.2585
Gaus Width: 3.9070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

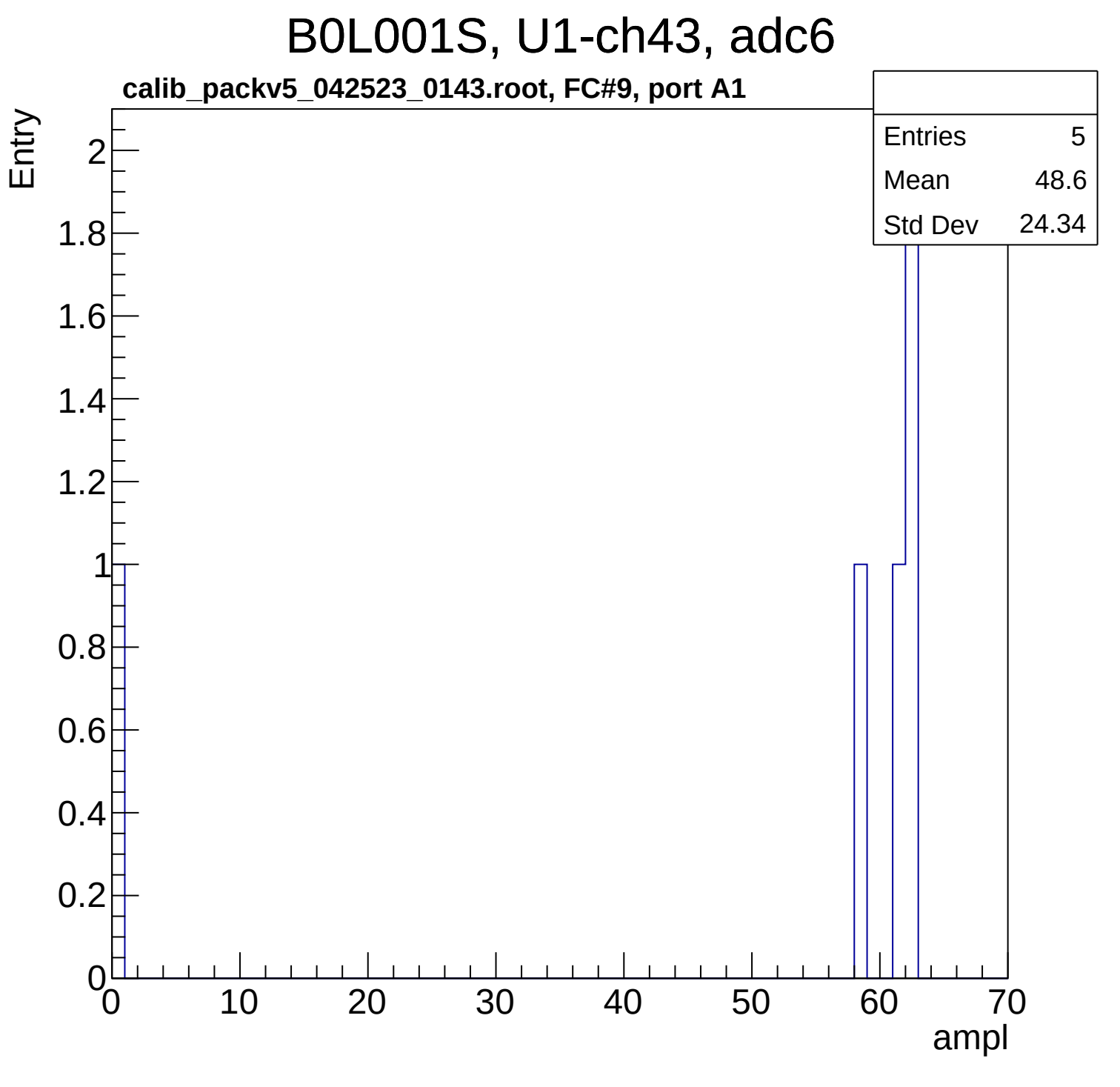

B0L001S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.86
Std Dev 3.408
Gaus mean : 37.8156
Gaus Width: 3.5554
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

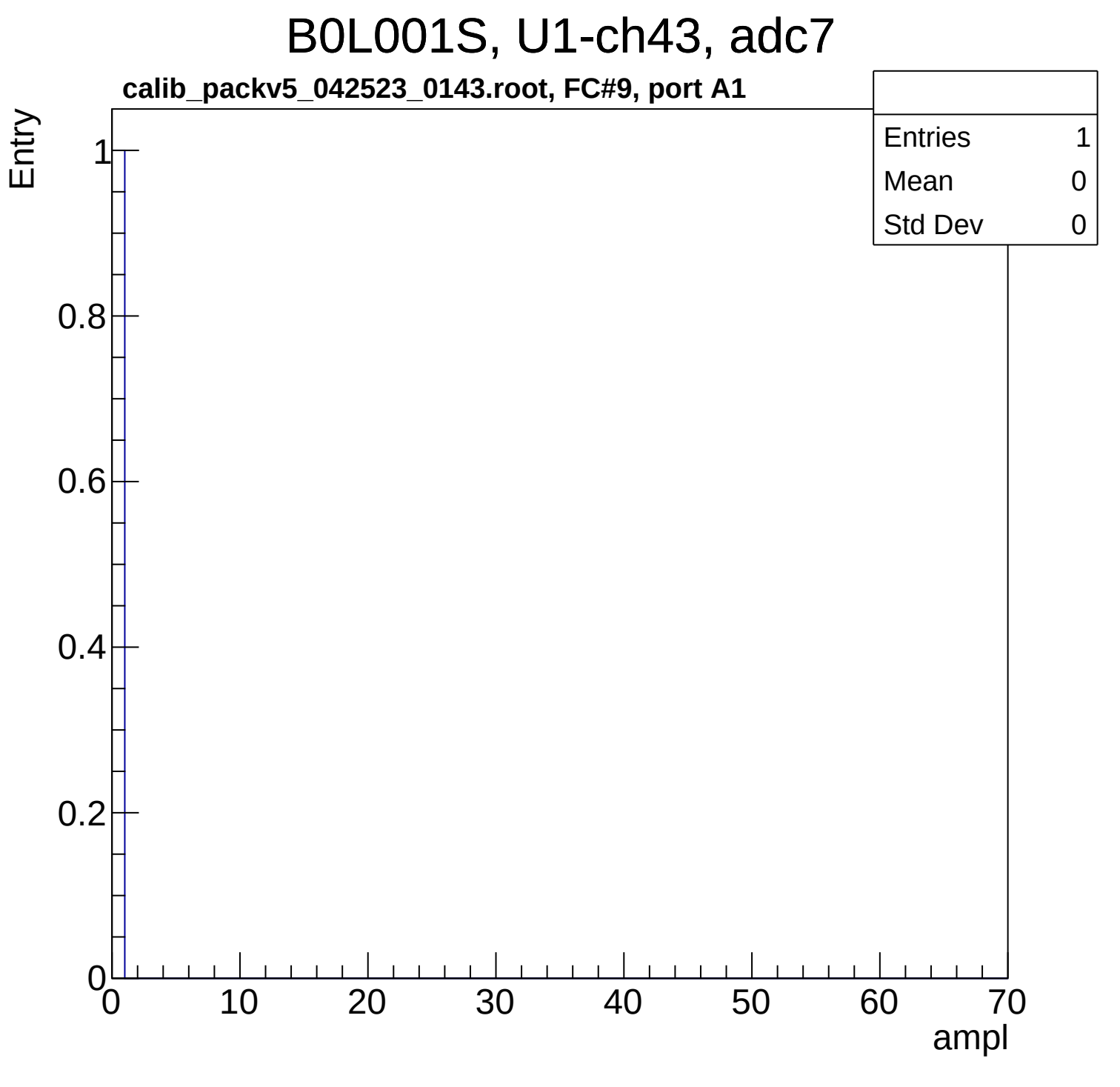

B0L001S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 44.31
Std Dev 3.644
Gaus mean : 44.8418
Gaus Width: 3.3944
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

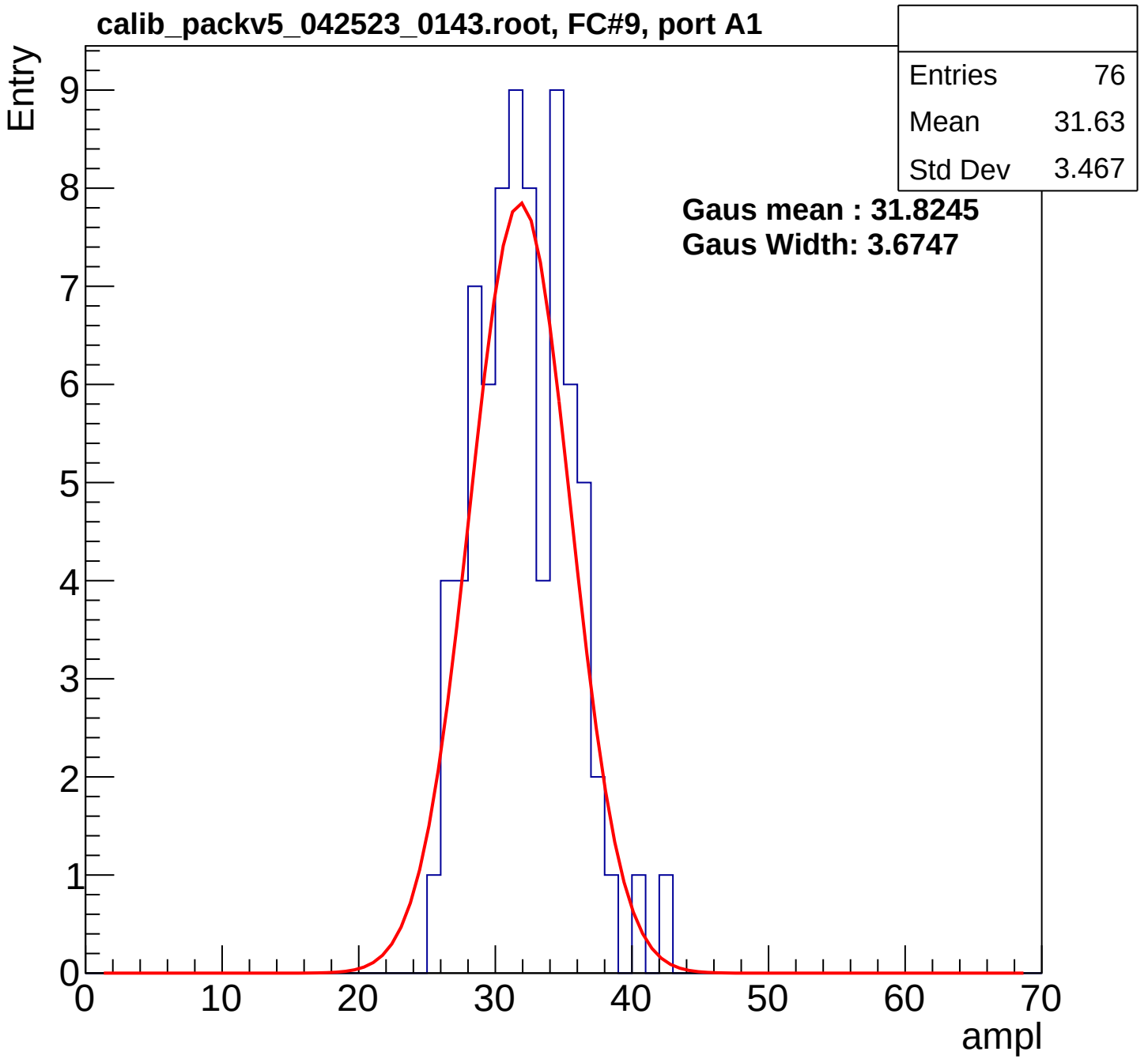

B0L001S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 49.66
Std Dev 3.73
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

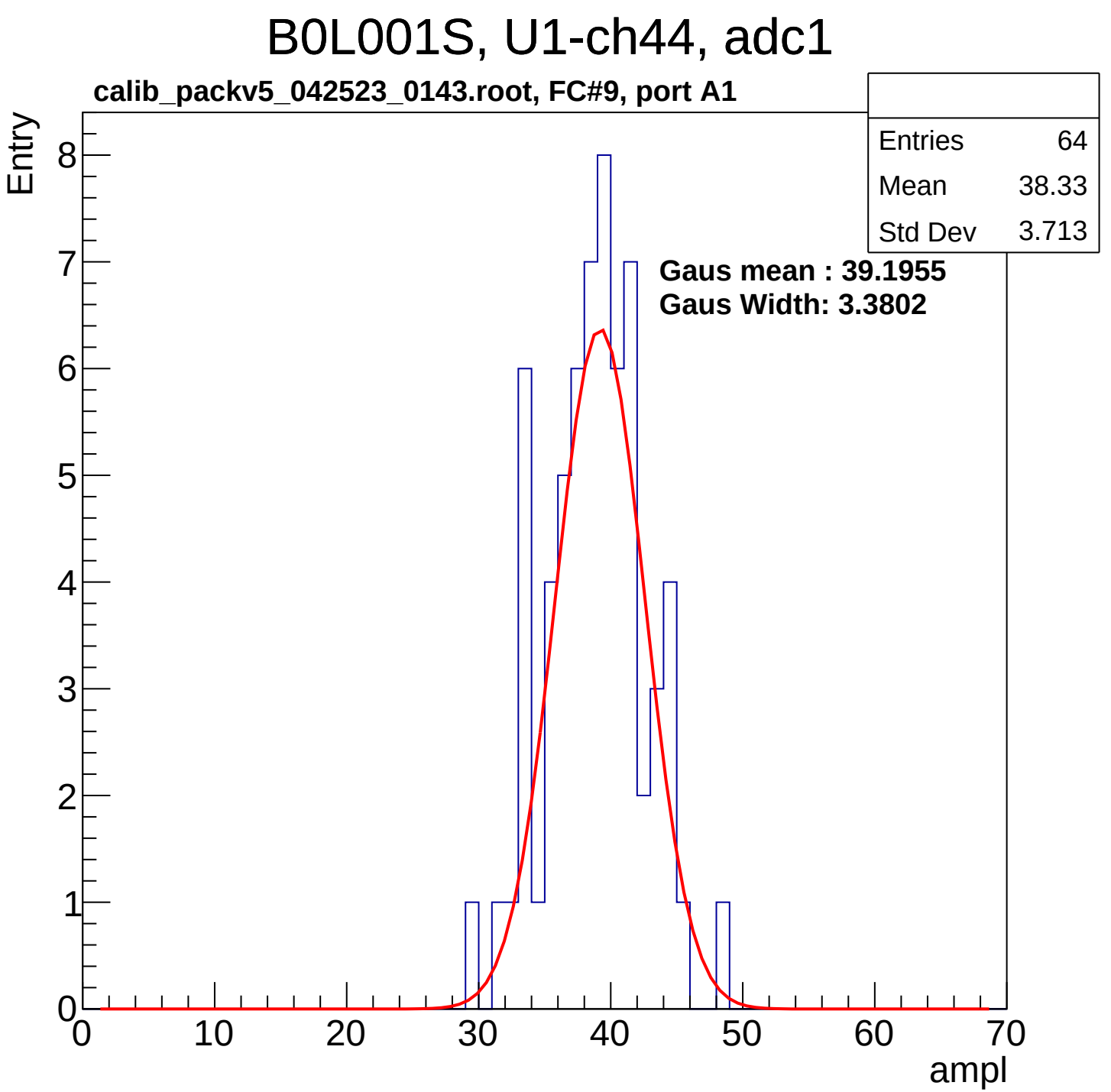

B0L001S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.08
Std Dev 3.253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

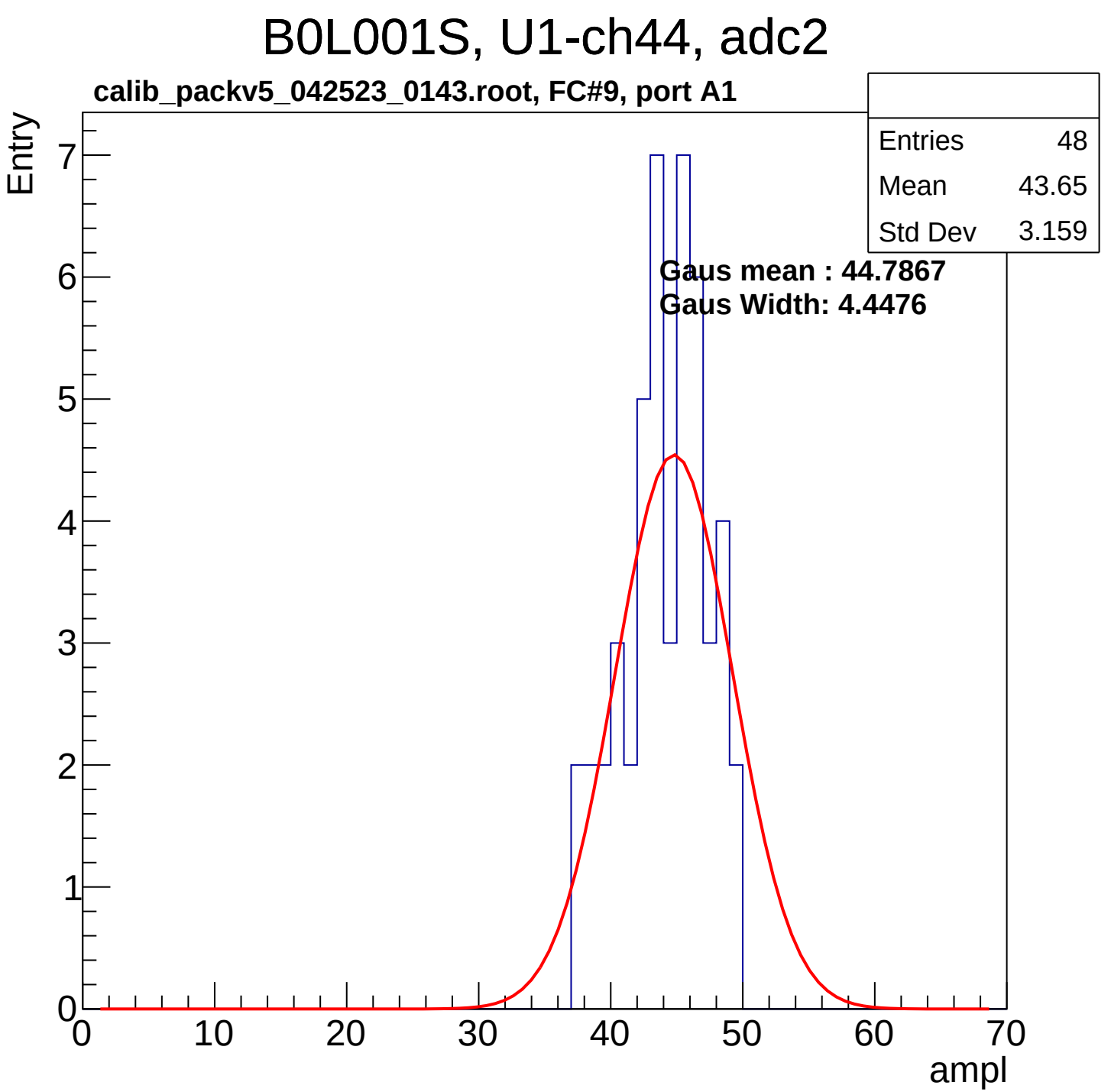

B0L001S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 60.68
Std Dev 2.172
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

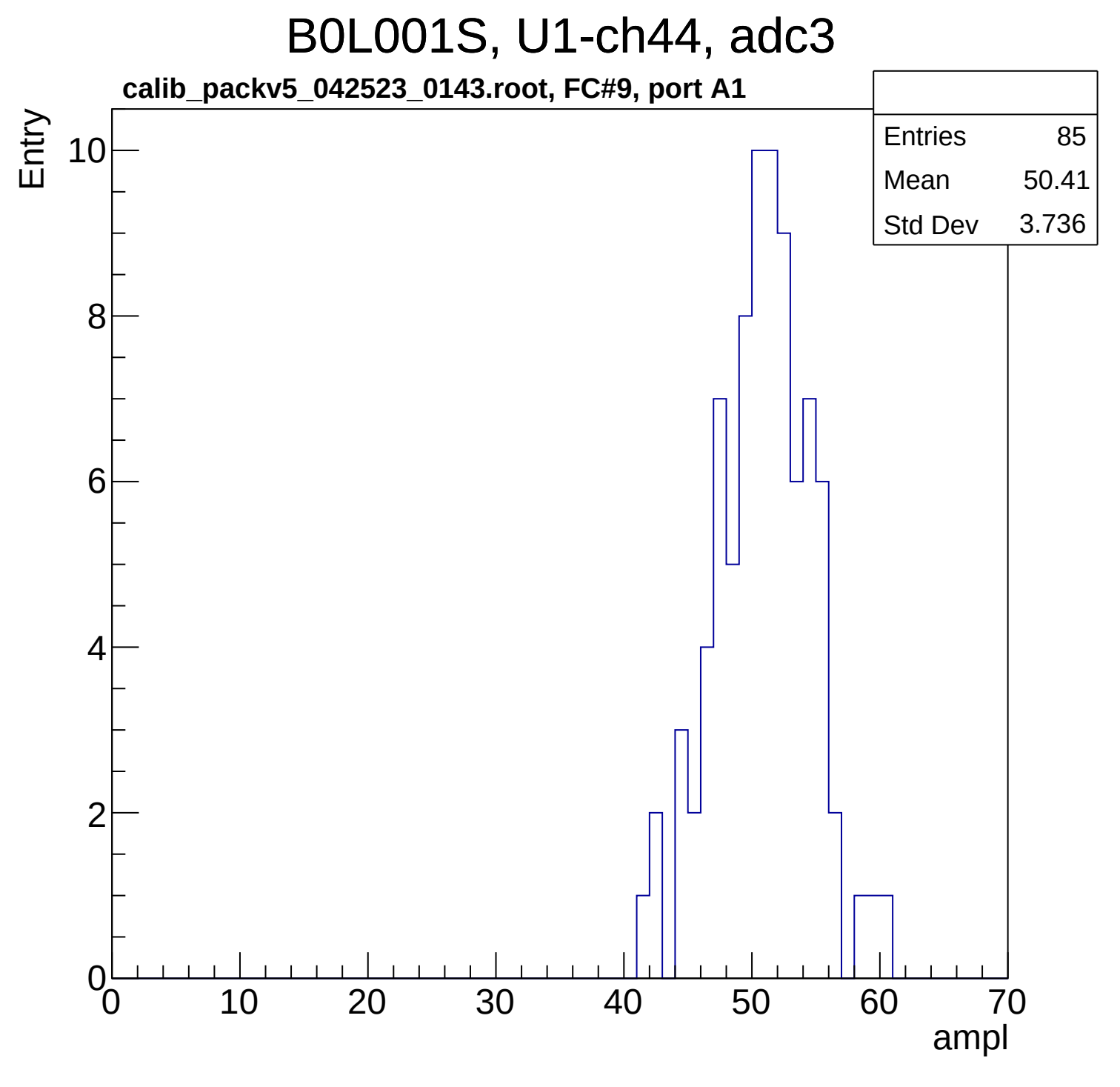

B0L001S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 48.6
Std Dev 24.34
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

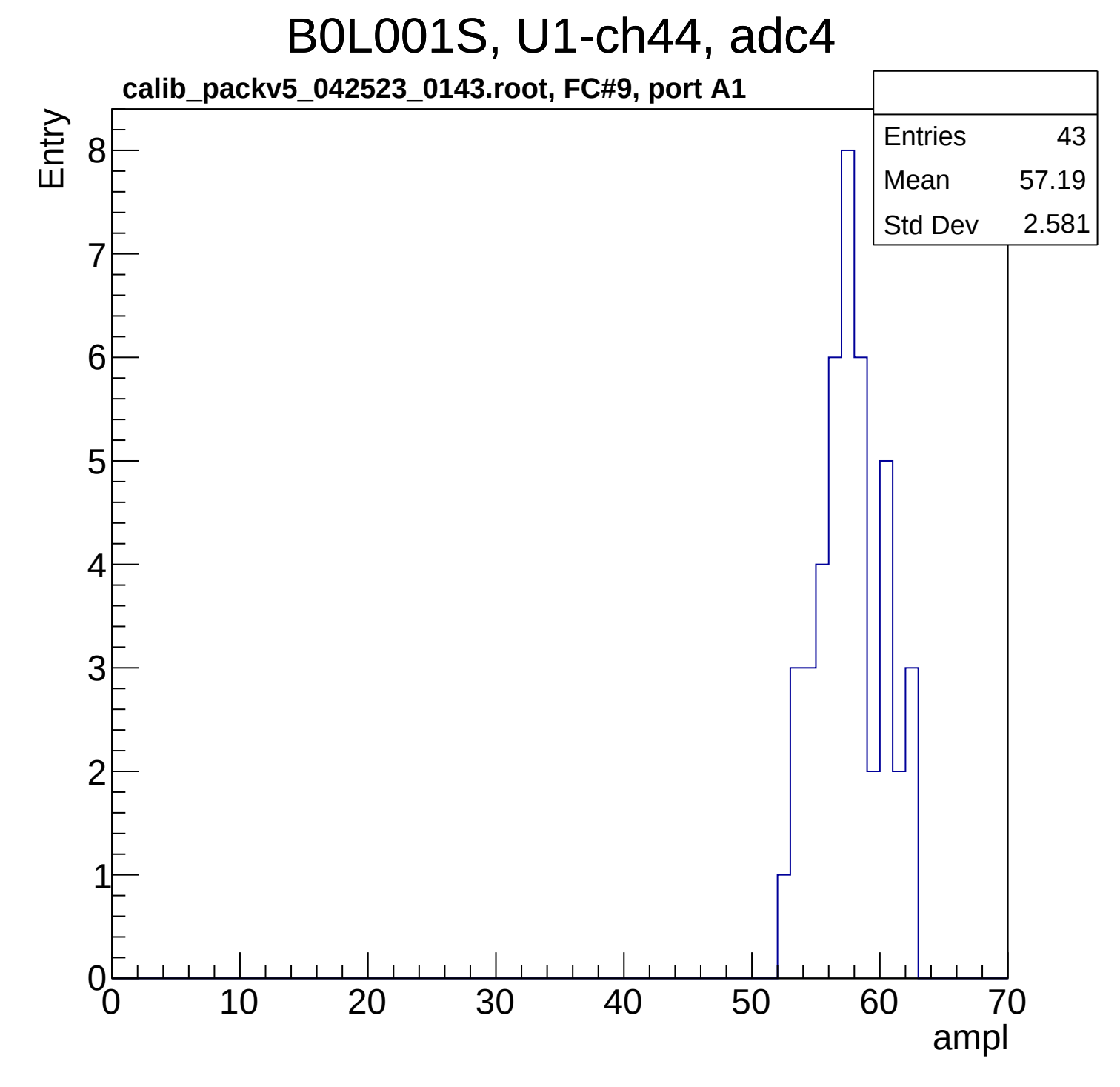

B0L001S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

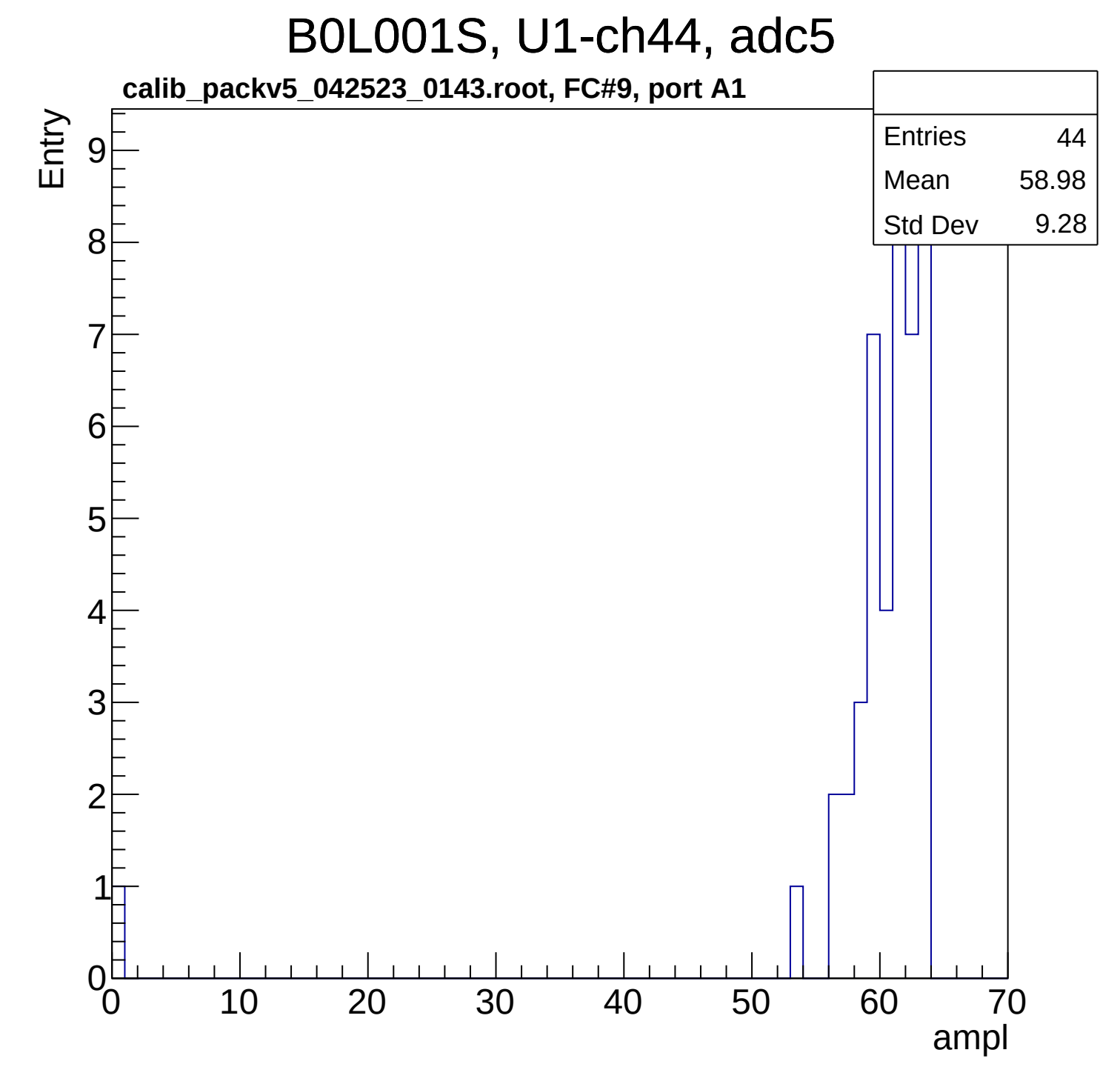

B0L001S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 31.63
Std Dev 3.467
Gaus mean : 31.8245
Gaus Width: 3.6747
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

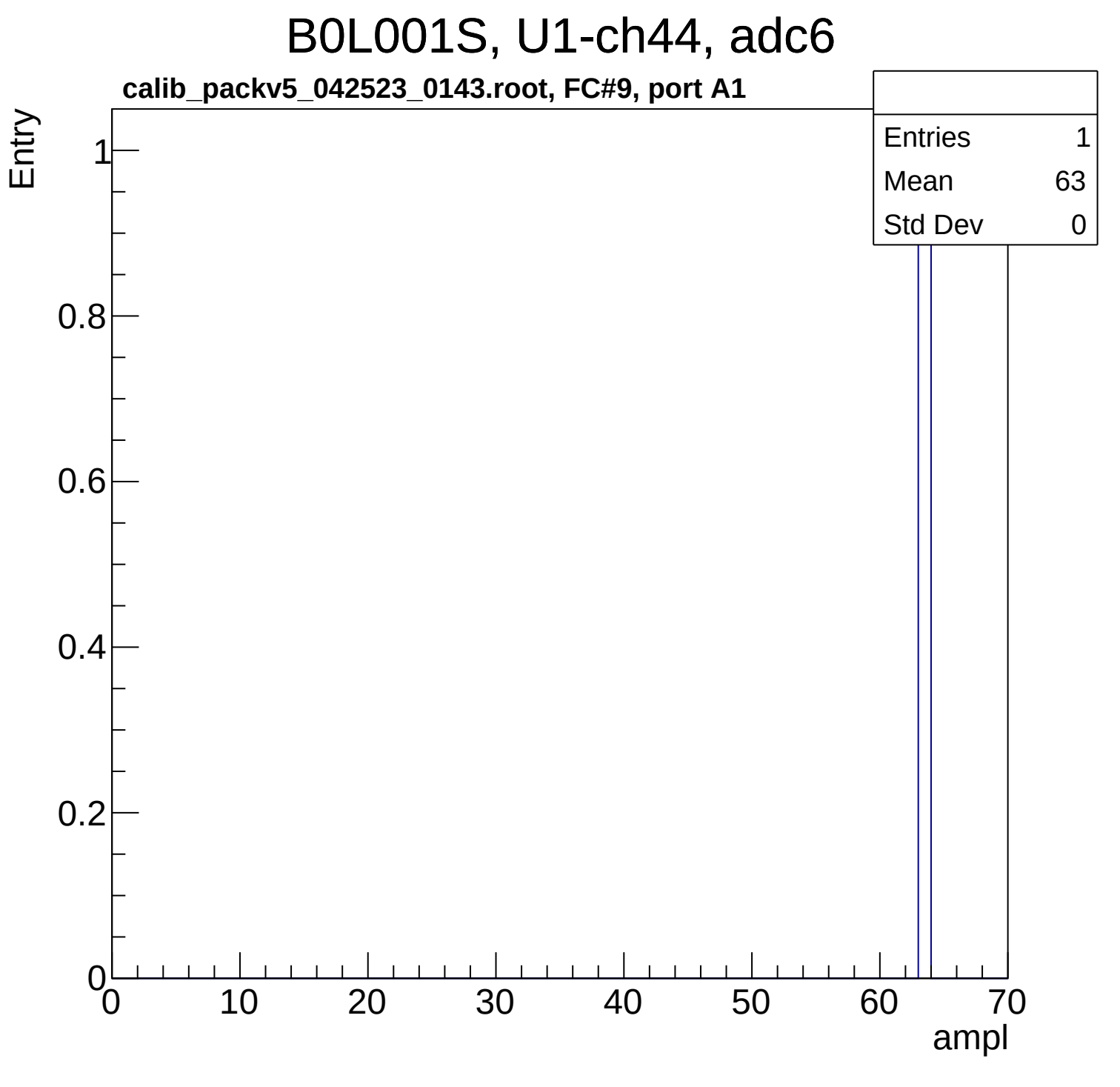

B0L001S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.33
Std Dev 3.713
Gaus mean : 39.1955
Gaus Width: 3.3802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

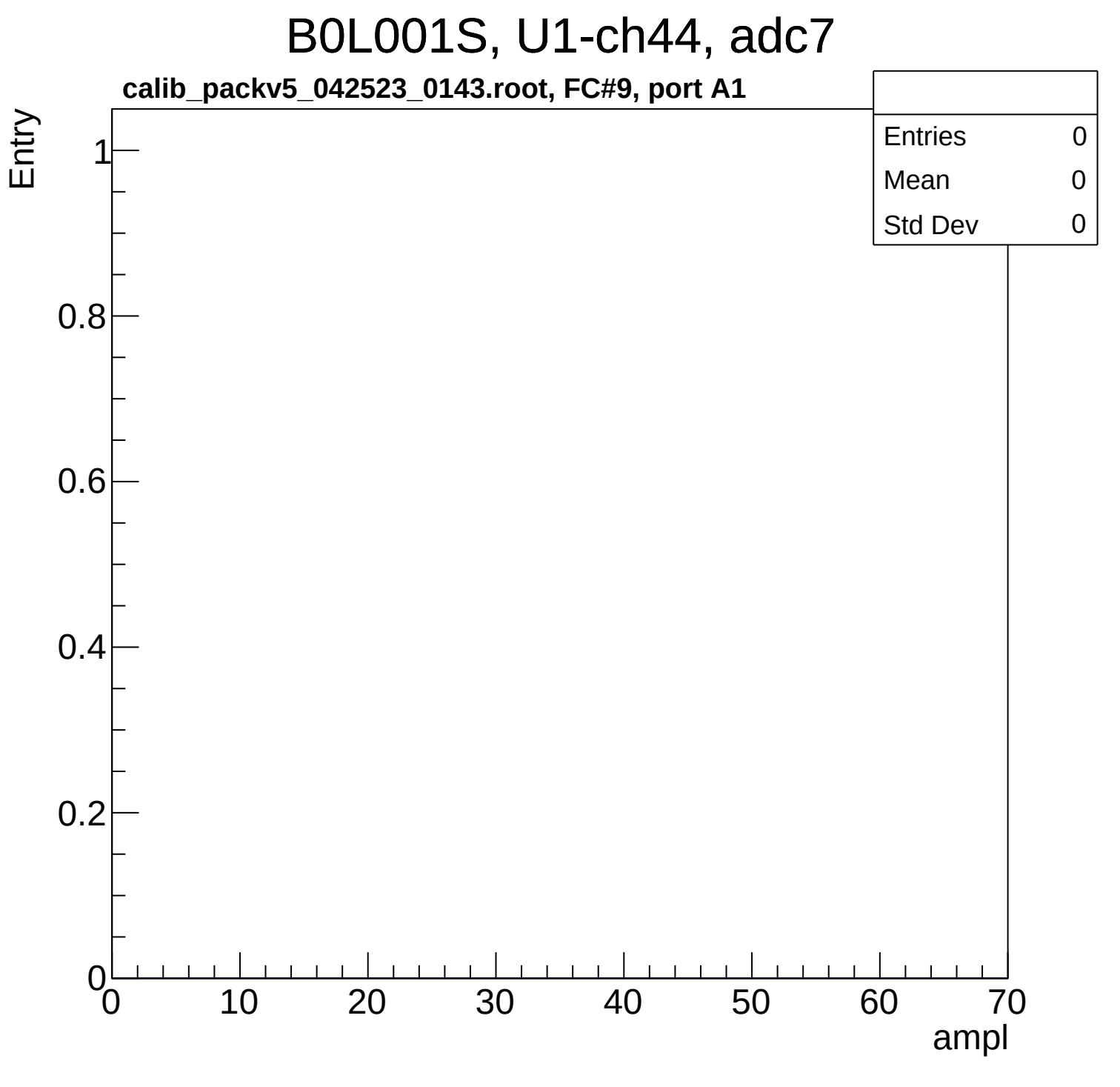

B0L001S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 43.65
Std Dev 3.159
Gaus mean : 44.7867
Gaus Width: 4.4476
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 50.41
Std Dev 3.736
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 57.19
Std Dev 2.581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.98
Std Dev 9.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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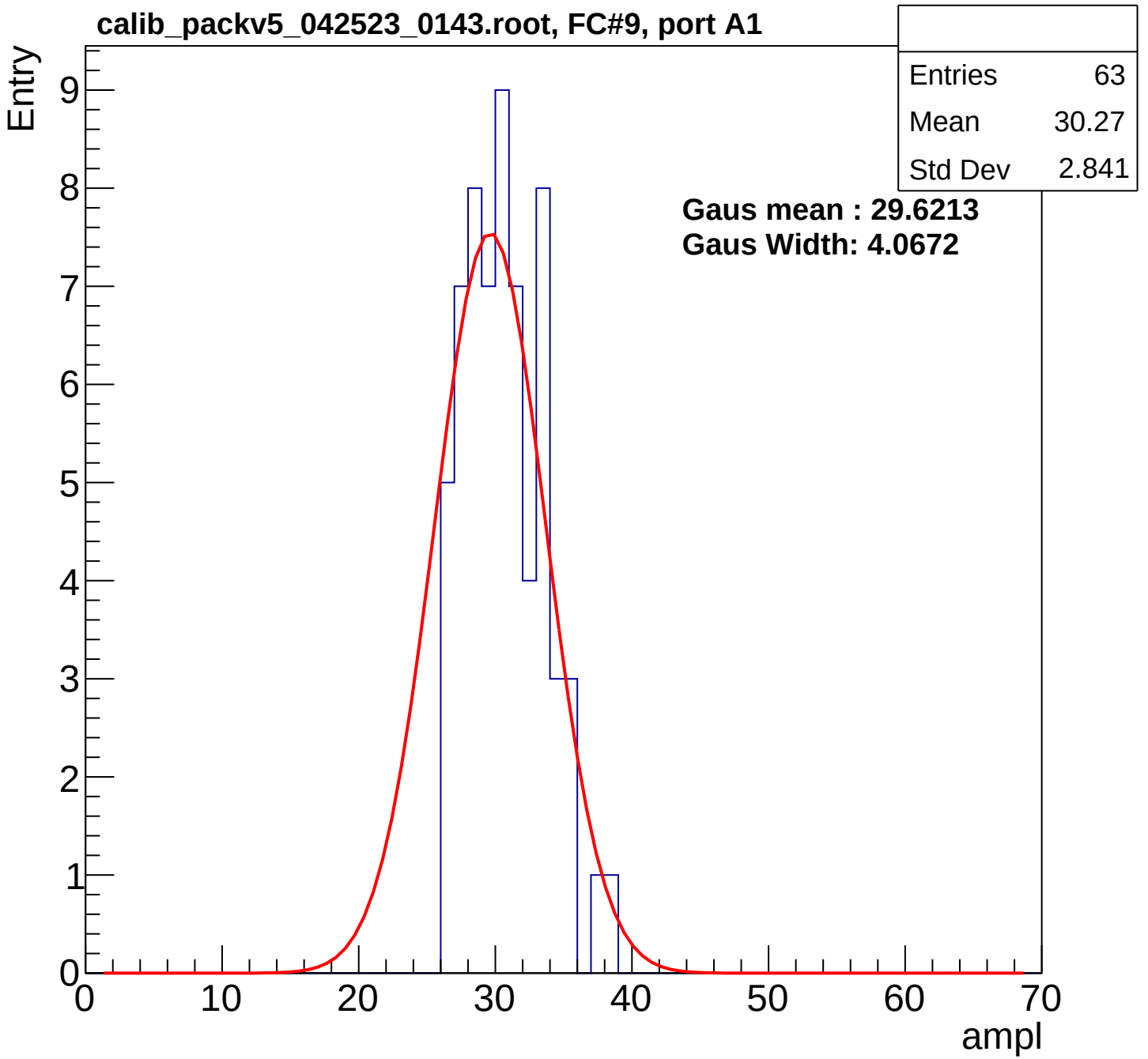
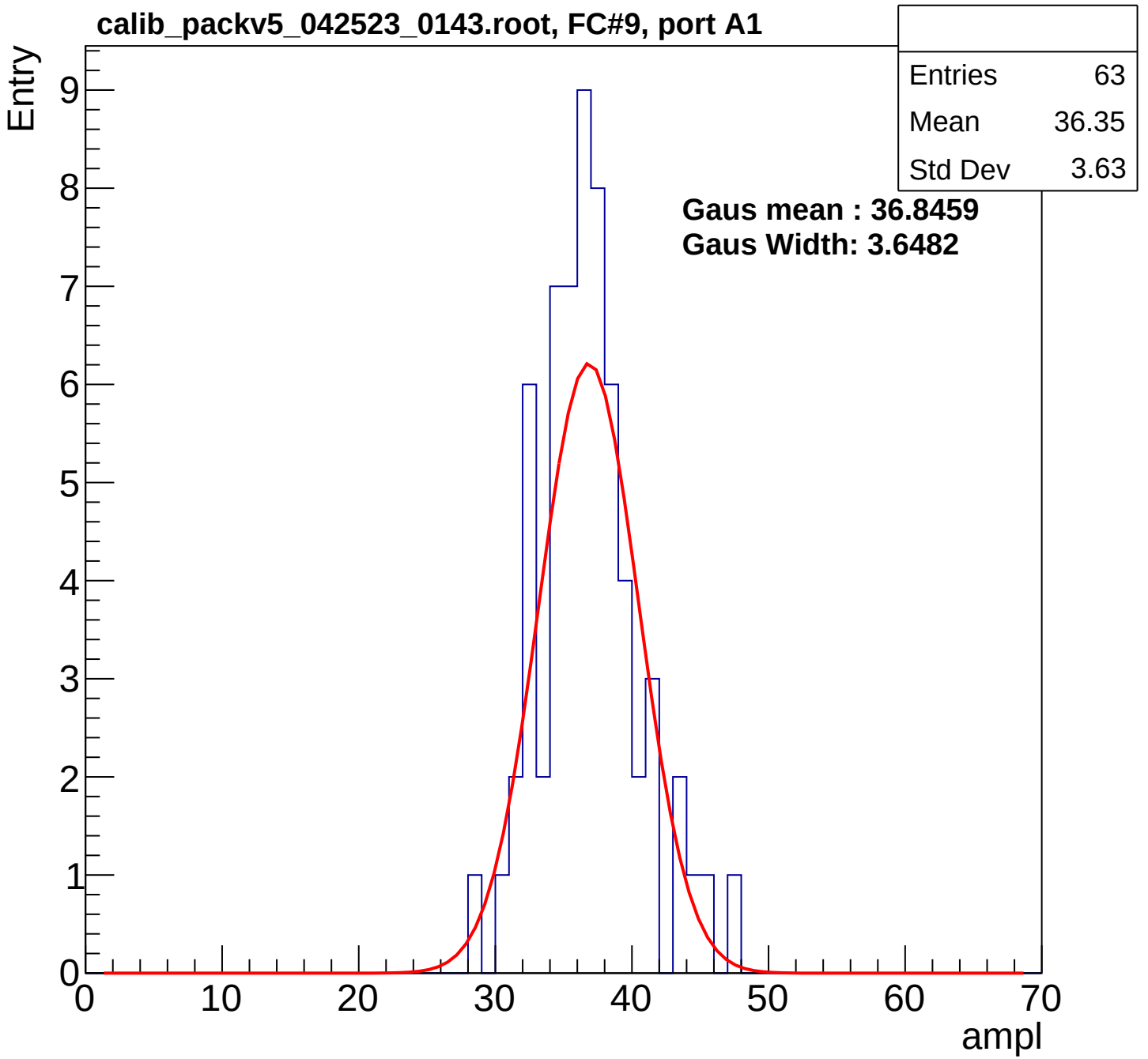
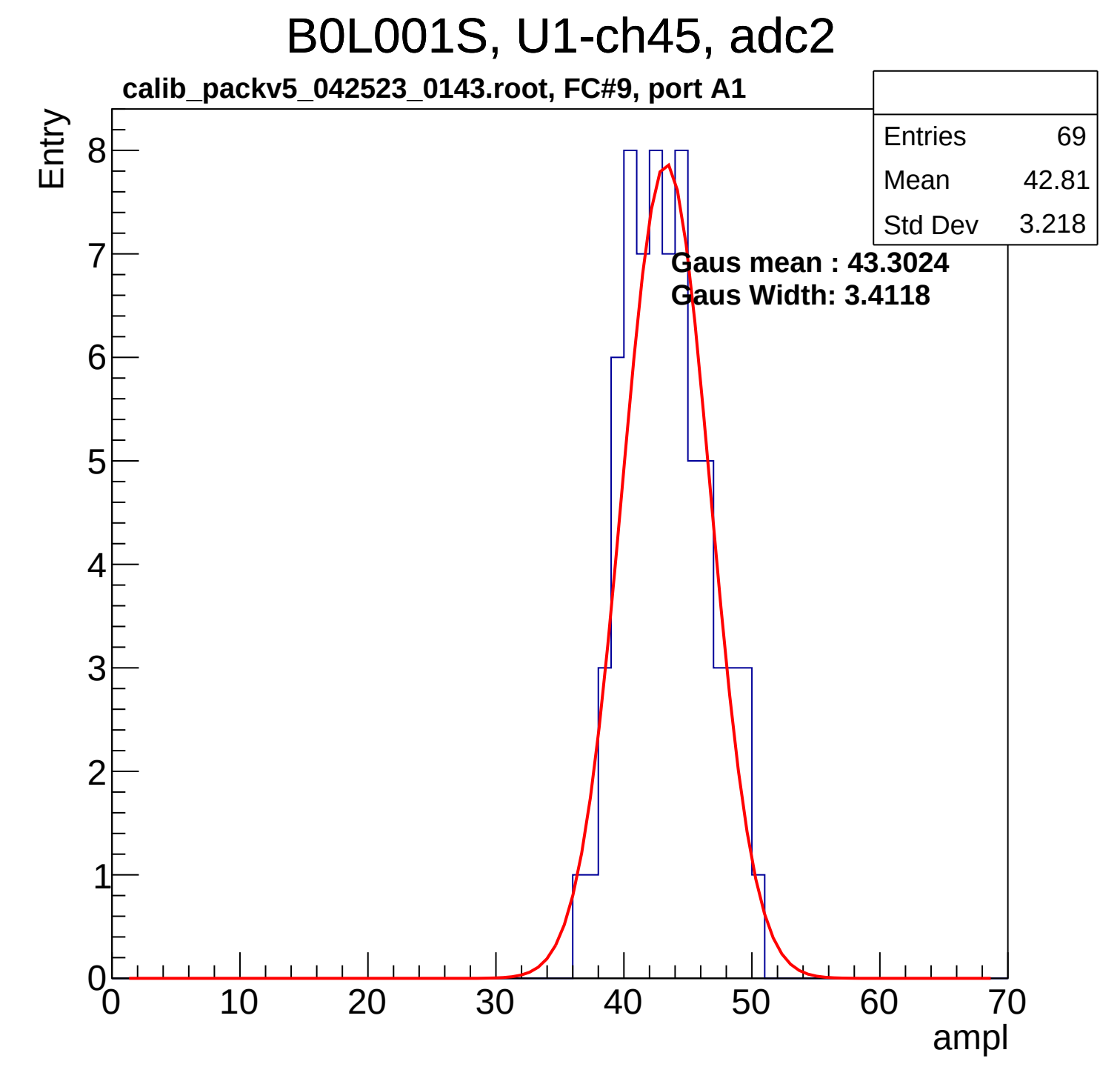
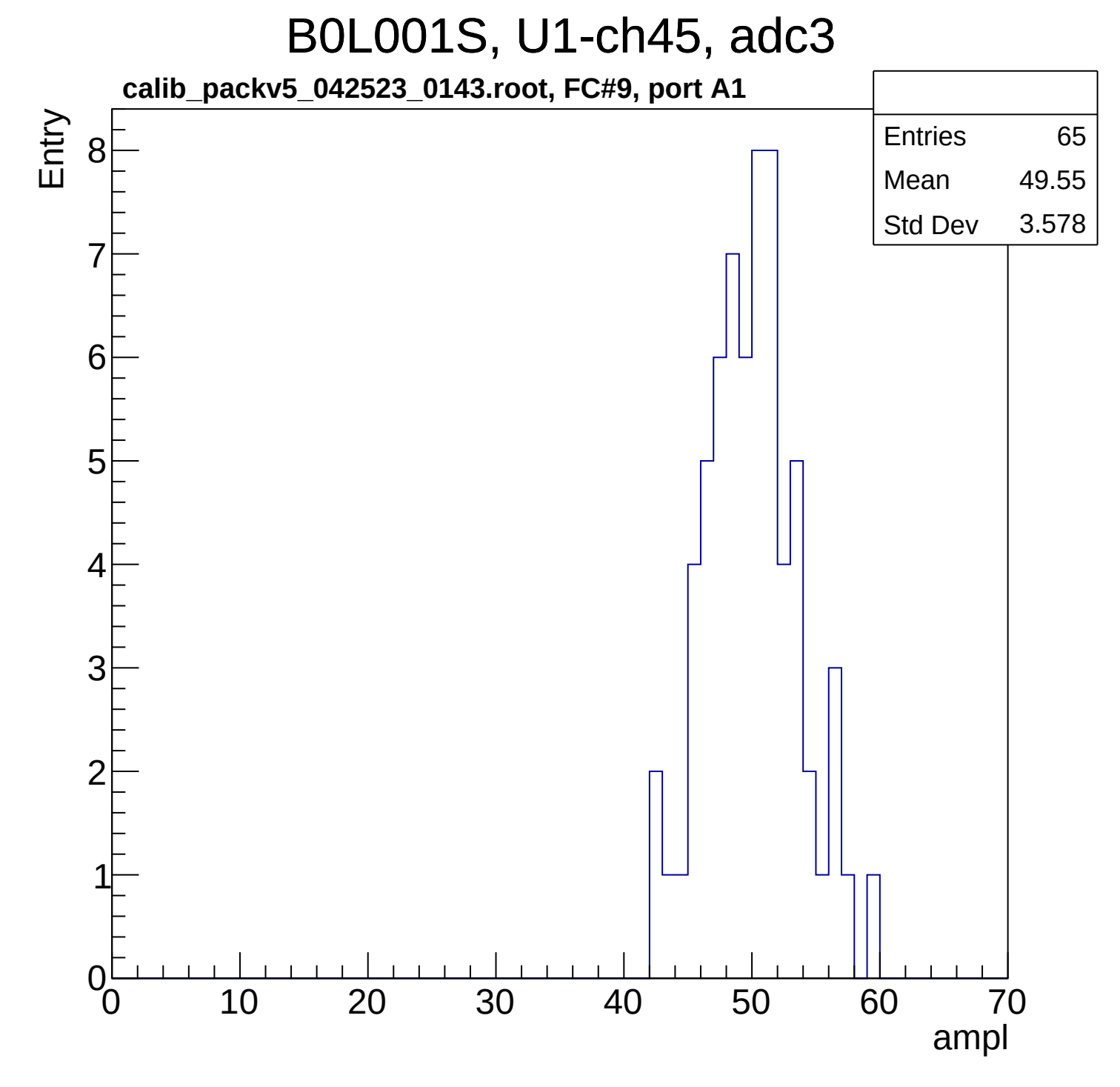
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.27
Std Dev 2.841
Gaus mean : 29.6213
Gaus Width: 4.0672
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 36.35
Std Dev 3.63
Gaus mean : 36.8459
Gaus Width: 3.6482
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 42.81
Std Dev 3.218
Gaus mean : 43.3024
Gaus Width: 3.4118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.55
Std Dev 3.578
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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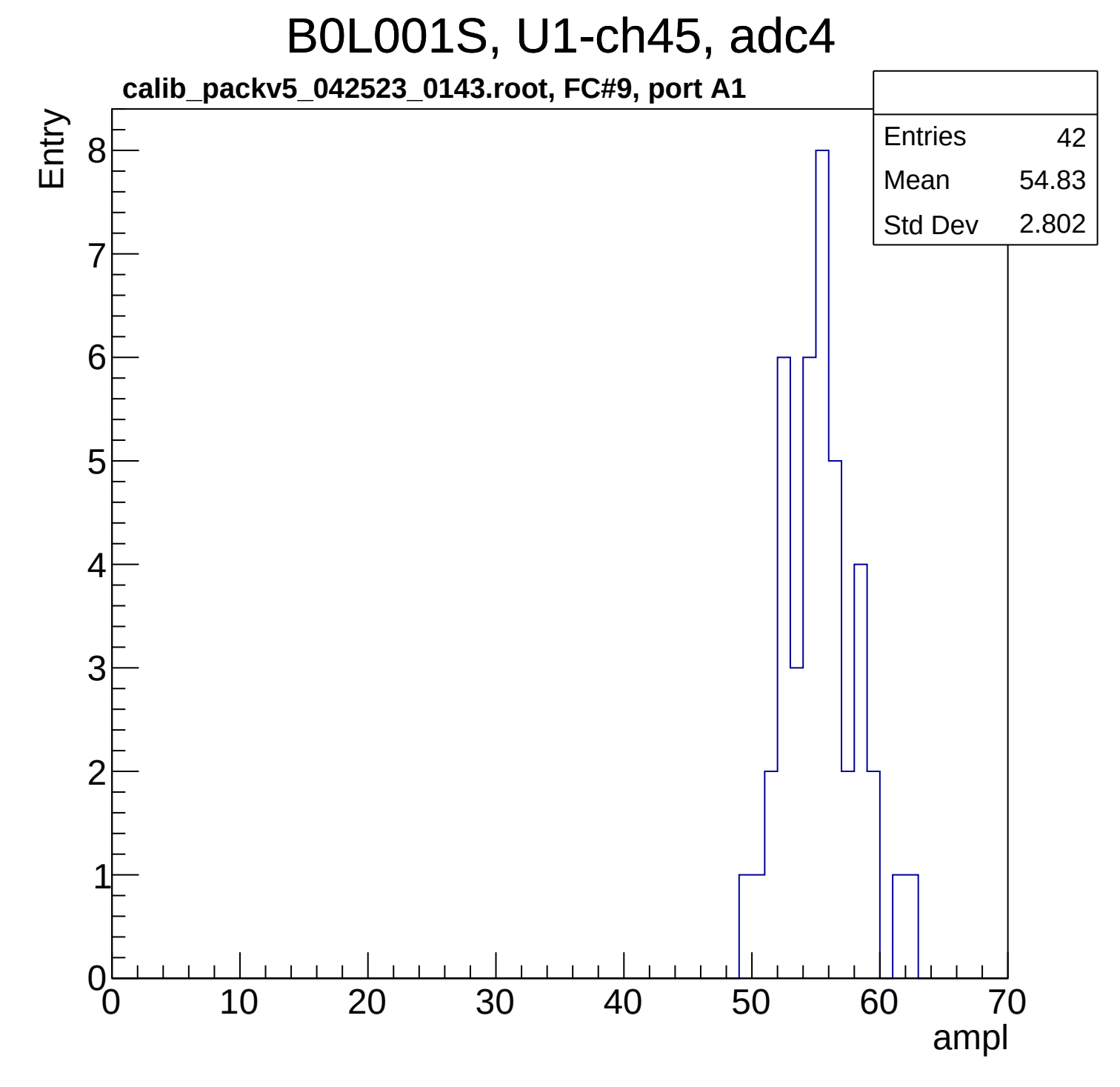
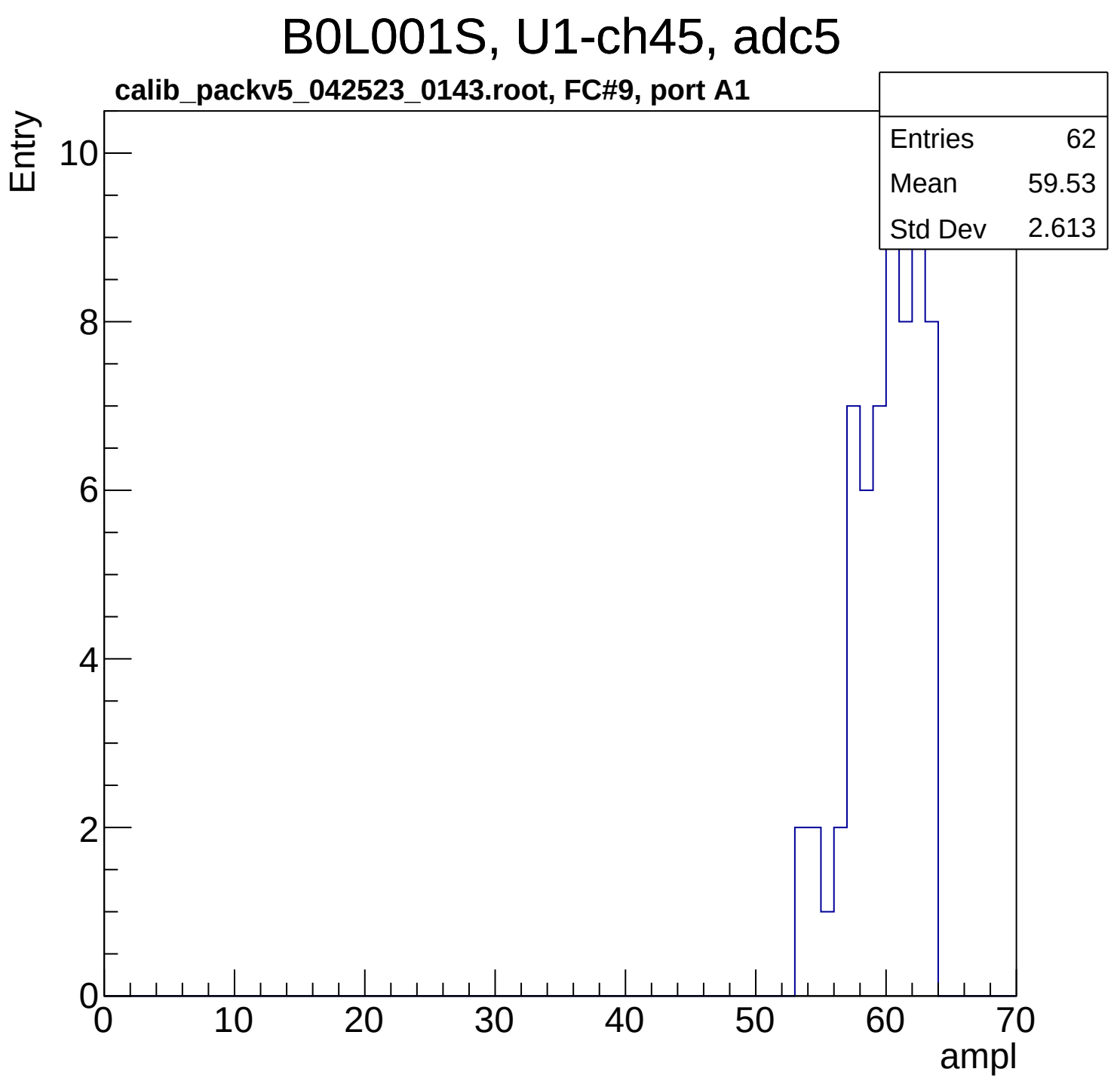
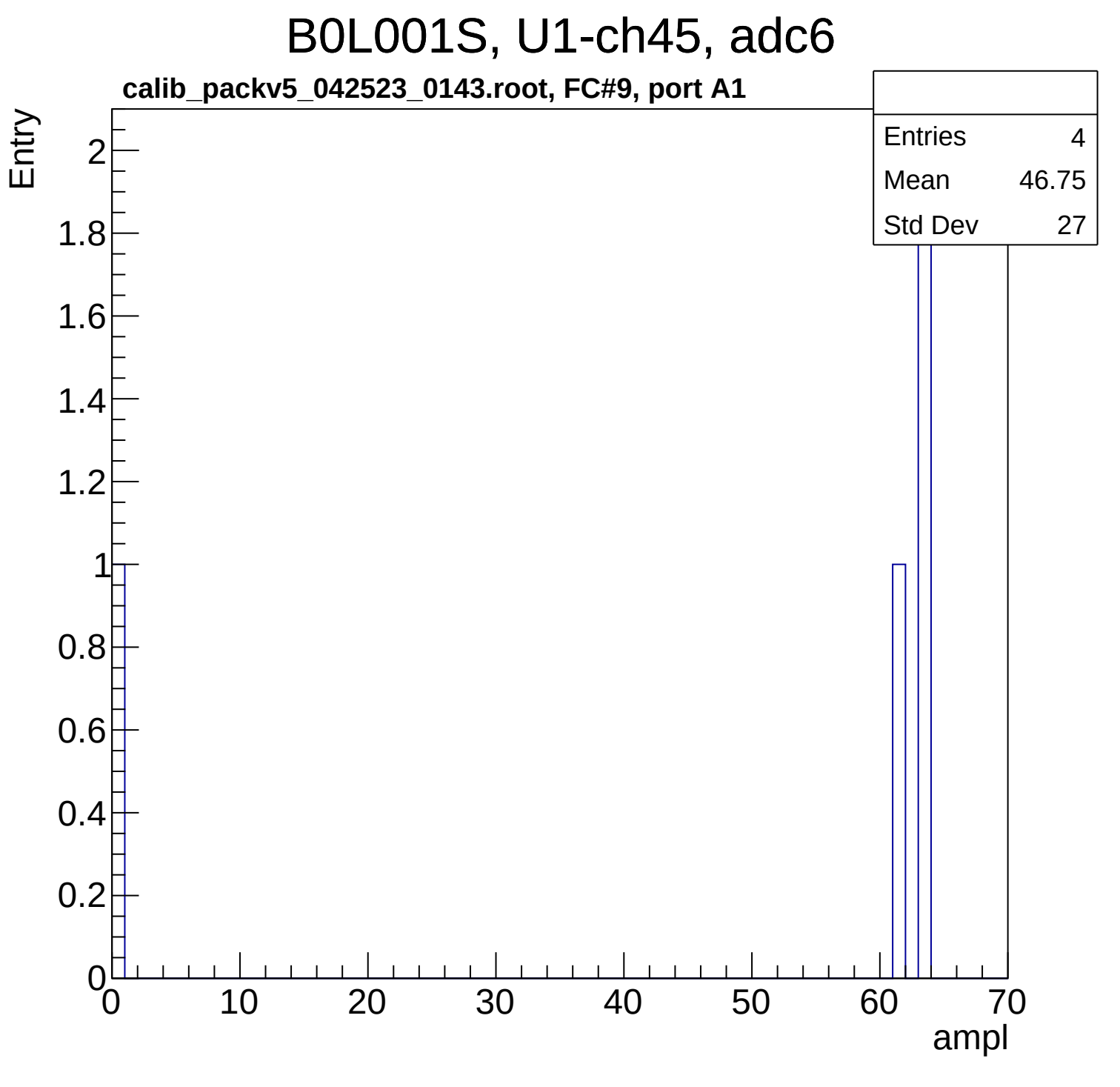
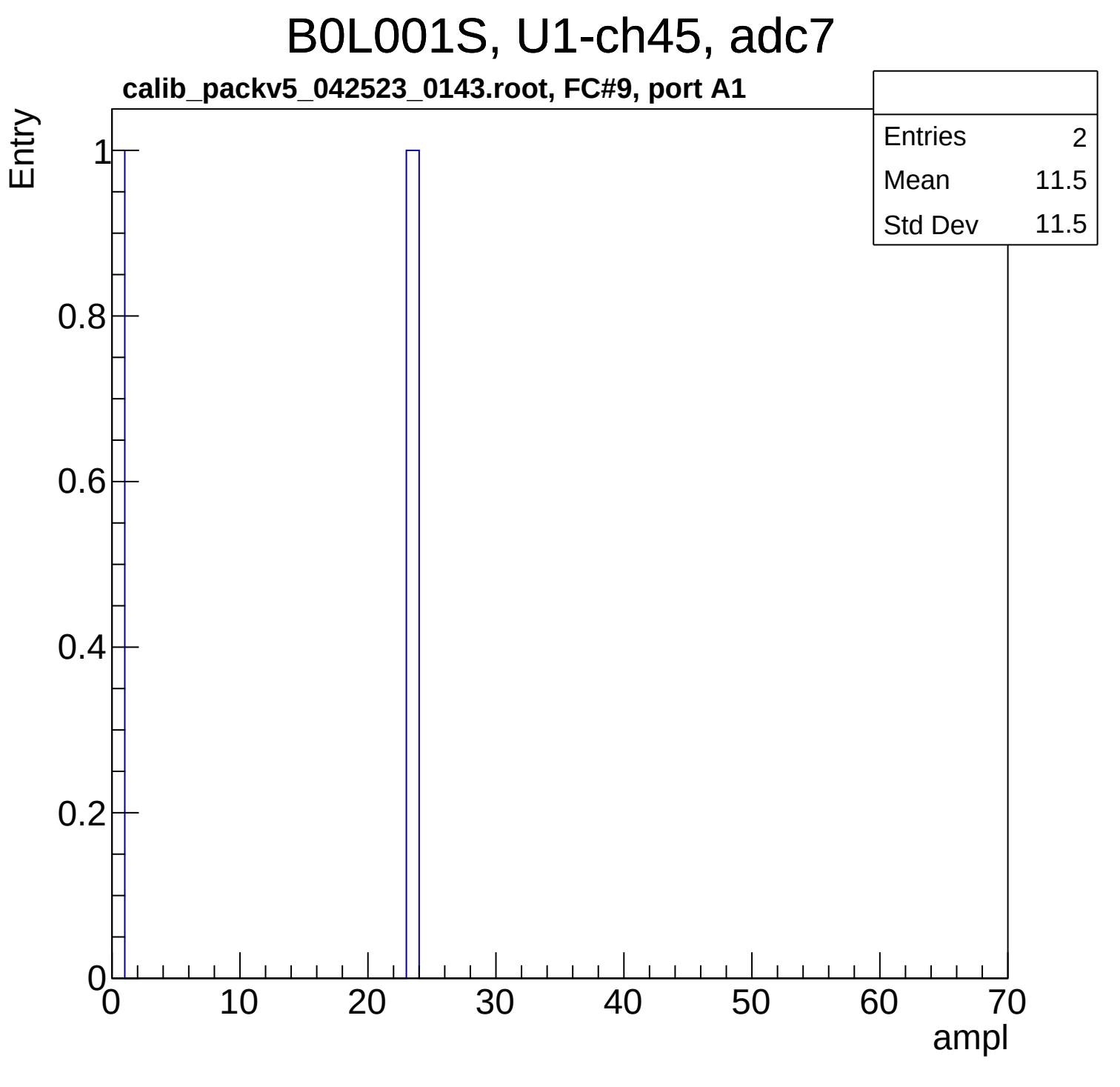
40
50
60
70


B0L001S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 54.83
Std Dev 2.802
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 59.53
Std Dev 2.613
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 46.75
Std Dev 27
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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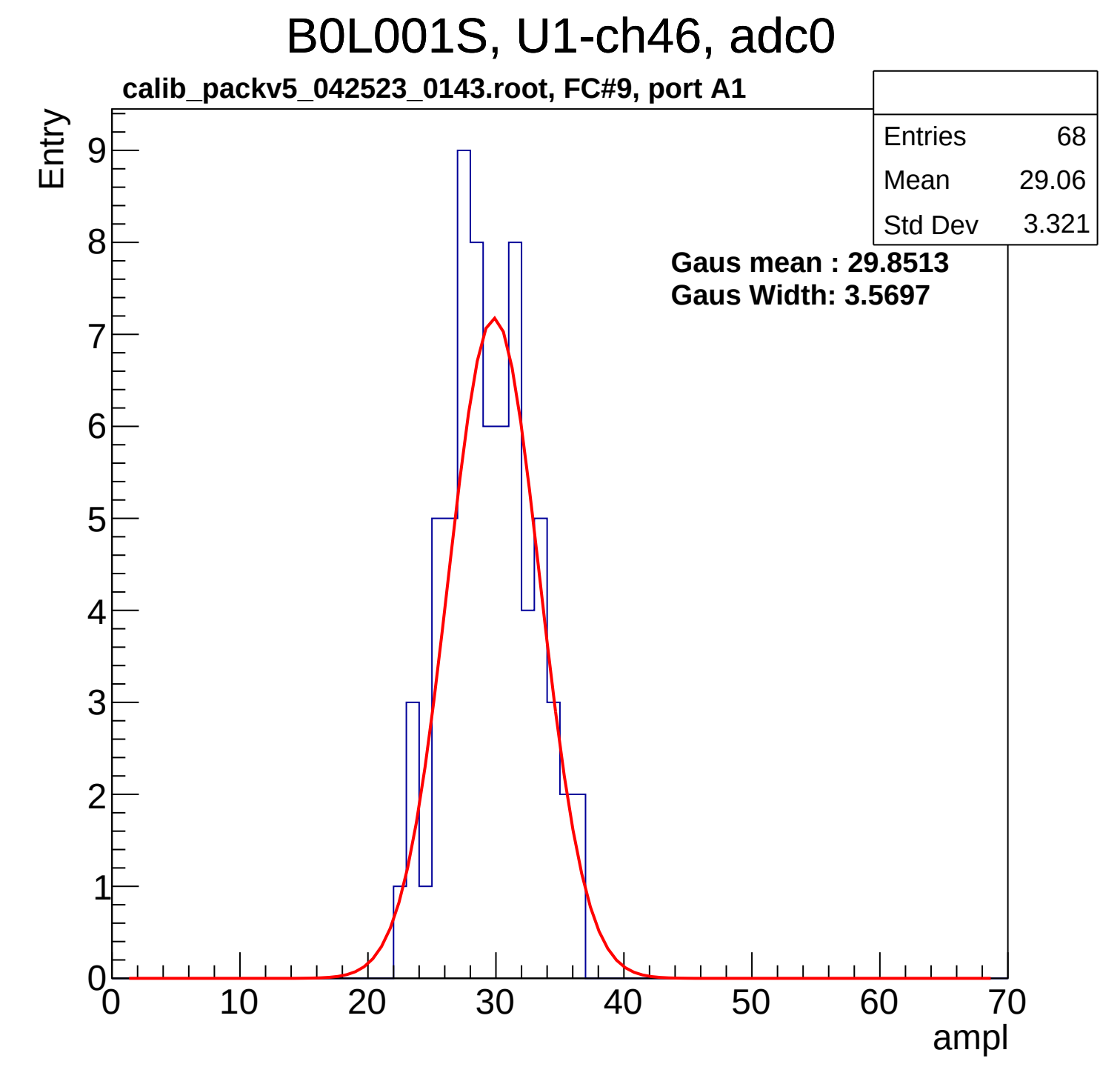
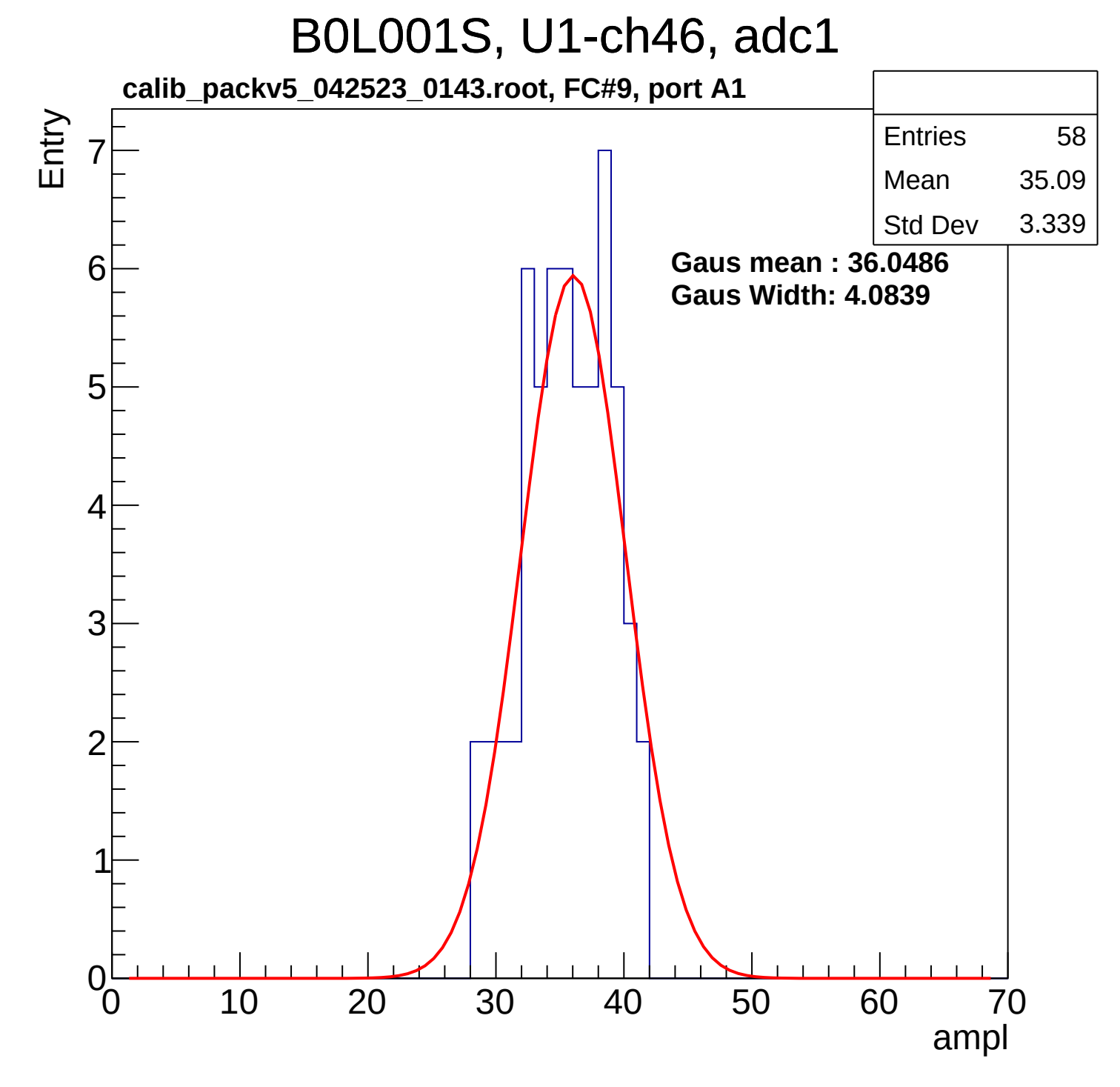
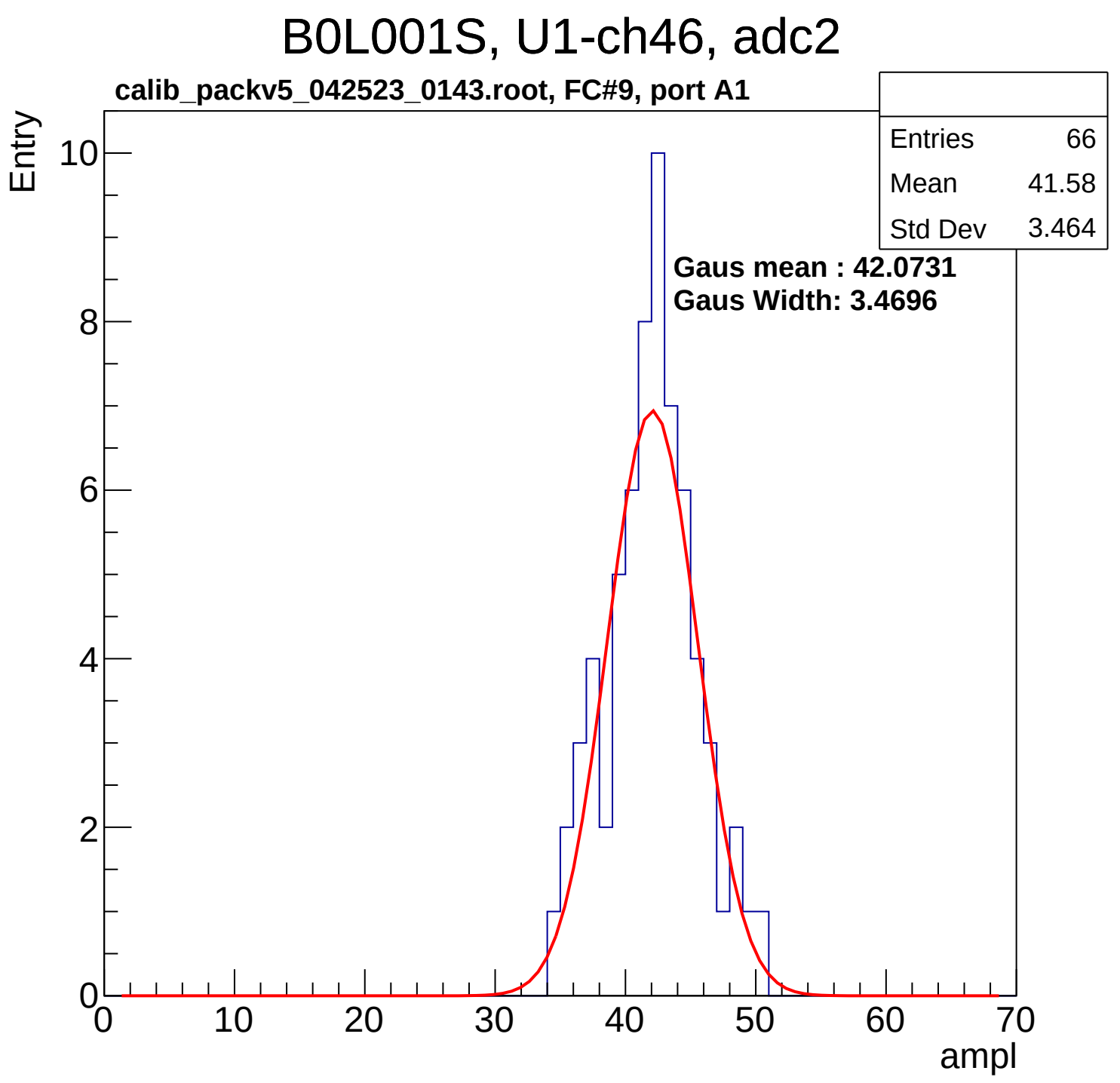
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 29.06
Std Dev 3.321
Gaus mean : 29.8513
Gaus Width: 3.5697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 35.09
Std Dev 3.339
Gaus mean : 36.0486
Gaus Width: 4.0839
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 41.58
Std Dev 3.464
Gaus mean : 42.0731
Gaus Width: 3.4696
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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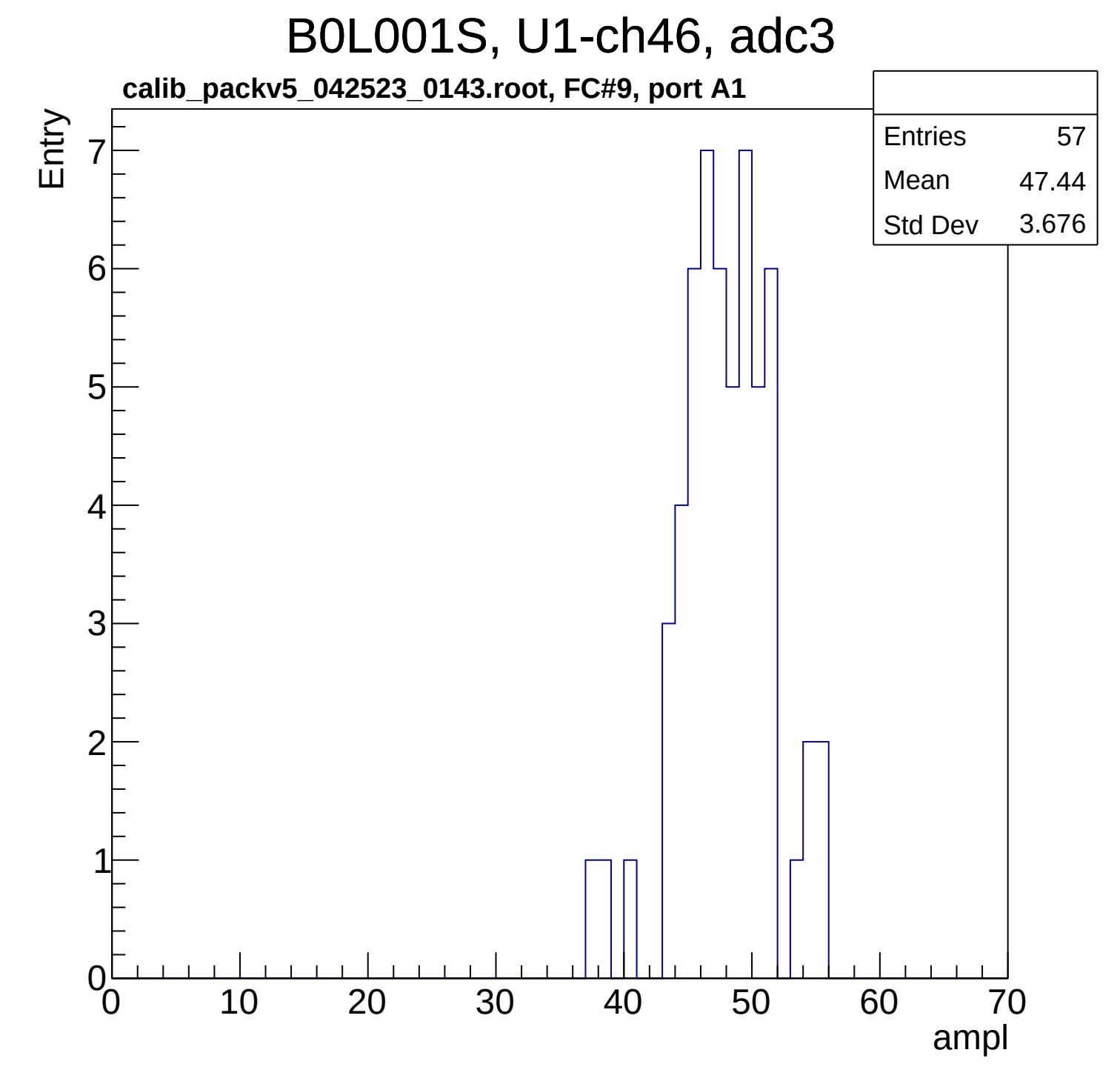
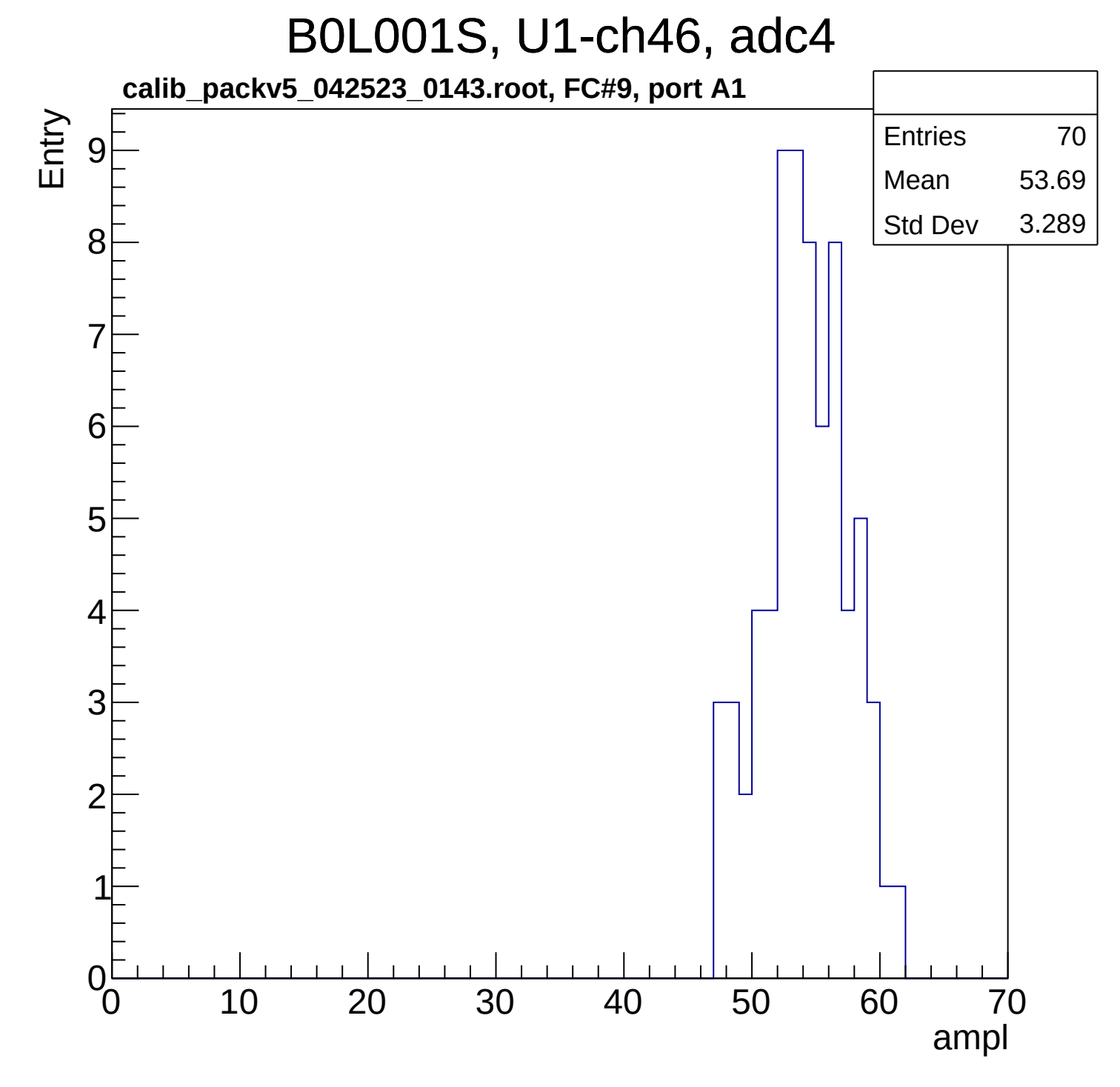
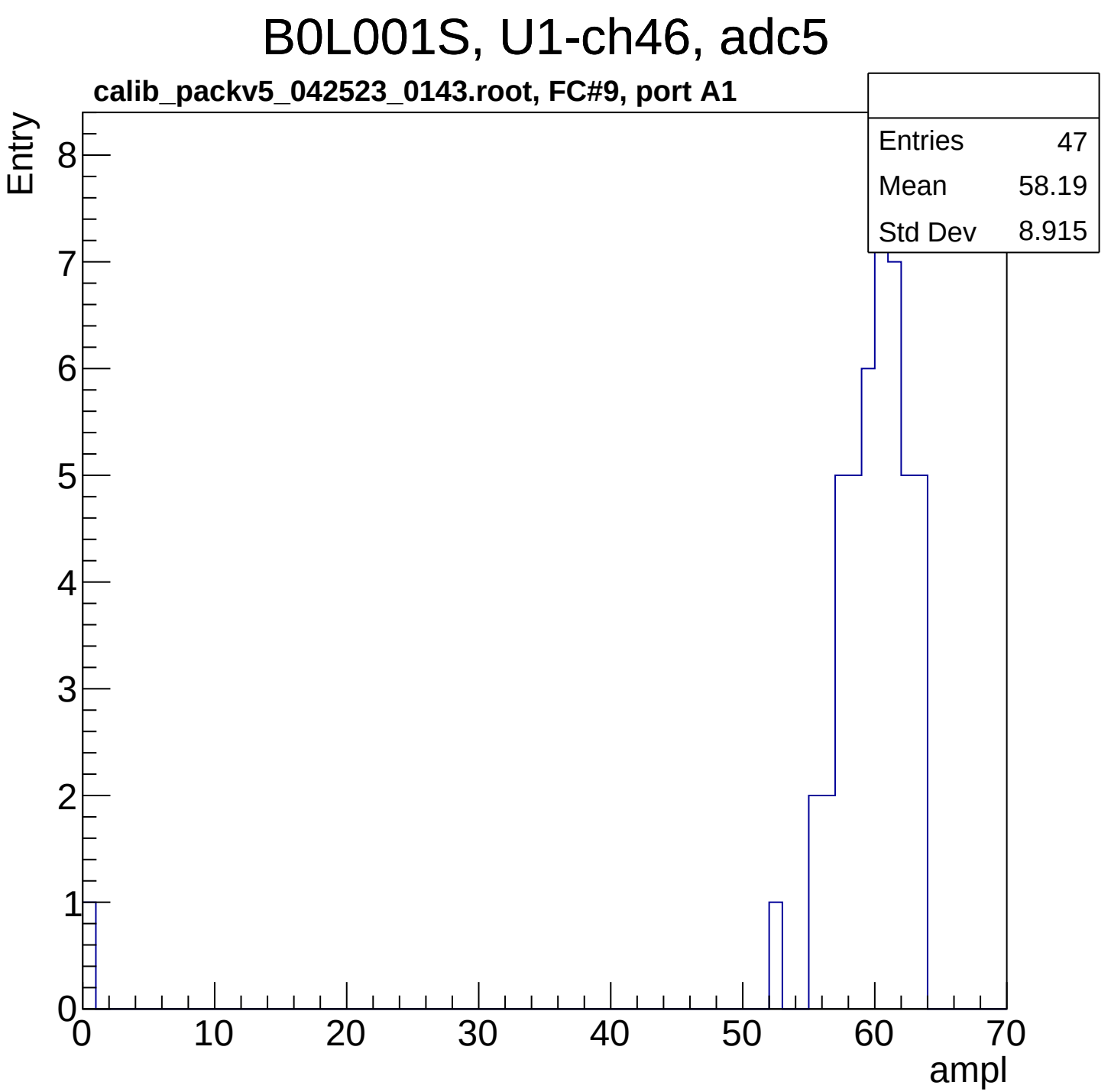
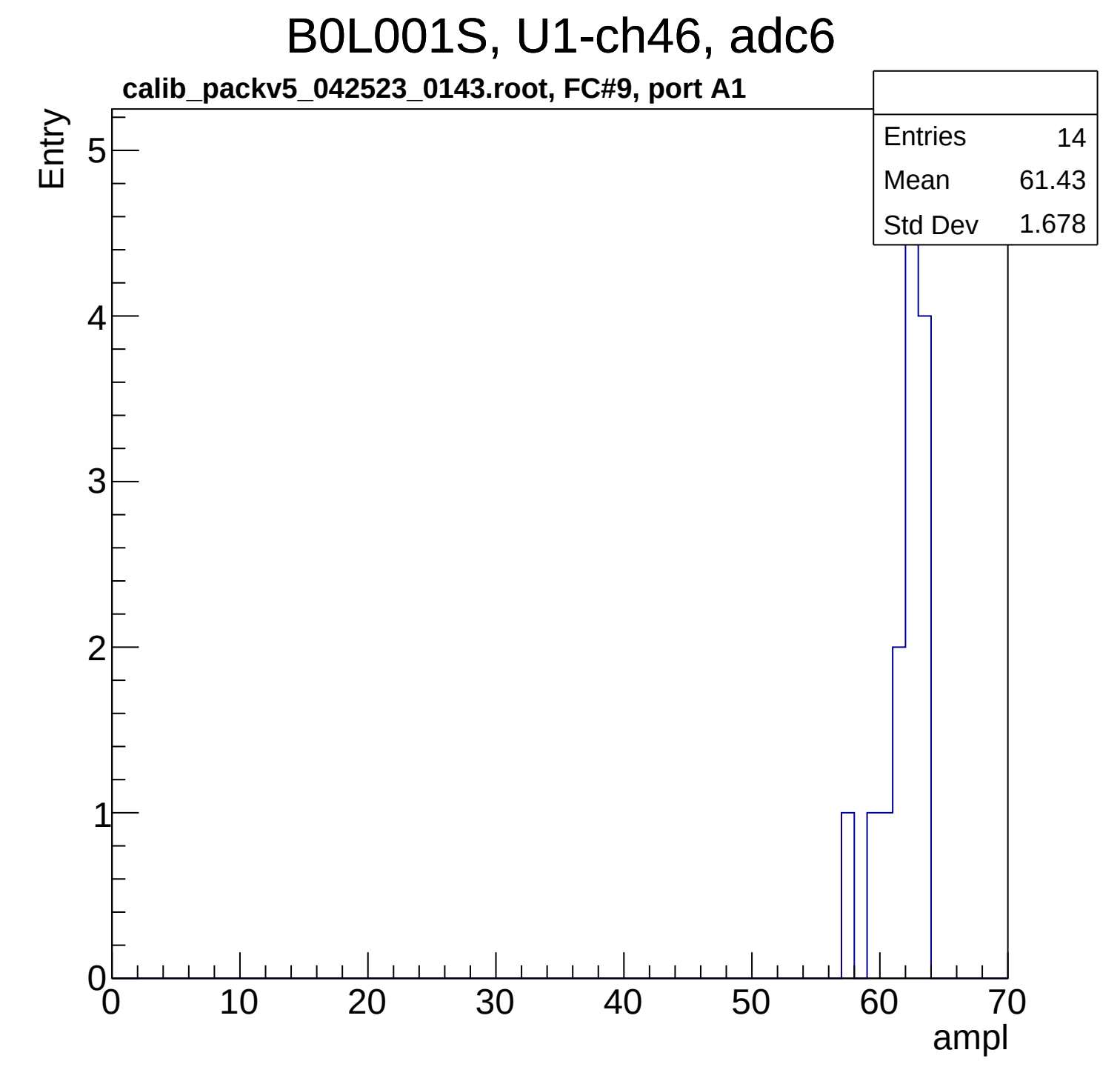
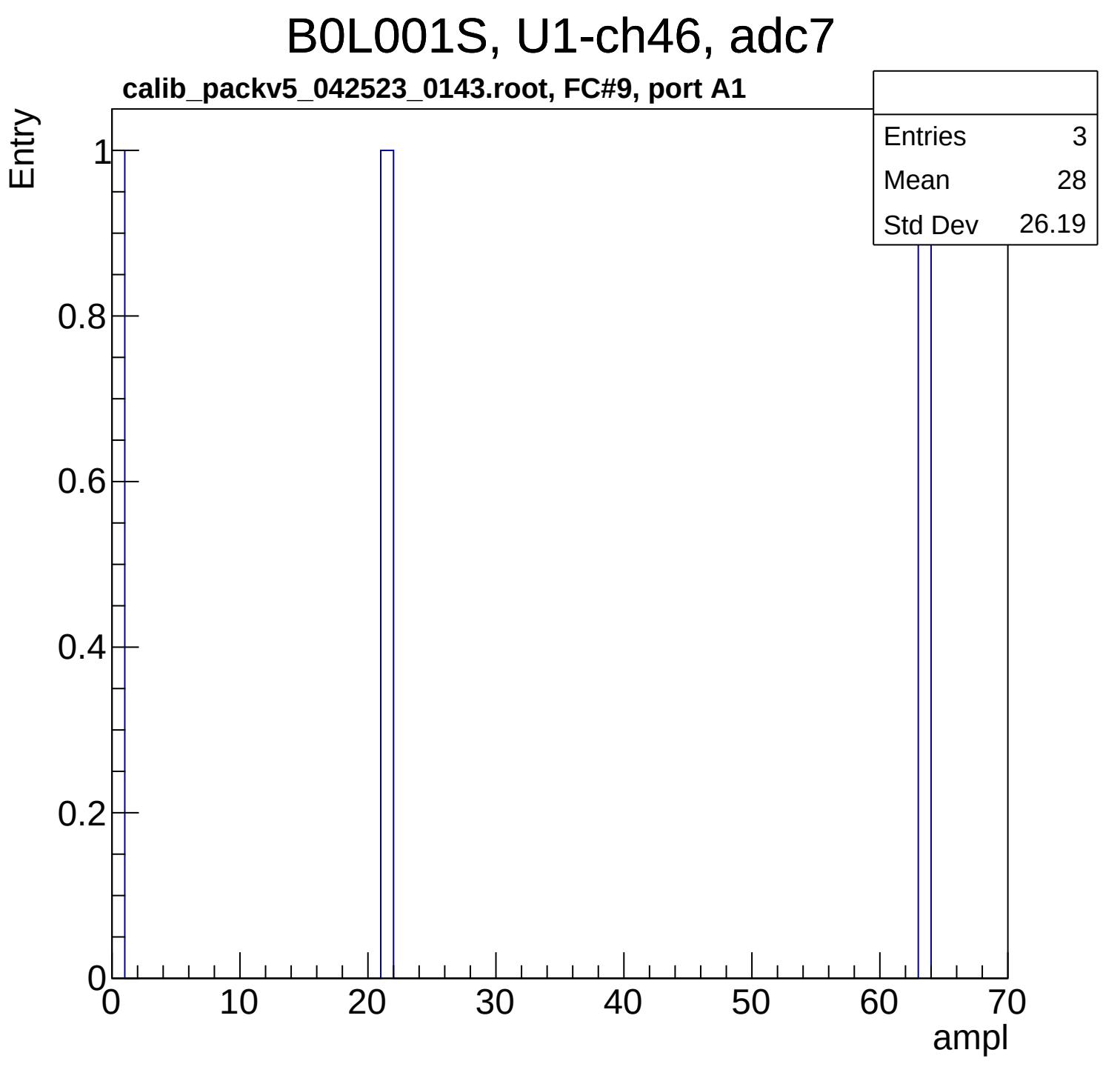
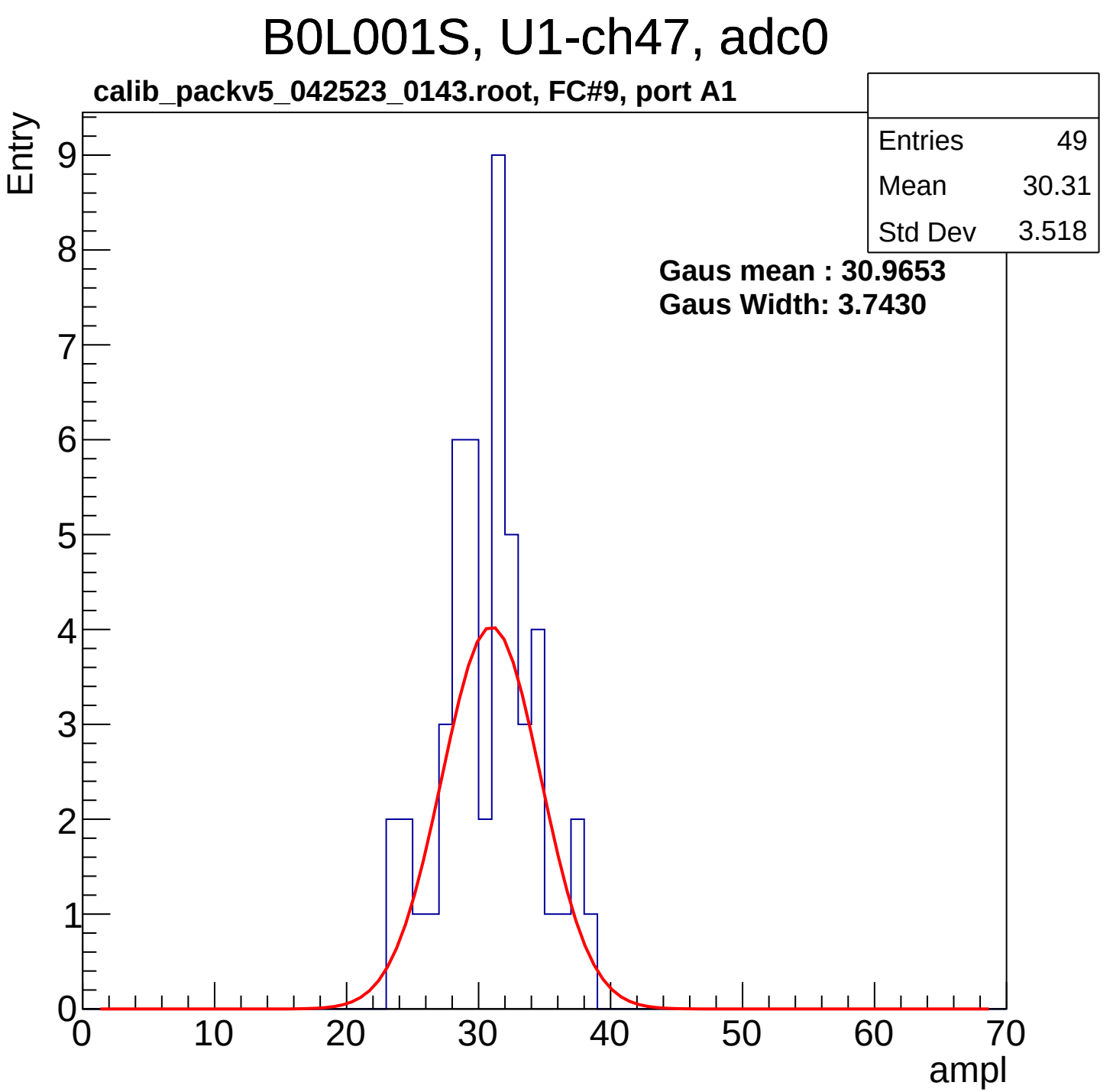

B0L001S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 47.44
Std Dev 3.676
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

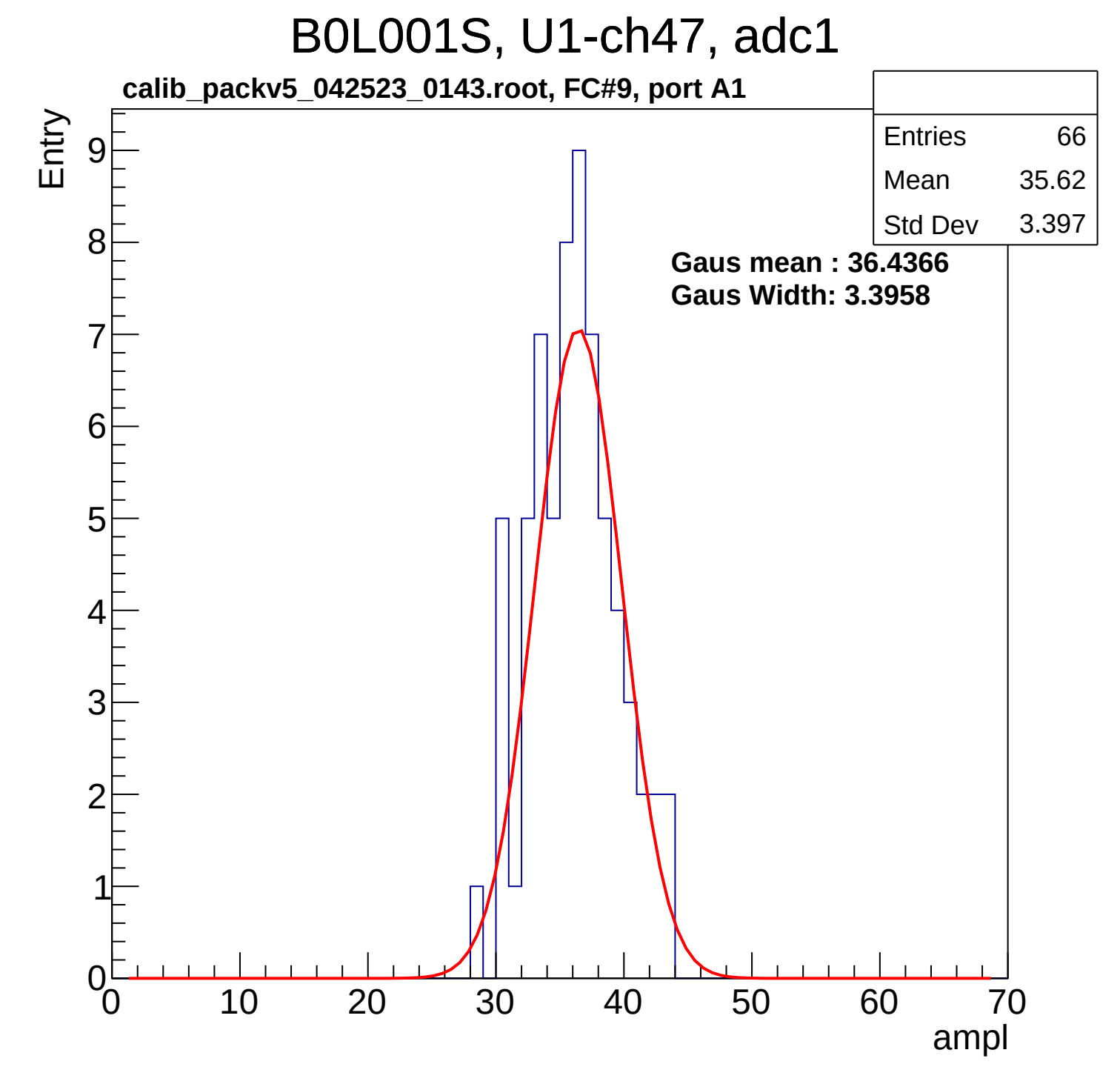

B0L001S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 53.69
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

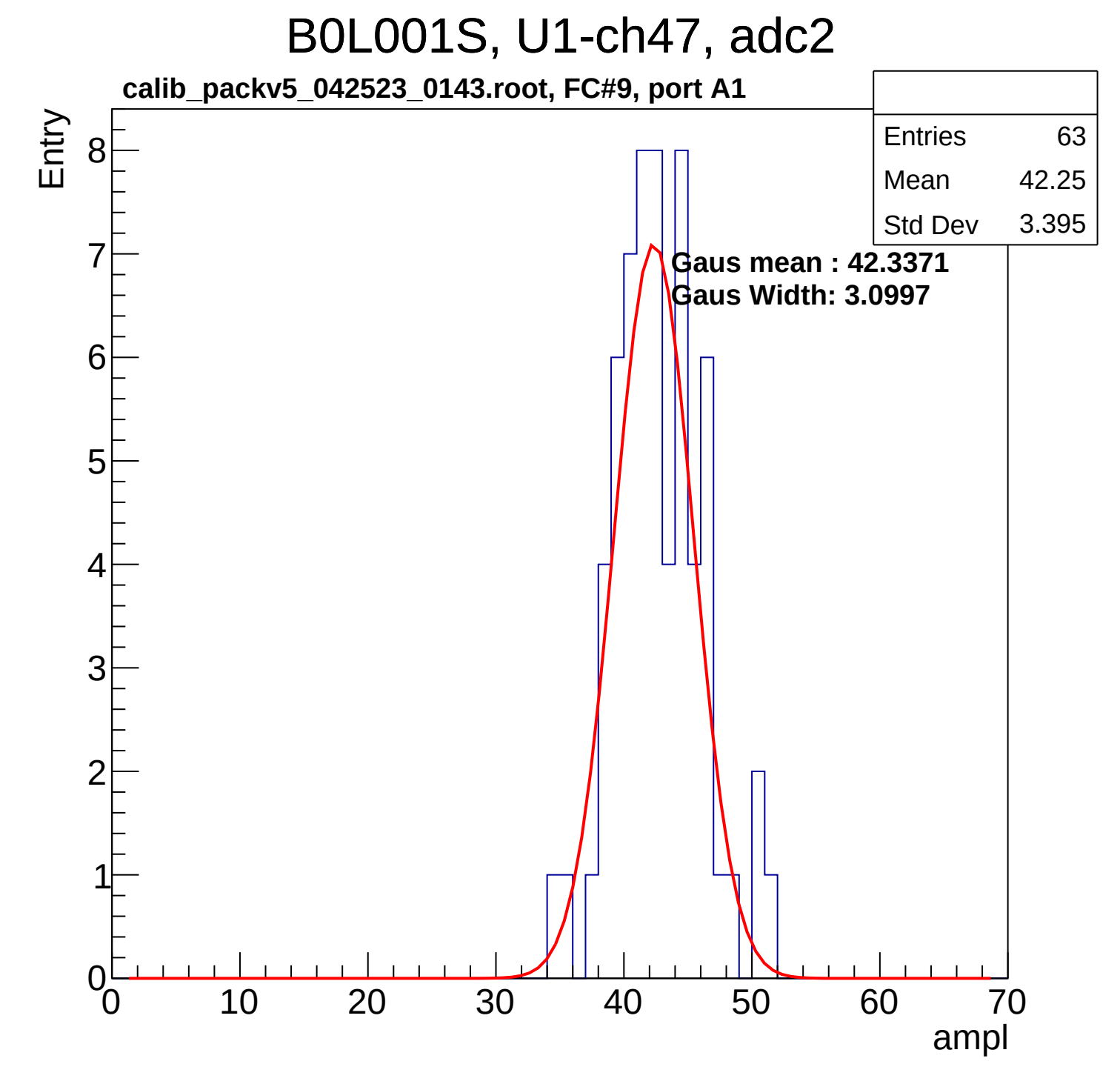

B0L001S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.19
Std Dev 8.915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

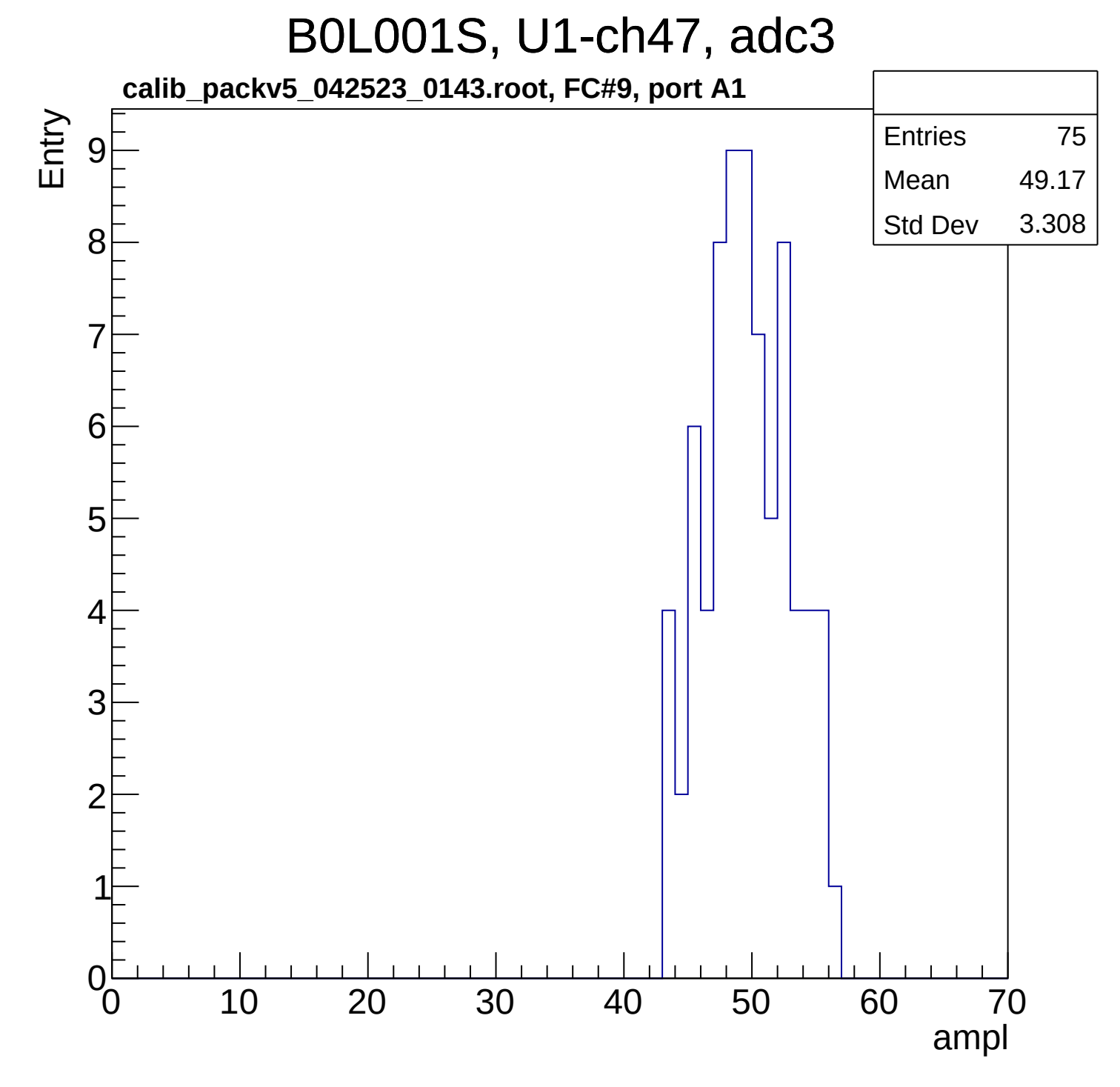

B0L001S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 61.43
Std Dev 1.678
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

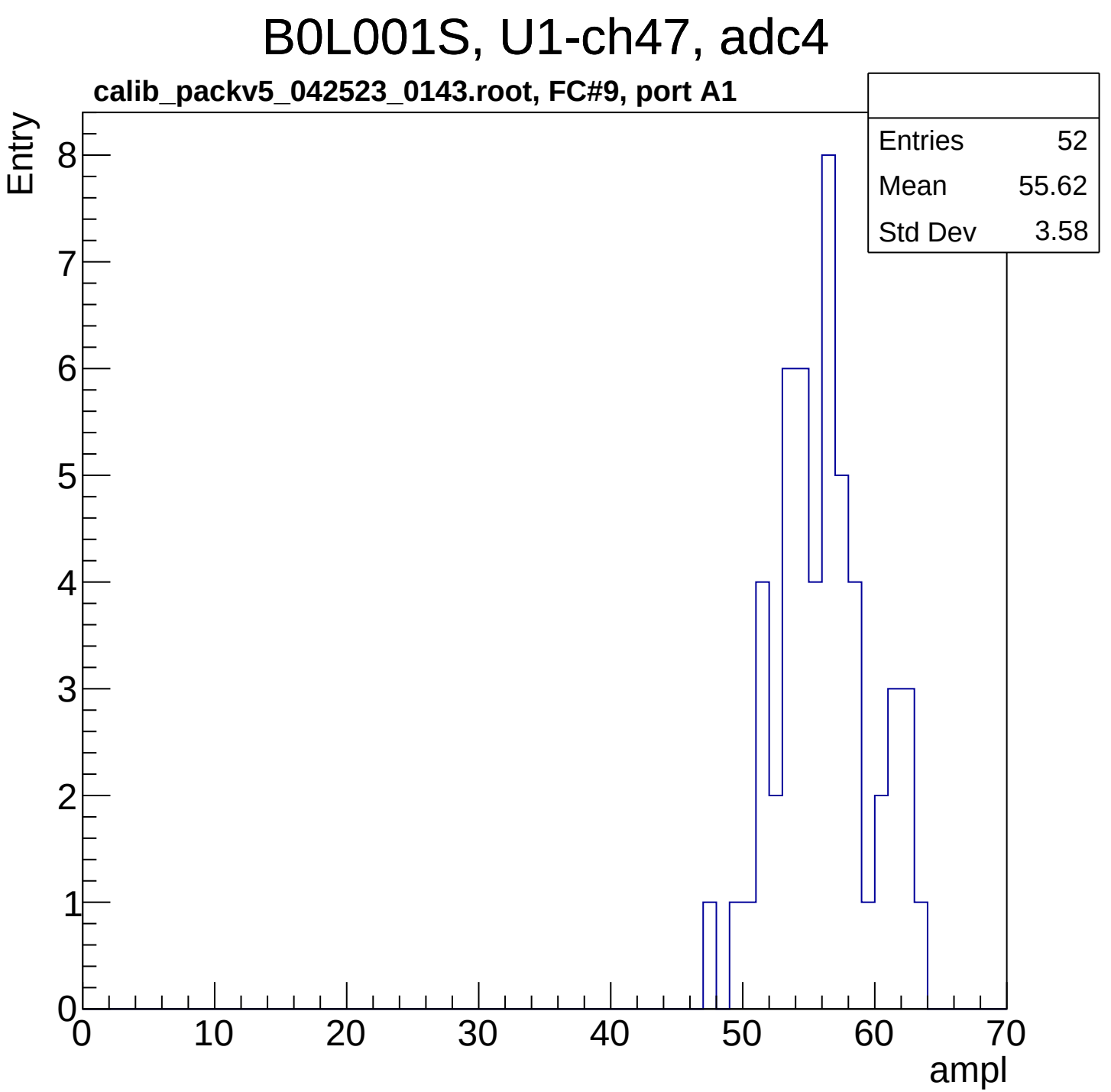

B0L001S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 28
Std Dev 26.19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

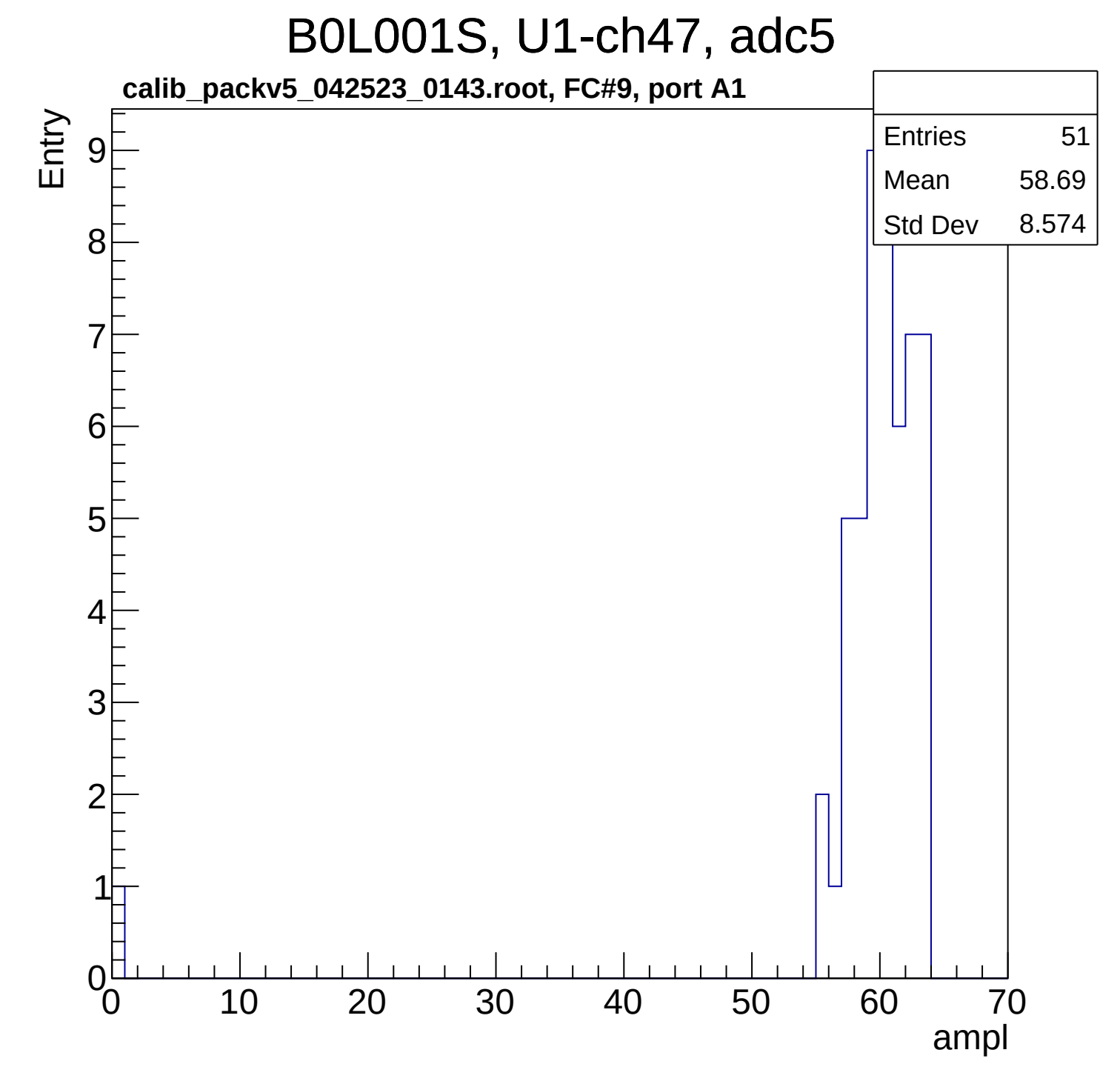

B0L001S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 30.31
Std Dev 3.518
Gaus mean : 30.9653
Gaus Width: 3.7430
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

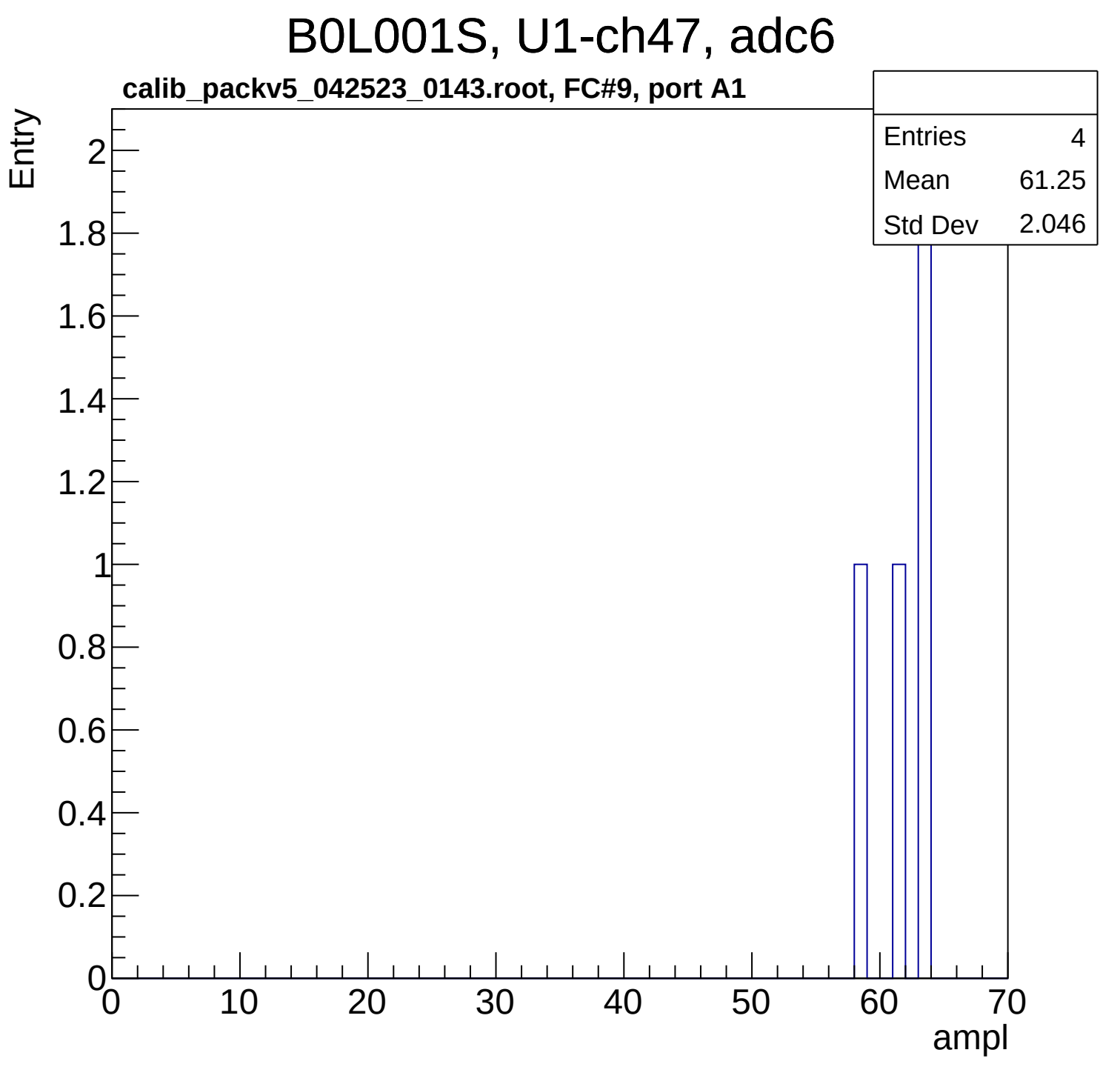

B0L001S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 35.62
Std Dev 3.397
Gaus mean : 36.4366
Gaus Width: 3.3958
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

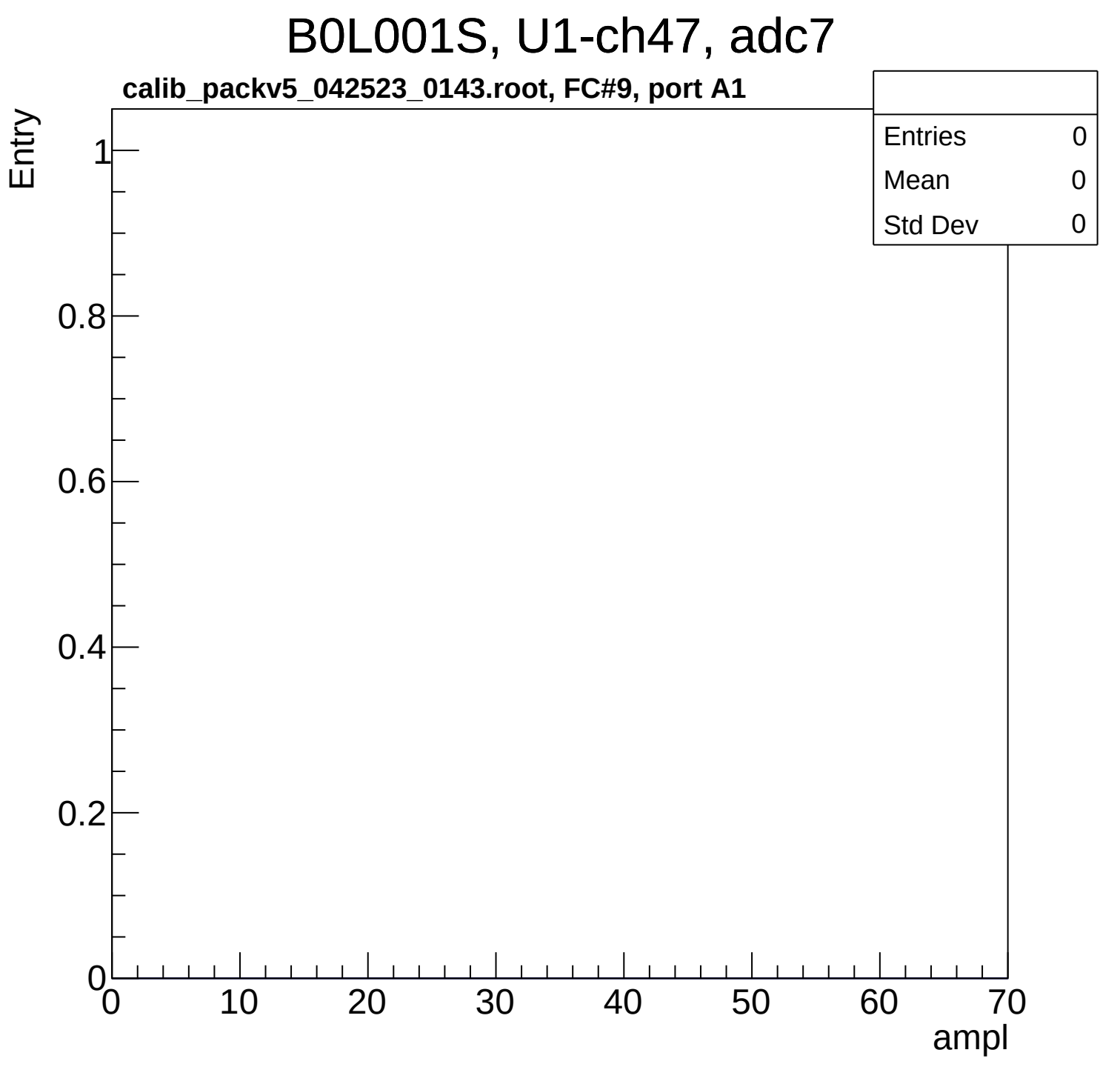

B0L001S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.25
Std Dev 3.395
Gaus mean : 42.3371
Gaus Width: 3.0997
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

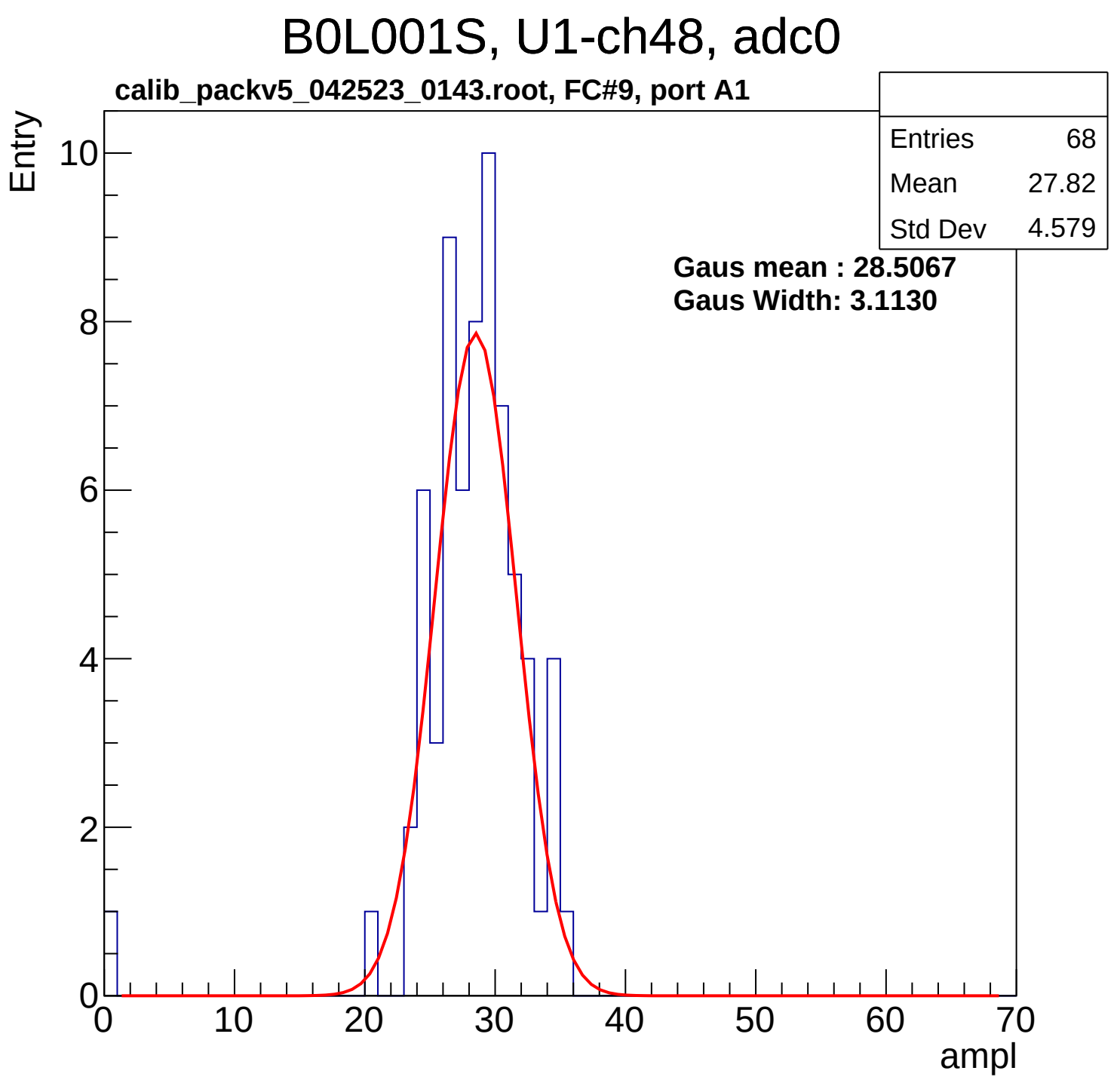

B0L001S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.17
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

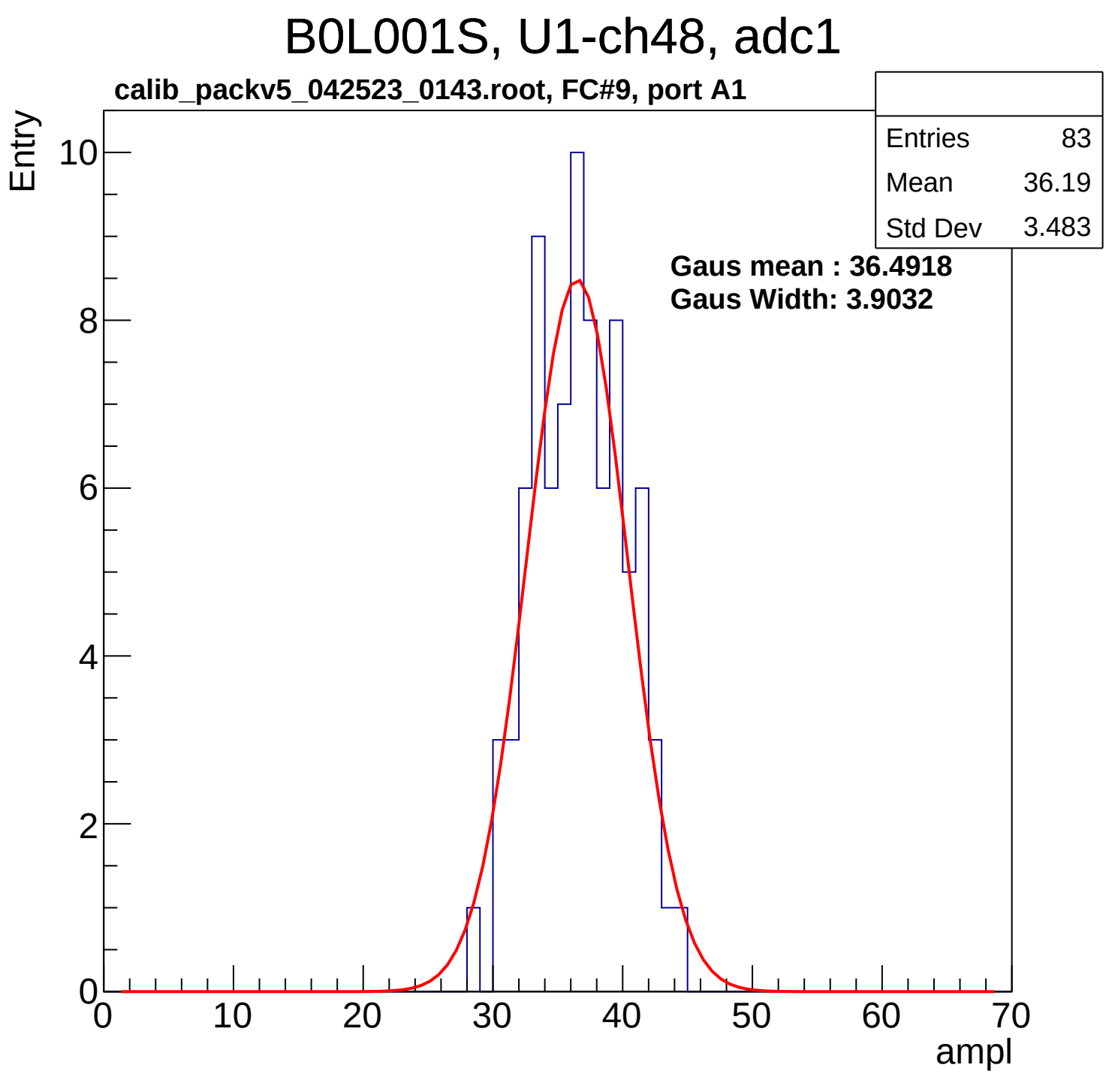

B0L001S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.62
Std Dev 3.58
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

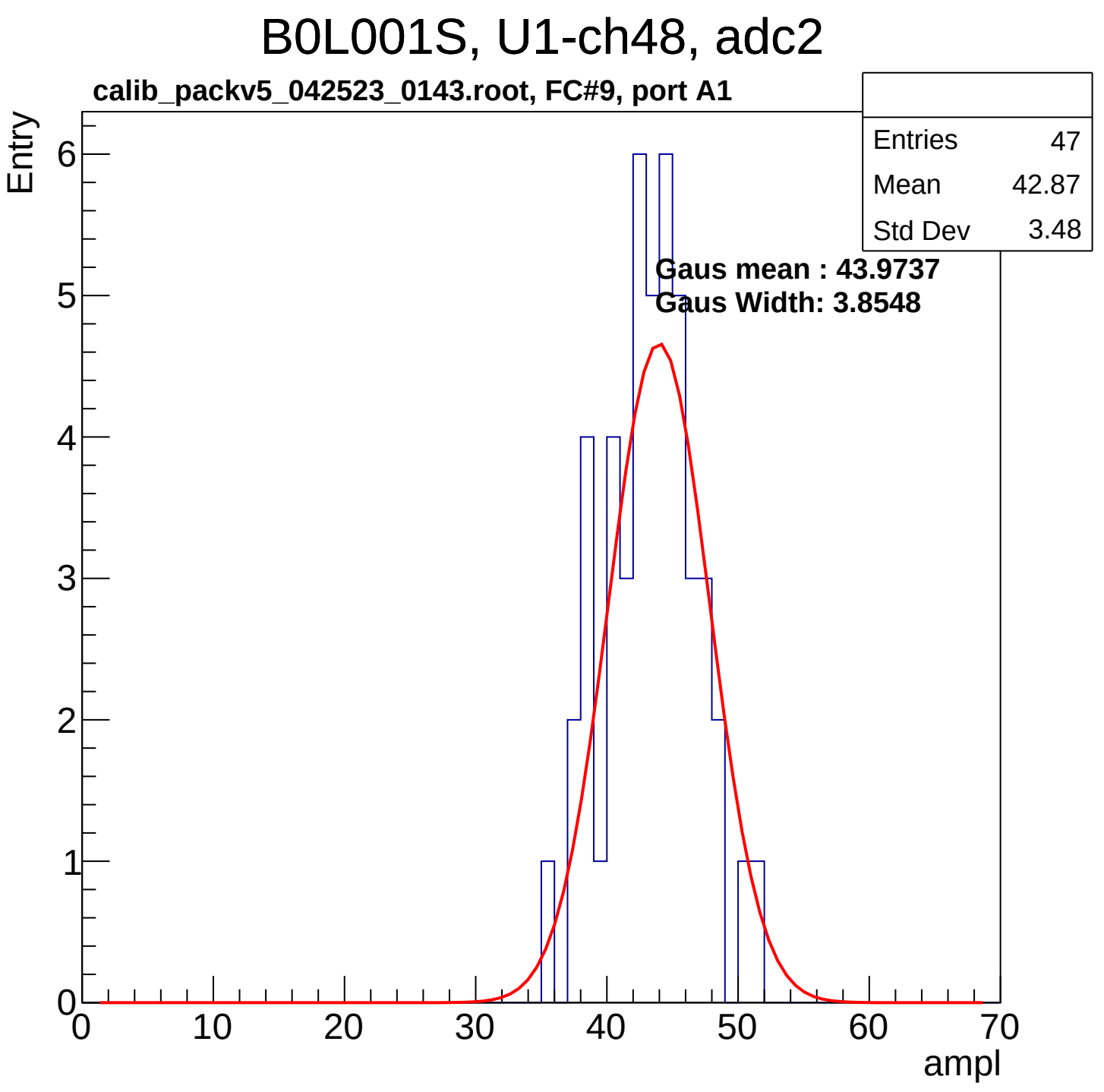

B0L001S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.69
Std Dev 8.574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

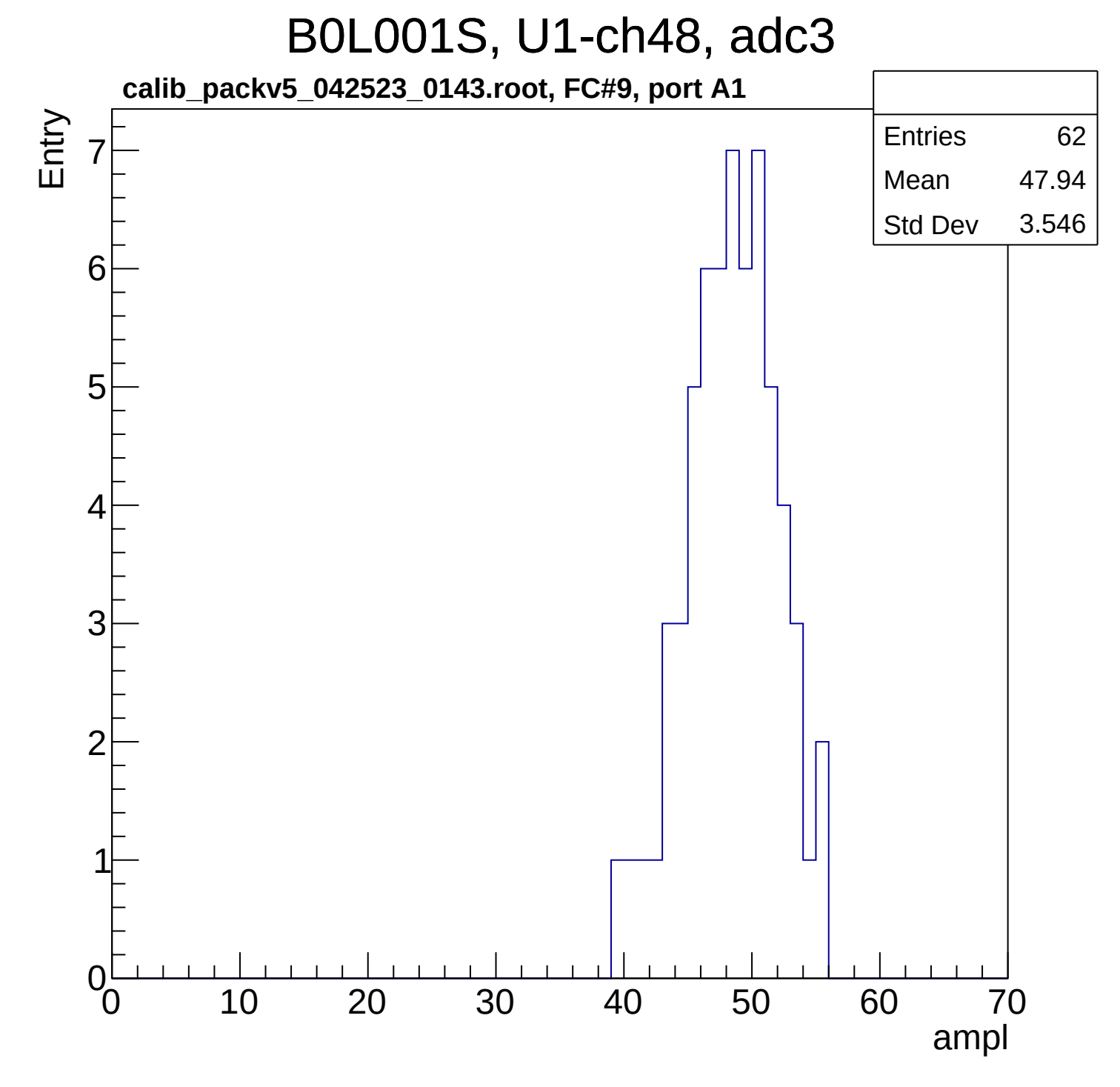

B0L001S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.25
Std Dev 2.046
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

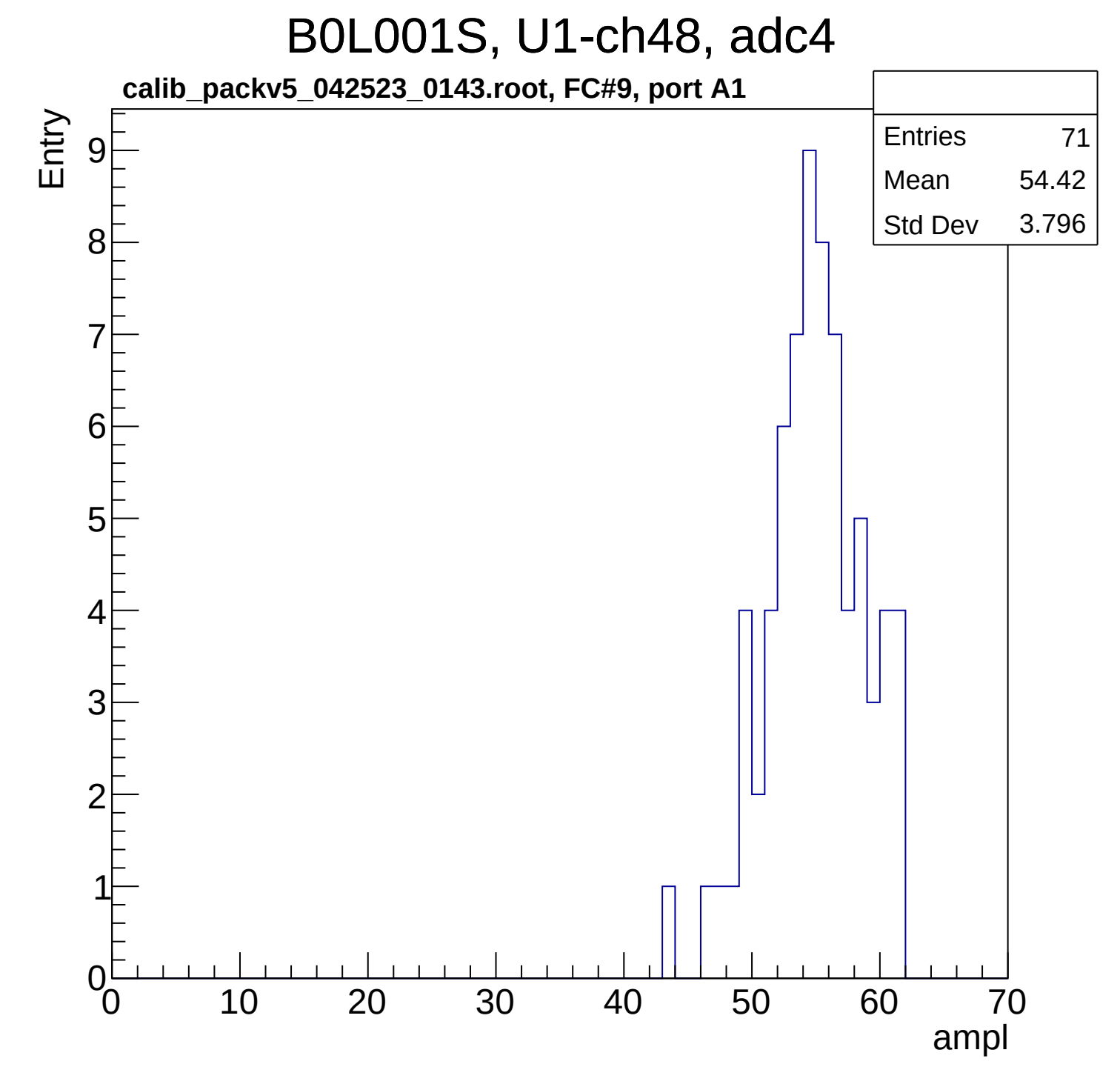

B0L001S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

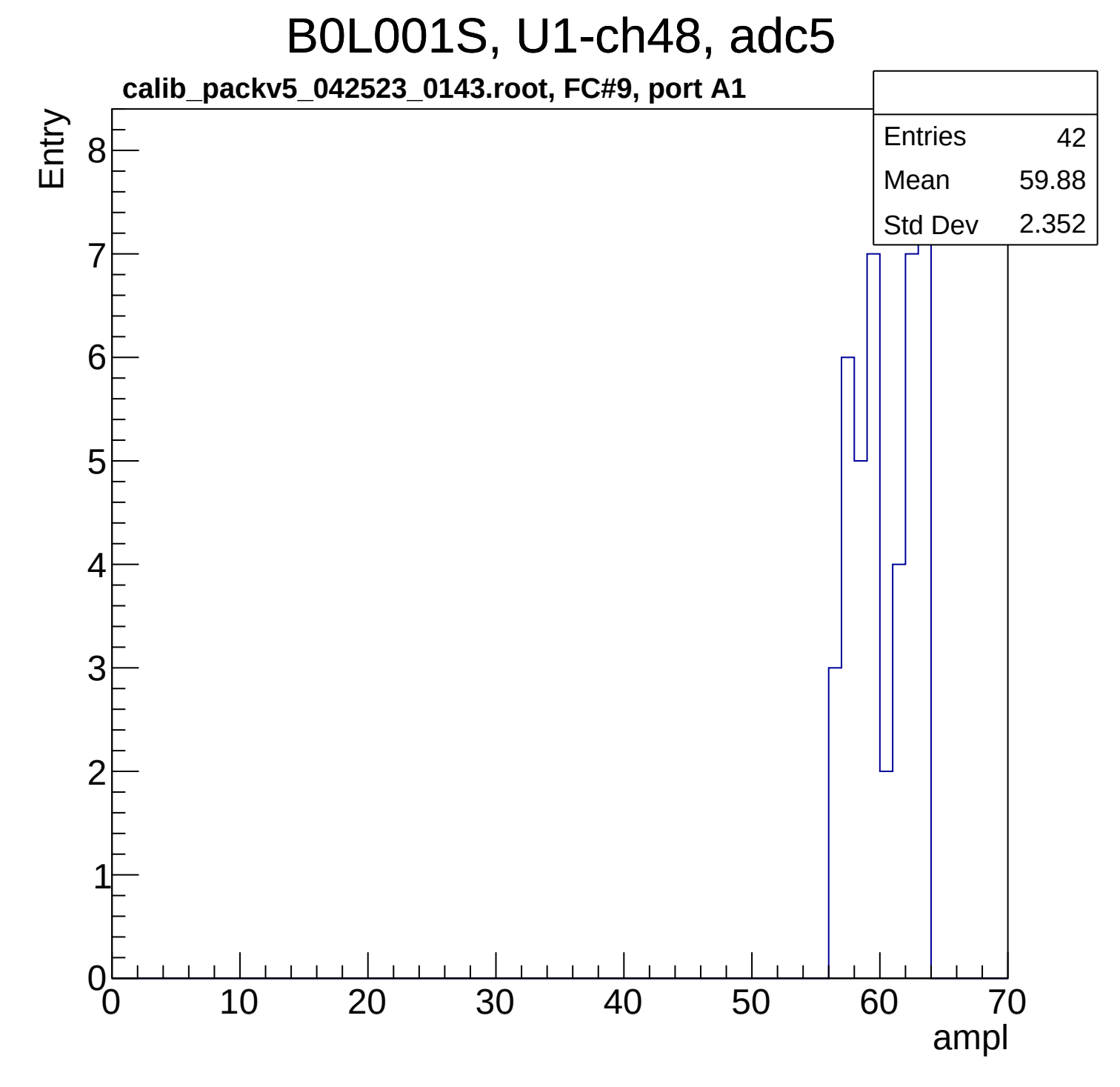

B0L001S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 27.82
Std Dev 4.579
Gaus mean : 28.5067
Gaus Width: 3.1130
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

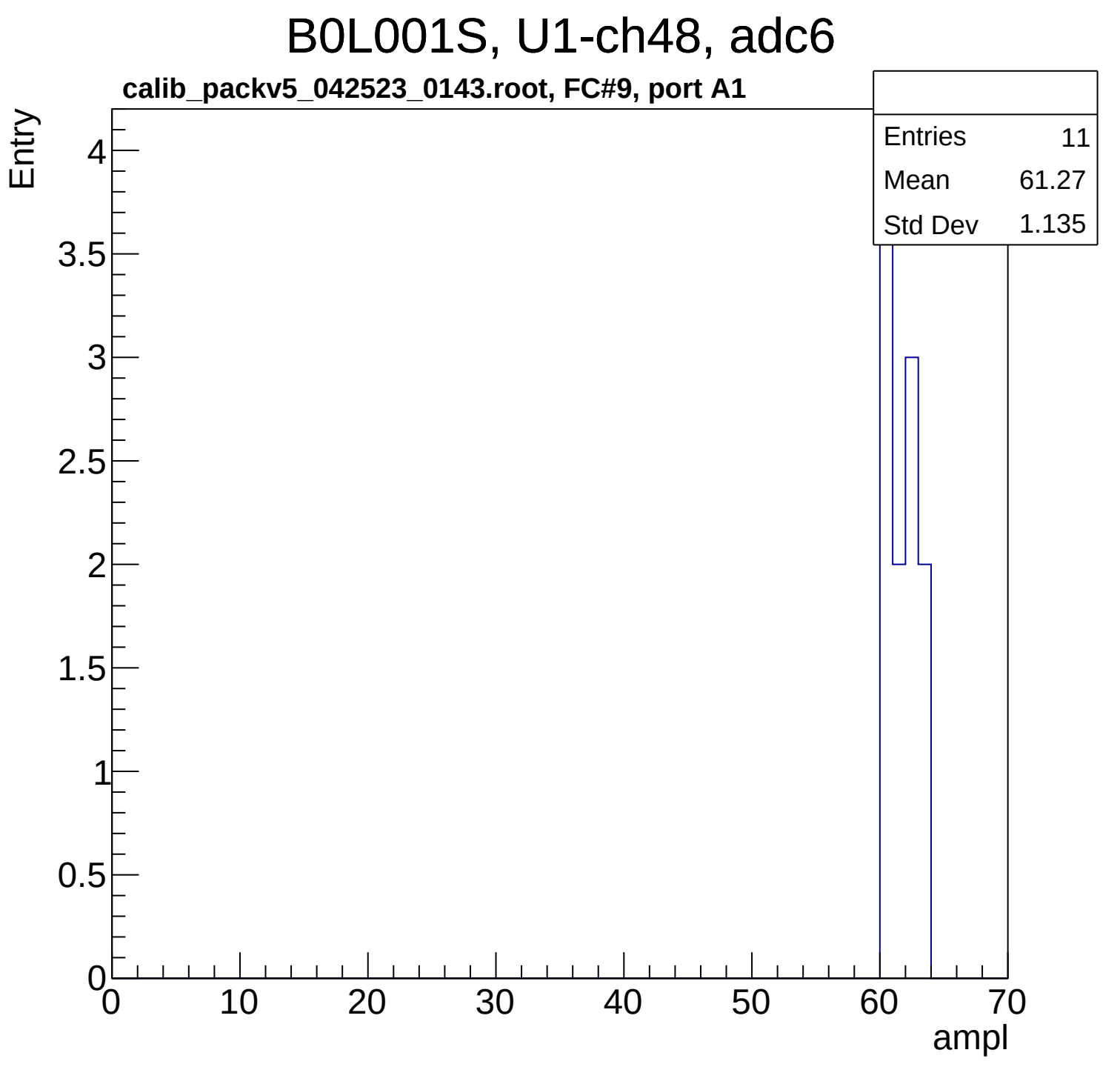

B0L001S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 36.19
Std Dev 3.483
Gaus mean : 36.4918
Gaus Width: 3.9032
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

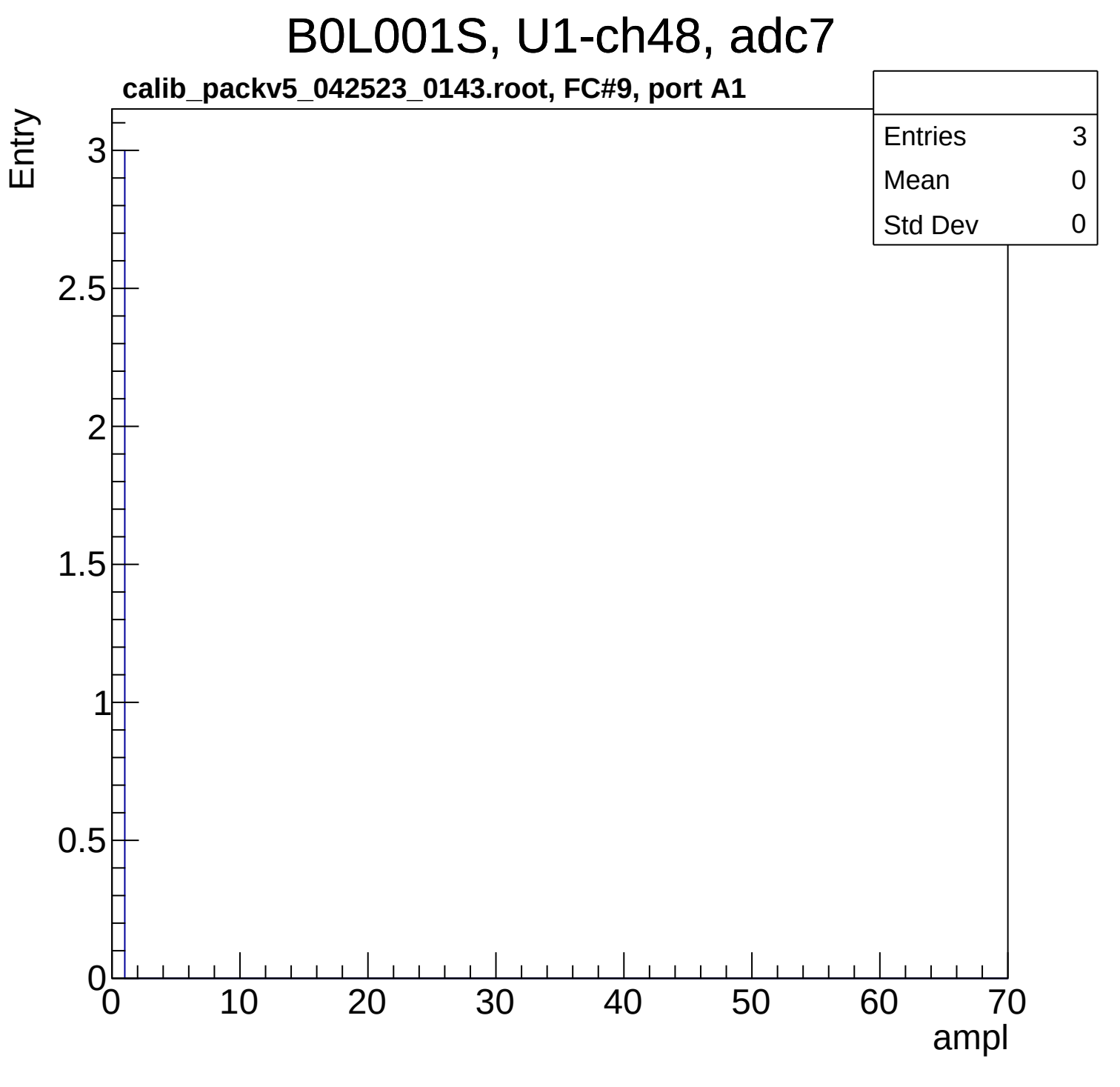

B0L001S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 42.87
Std Dev 3.48
Gaus mean : 43.9737
Gaus Width: 3.8548
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

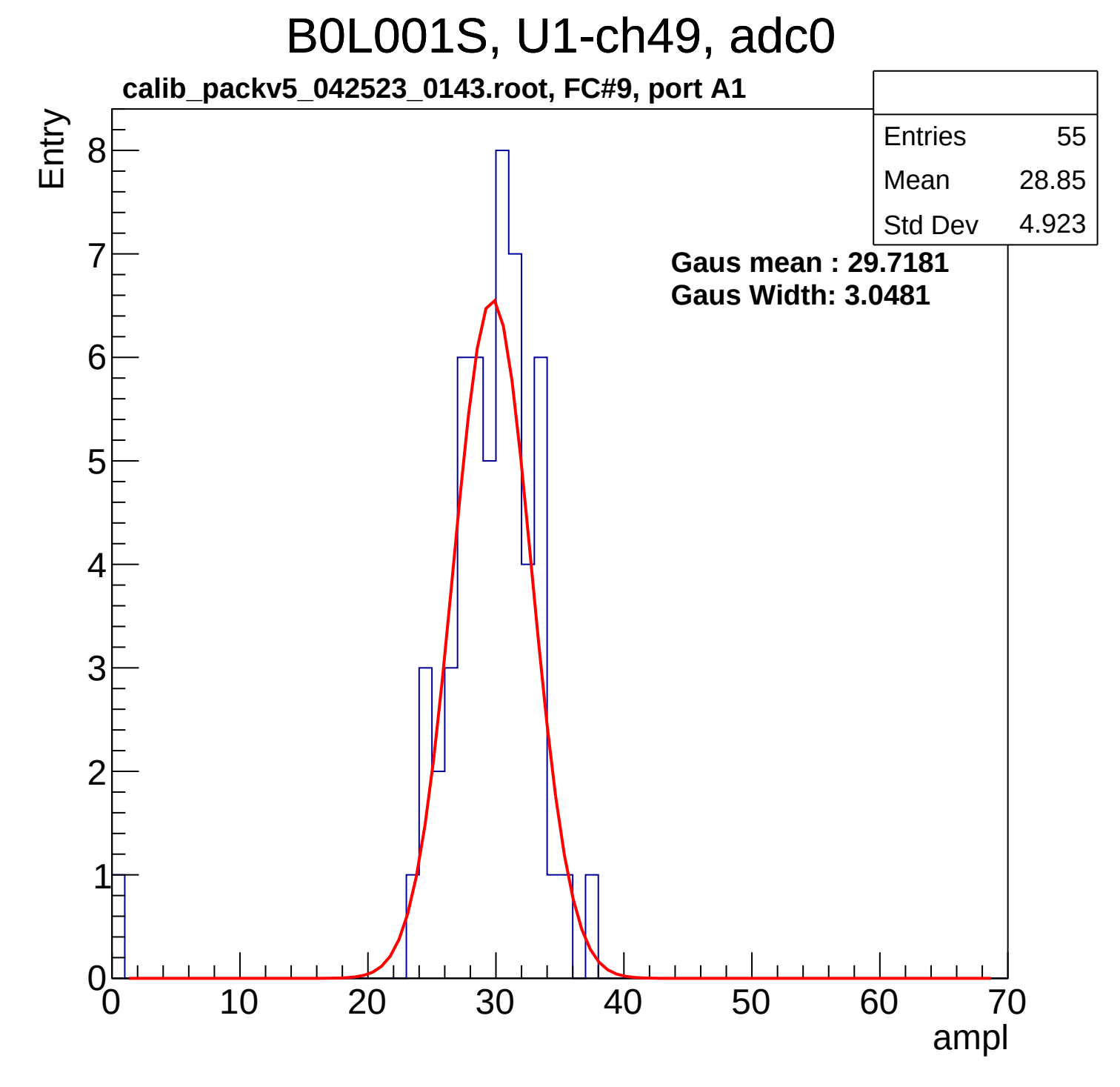

B0L001S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 47.94
Std Dev 3.546
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

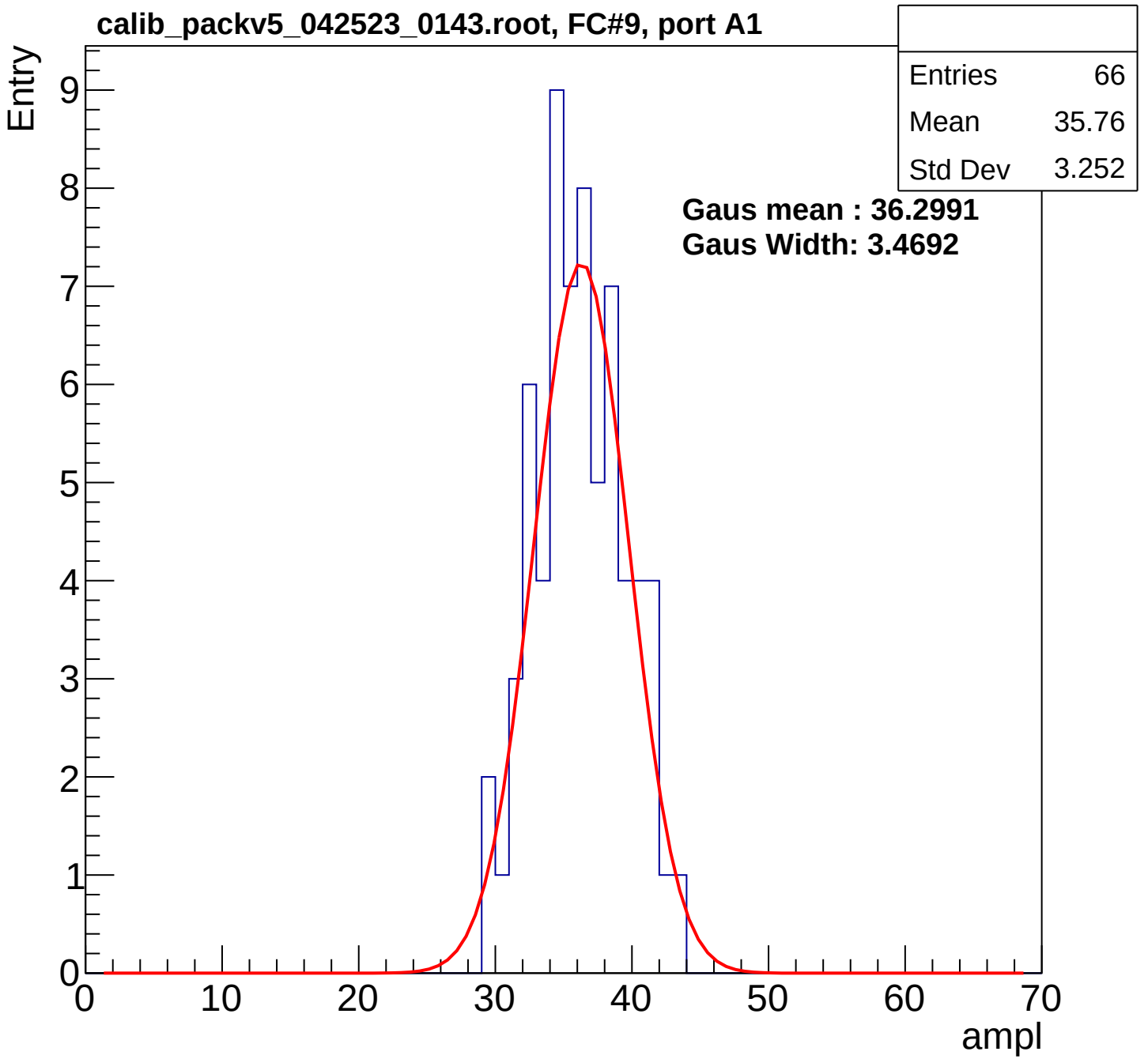

B0L001S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 54.42
Std Dev 3.796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

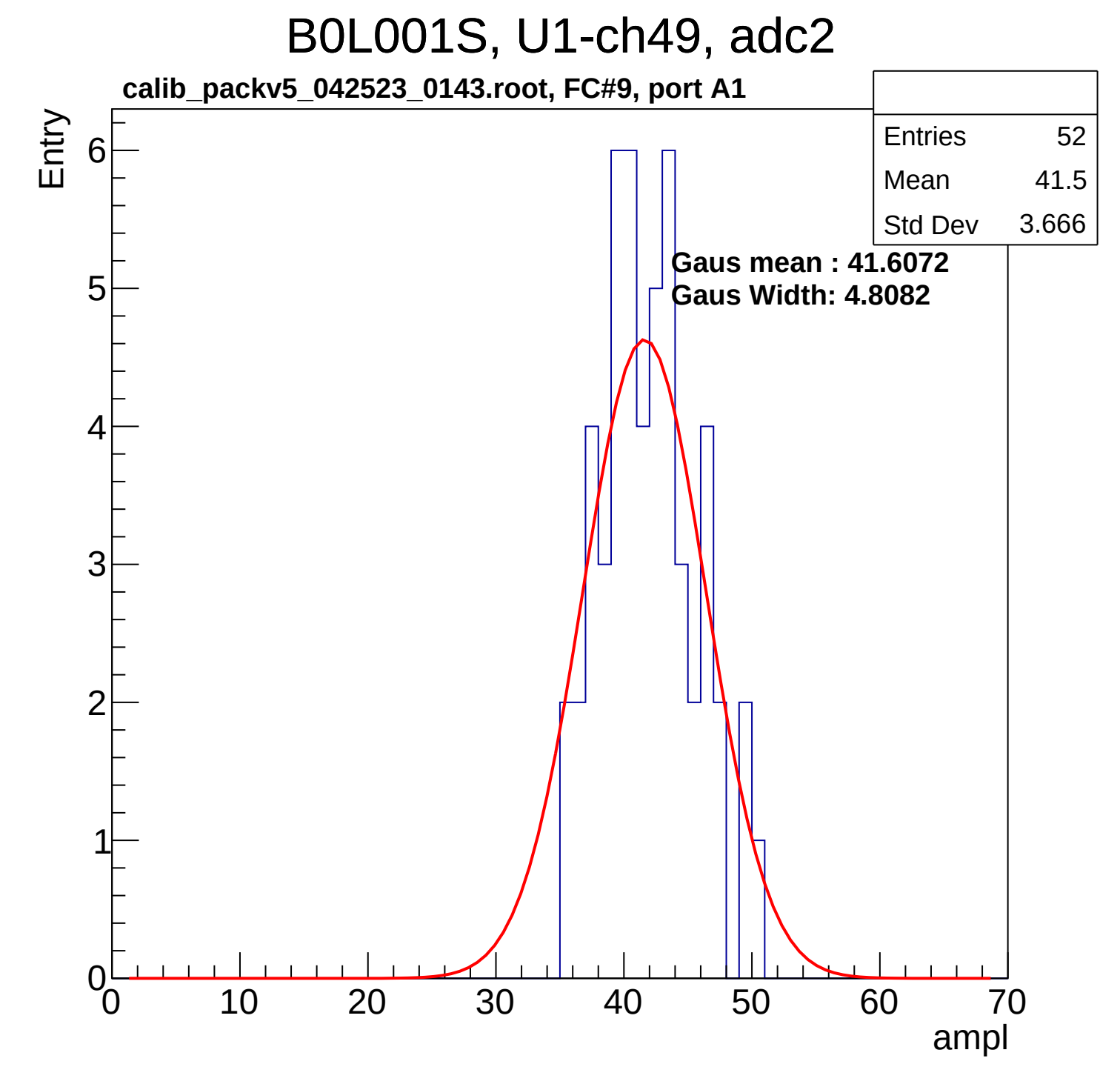

B0L001S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.88
Std Dev 2.352
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

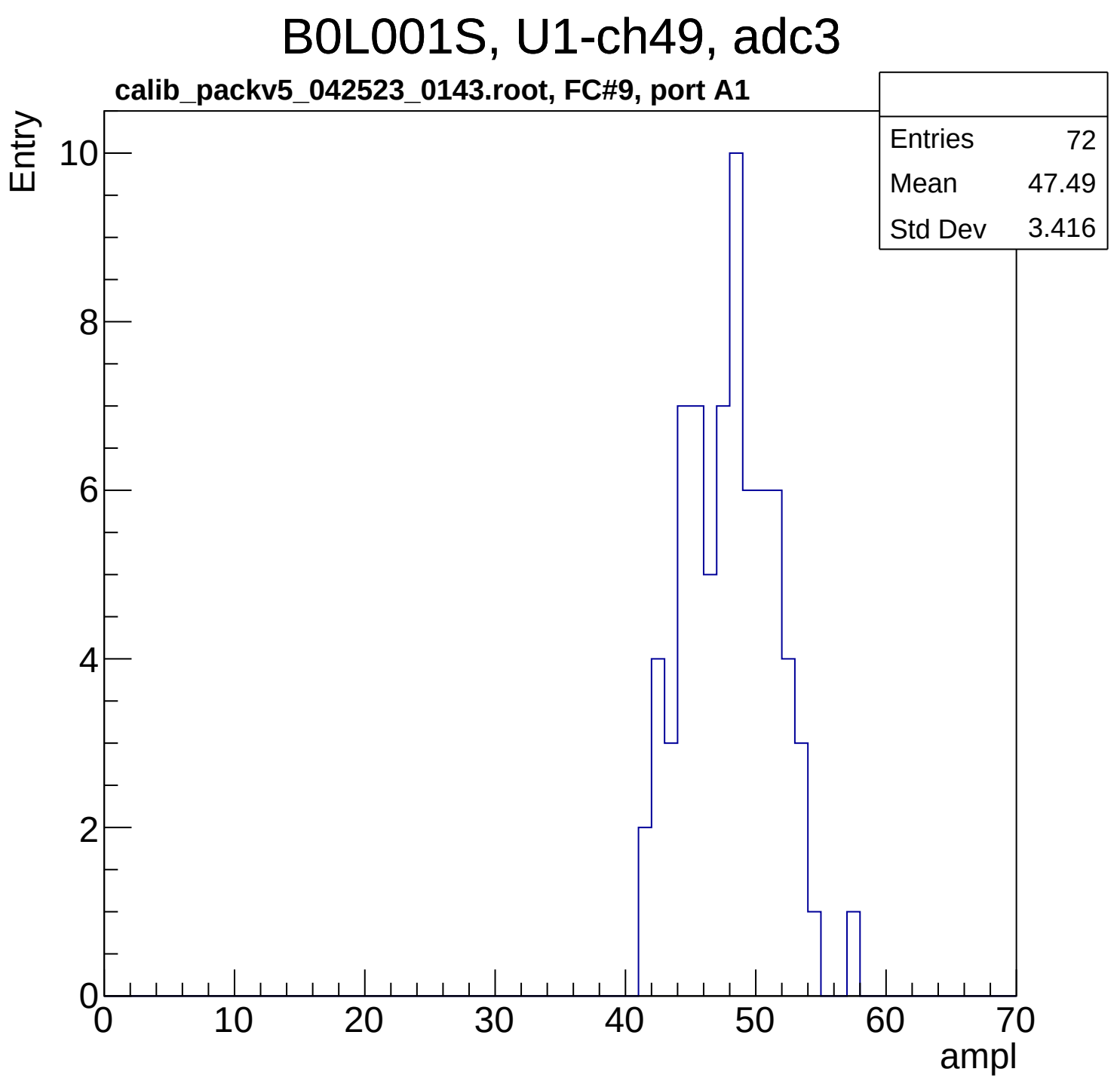

B0L001S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.27
Std Dev 1.135
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

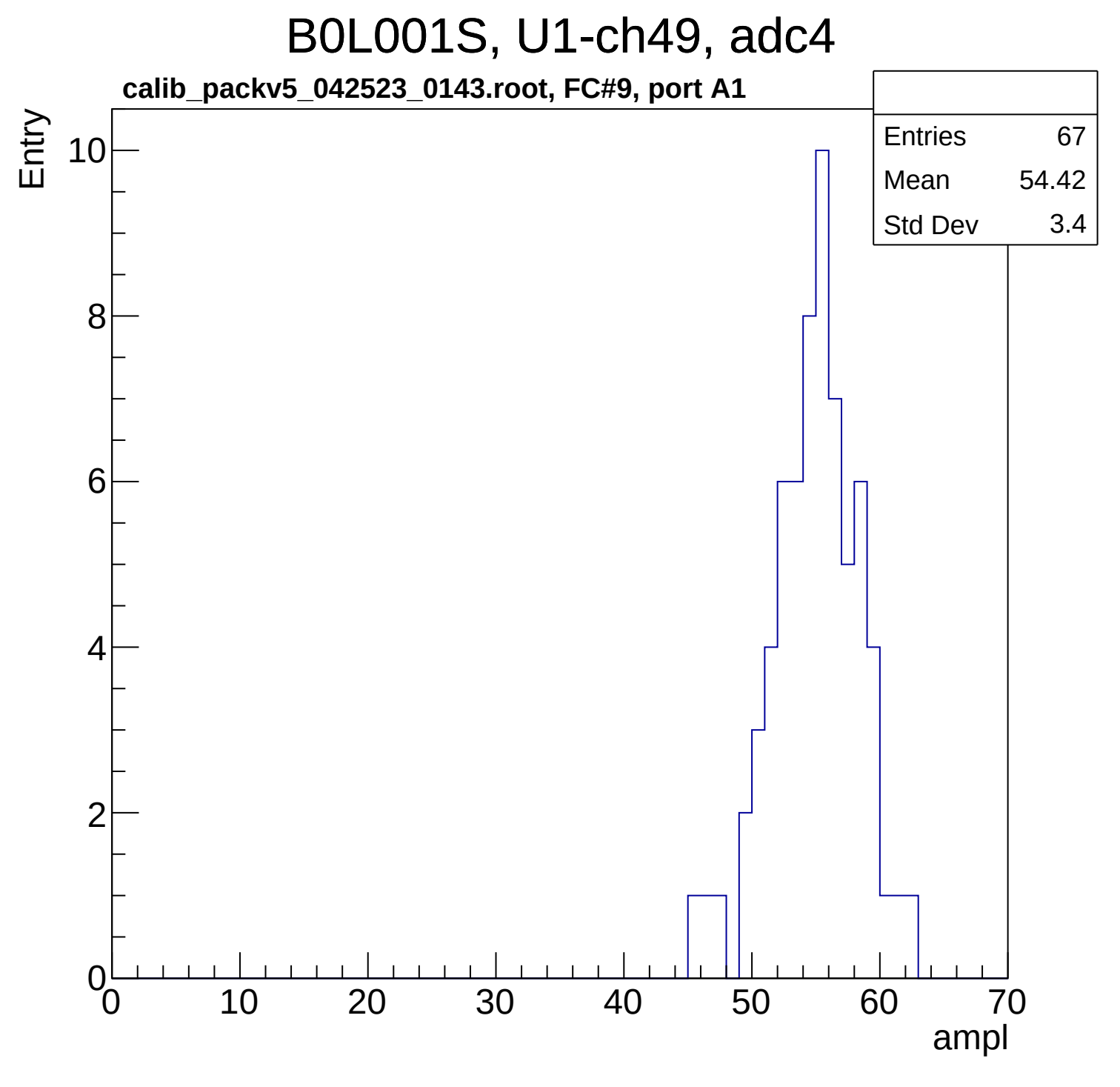

B0L001S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

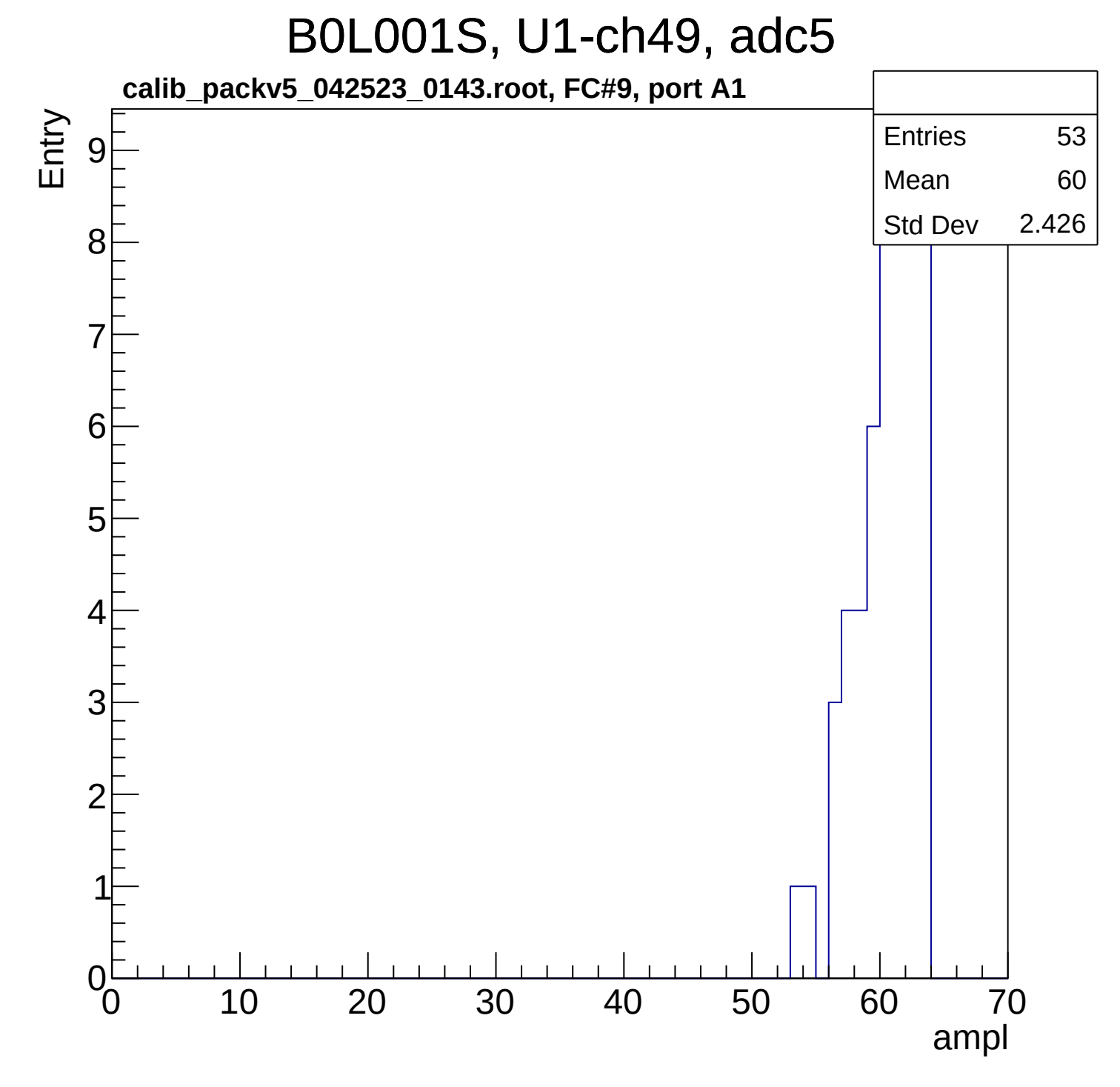

B0L001S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 28.85
Std Dev 4.923
Gaus mean : 29.7181
Gaus Width: 3.0481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

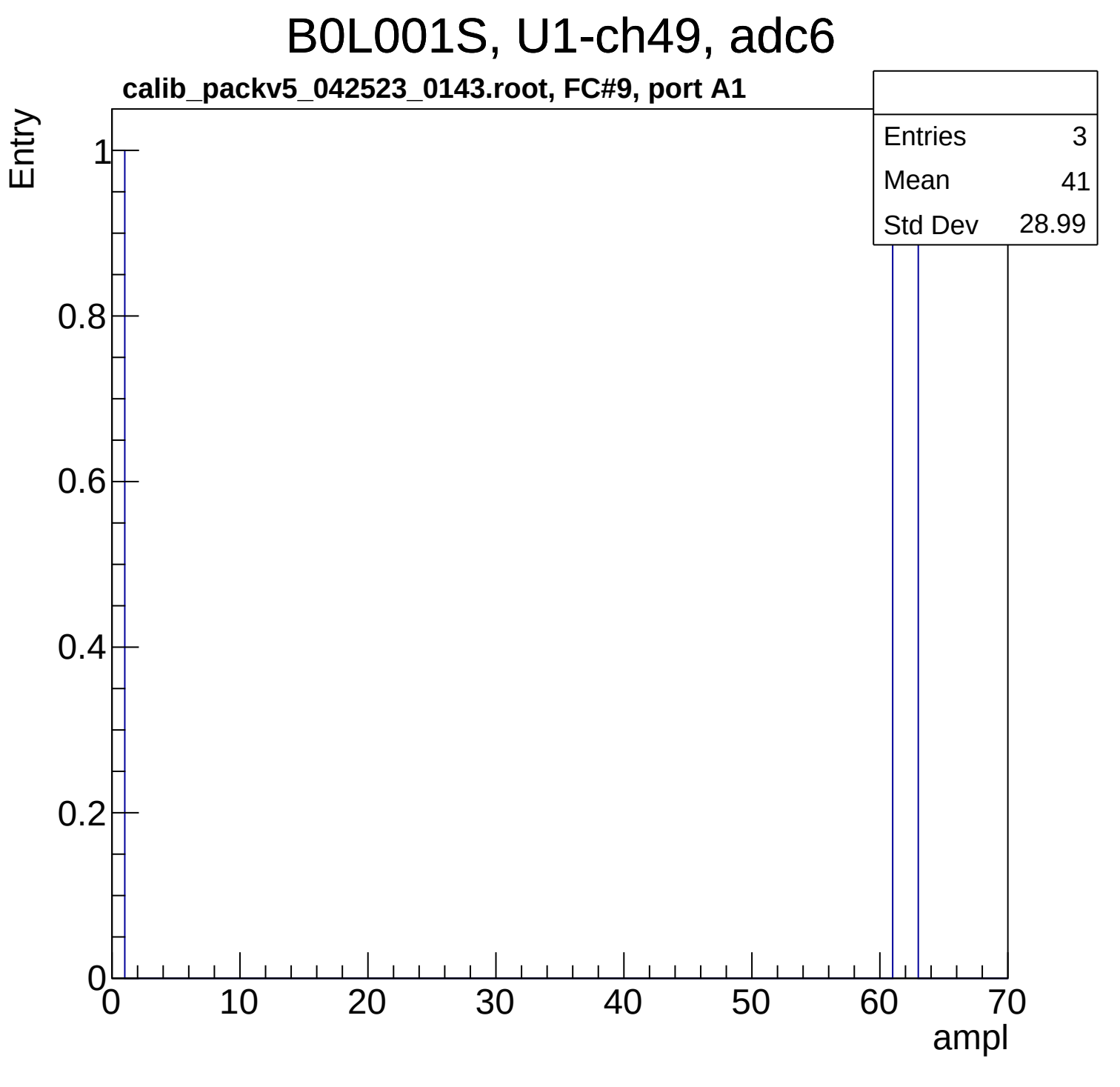

B0L001S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 35.76
Std Dev 3.252
Gaus mean : 36.2991
Gaus Width: 3.4692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

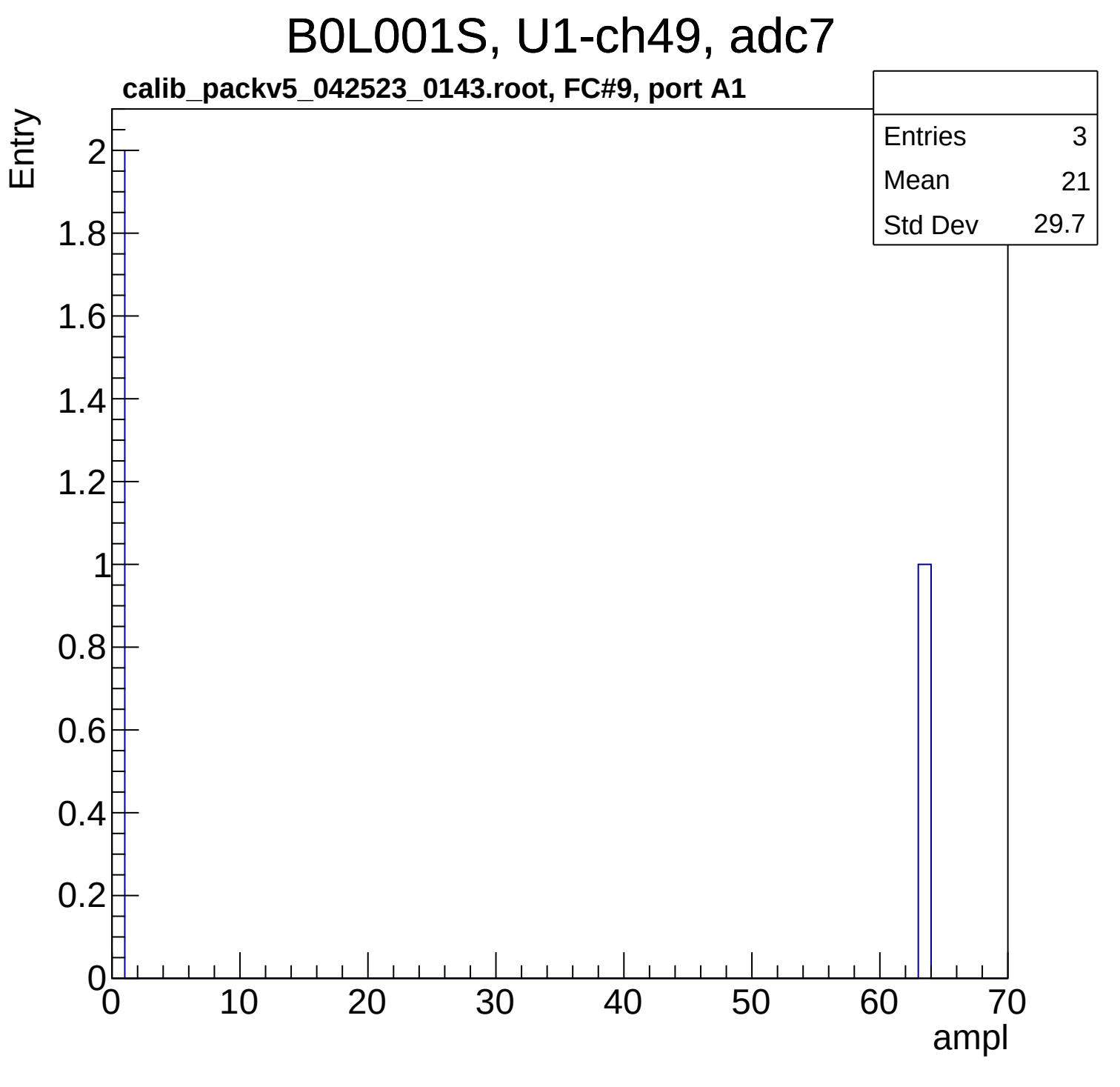

B0L001S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 41.5
Std Dev 3.666
Gaus mean : 41.6072
Gaus Width: 4.8082
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

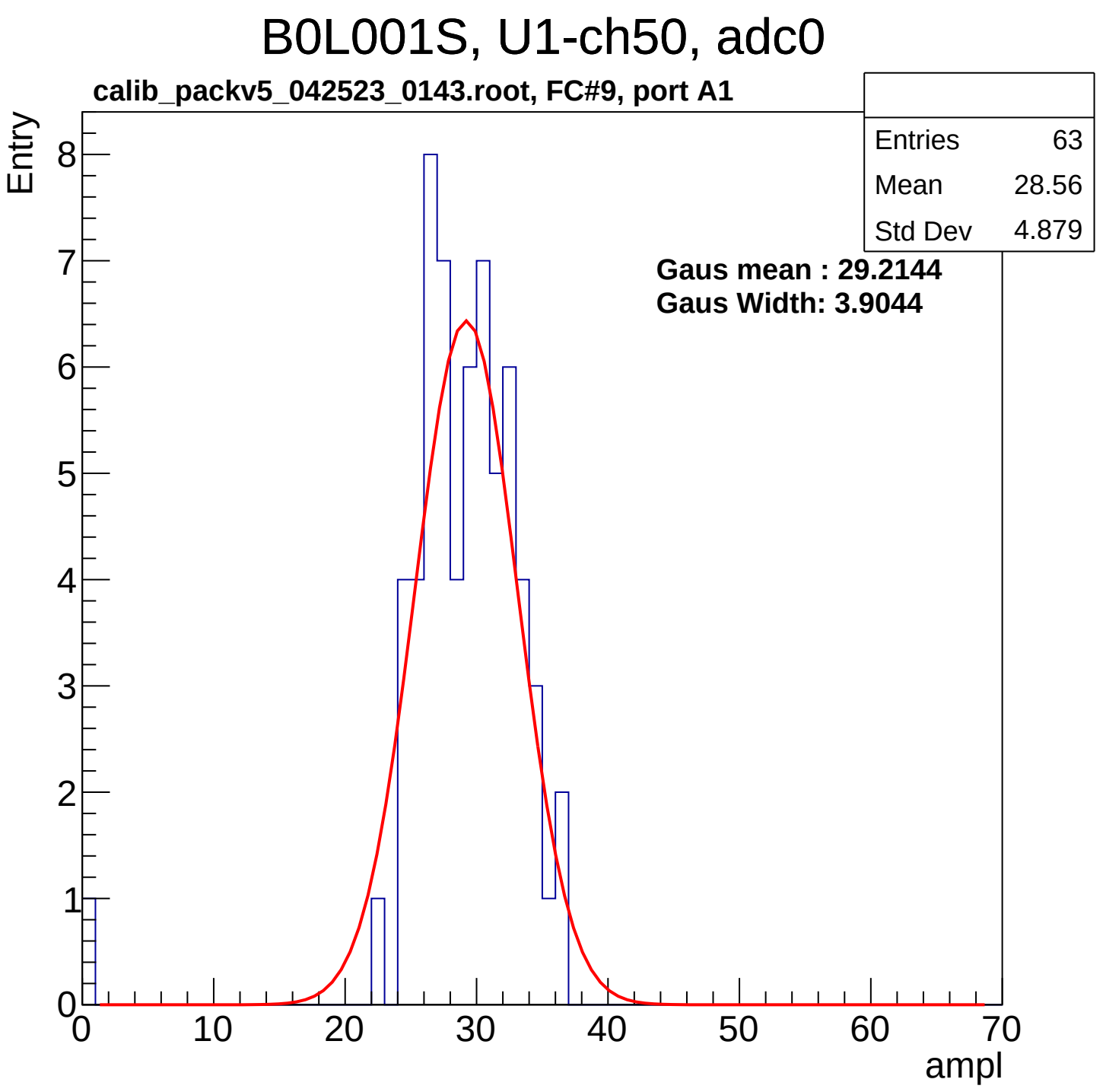

B0L001S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 47.49
Std Dev 3.416
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

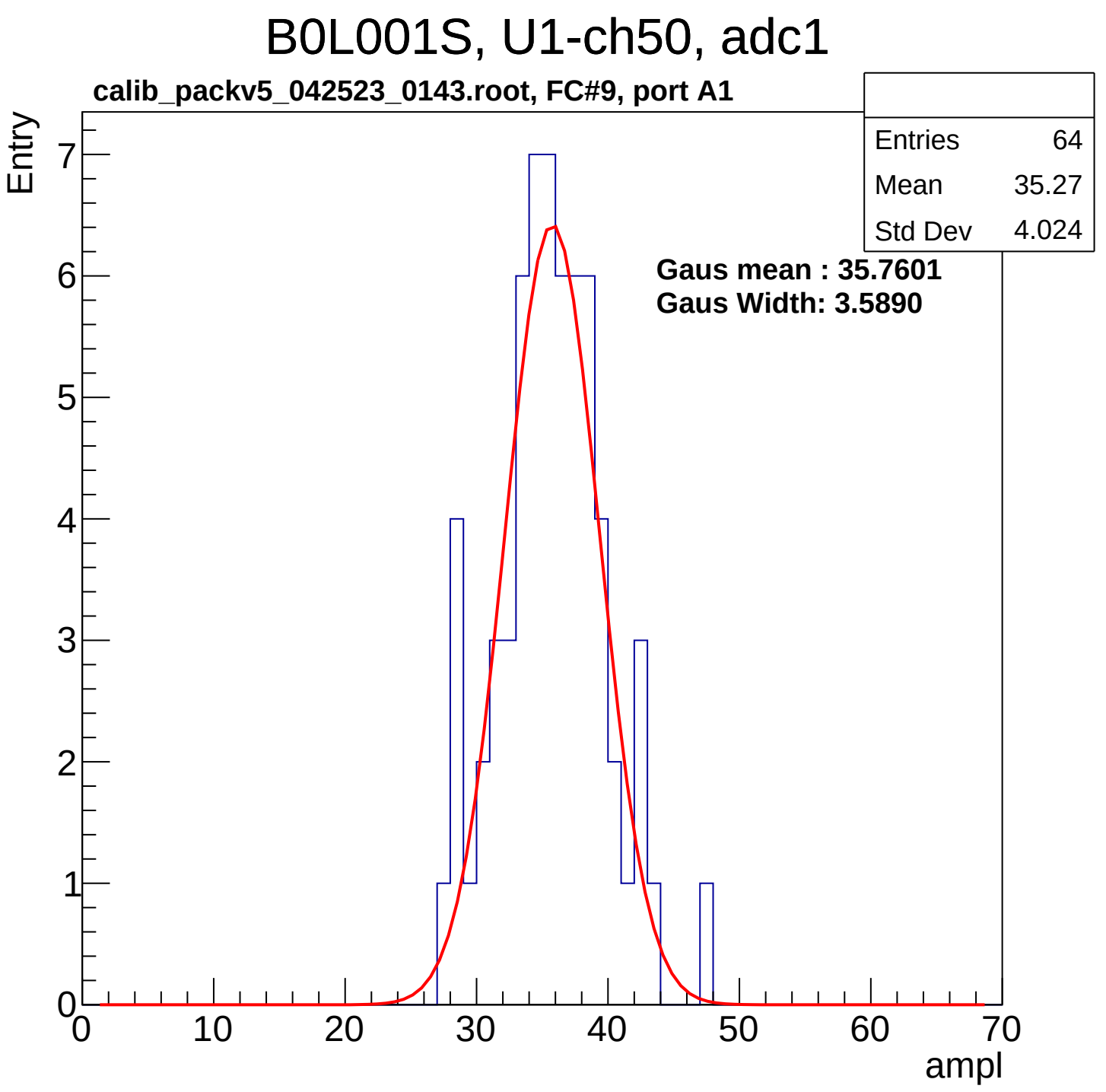

B0L001S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 54.42
Std Dev 3.4
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 60
Std Dev 2.426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41
Std Dev 28.99
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 28.56
Std Dev 4.879
Gaus mean : 29.2144
Gaus Width: 3.9044
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 35.27
Std Dev 4.024
Gaus mean : 35.7601
Gaus Width: 3.5890
Entry
ampl
0
1
2
3
4
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6
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0
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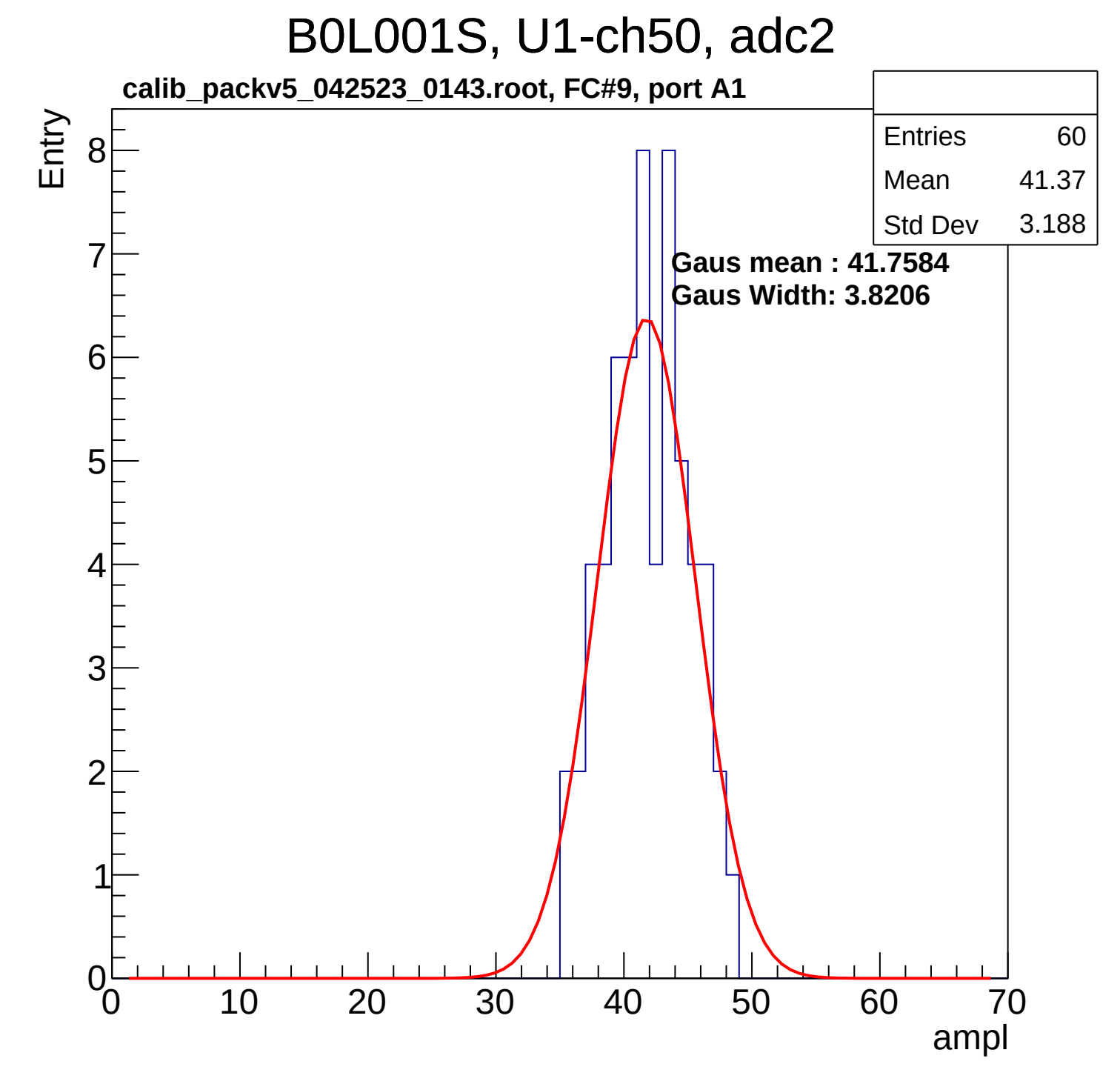
B0L001S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 41.37
Std Dev 3.188
Gaus mean : 41.7584
Gaus Width: 3.8206
Entry
ampl
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3
4
5
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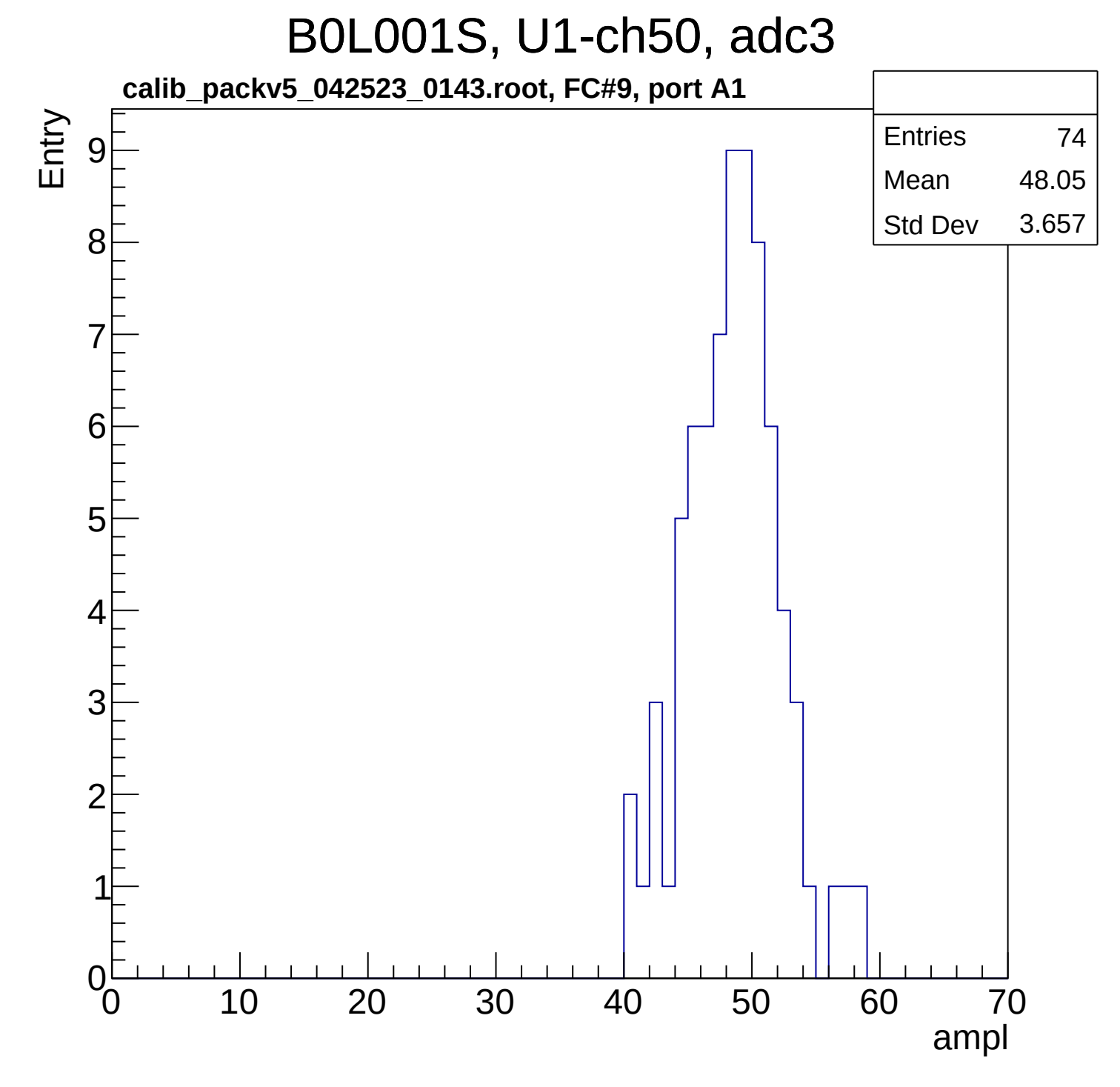
B0L001S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 48.05
Std Dev 3.657
Entry
ampl
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3
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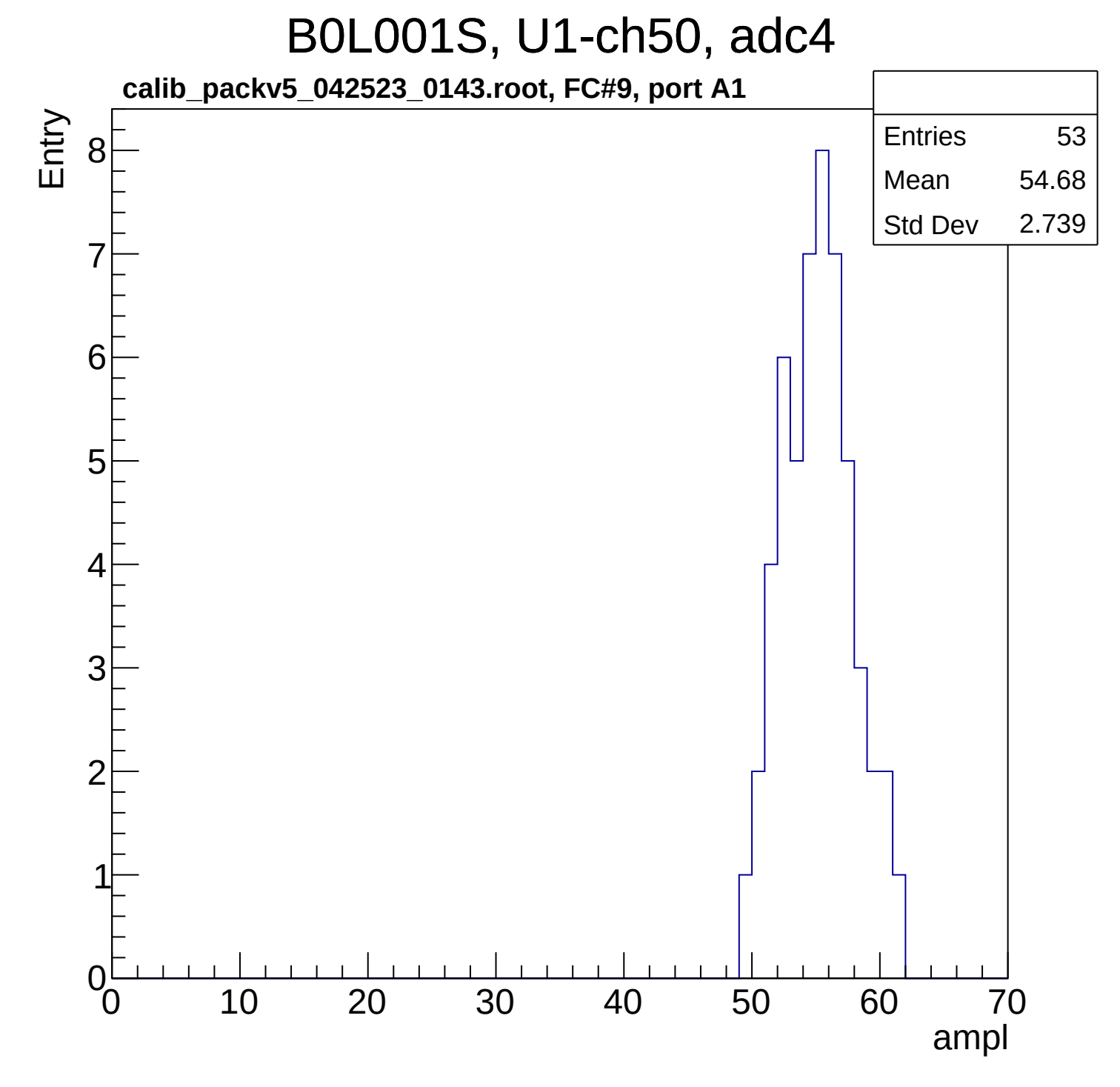
B0L001S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 54.68
Std Dev 2.739
Entry
ampl
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1
2
3
4
5
6
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0
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60
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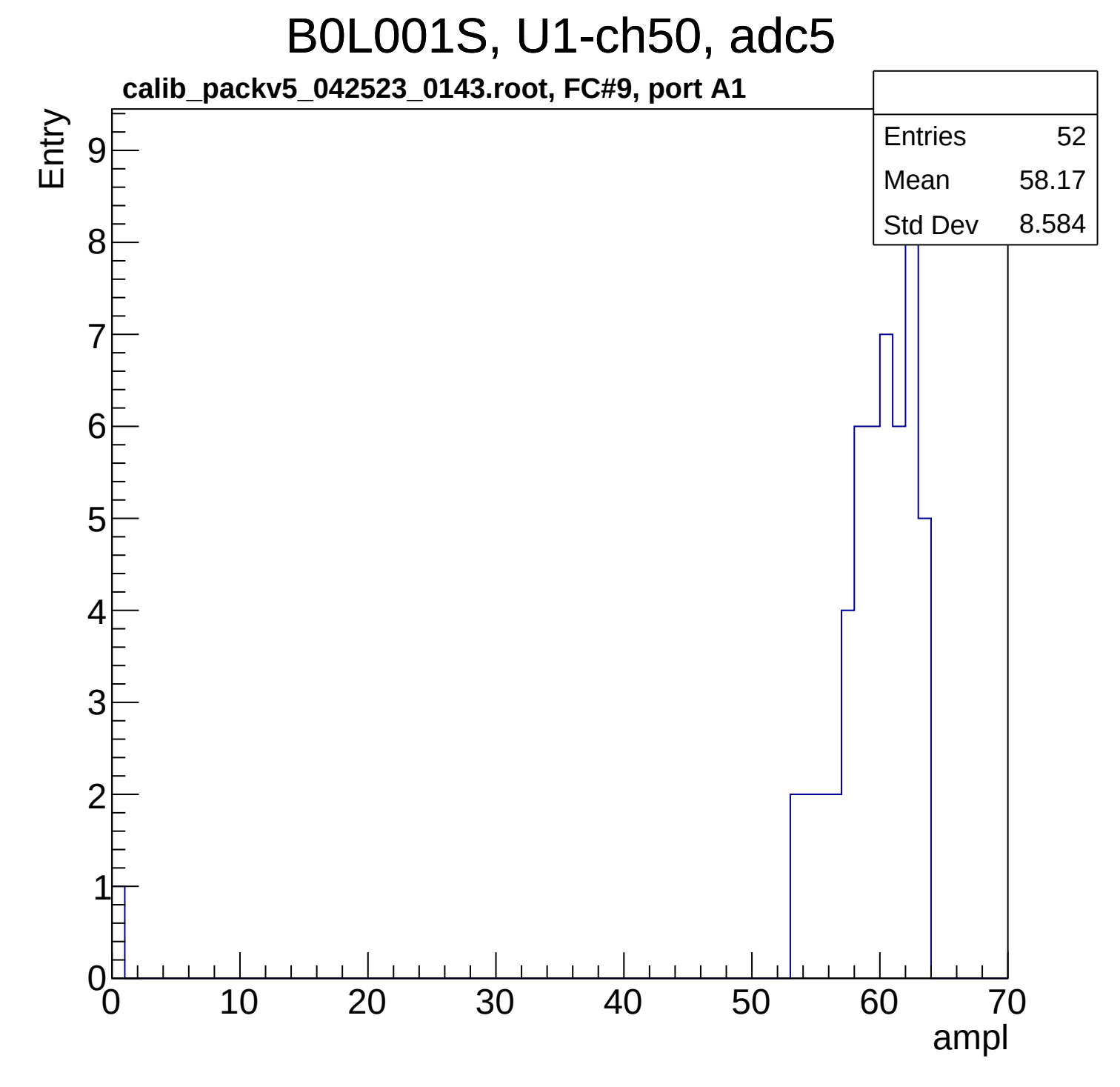
B0L001S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.17
Std Dev 8.584
Entry
ampl
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0
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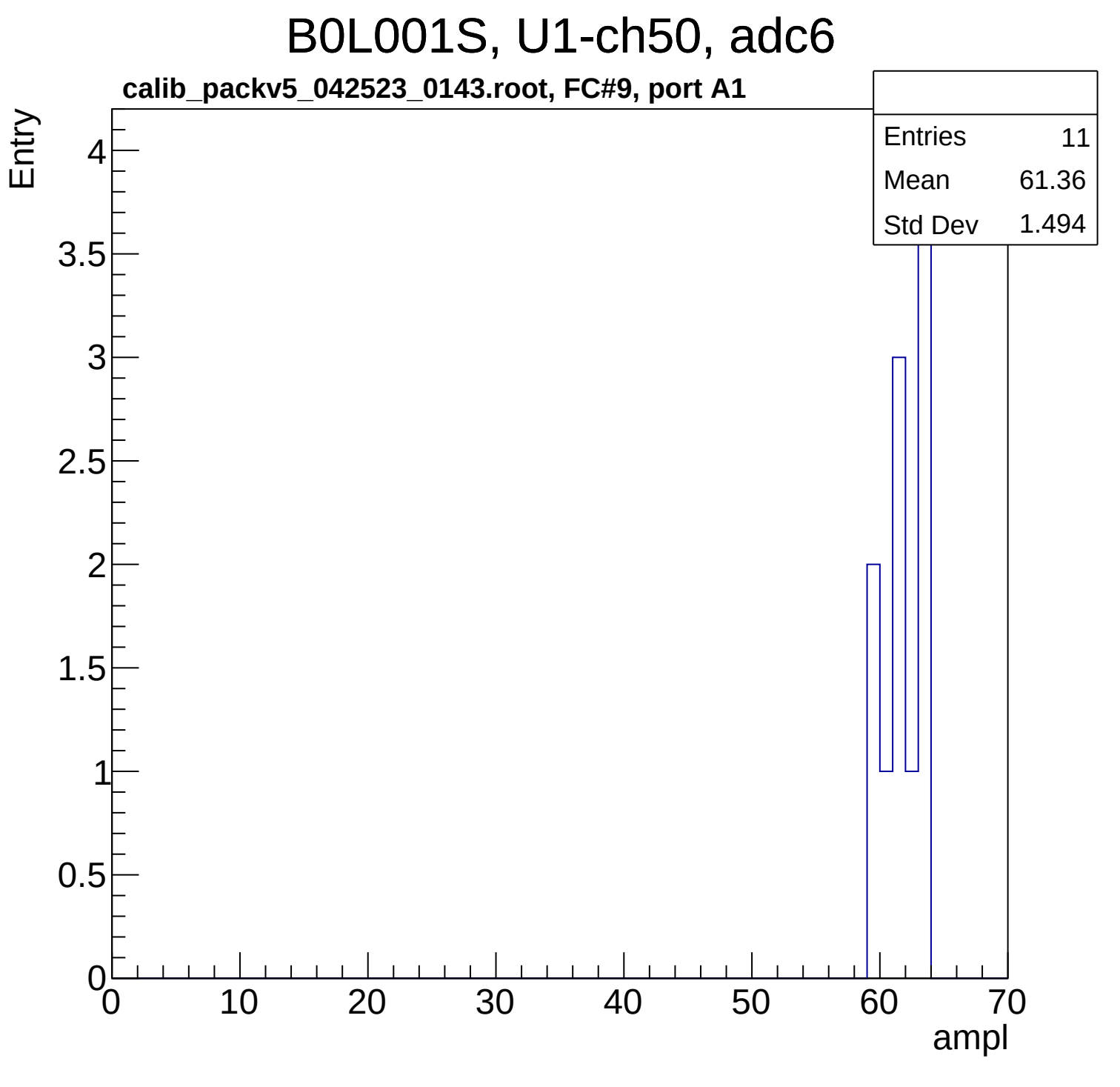
B0L001S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.36
Std Dev 1.494
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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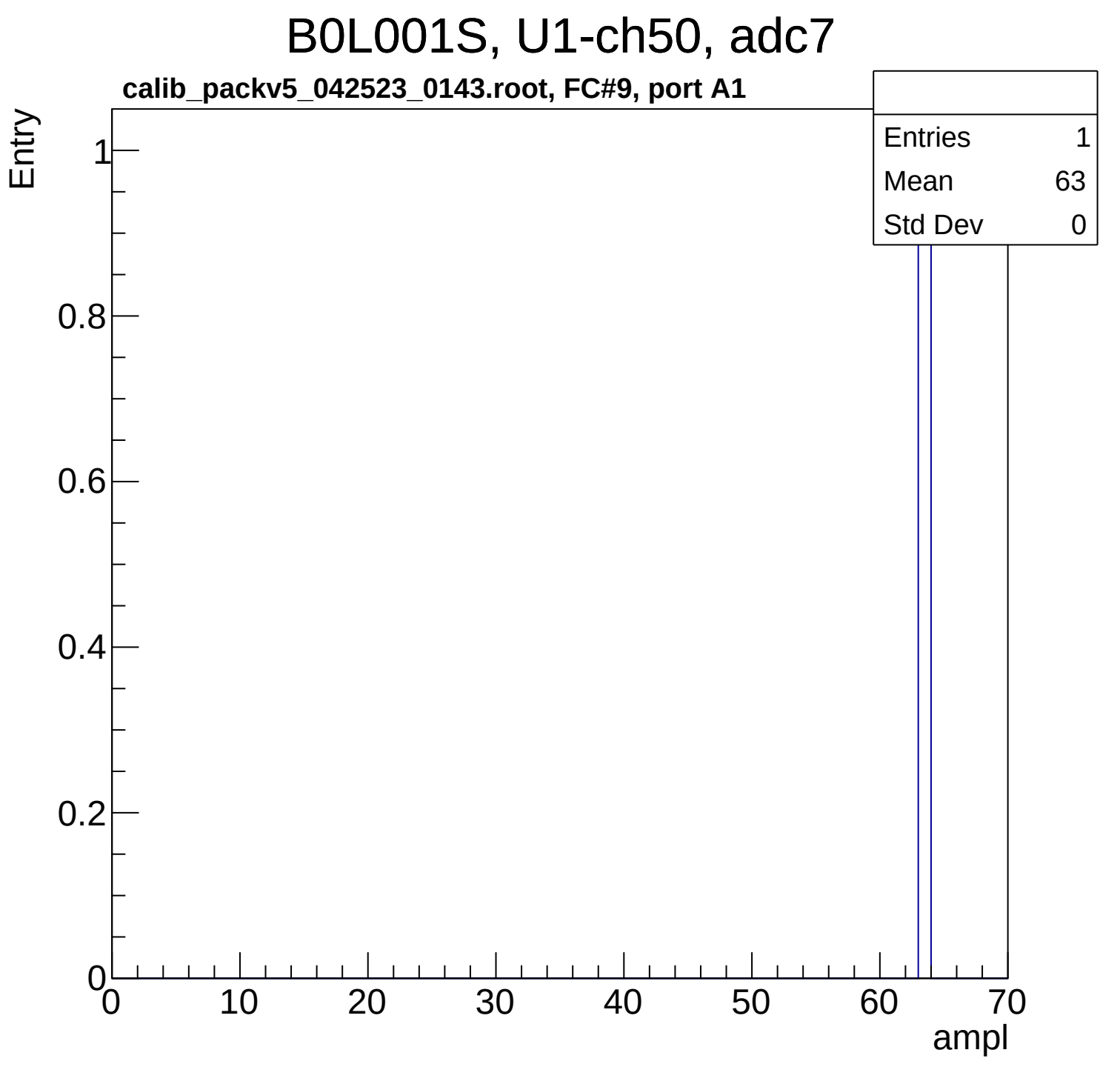
B0L001S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
30
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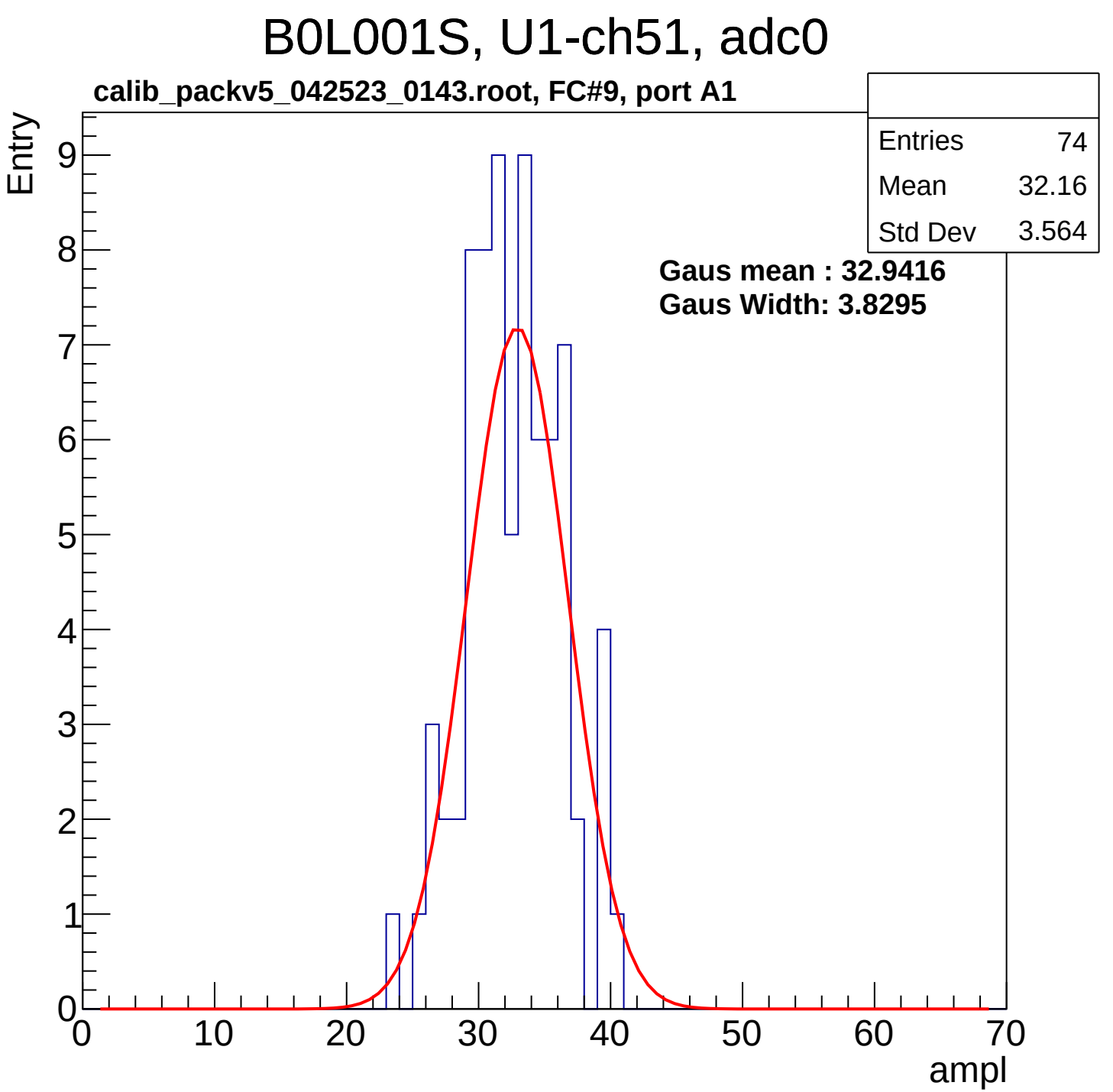
50
60
70
ampl


B0L001S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 32.16
Std Dev 3.564
Gaus mean : 32.9416
Gaus Width: 3.8295
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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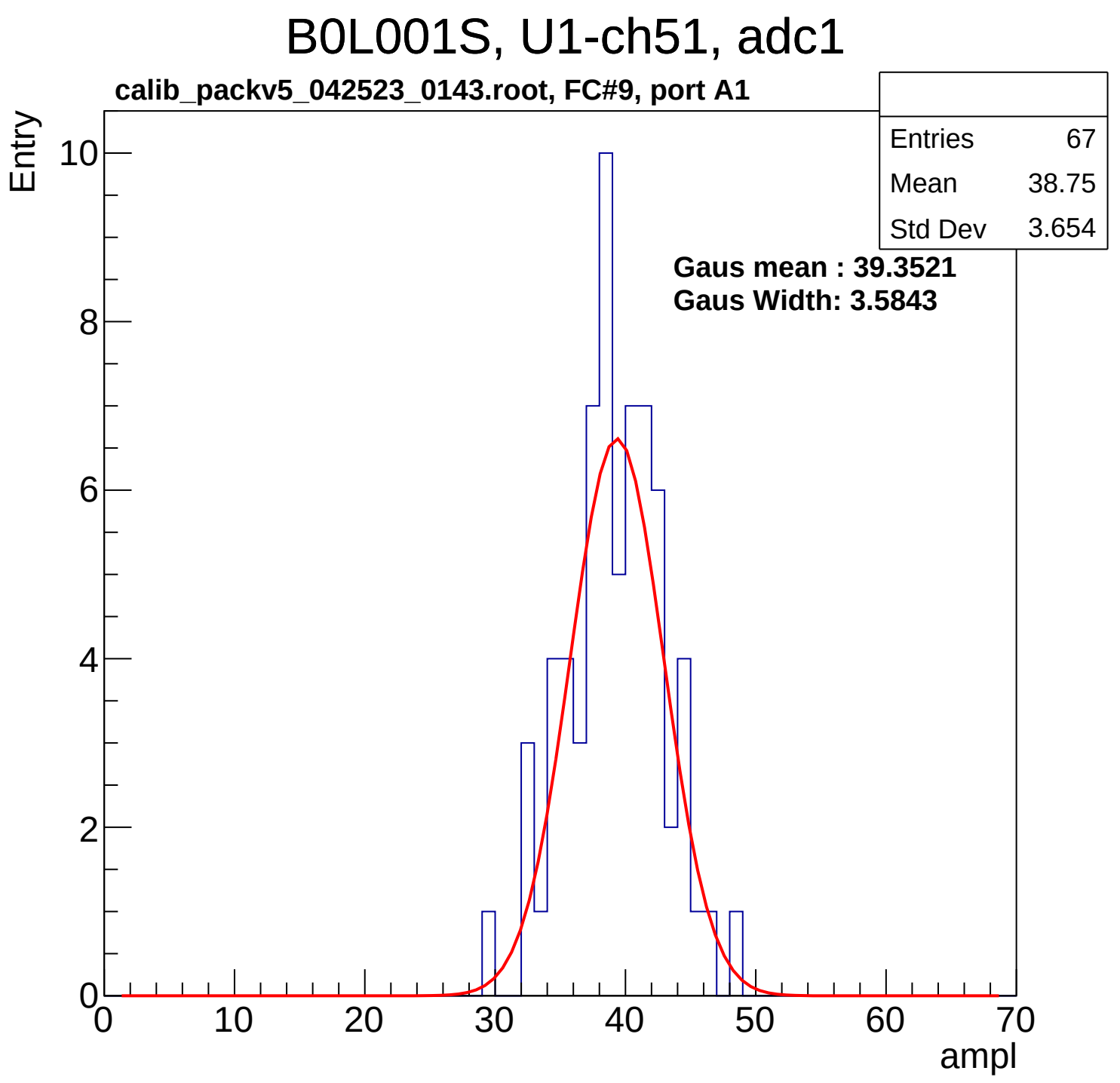
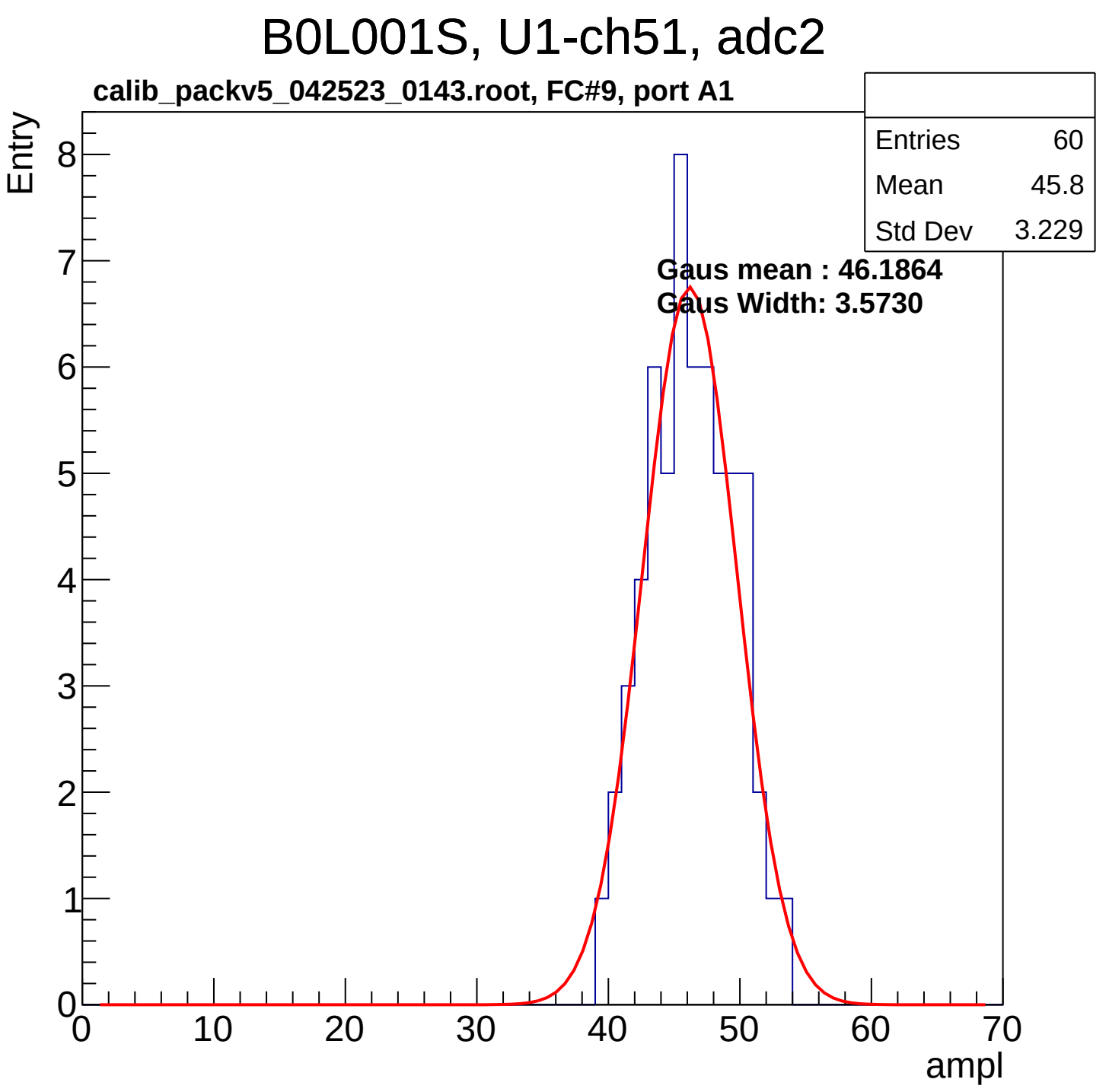

B0L001S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 38.75
Std Dev 3.654
Gaus mean : 39.3521
Gaus Width: 3.5843
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 45.8
Std Dev 3.229
Gaus mean : 46.1864
Gaus Width: 3.5730
Entry
ampl
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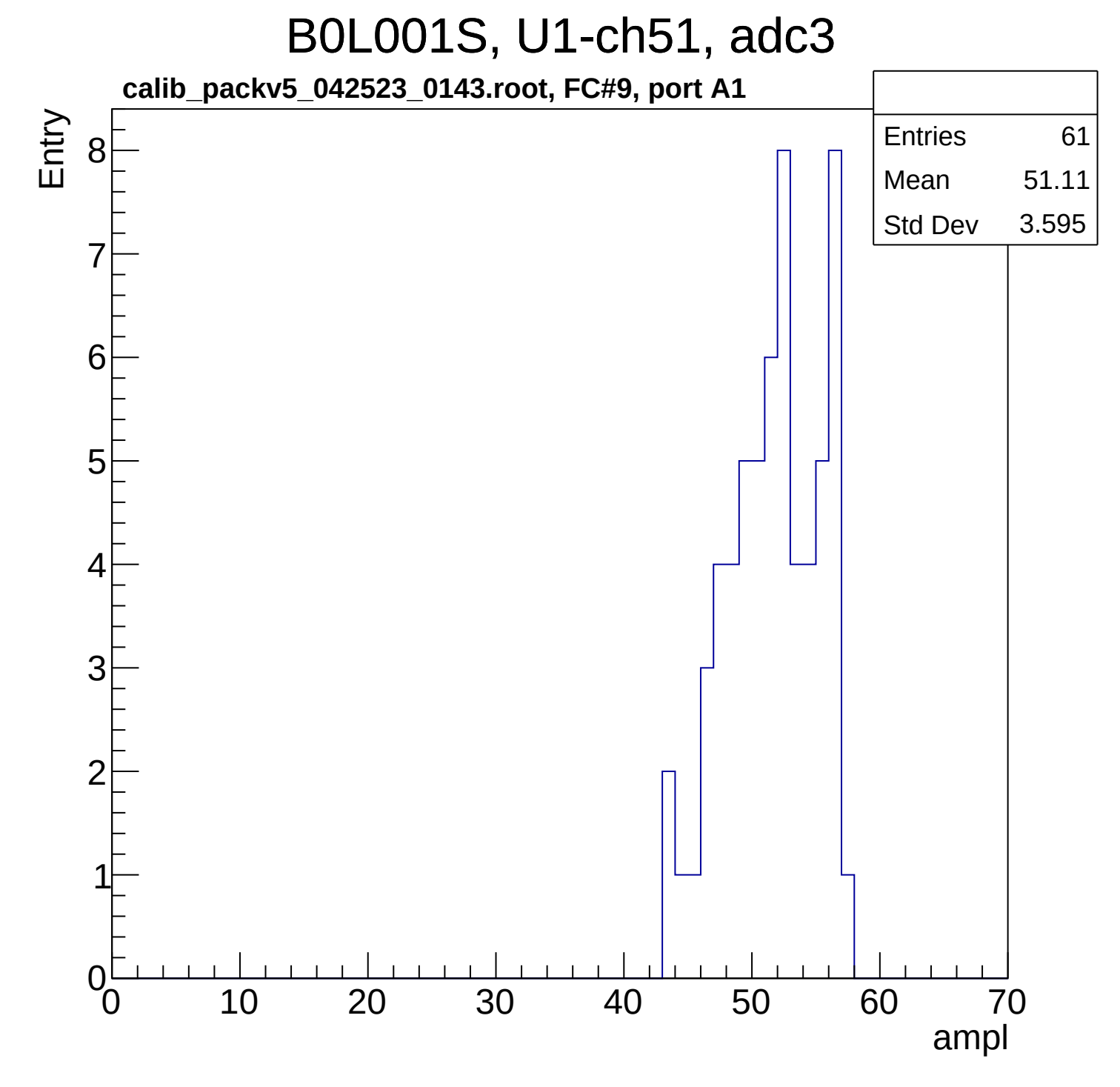
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 51.11
Std Dev 3.595
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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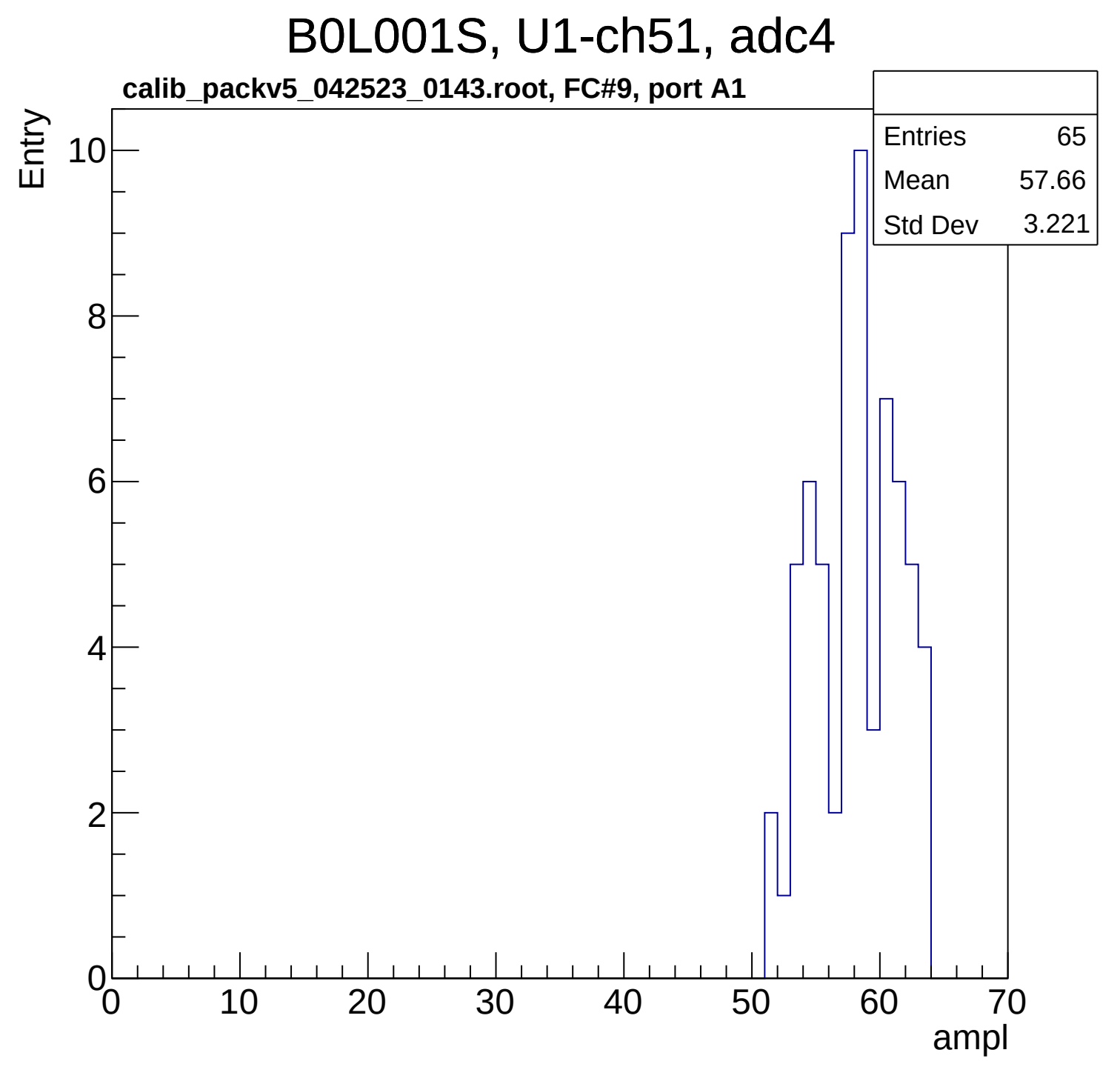
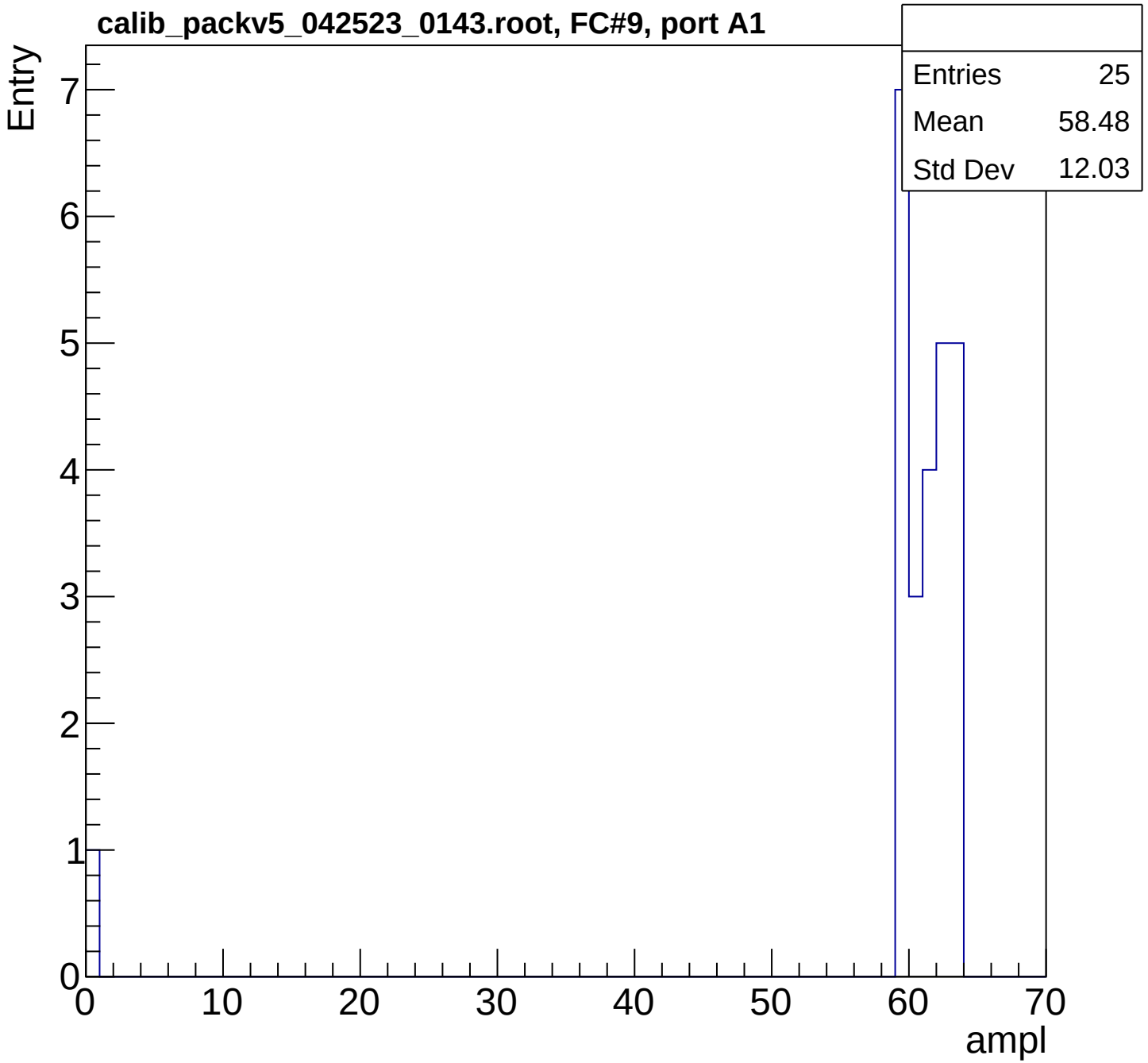
40
50
60
70


B0L001S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.66
Std Dev 3.221
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 58.48
Std Dev 12.03
Entry
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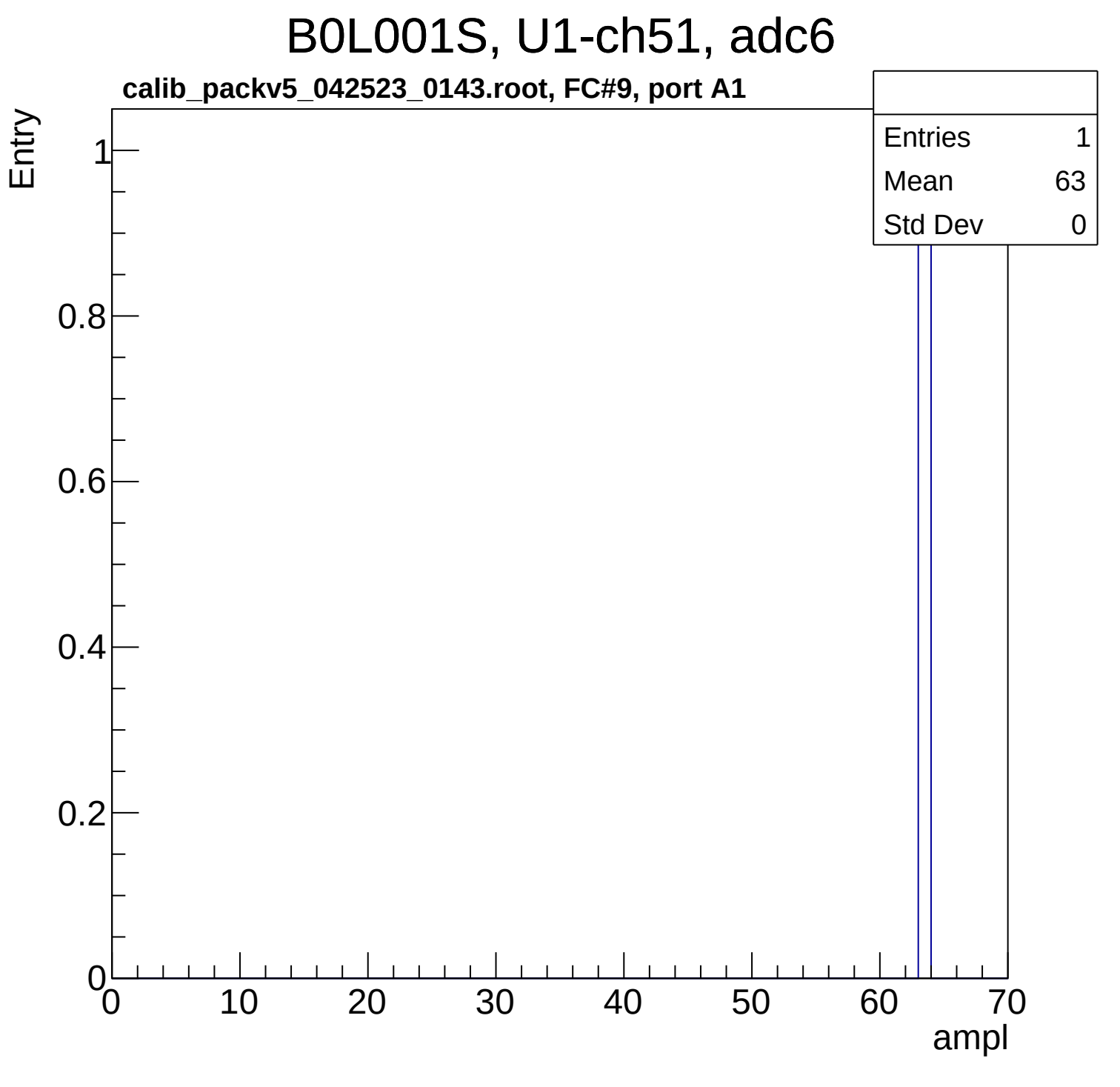
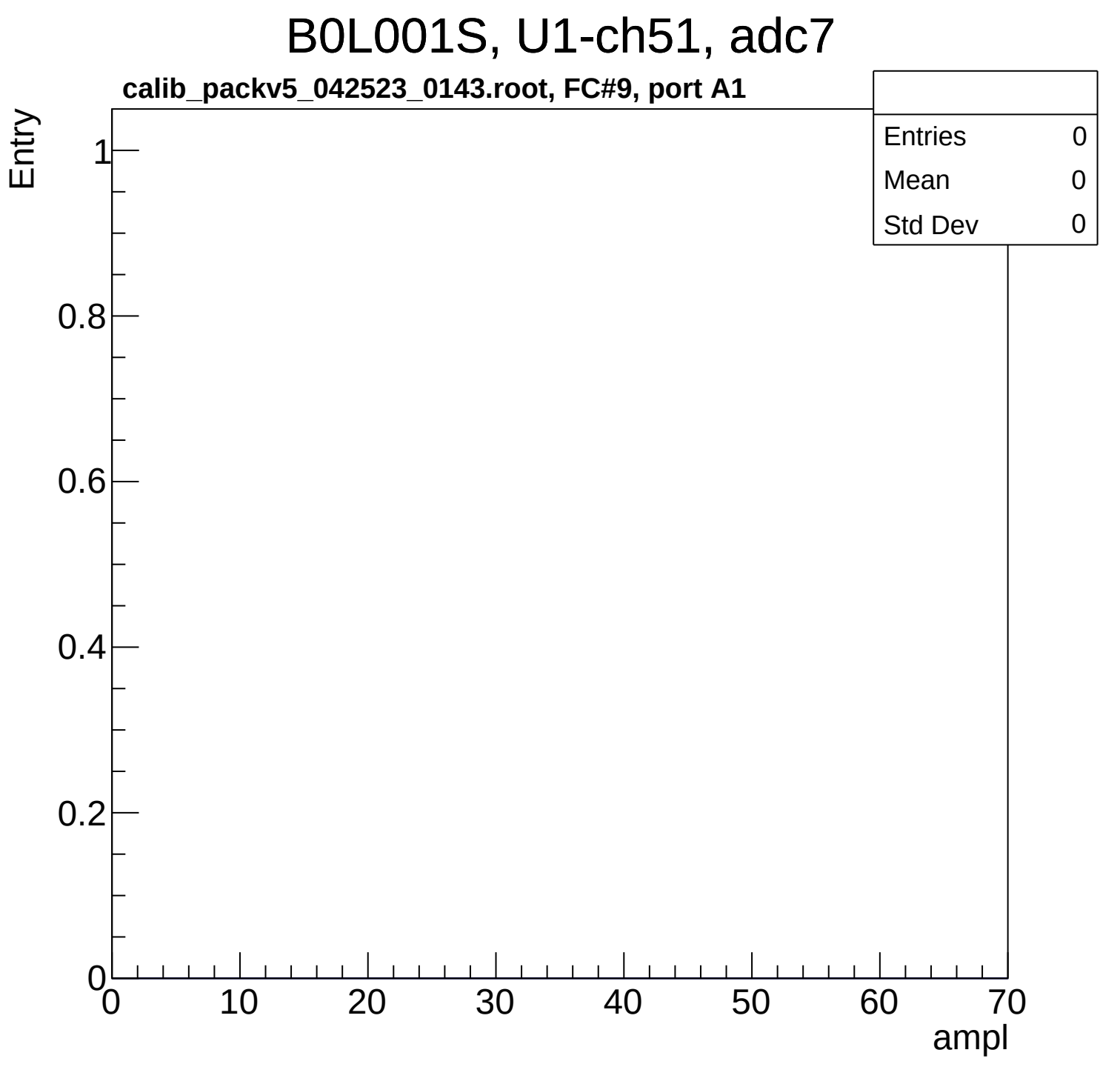
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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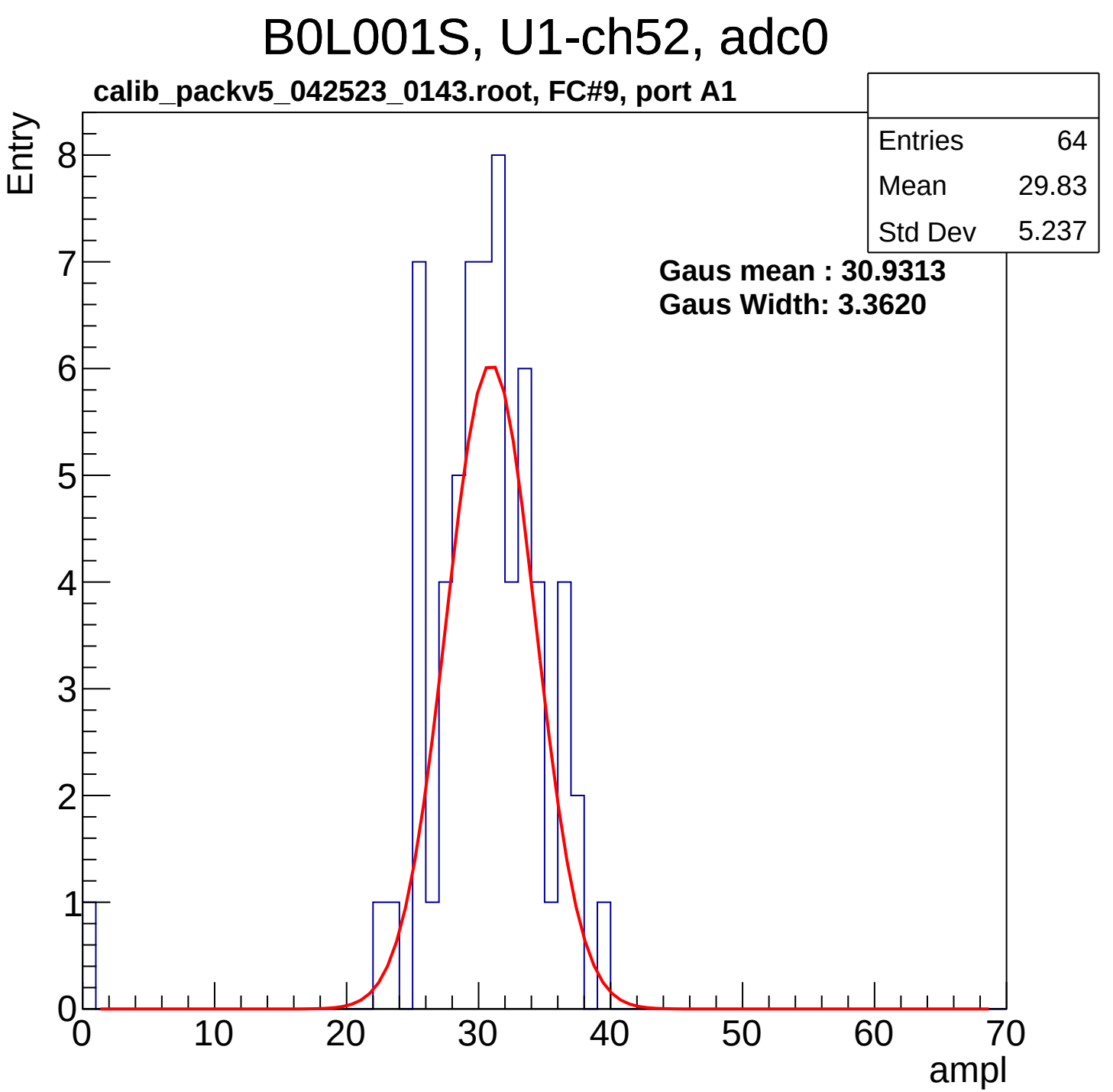
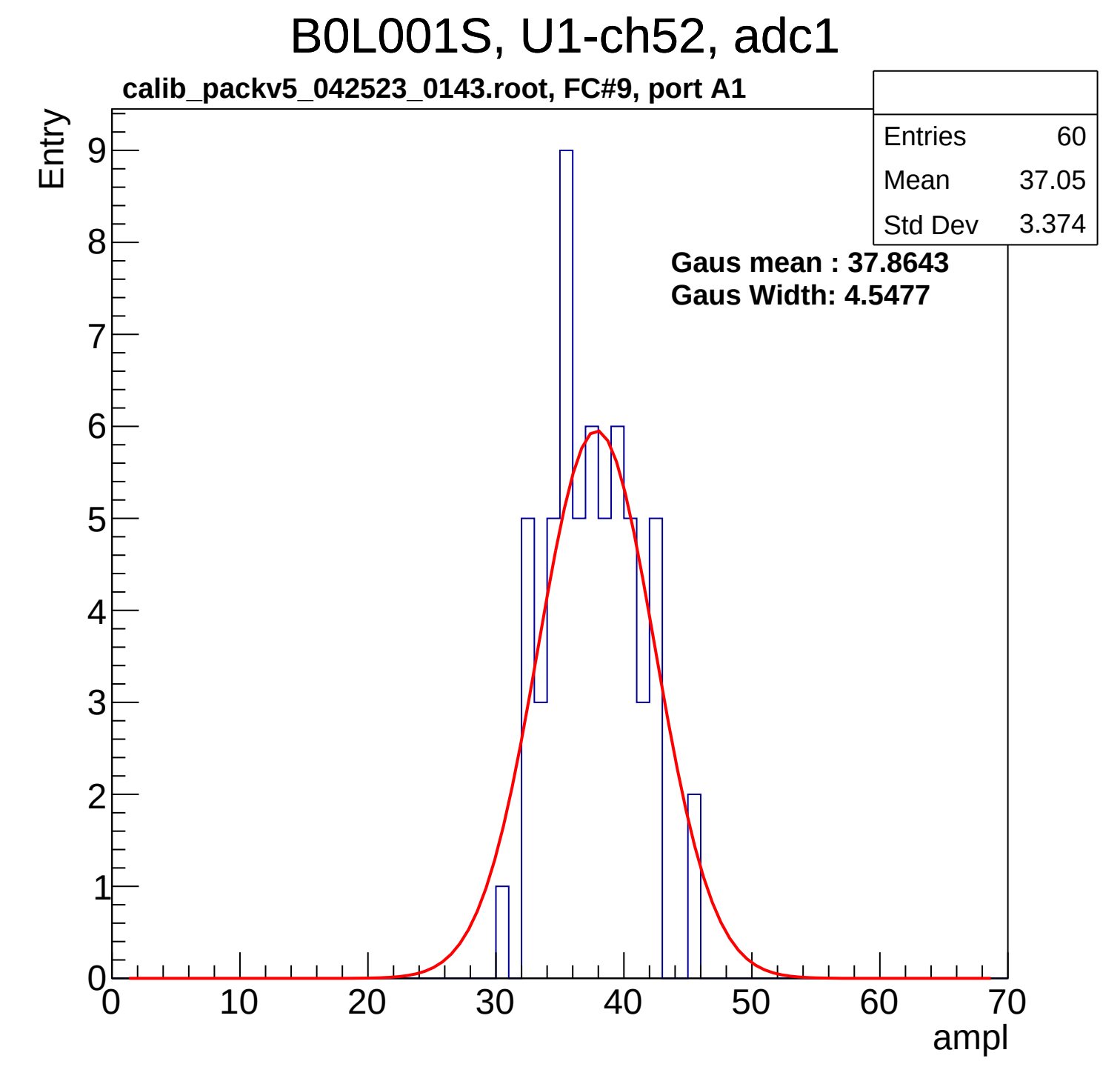
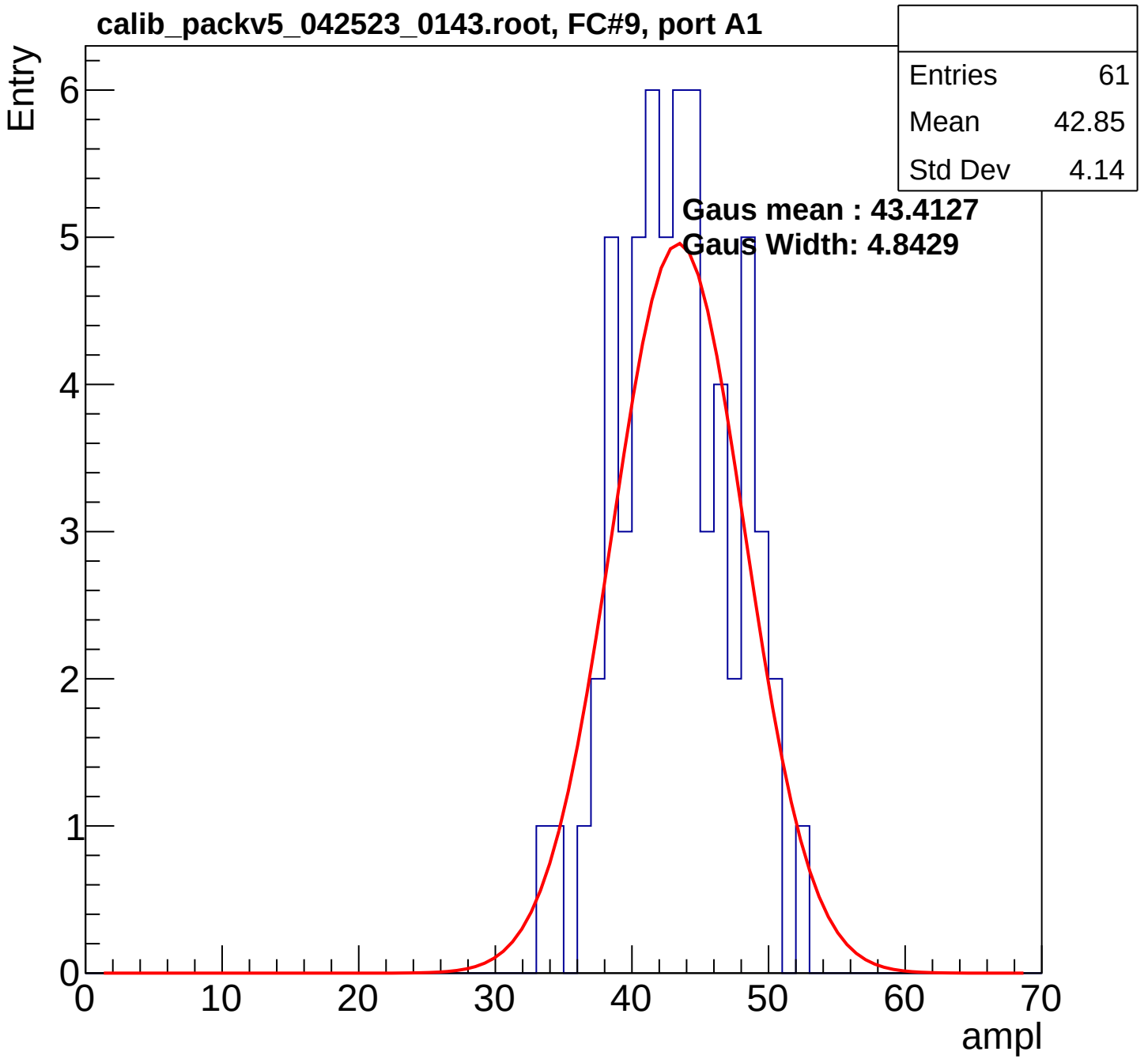
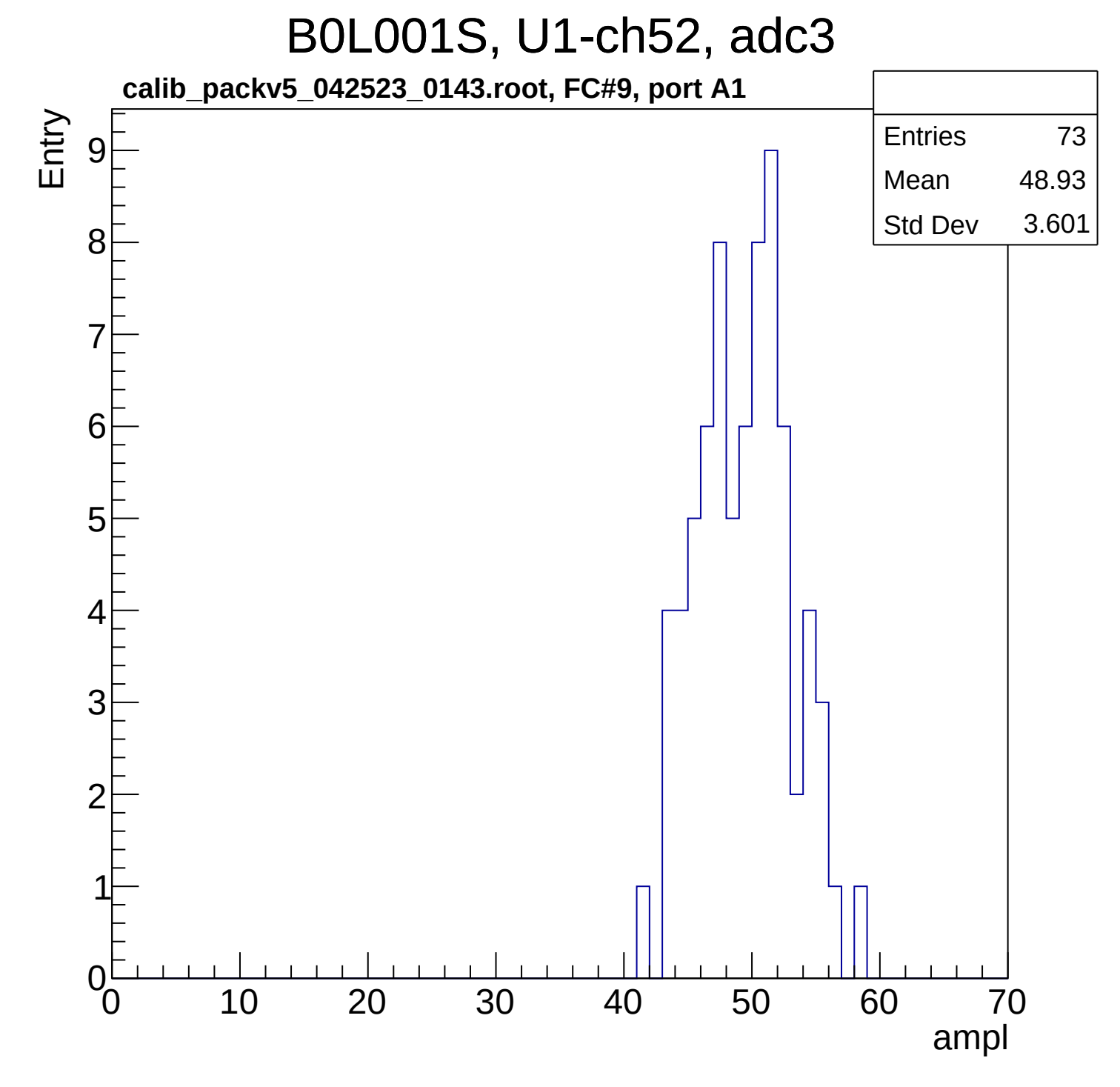
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 29.83
Std Dev 5.237
Gaus mean : 30.9313
Gaus Width: 3.3620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.05
Std Dev 3.374
Gaus mean : 37.8643
Gaus Width: 4.5477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 42.85
Std Dev 4.14
Gaus mean : 43.4127
Gaus Width: 4.8429
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 48.93
Std Dev 3.601
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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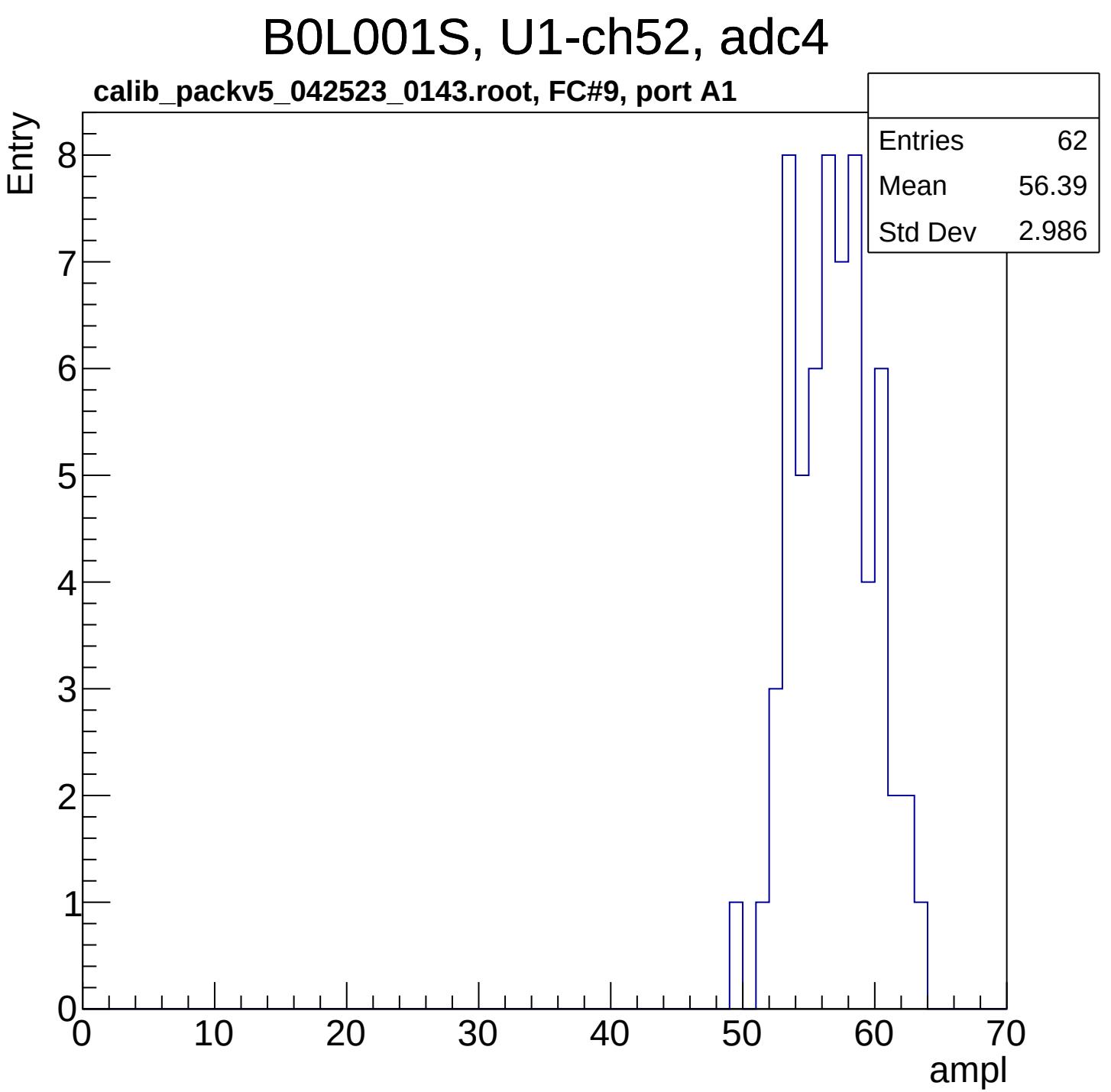
B0L001S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 56.39
Std Dev 2.986
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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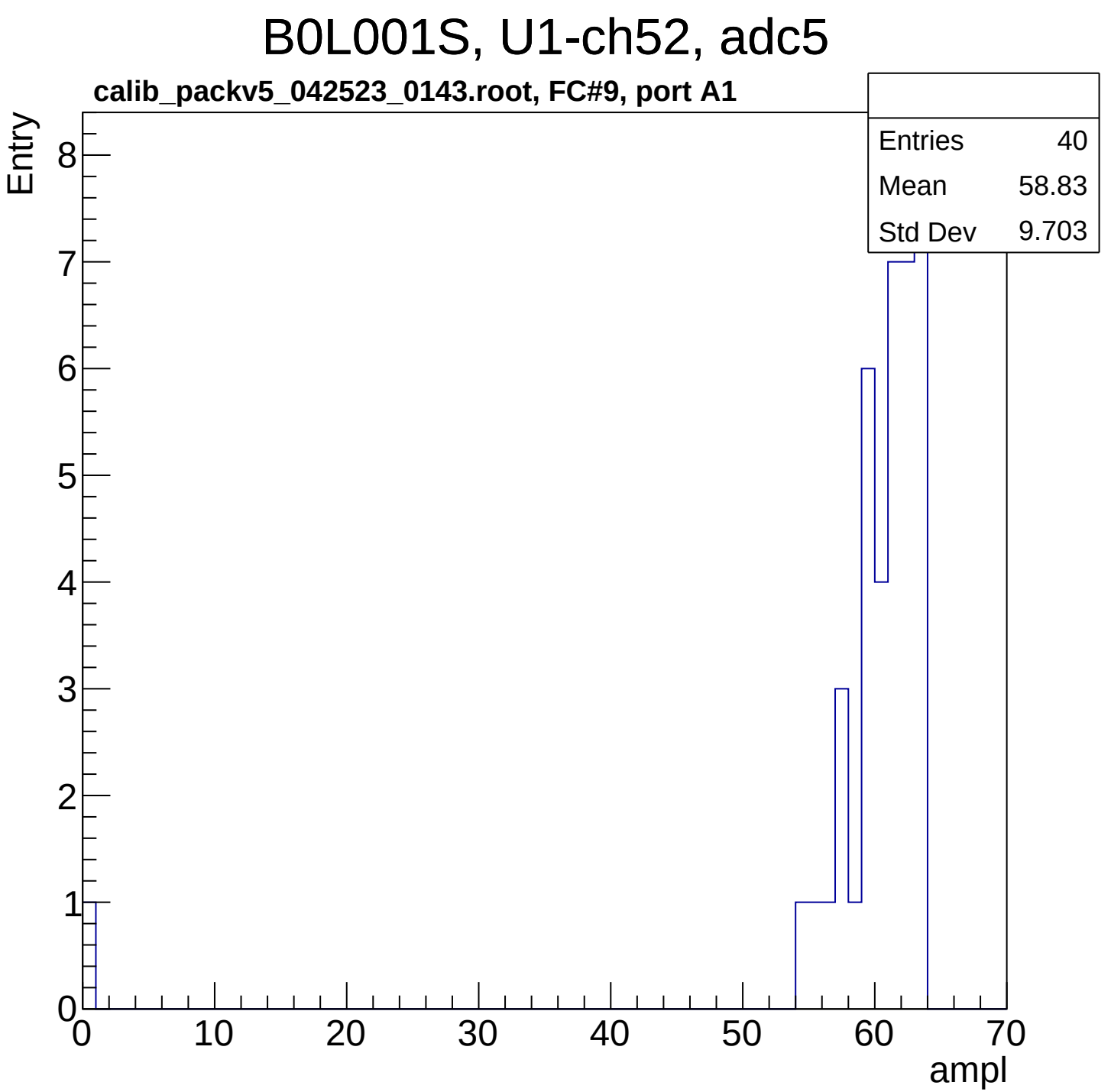
B0L001S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.83
Std Dev 9.703
Entry
ampl
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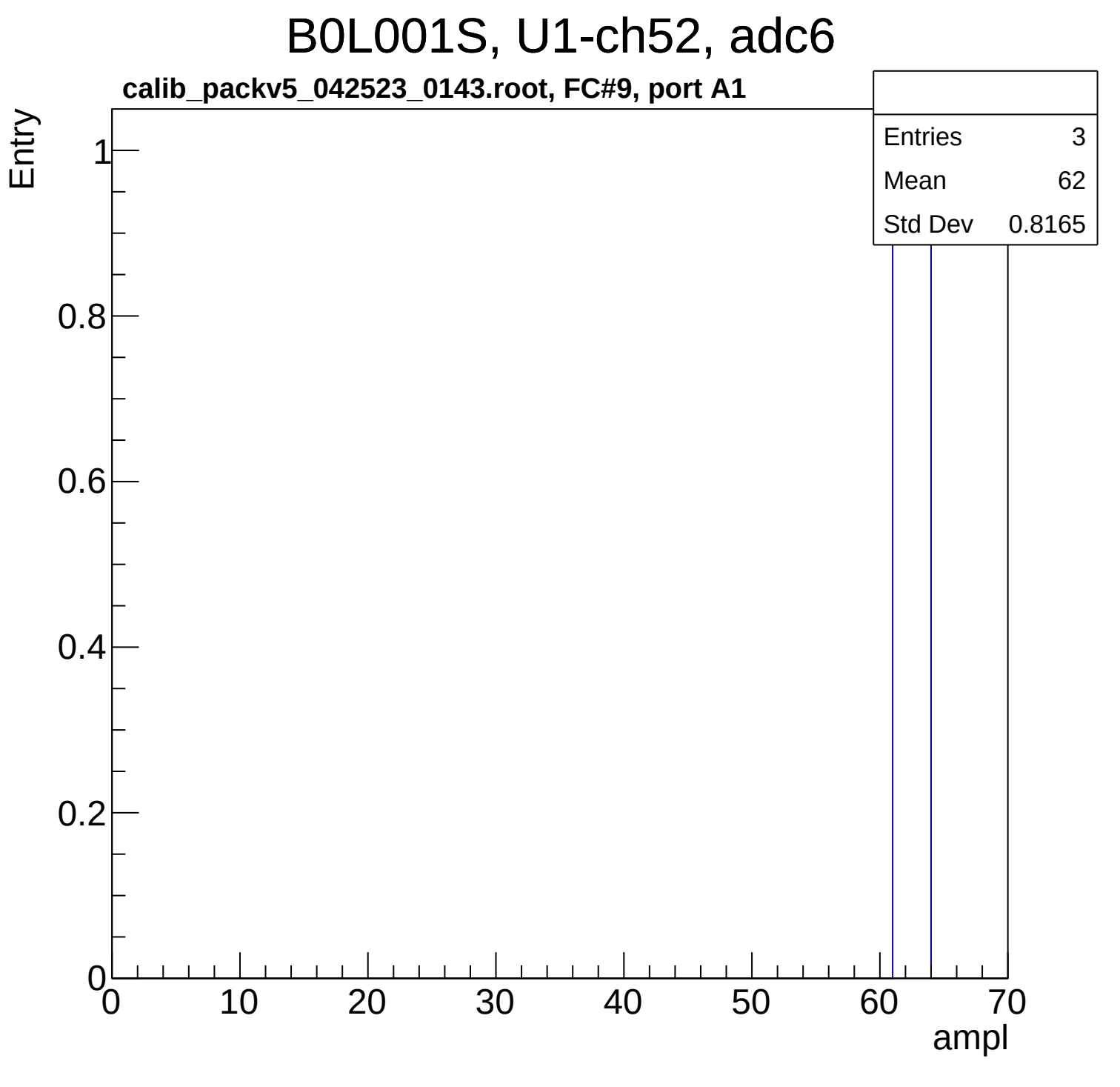
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62
Std Dev 0.8165
Entry
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0.6
0.4
0.2
0
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10
20
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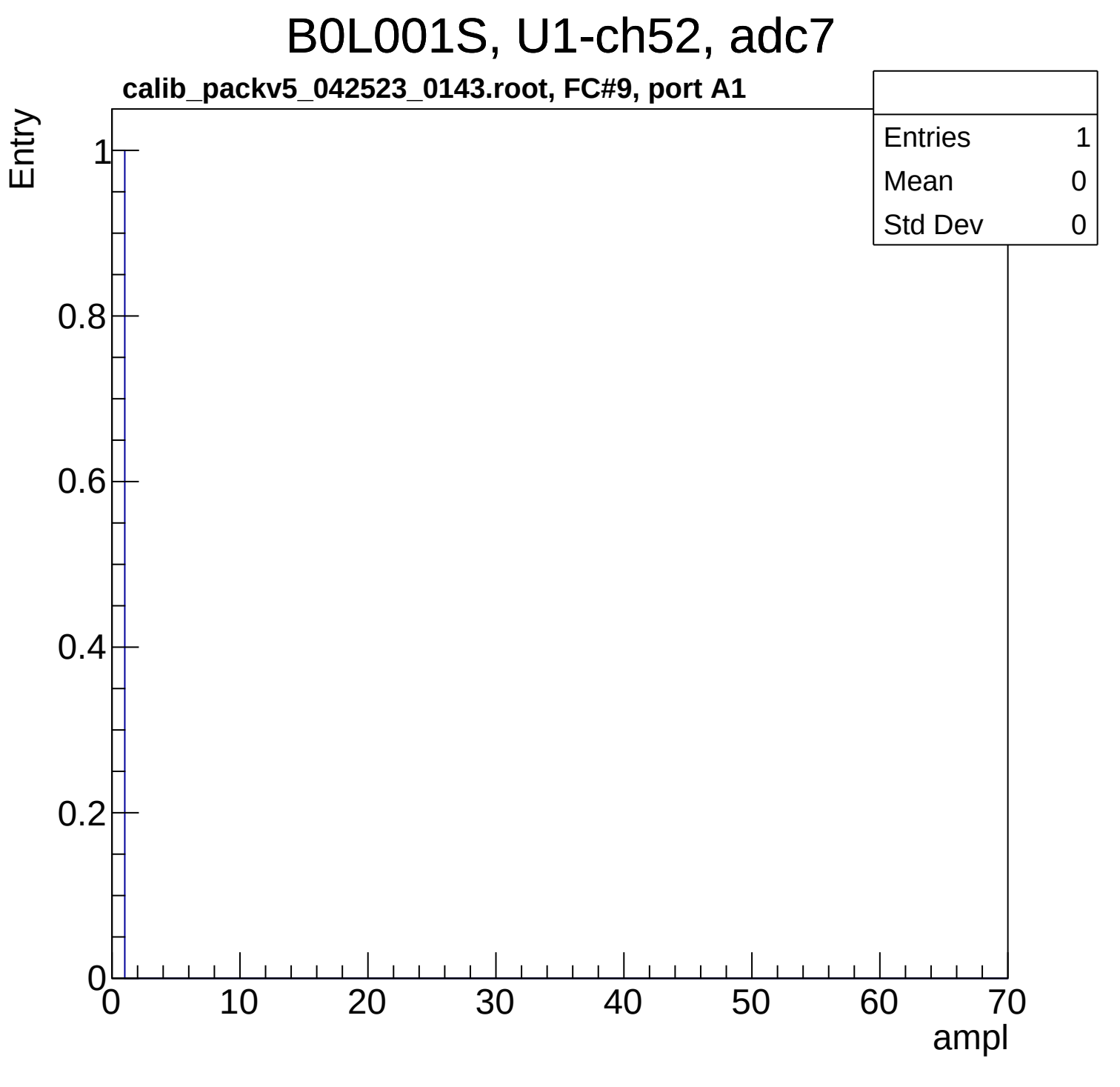
50
60
70
ampl


B0L001S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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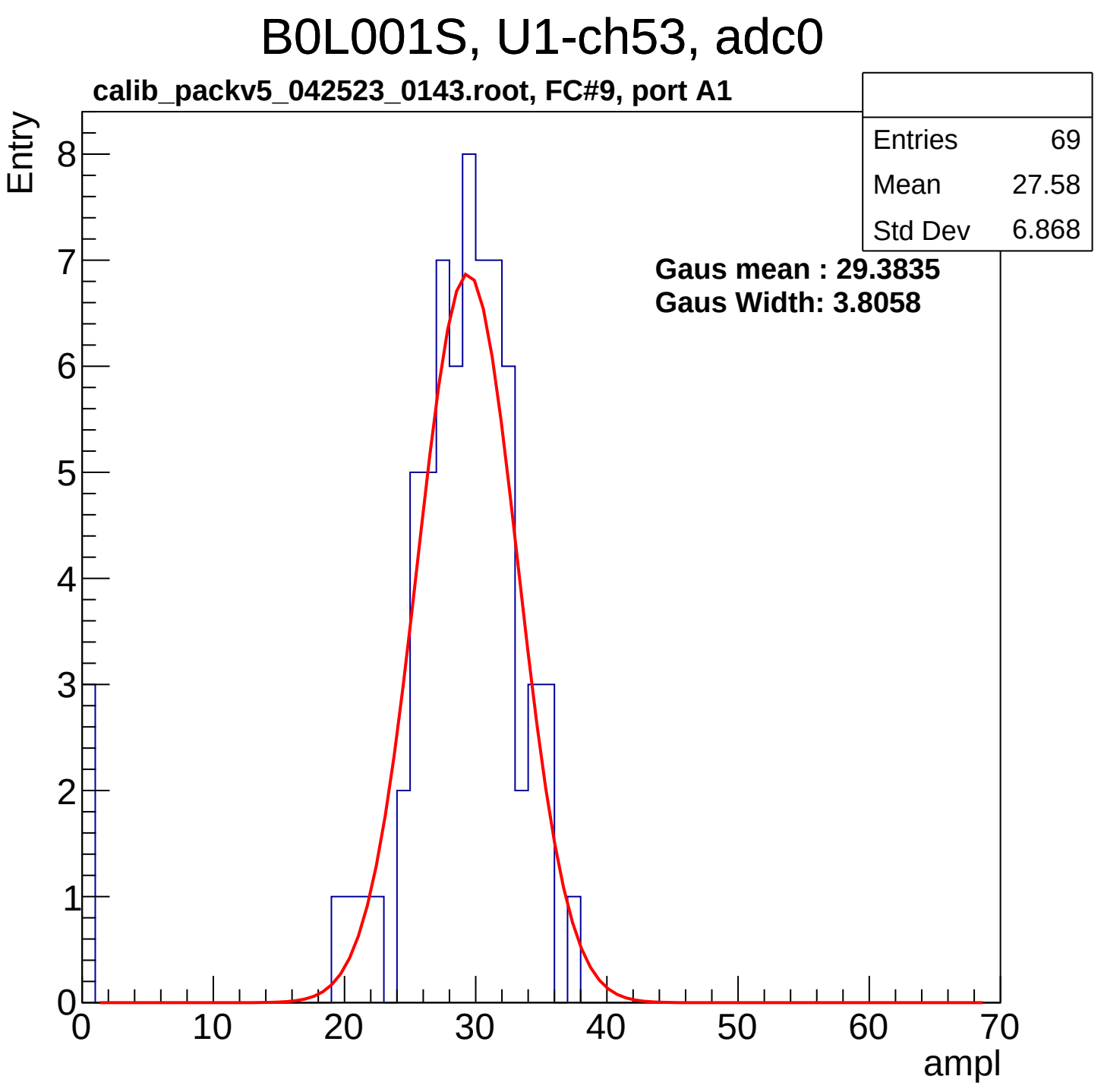
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 27.58
Std Dev 6.868
Gaus mean : 29.3835
Gaus Width: 3.8058
Entry
ampl
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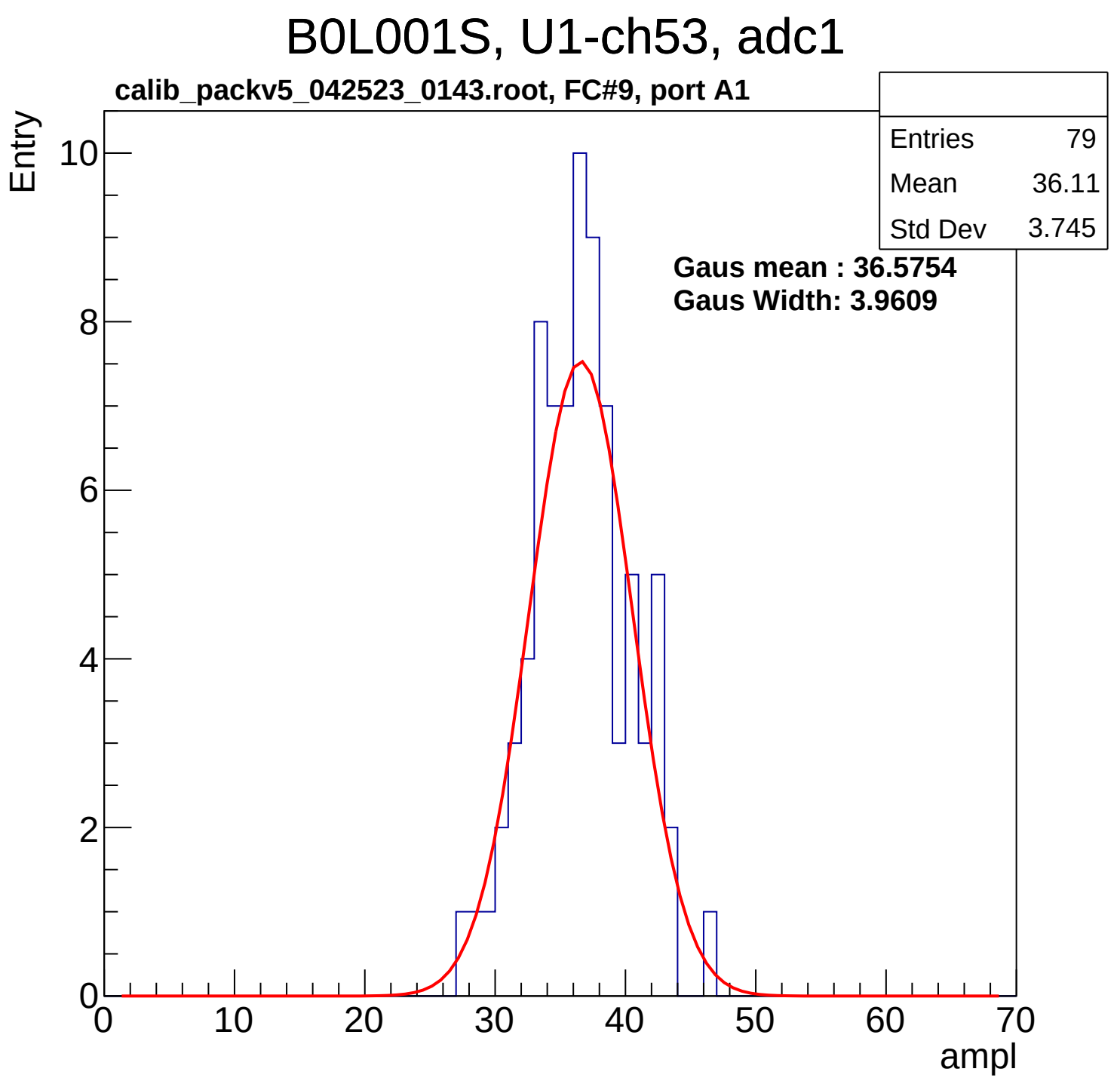
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 36.11
Std Dev 3.745
Gaus mean : 36.5754
Gaus Width: 3.9609
Entry
ampl
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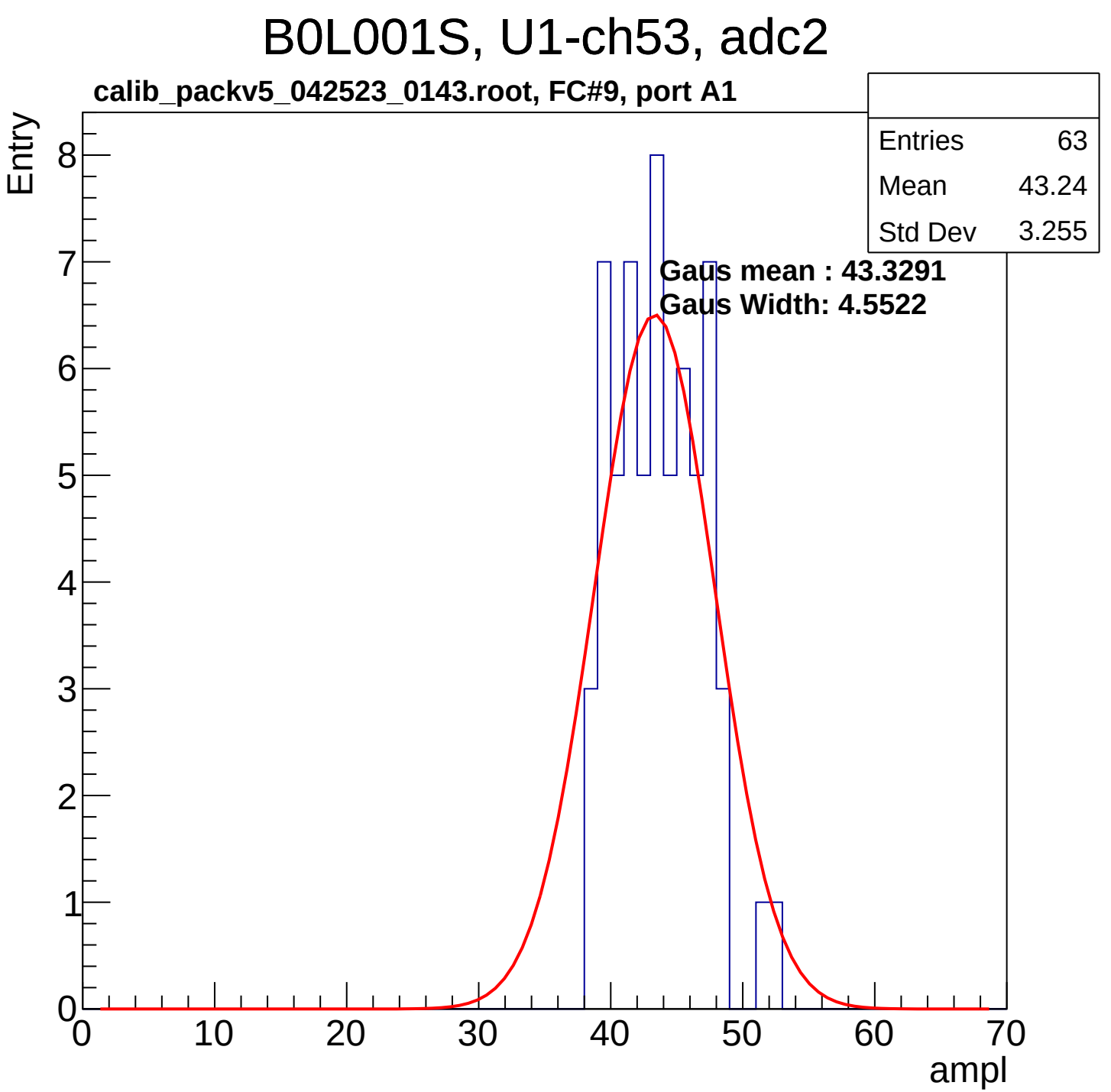
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.24
Std Dev 3.255
Gaus mean : 43.3291
Gaus Width: 4.5522
Entry
ampl
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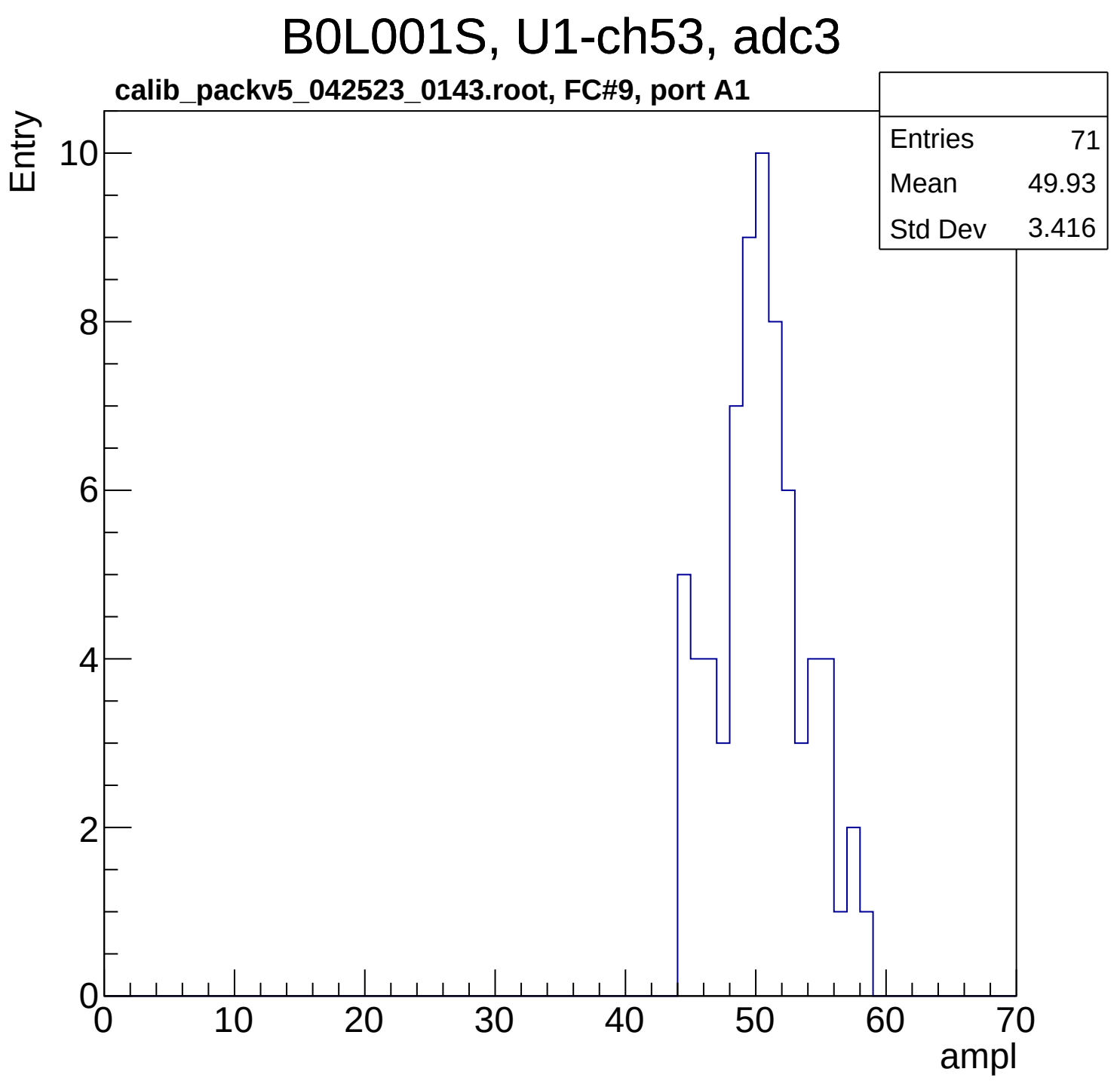
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 49.93
Std Dev 3.416
Entry
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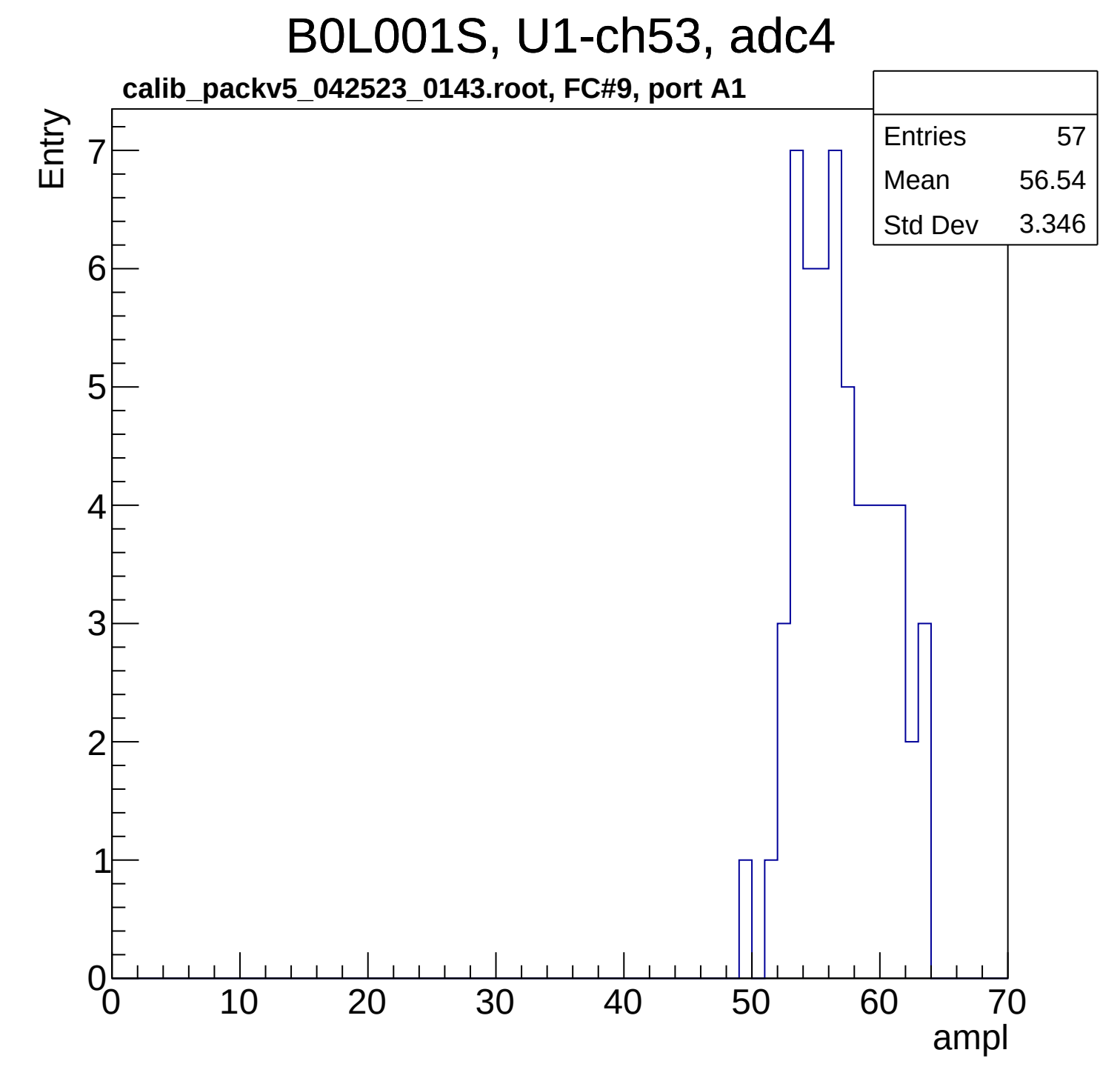
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 56.54
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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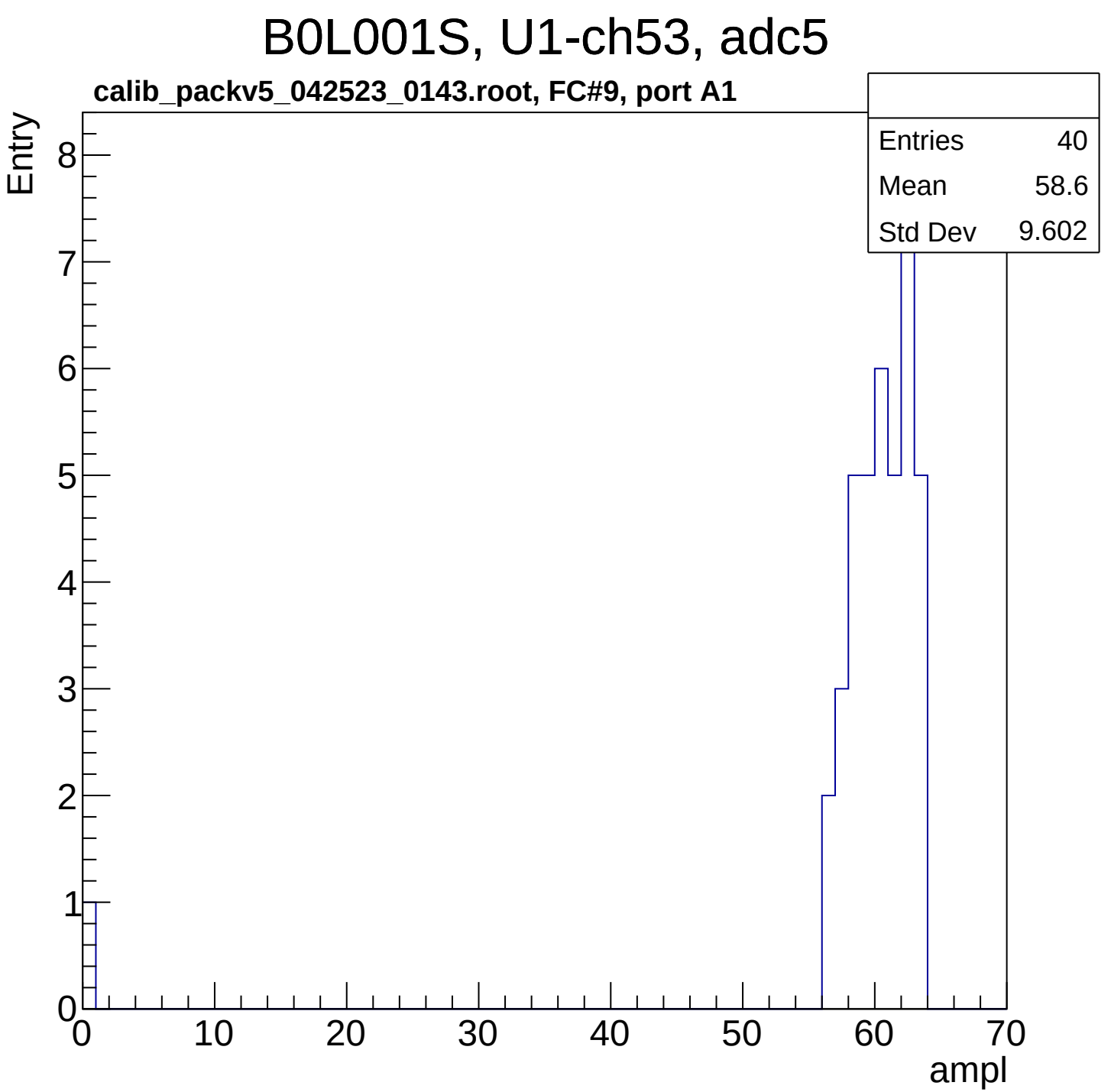
B0L001S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.6
Std Dev 9.602
Entry
ampl
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4
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7
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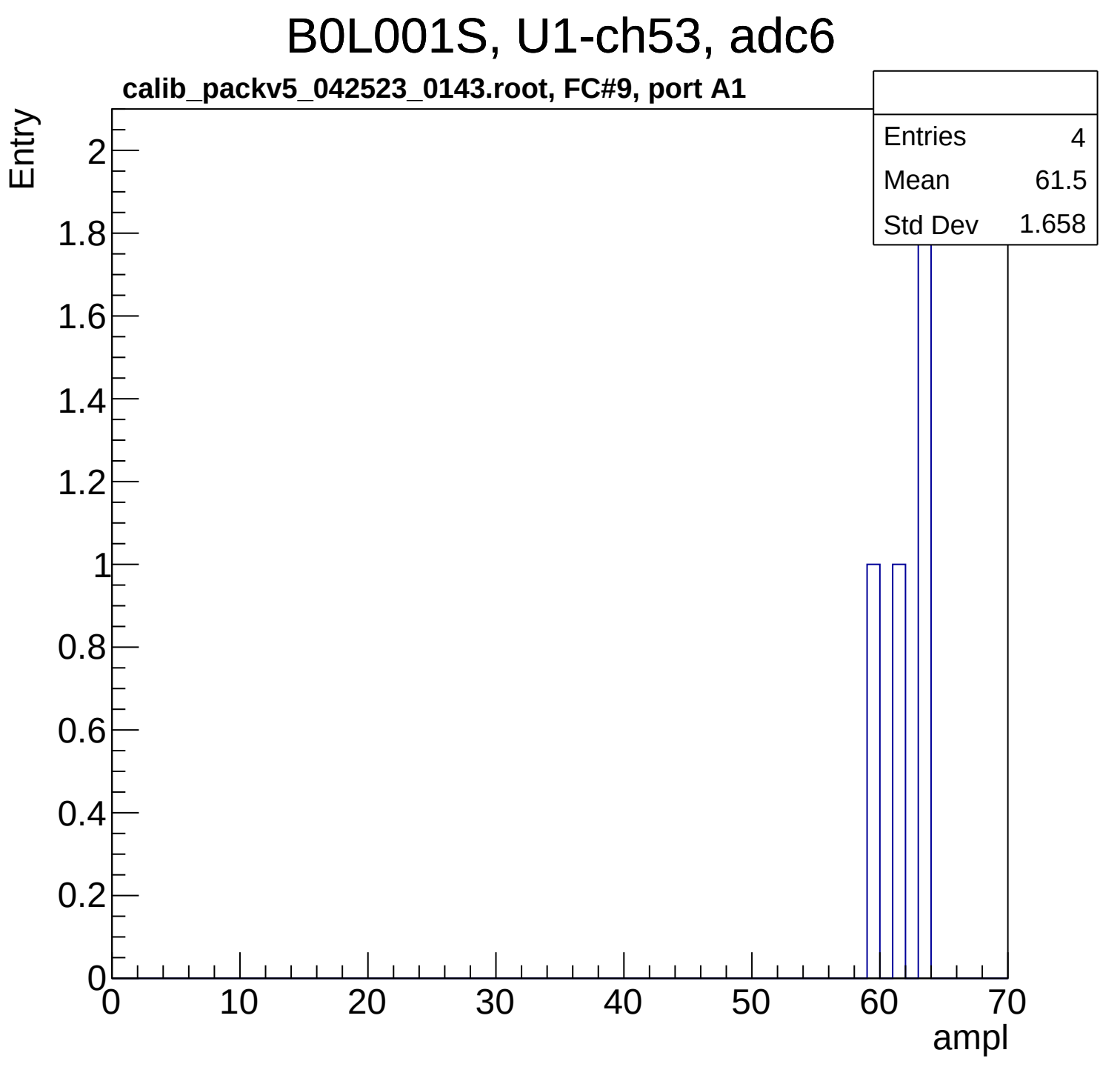
B0L001S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
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0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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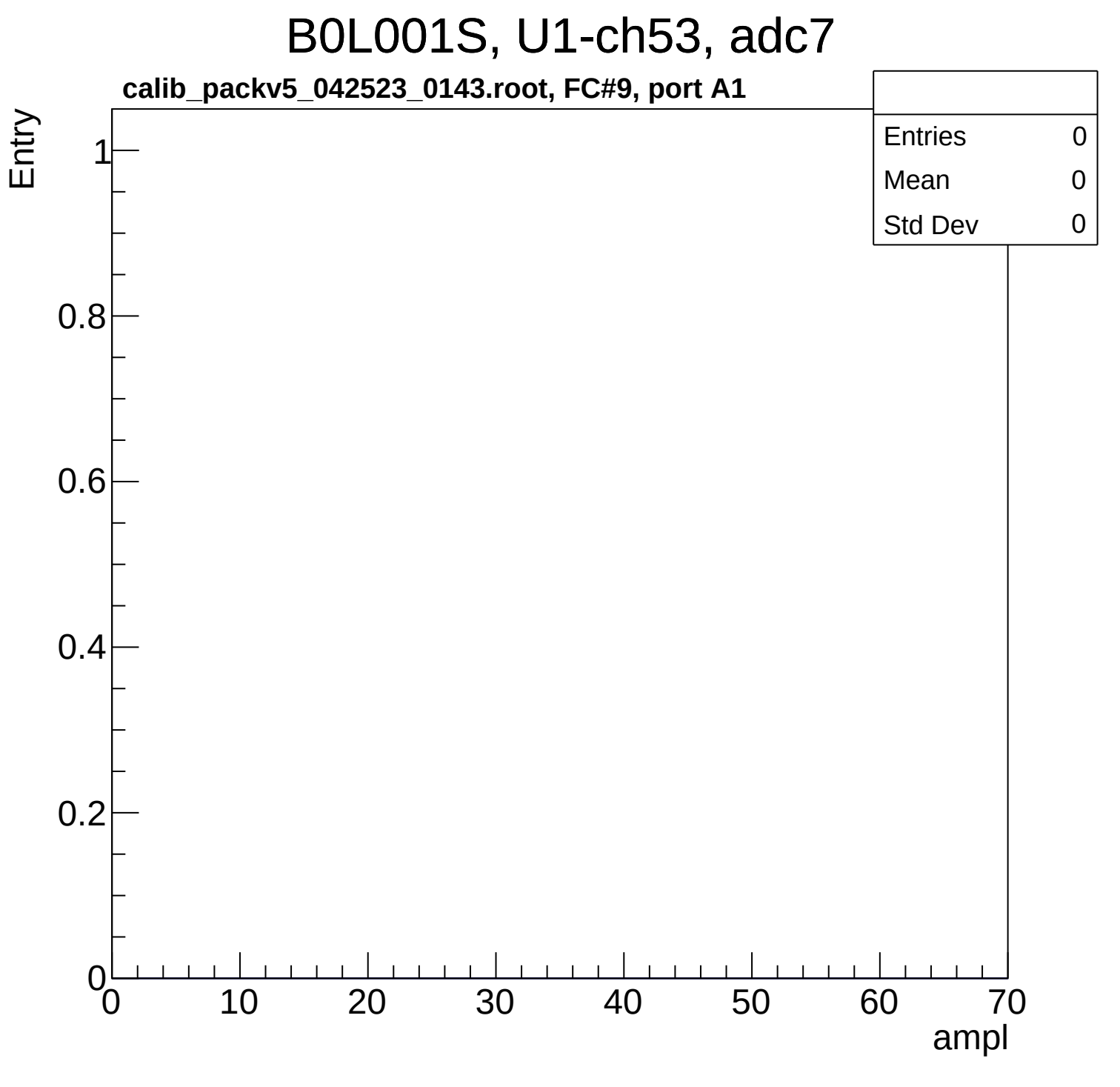
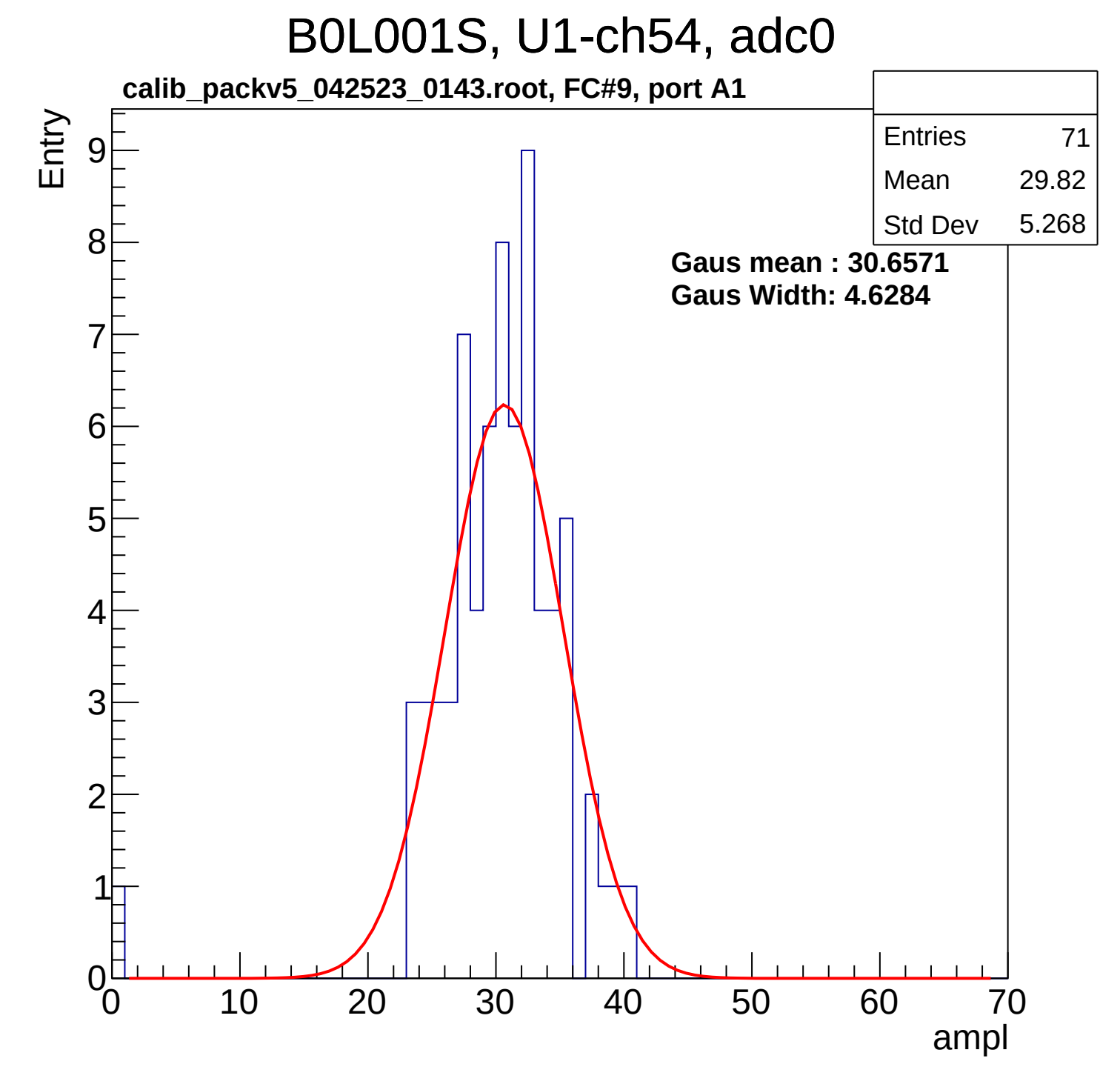
40
50
60
70


B0L001S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.82
Std Dev 5.268
Gaus mean : 30.6571
Gaus Width: 4.6284
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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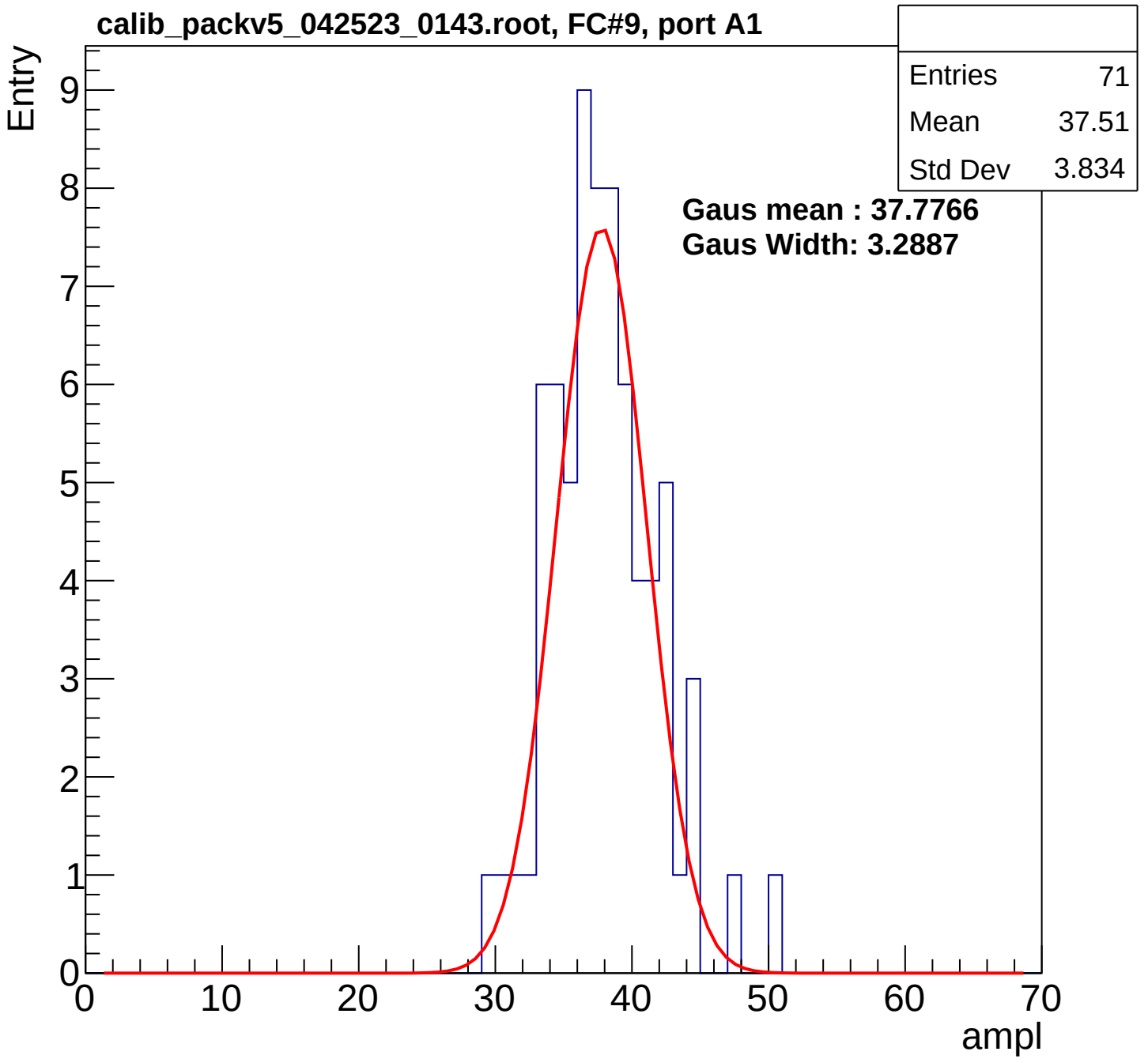
B0L001S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.51
Std Dev 3.834
Gaus mean : 37.7766
Gaus Width: 3.2887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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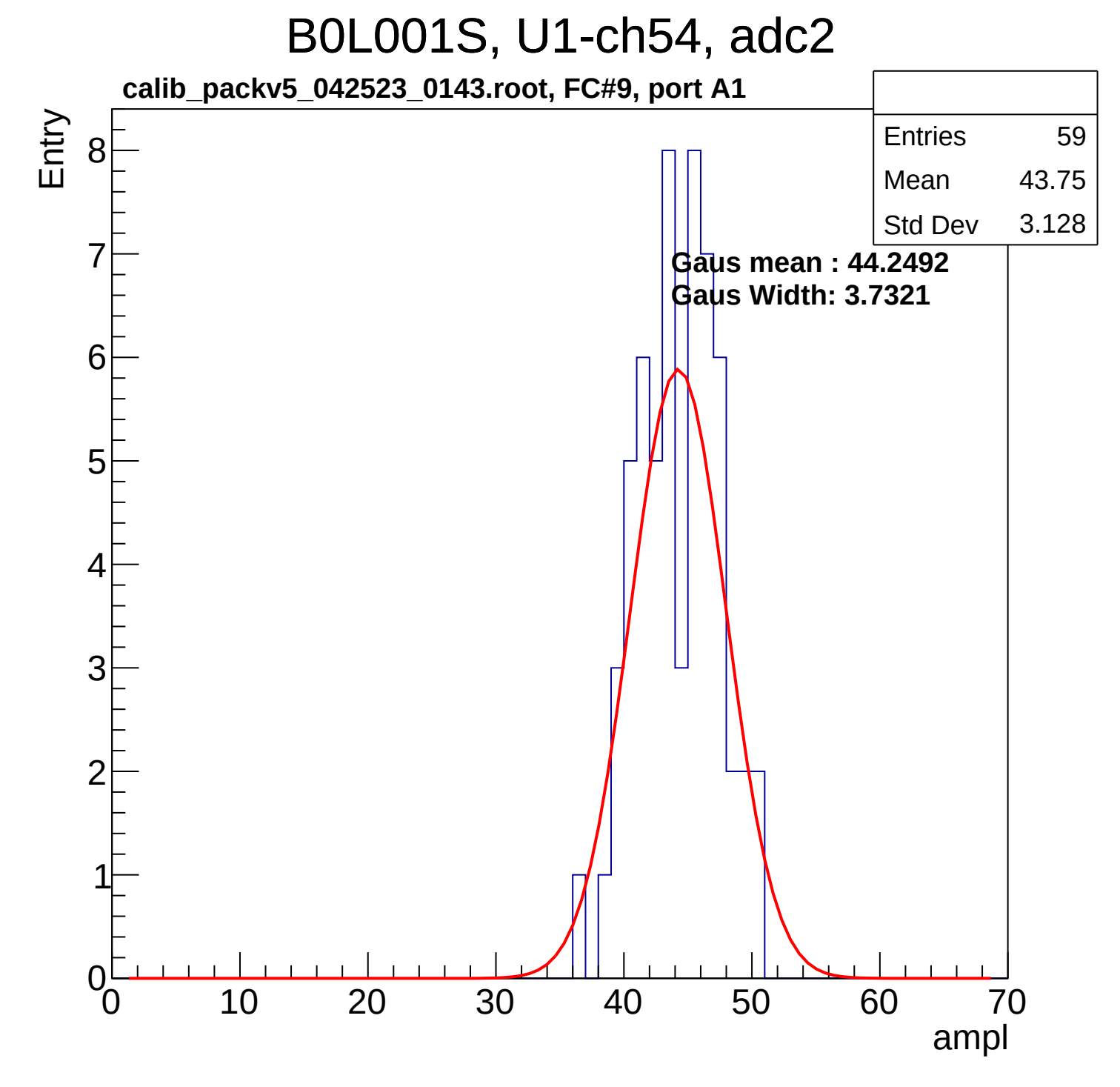
B0L001S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.75
Std Dev 3.128
Gaus mean : 44.2492
Gaus Width: 3.7321
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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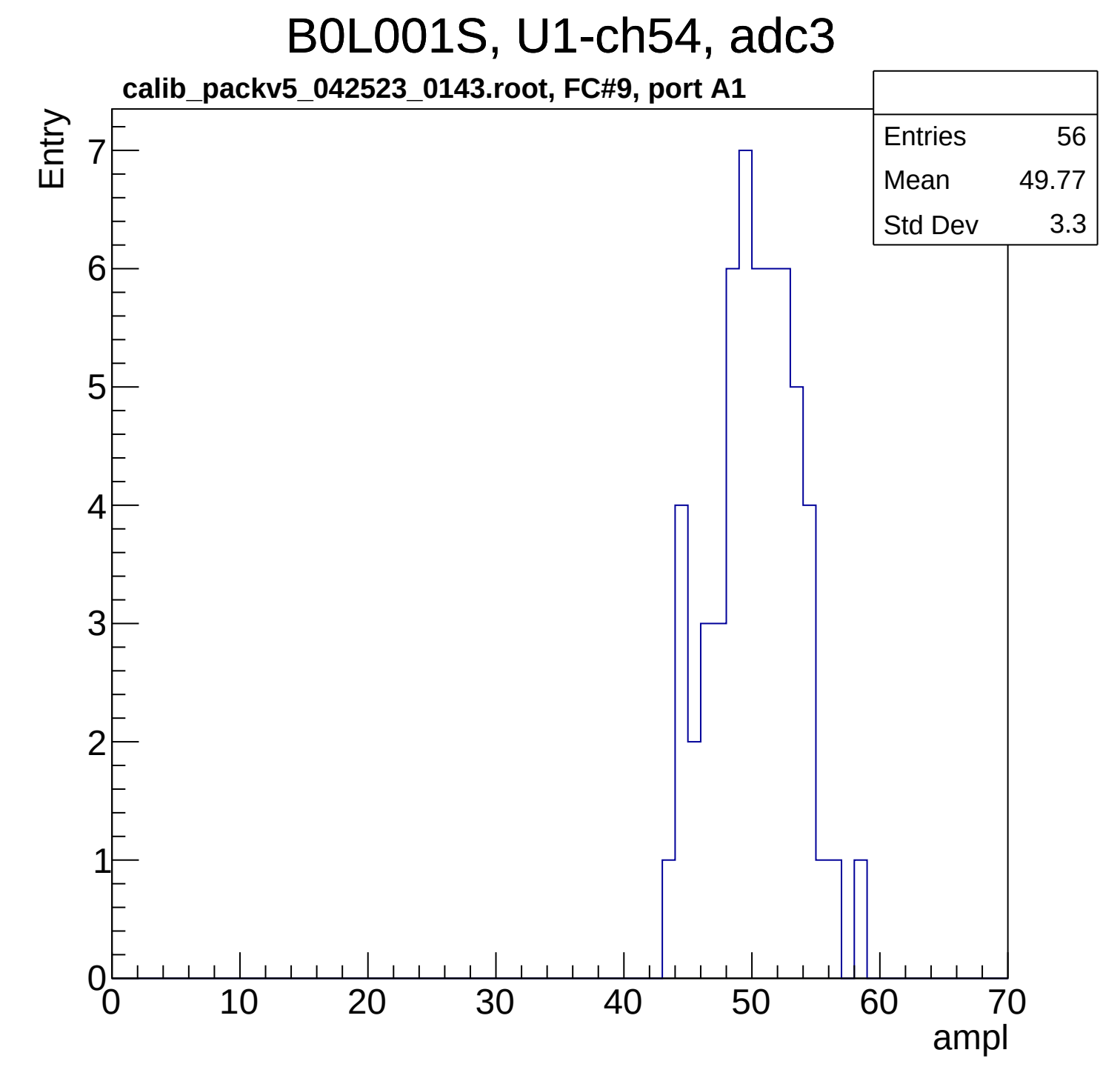
B0L001S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 49.77
Std Dev 3.3
Entry
ampl
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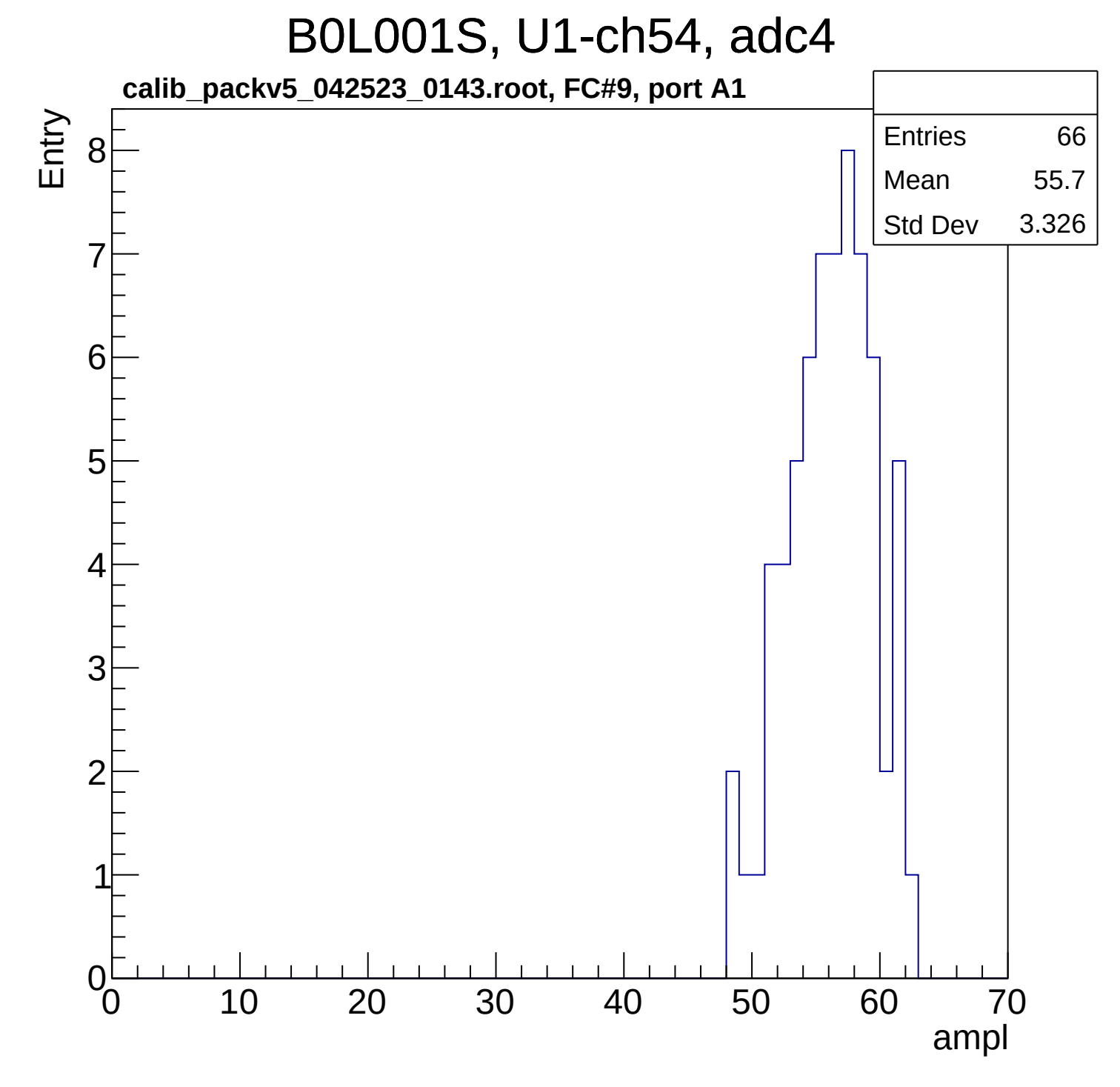
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.7
Std Dev 3.326
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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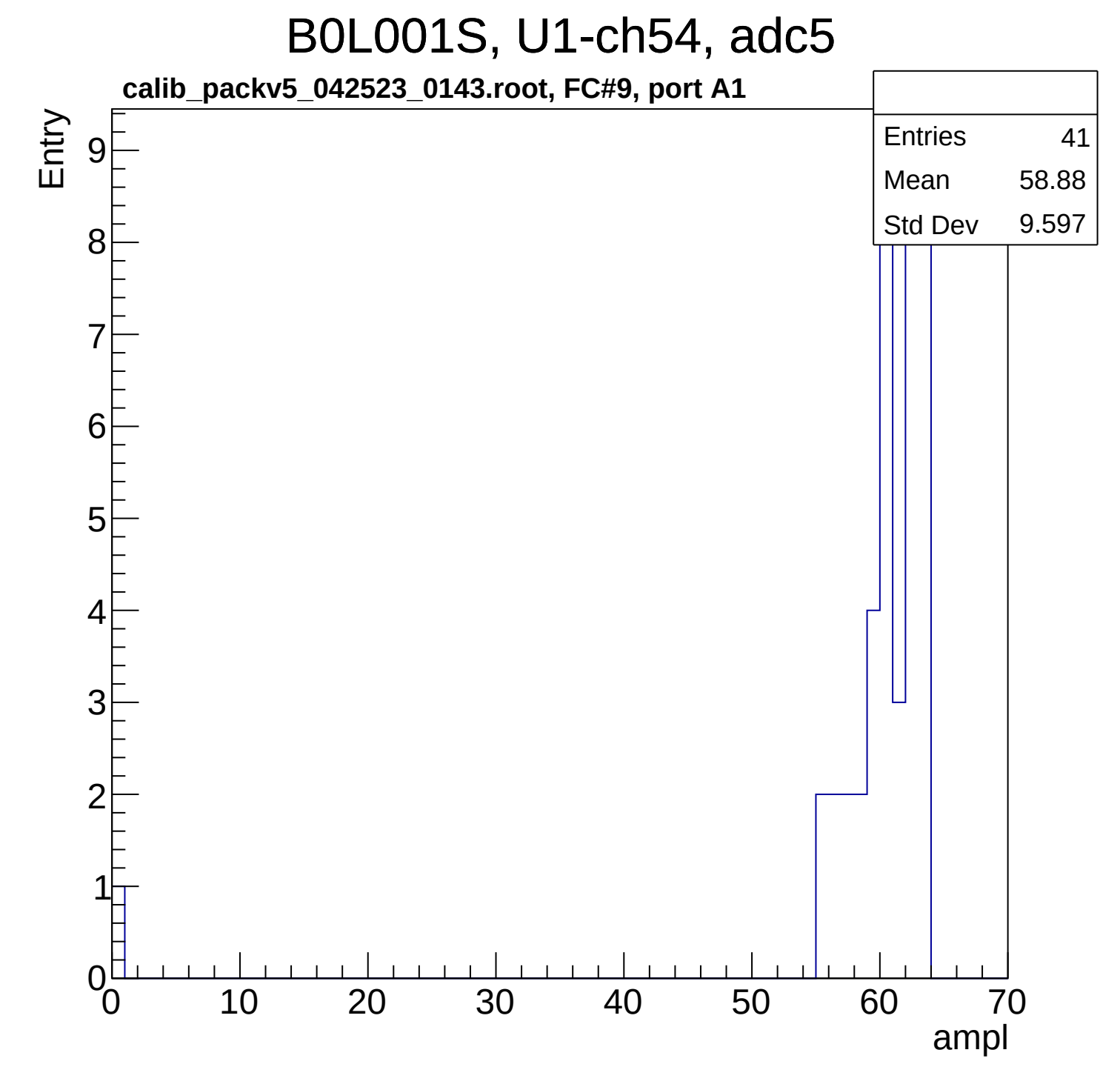
B0L001S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.88
Std Dev 9.597
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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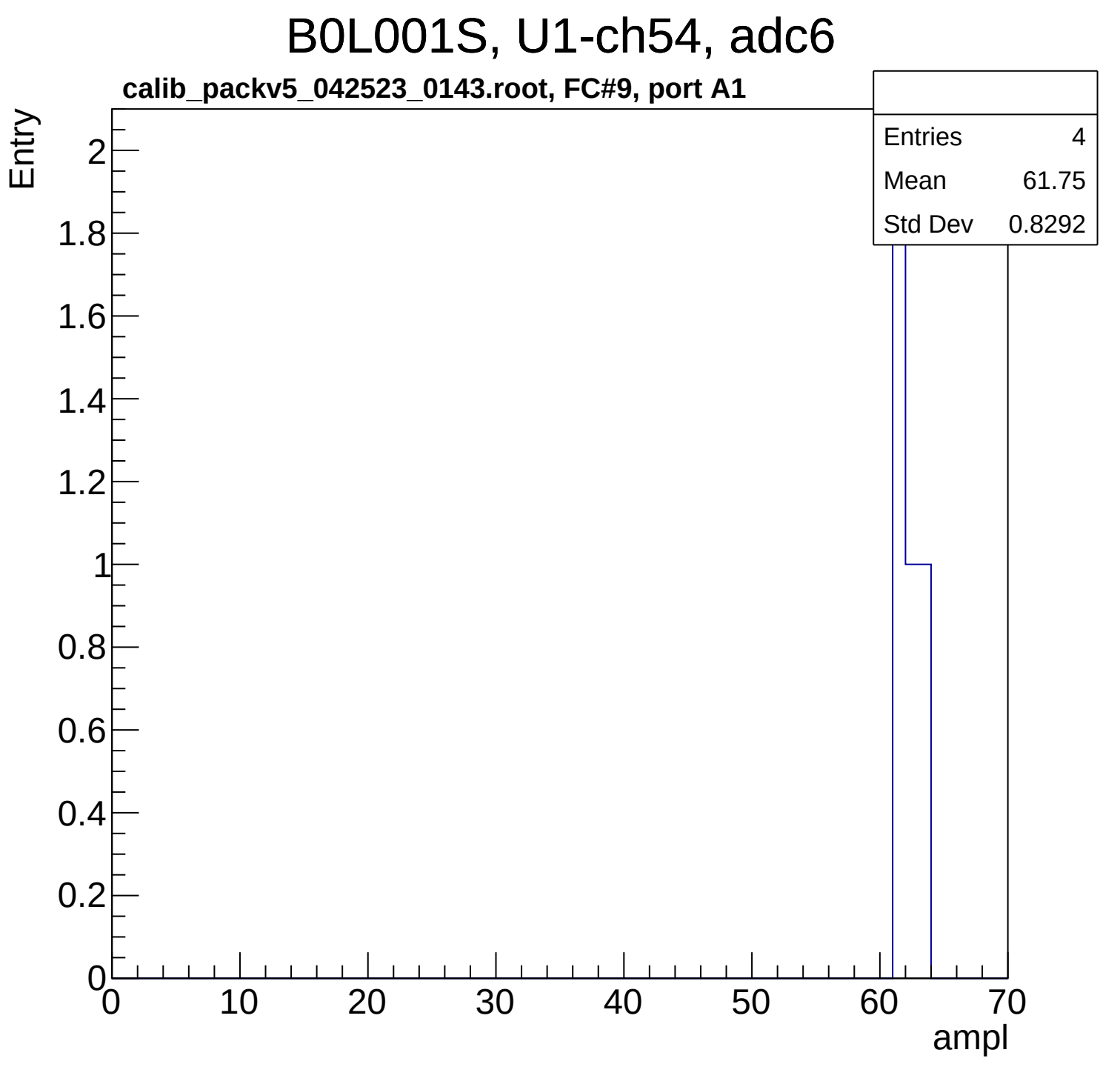
B0L001S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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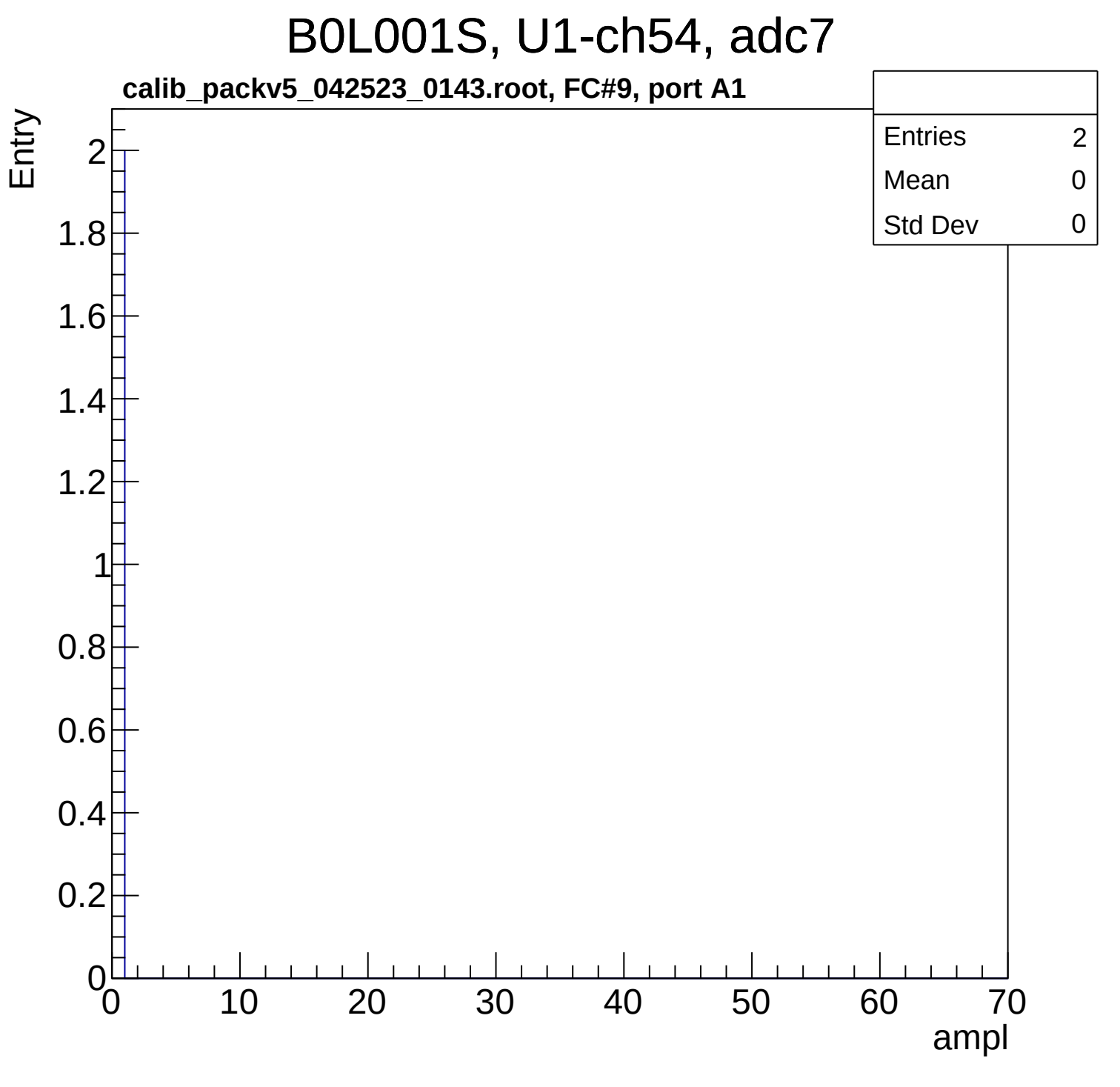
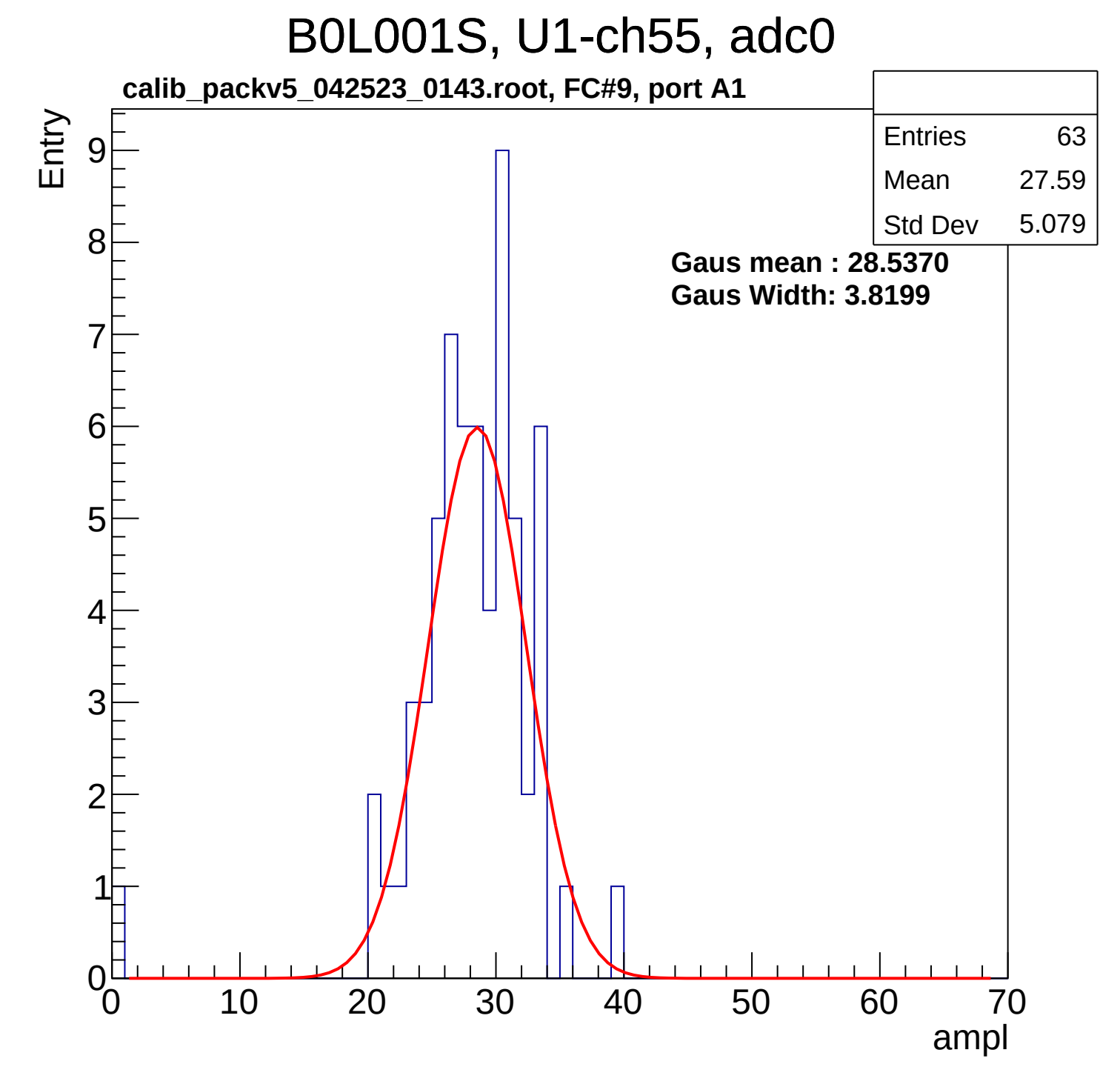
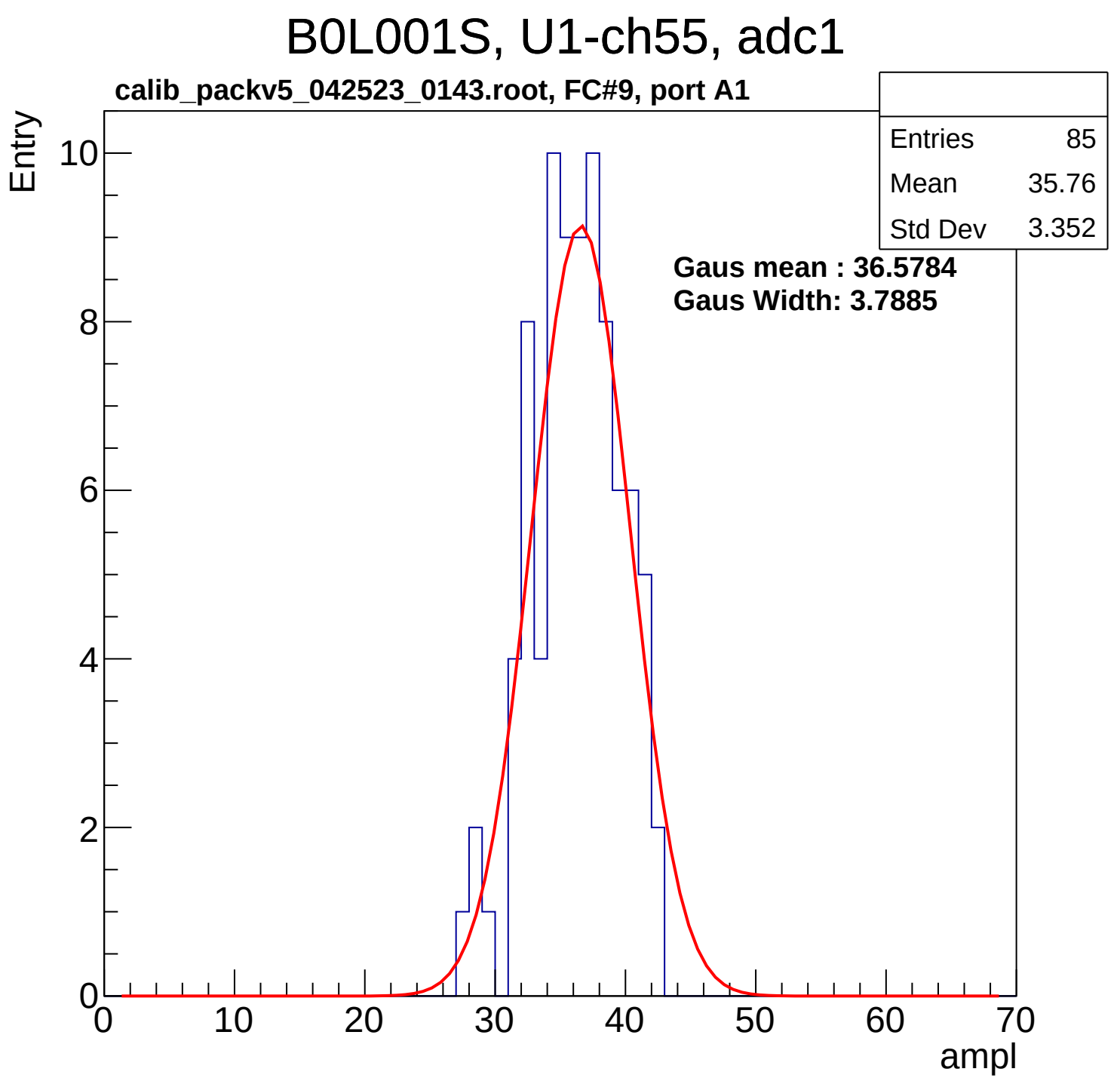
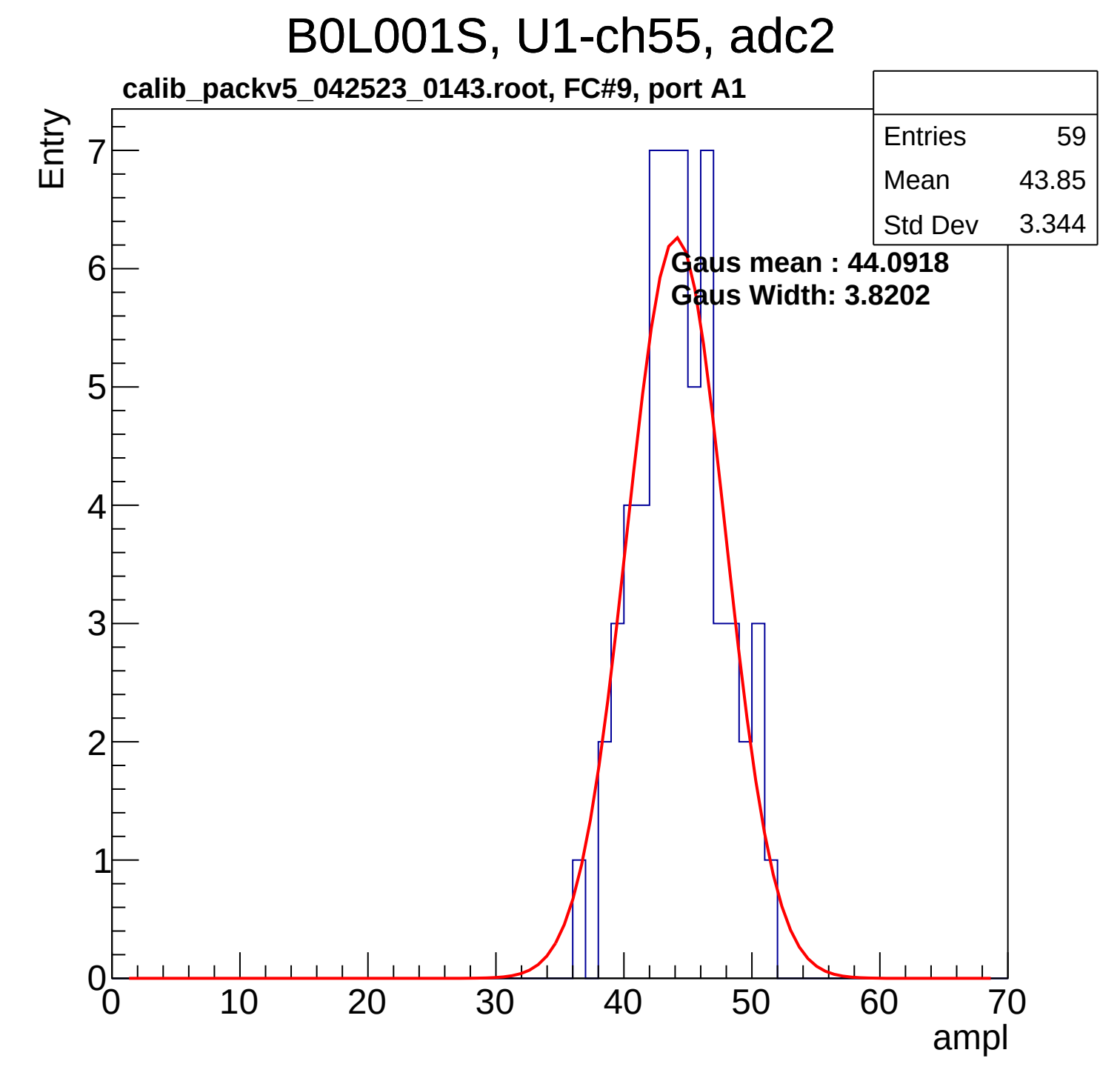

B0L001S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 27.59
Std Dev 5.079
Gaus mean : 28.5370
Gaus Width: 3.8199
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 35.76
Std Dev 3.352
Gaus mean : 36.5784
Gaus Width: 3.7885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.85
Std Dev 3.344
Gaus mean : 44.0918
Gaus Width: 3.8202
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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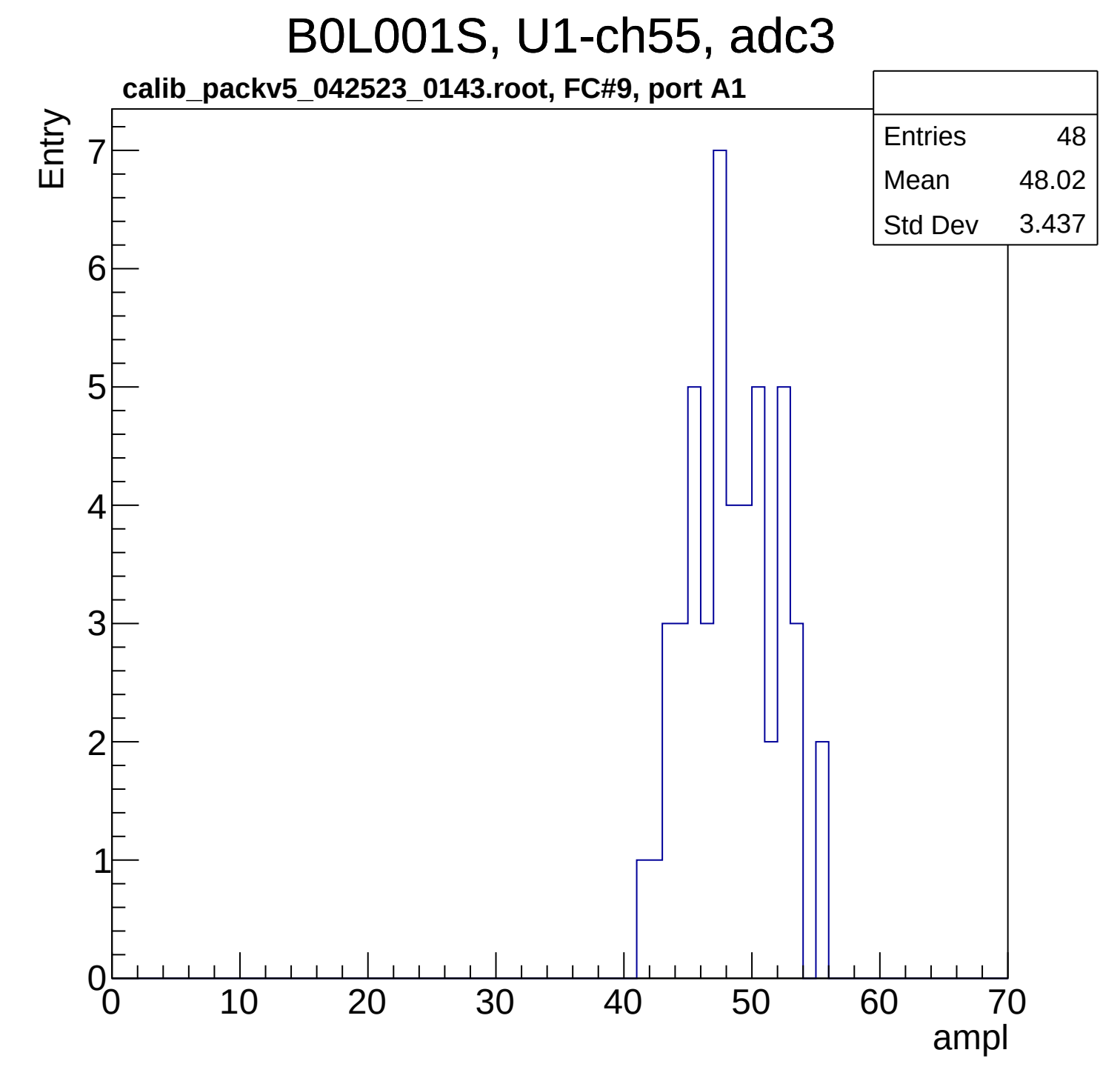
B0L001S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 48.02
Std Dev 3.437
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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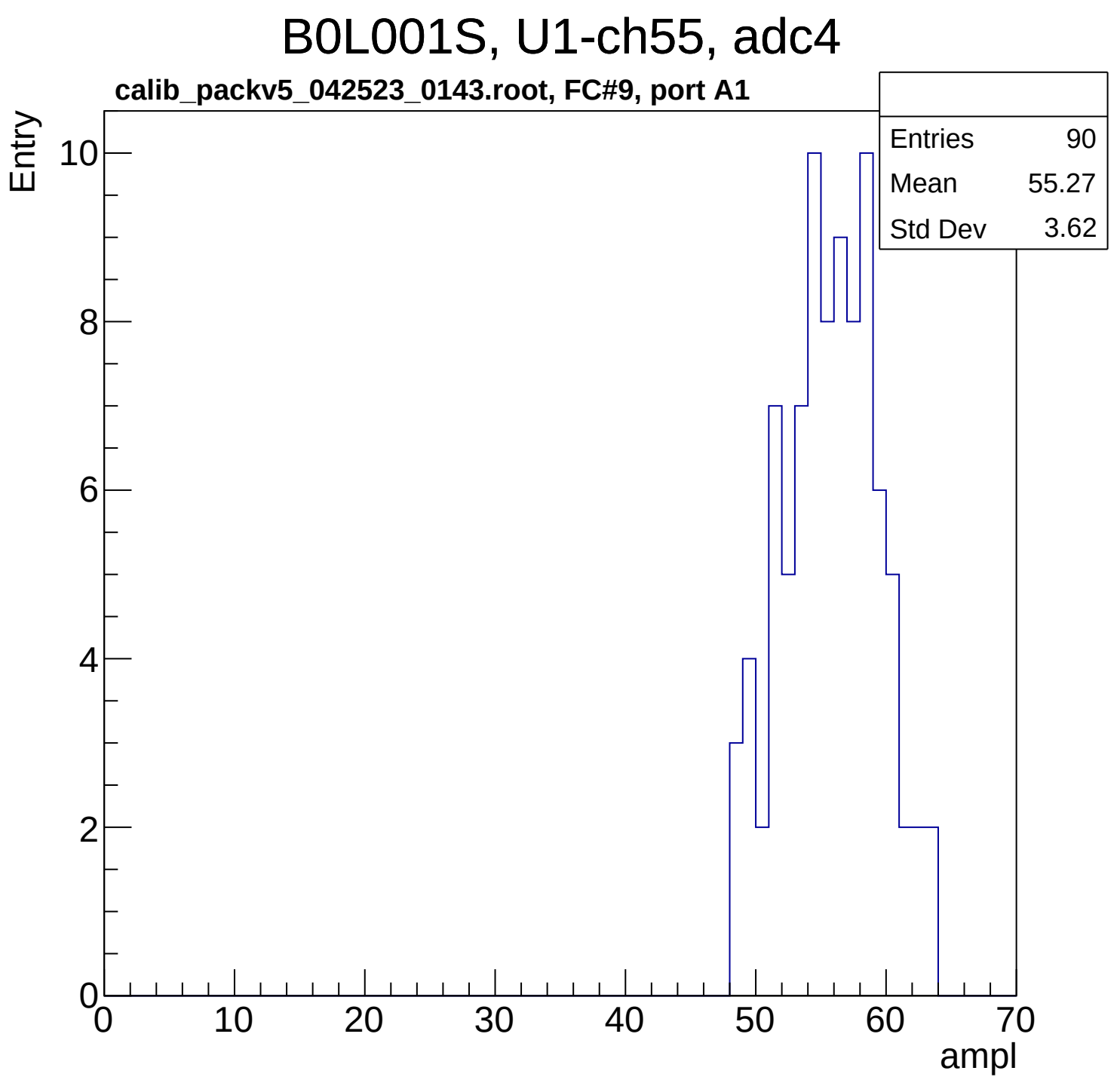
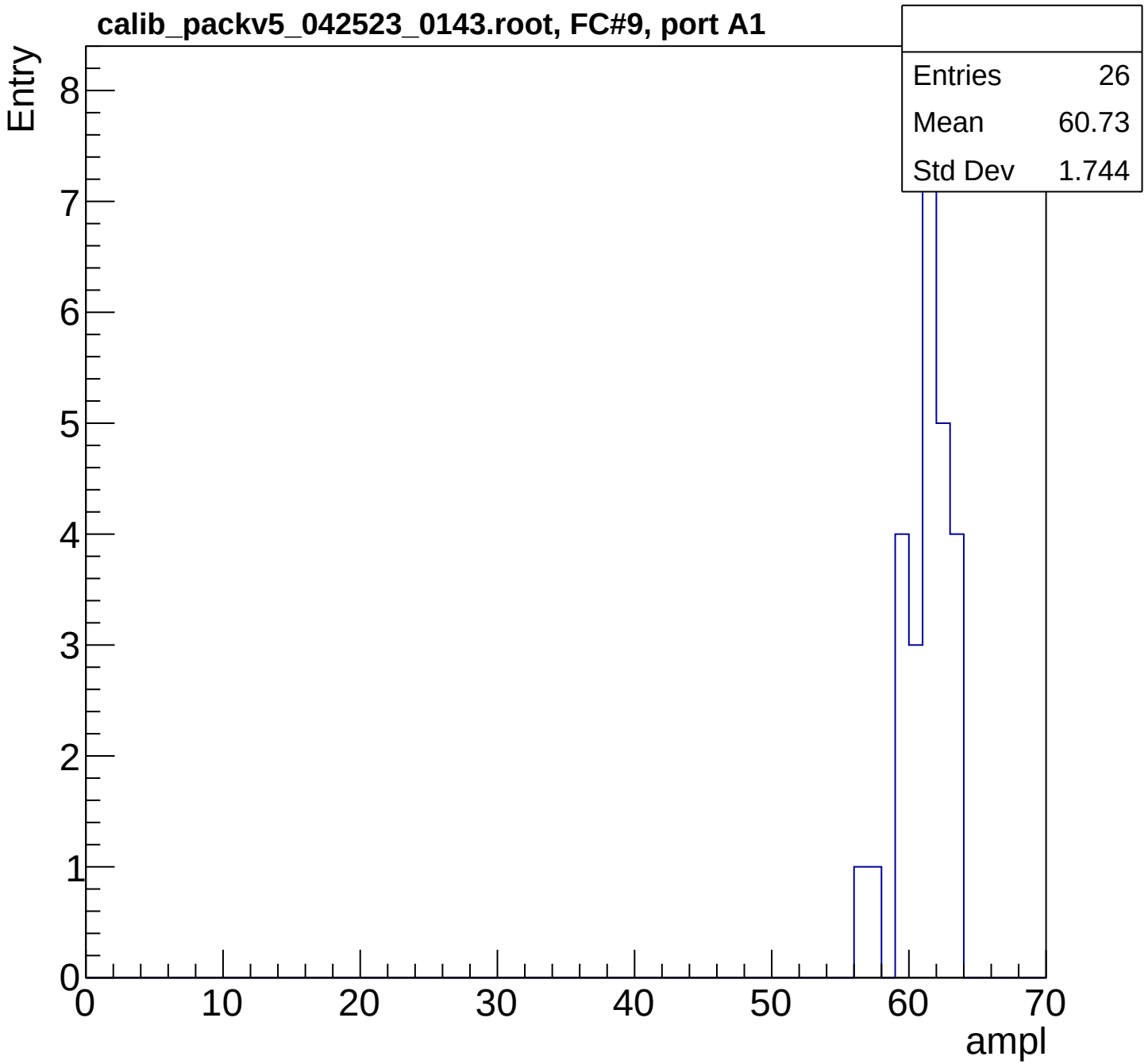

B0L001S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 90
Mean 55.27
Std Dev 3.62
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.73
Std Dev 1.744
Entry
ampl
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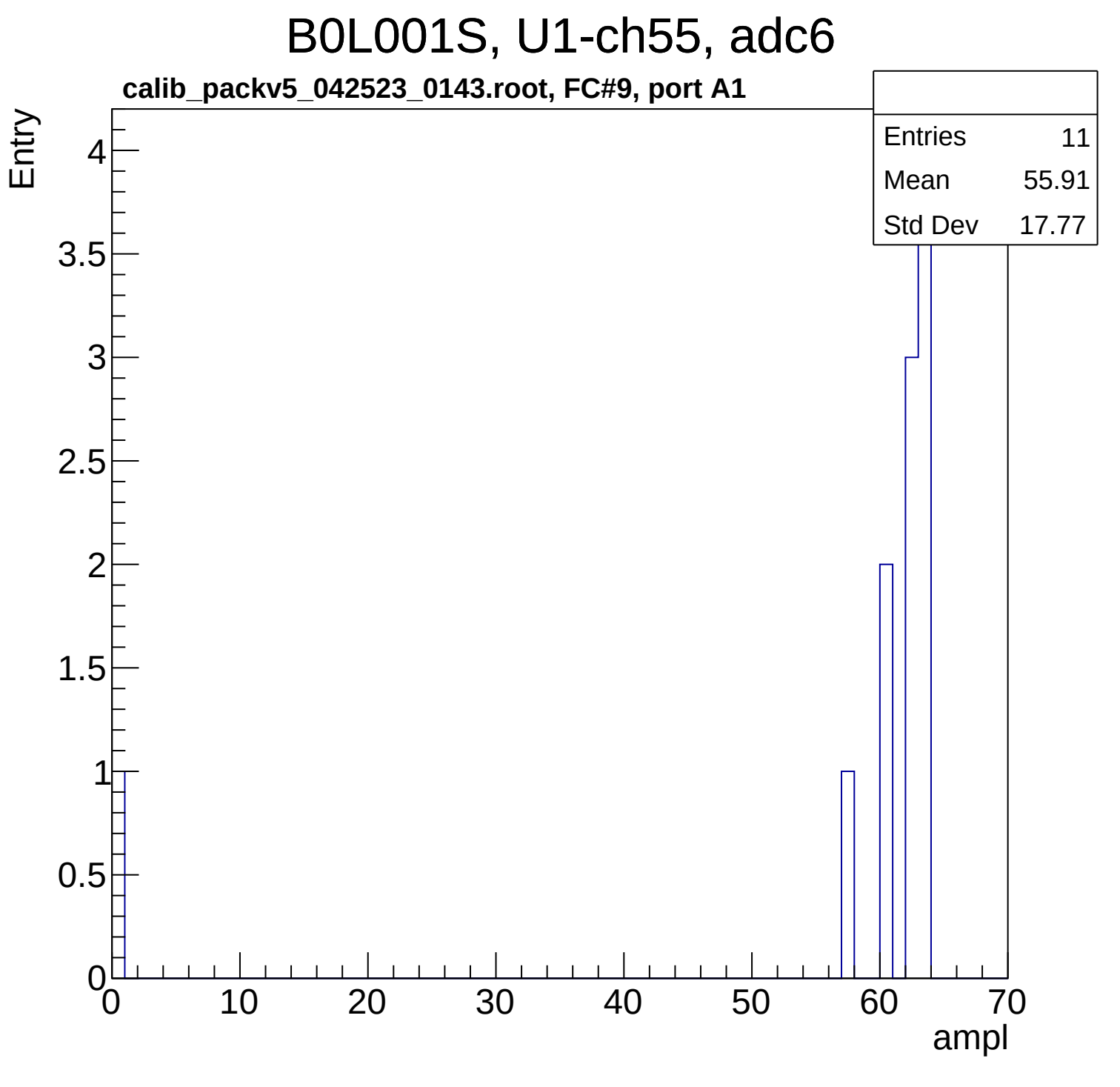
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 55.91
Std Dev 17.77
Entry
ampl
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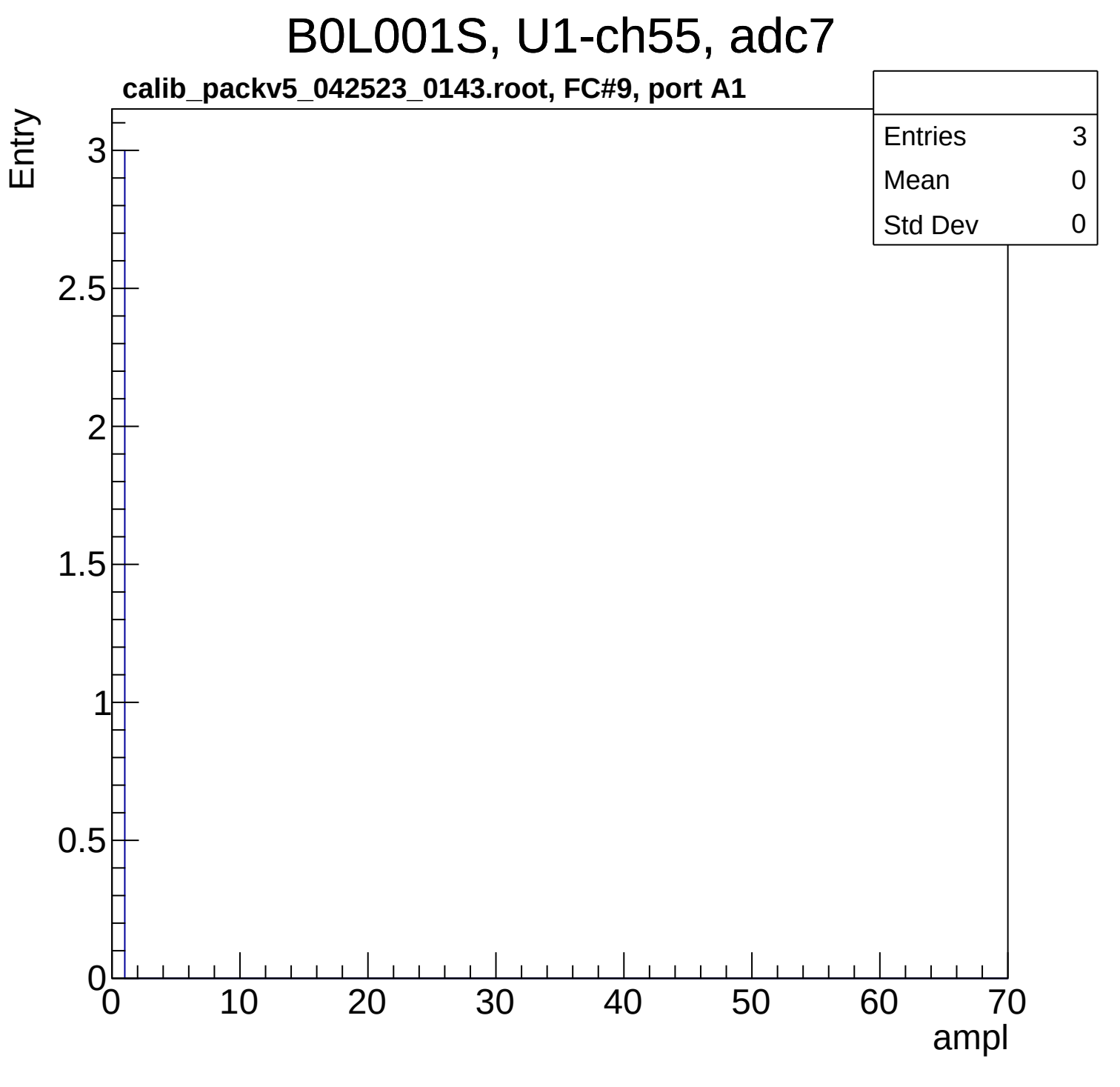
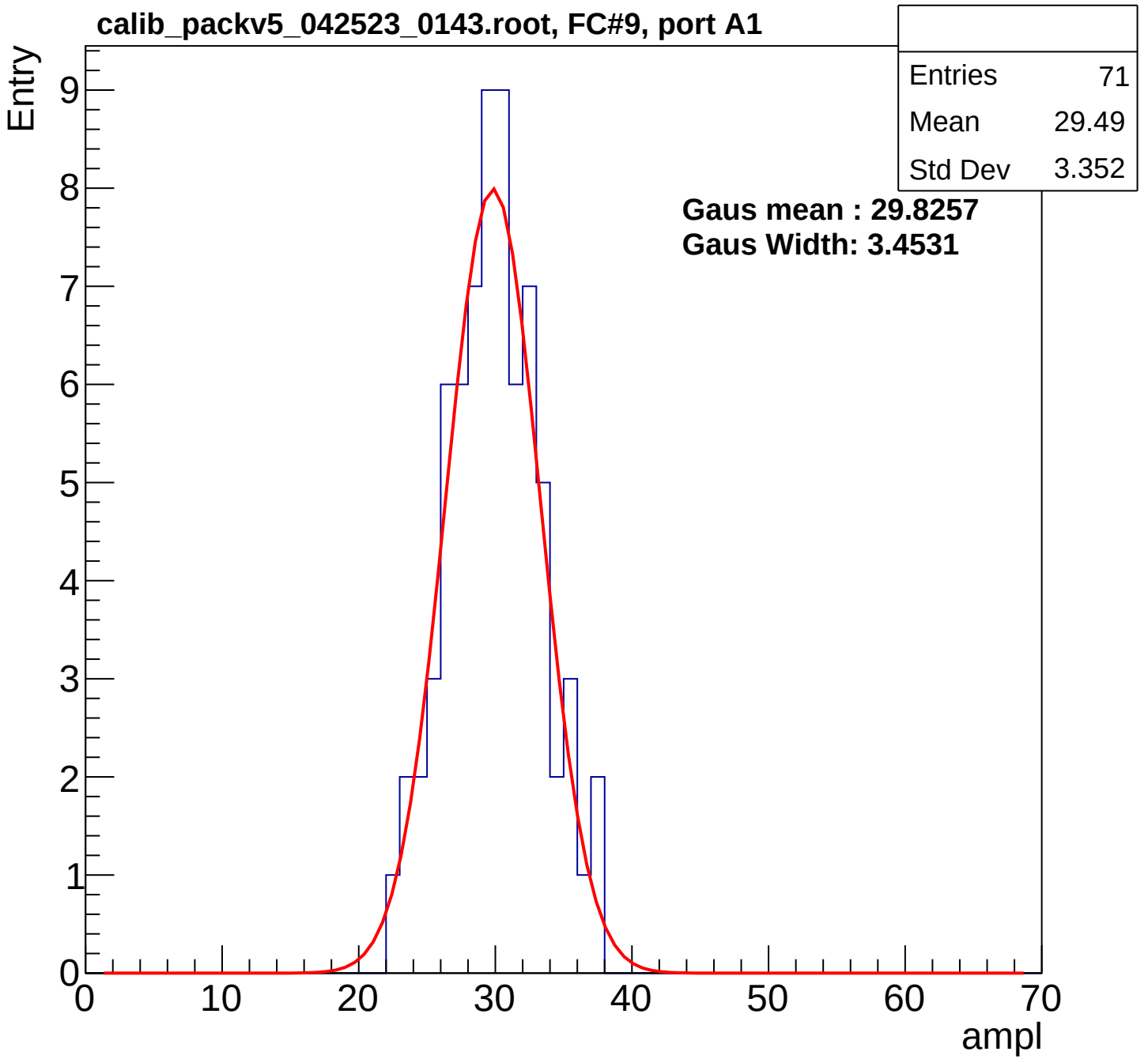
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 29.49
Std Dev 3.352
Gaus mean : 29.8257
Gaus Width: 3.4531
Entry
ampl
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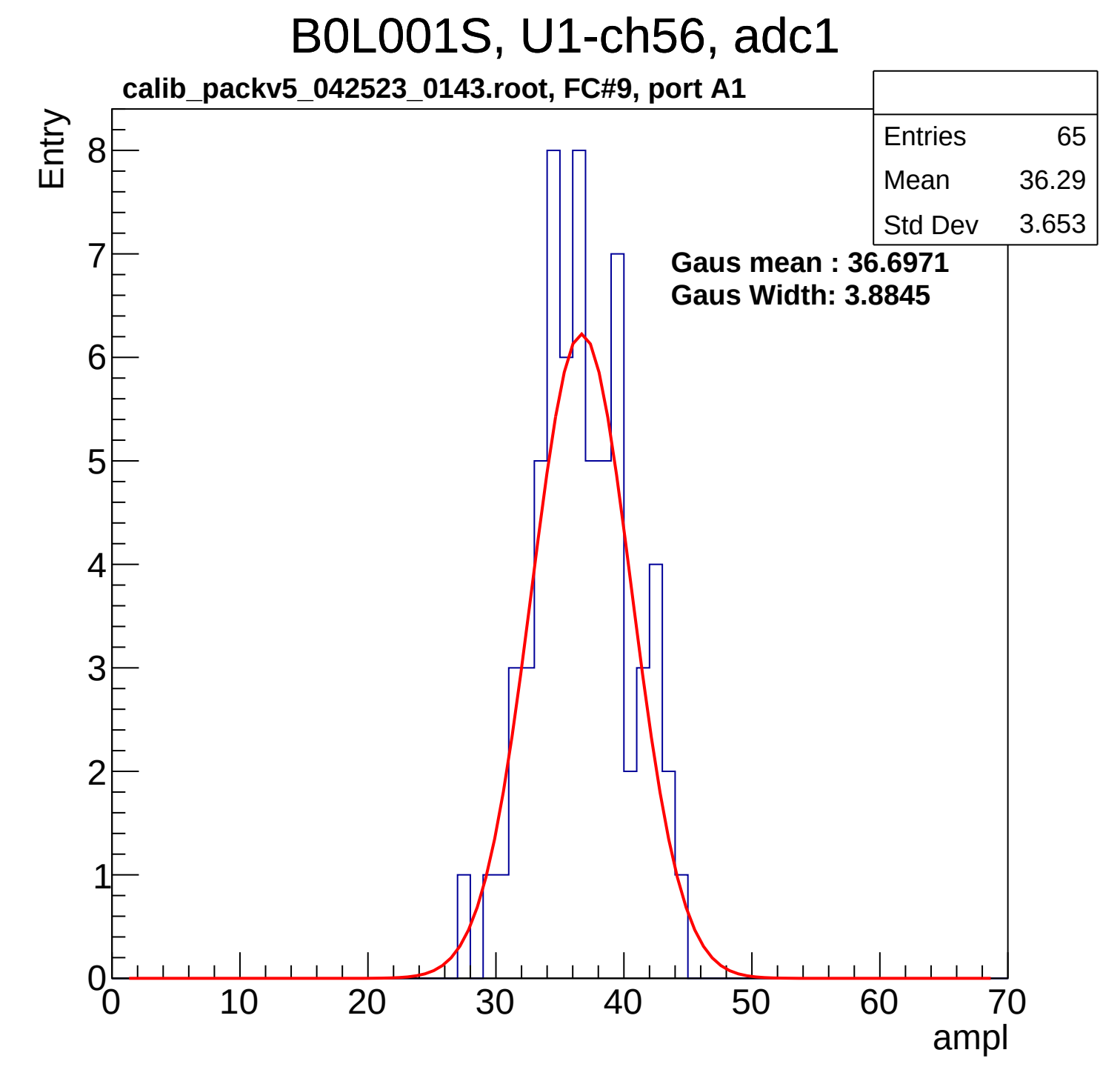
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.29
Std Dev 3.653
Gaus mean : 36.6971
Gaus Width: 3.8845
Entry
ampl
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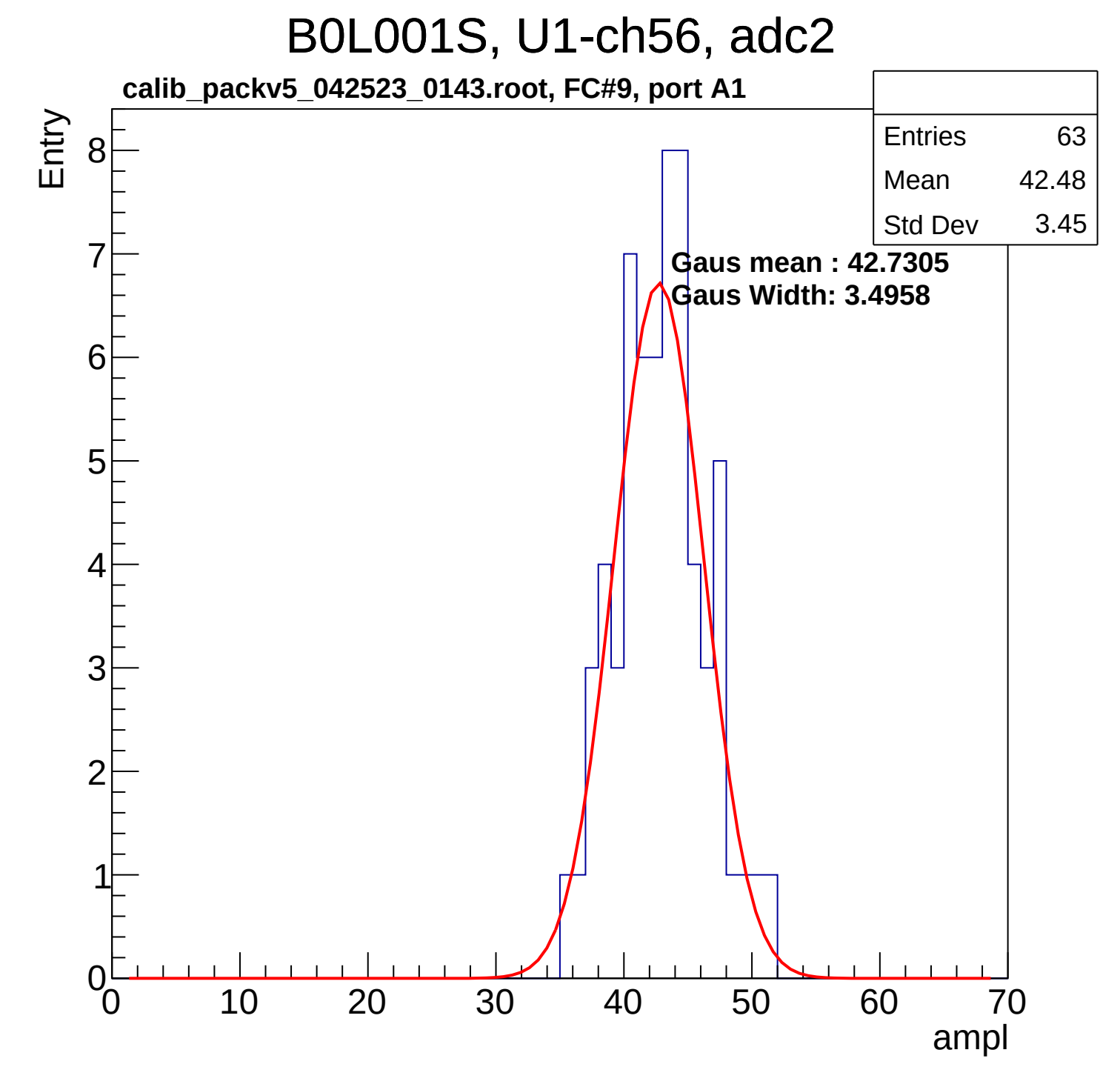
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 42.48
Std Dev 3.45
Gaus mean : 42.7305
Gaus Width: 3.4958
Entry
ampl
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1
2
3
4
5
6
7
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40
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60
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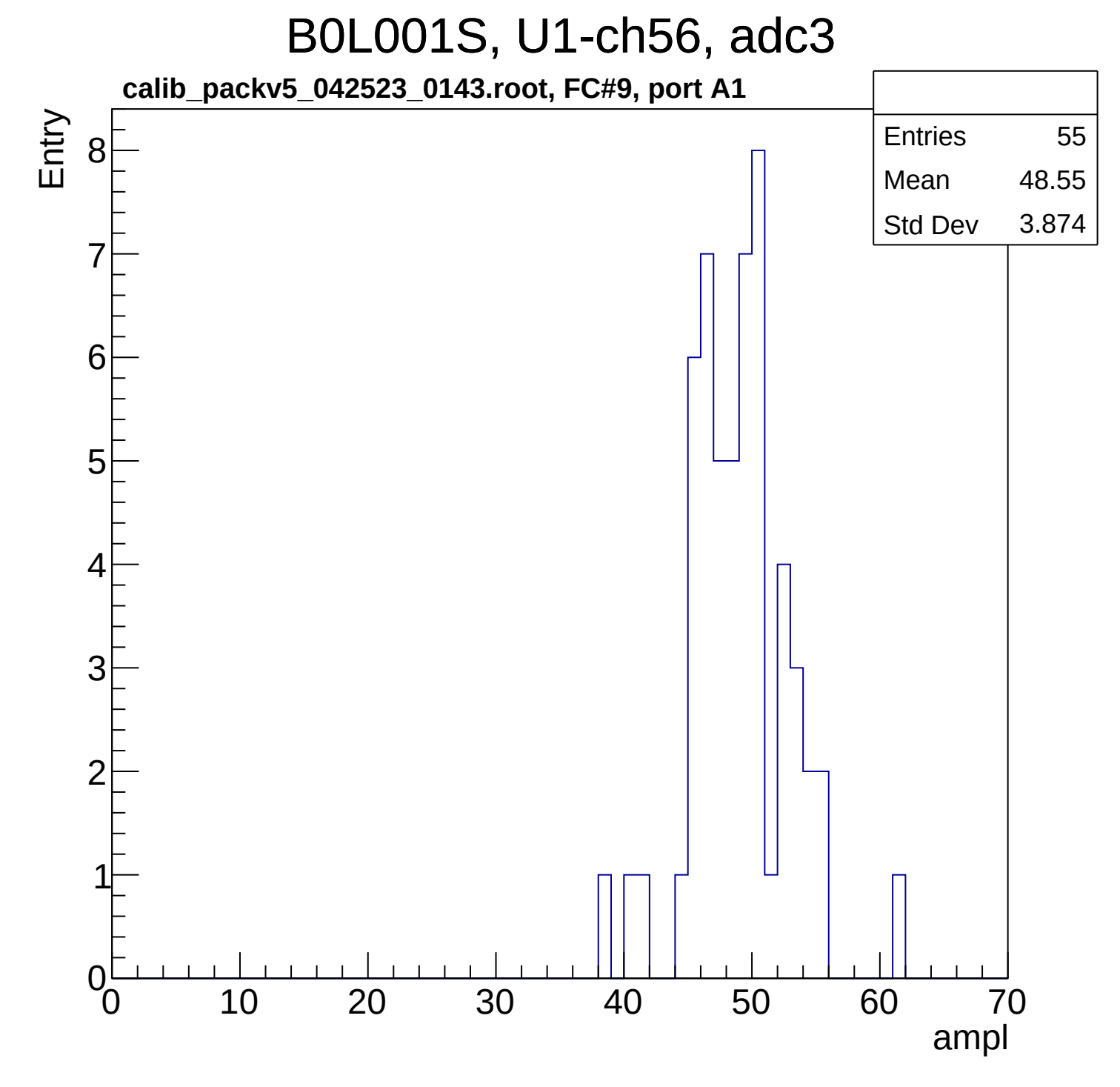
B0L001S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 48.55
Std Dev 3.874
Entry
ampl
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5
6
7
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60
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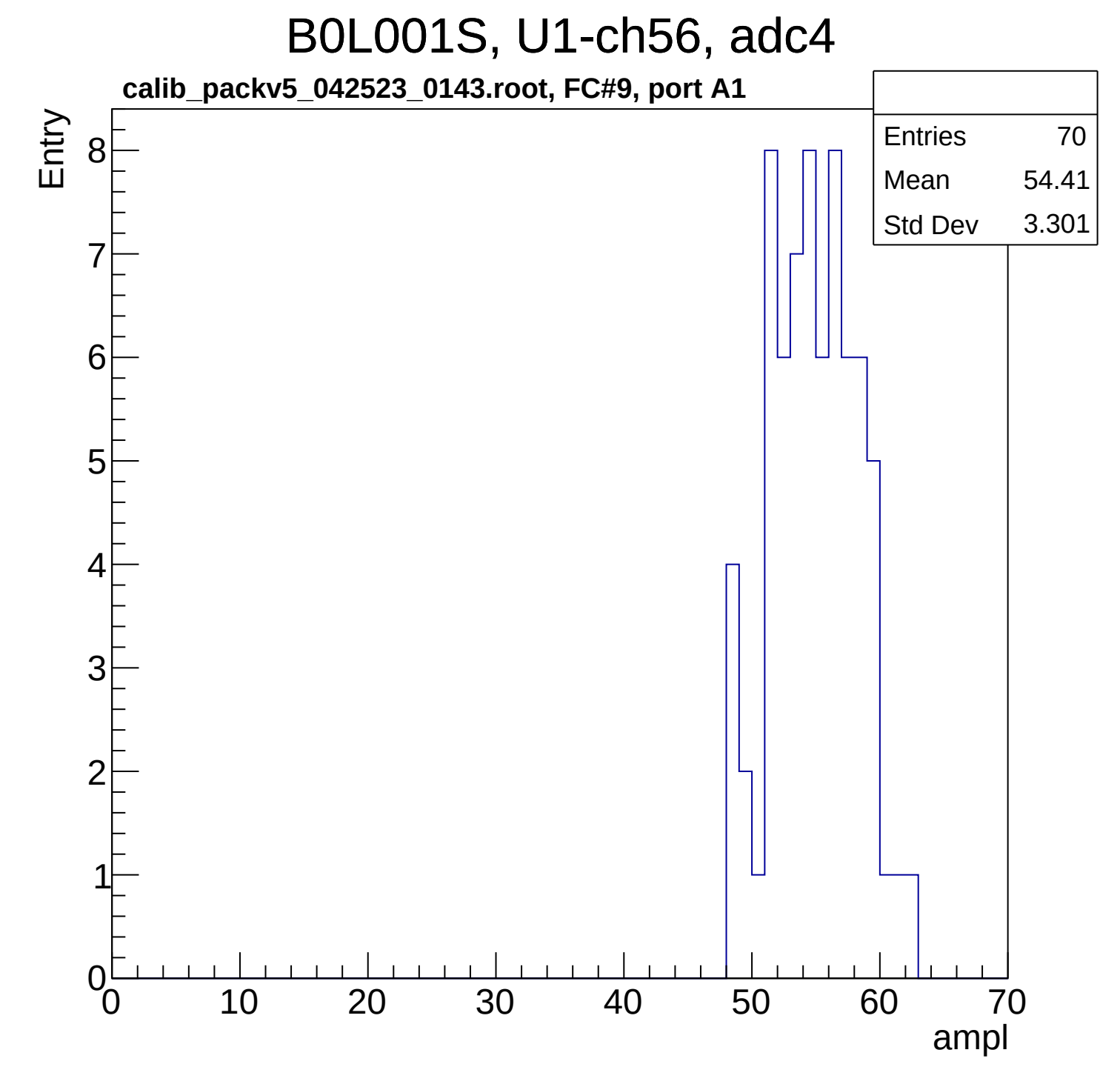
B0L001S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.41
Std Dev 3.301
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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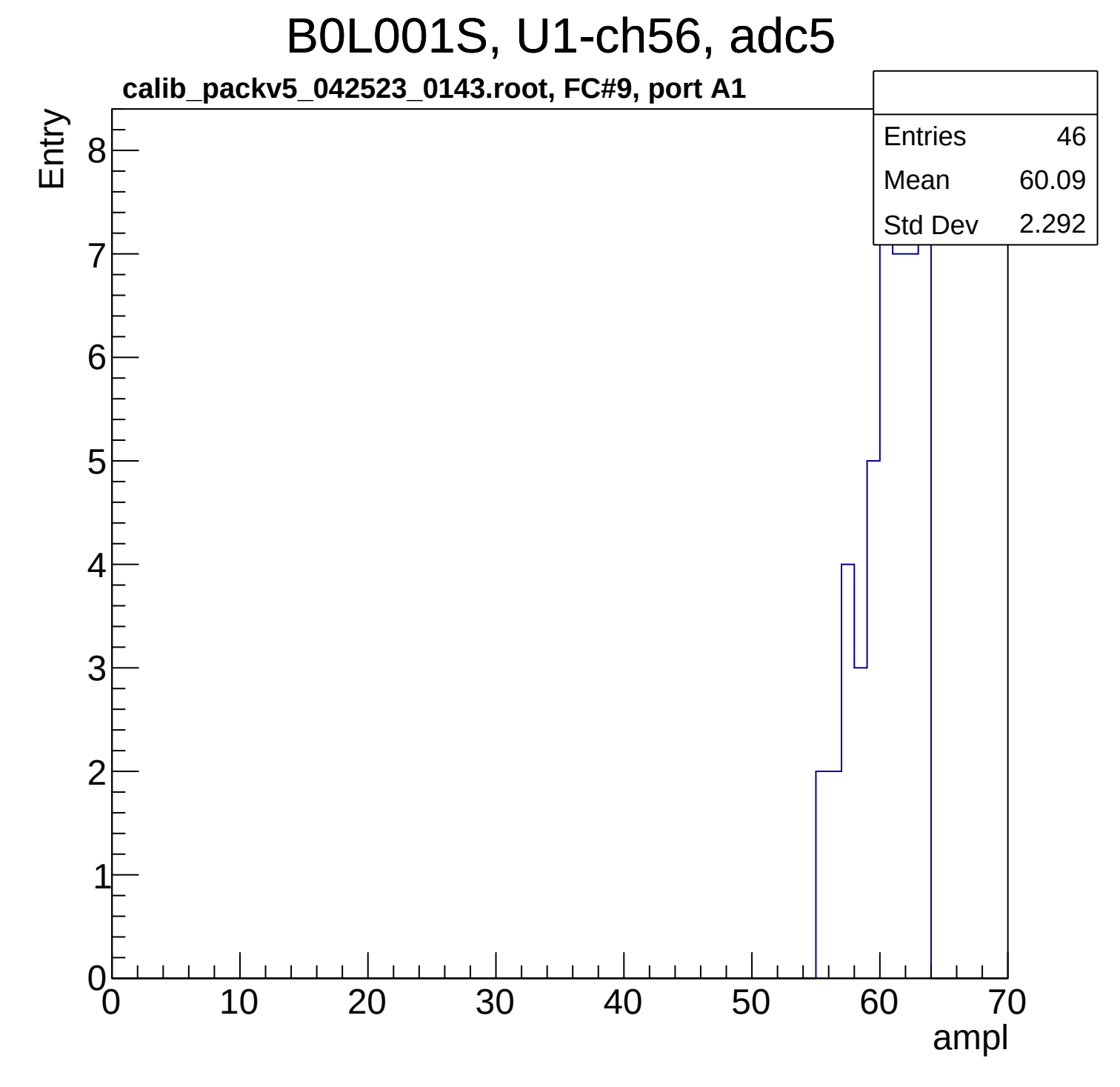
B0L001S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 60.09
Std Dev 2.292
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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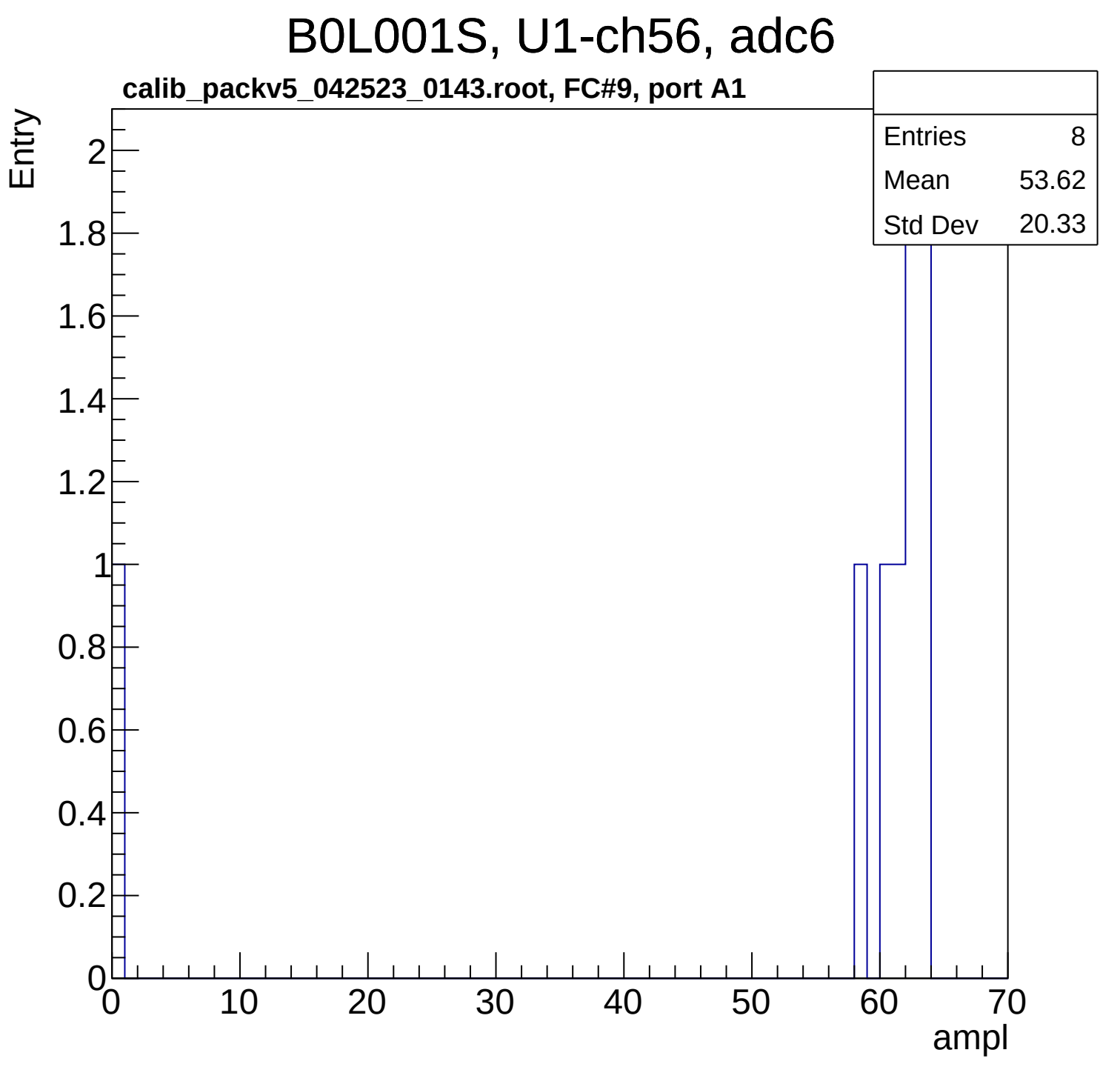
60
70


B0L001S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 53.62
Std Dev 20.33
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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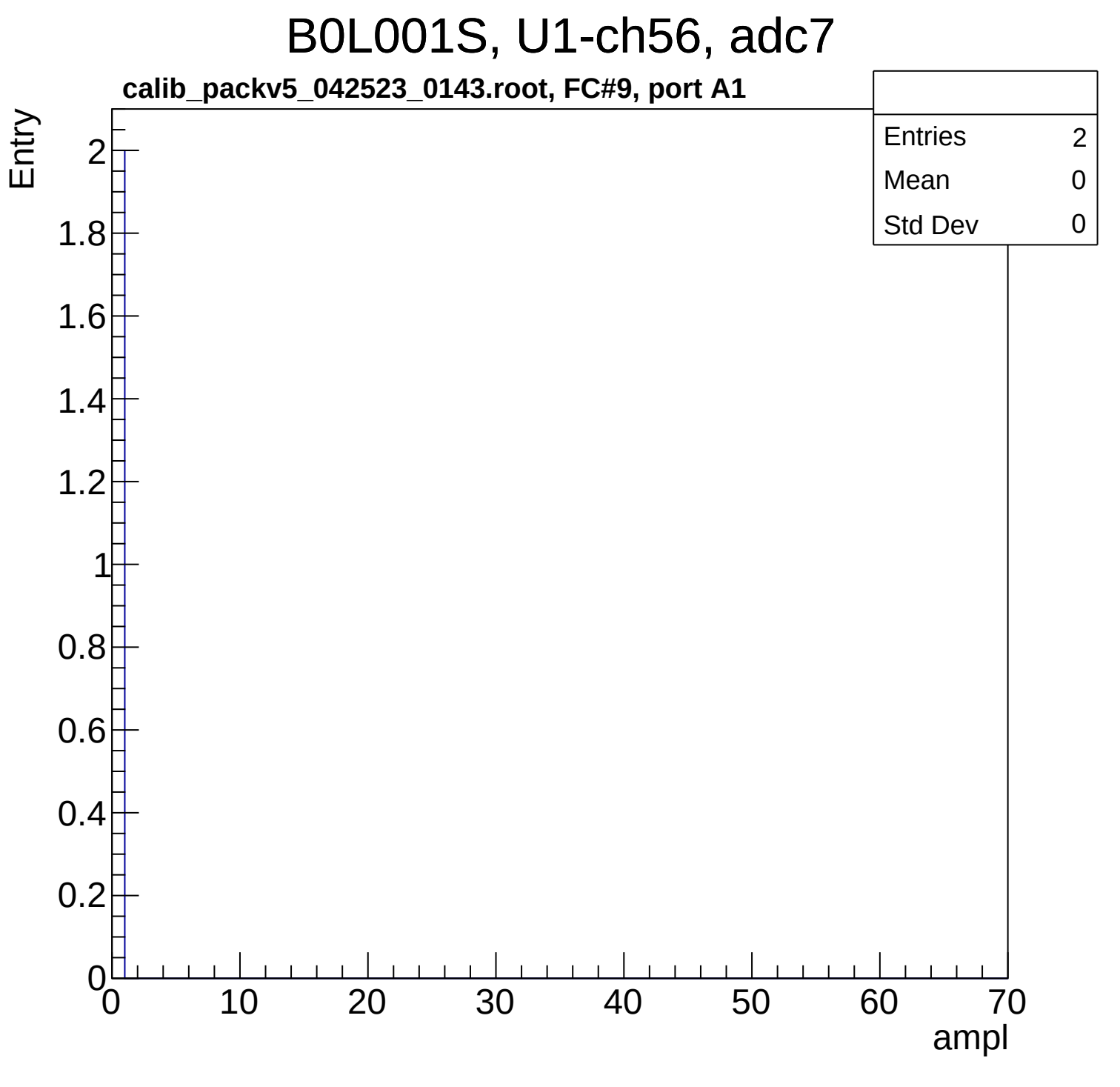
B0L001S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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10
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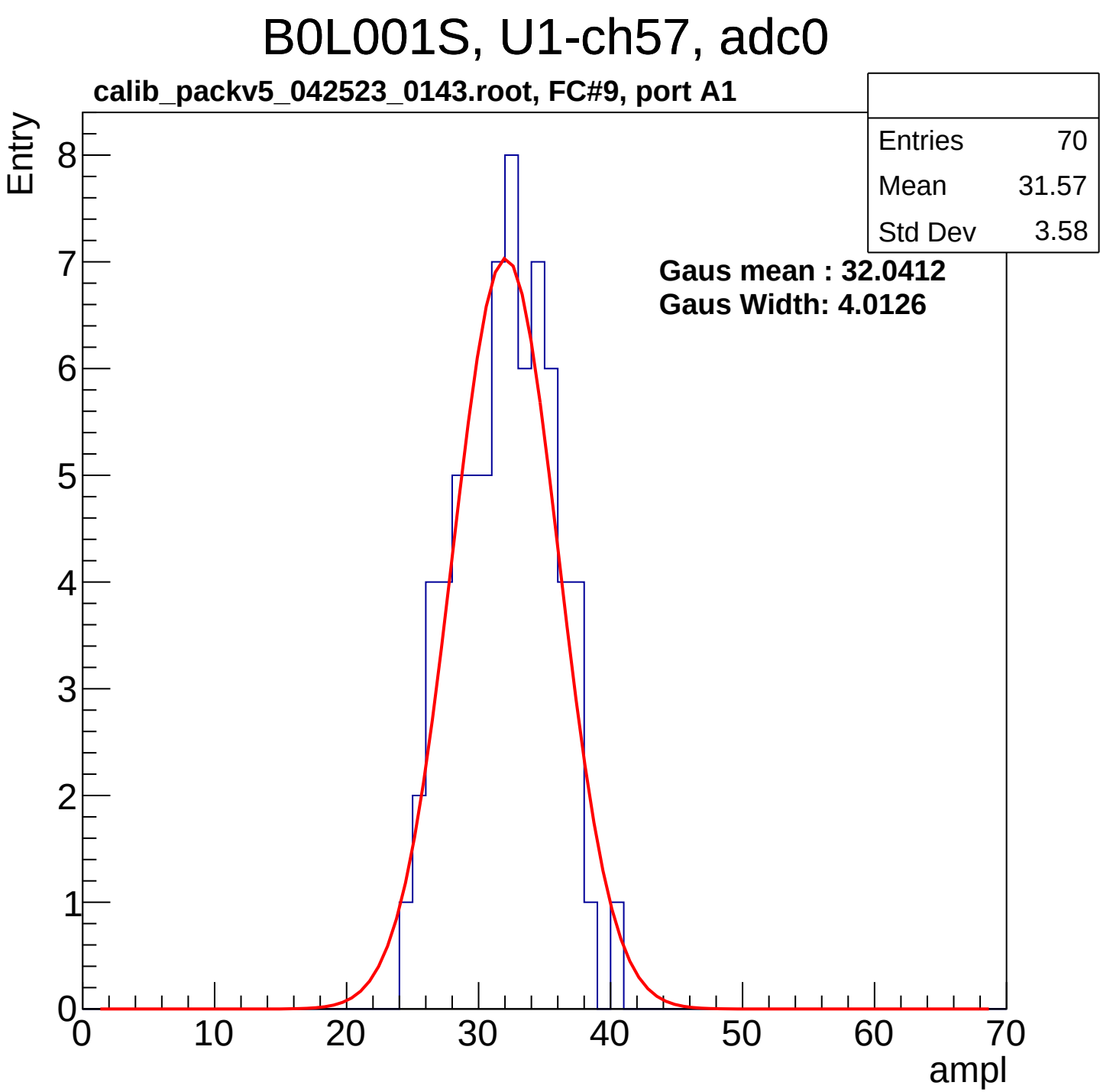
50
60
70
ampl


B0L001S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.57
Std Dev 3.58
Gaus mean : 32.0412
Gaus Width: 4.0126
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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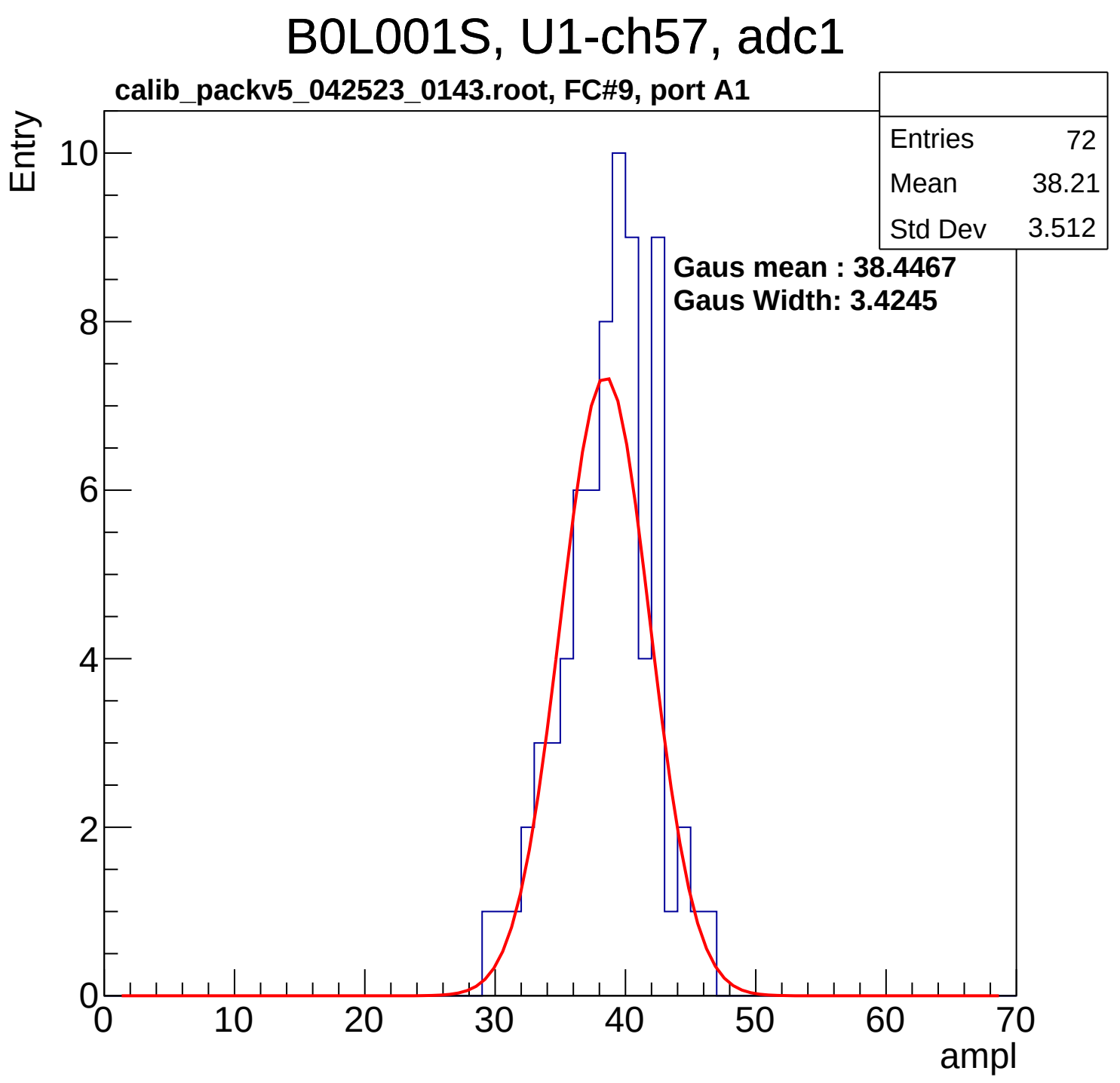
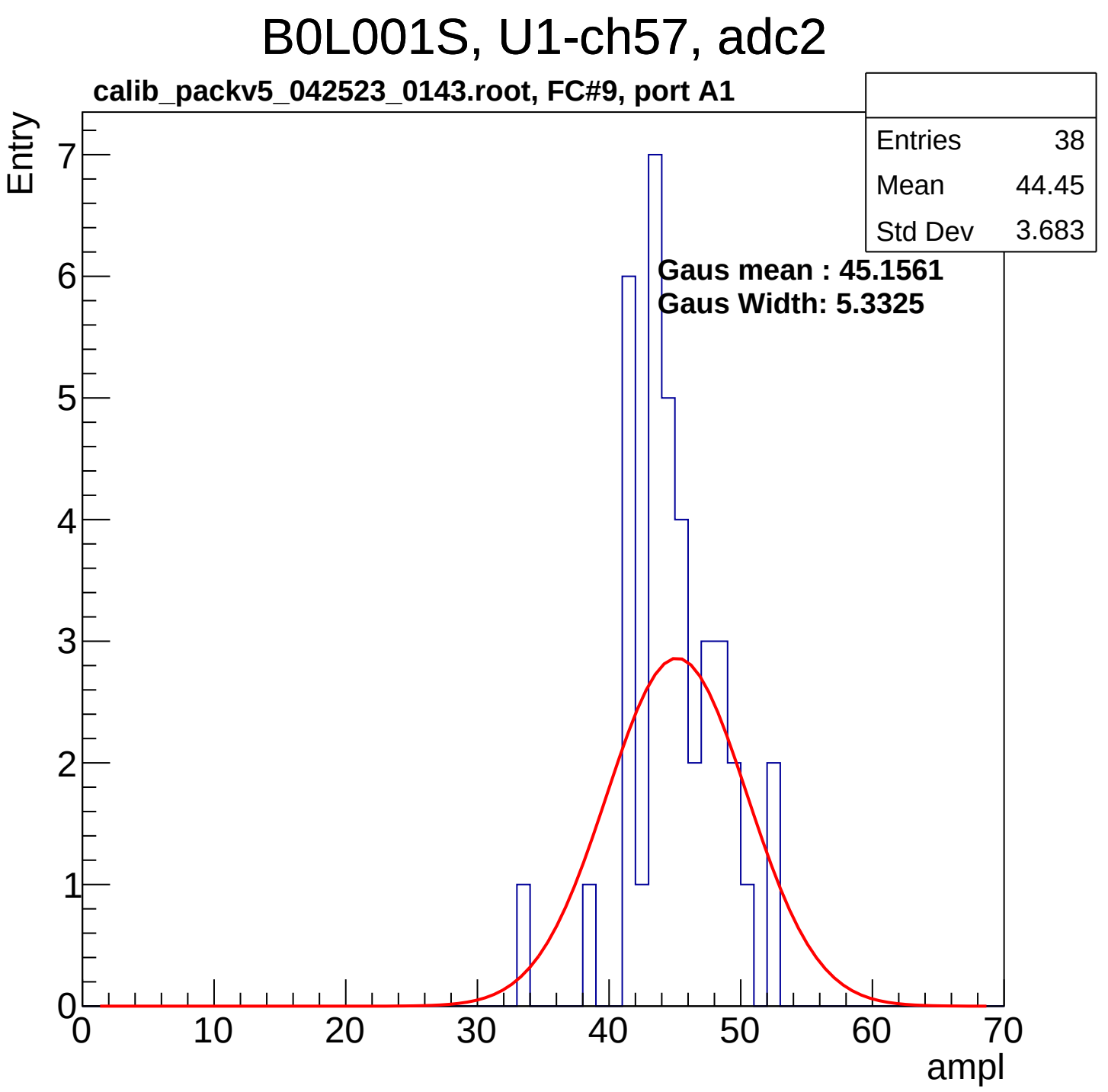
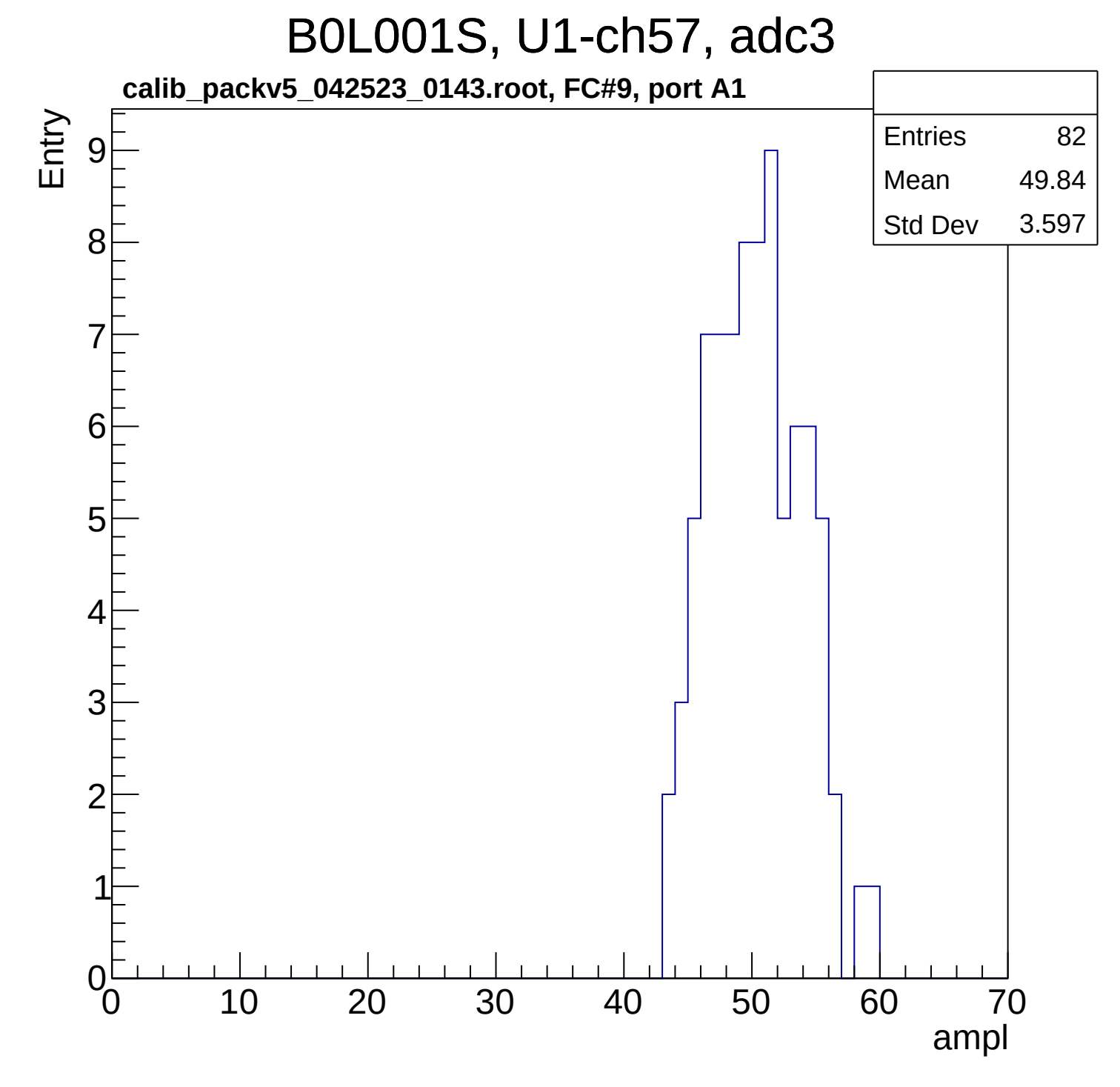
40
50
60
70


B0L001S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 38.21
Std Dev 3.512
Gaus mean : 38.4467
Gaus Width: 3.4245
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 44.45
Std Dev 3.683
Gaus mean : 45.1561
Gaus Width: 5.3325
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 49.84
Std Dev 3.597
Entry
ampl
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3
4
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7
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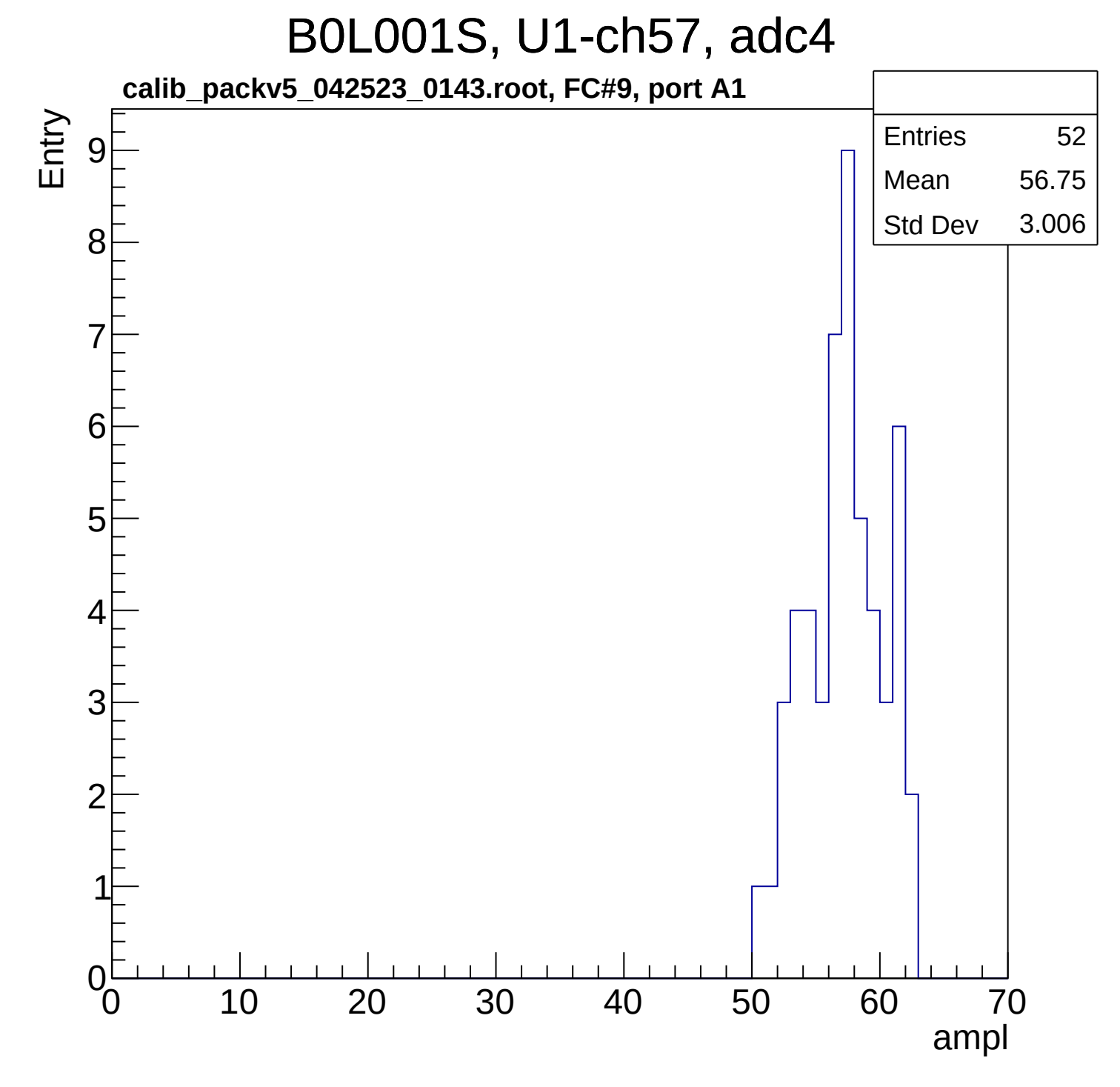
B0L001S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 56.75
Std Dev 3.006
Entry
ampl
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3
4
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6
7
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9
0
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20
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60
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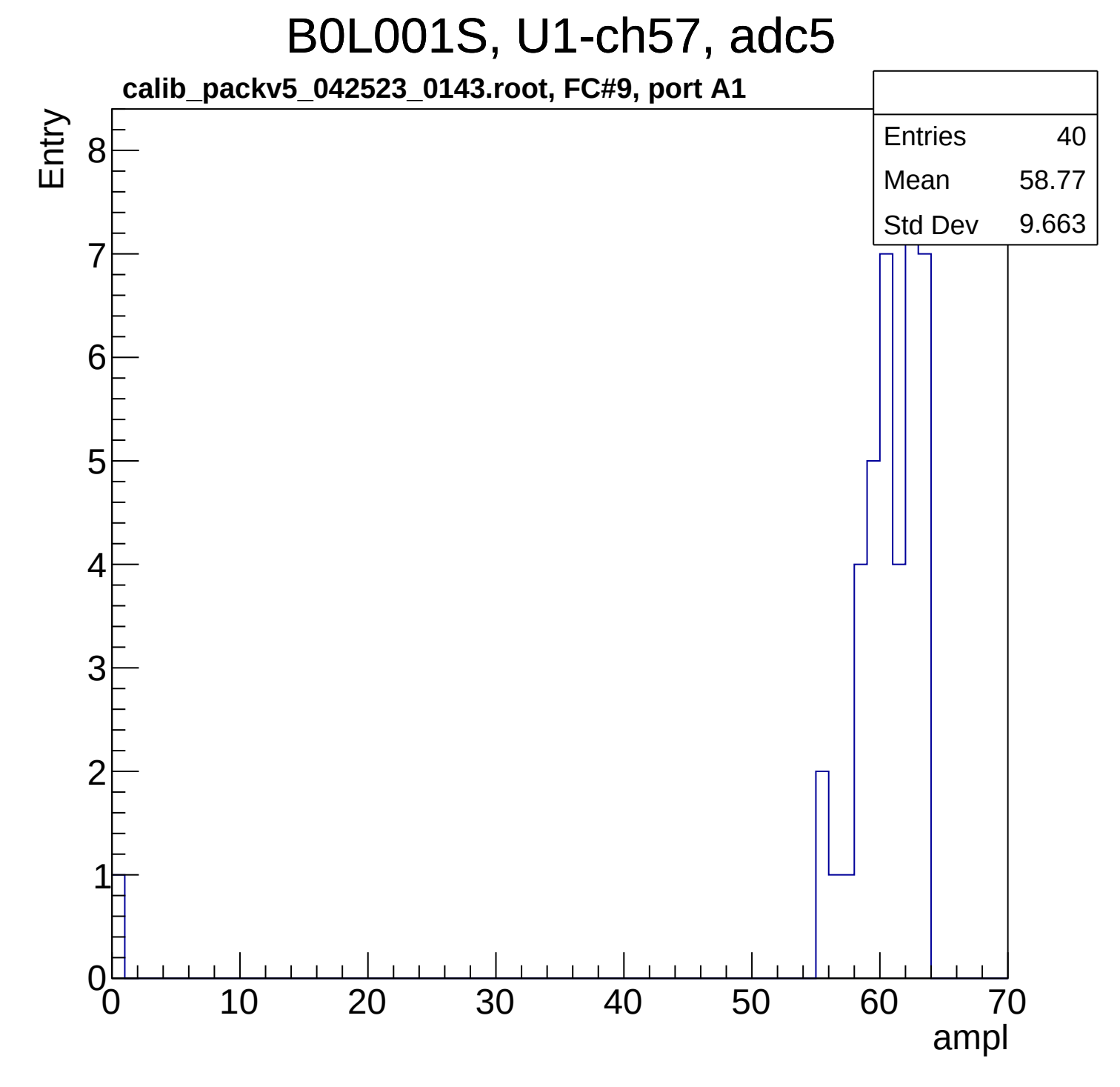
B0L001S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.77
Std Dev 9.663
Entry
ampl
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1
2
3
4
5
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7
8
0
10
20
30
40
50
60
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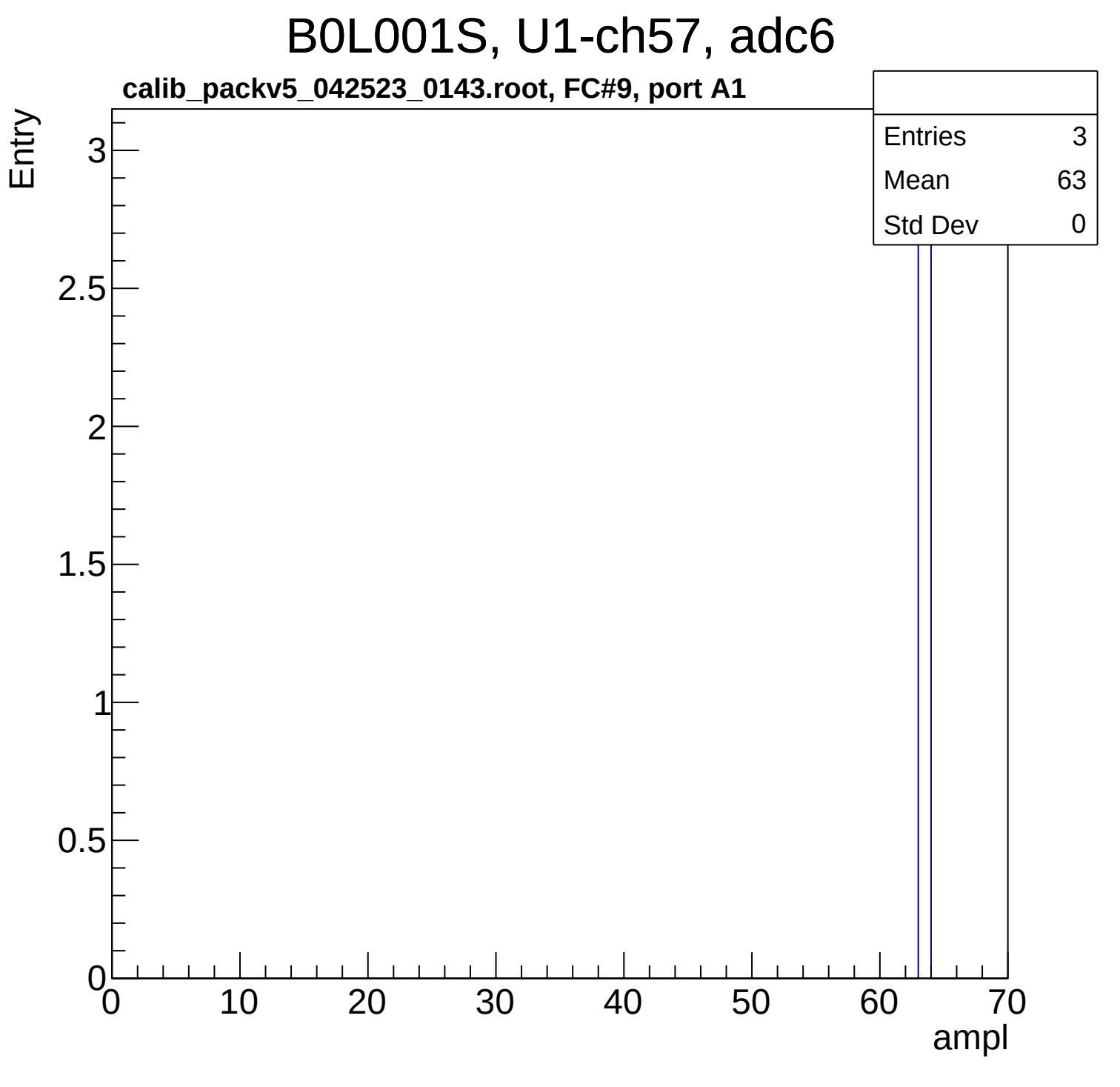
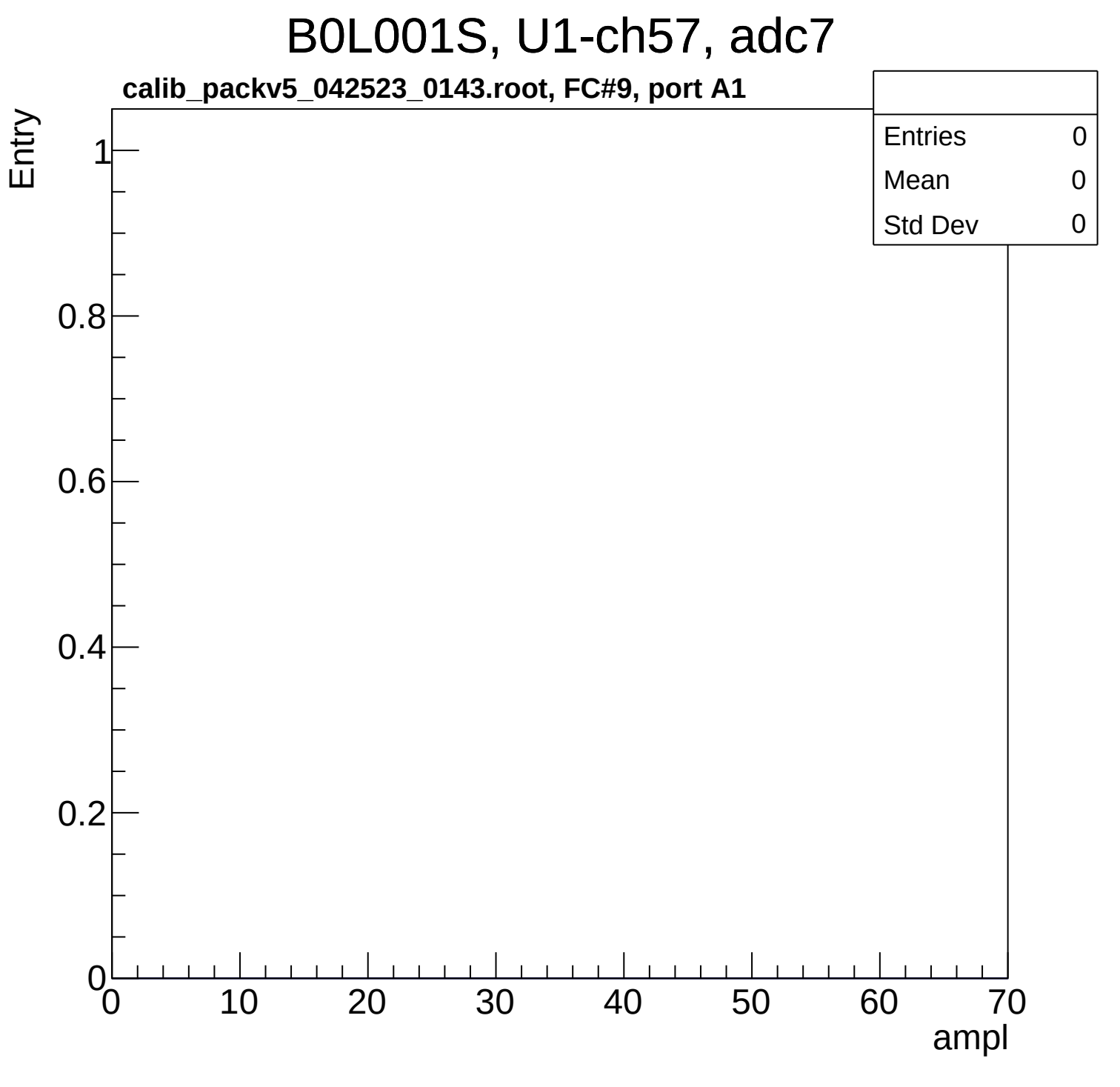
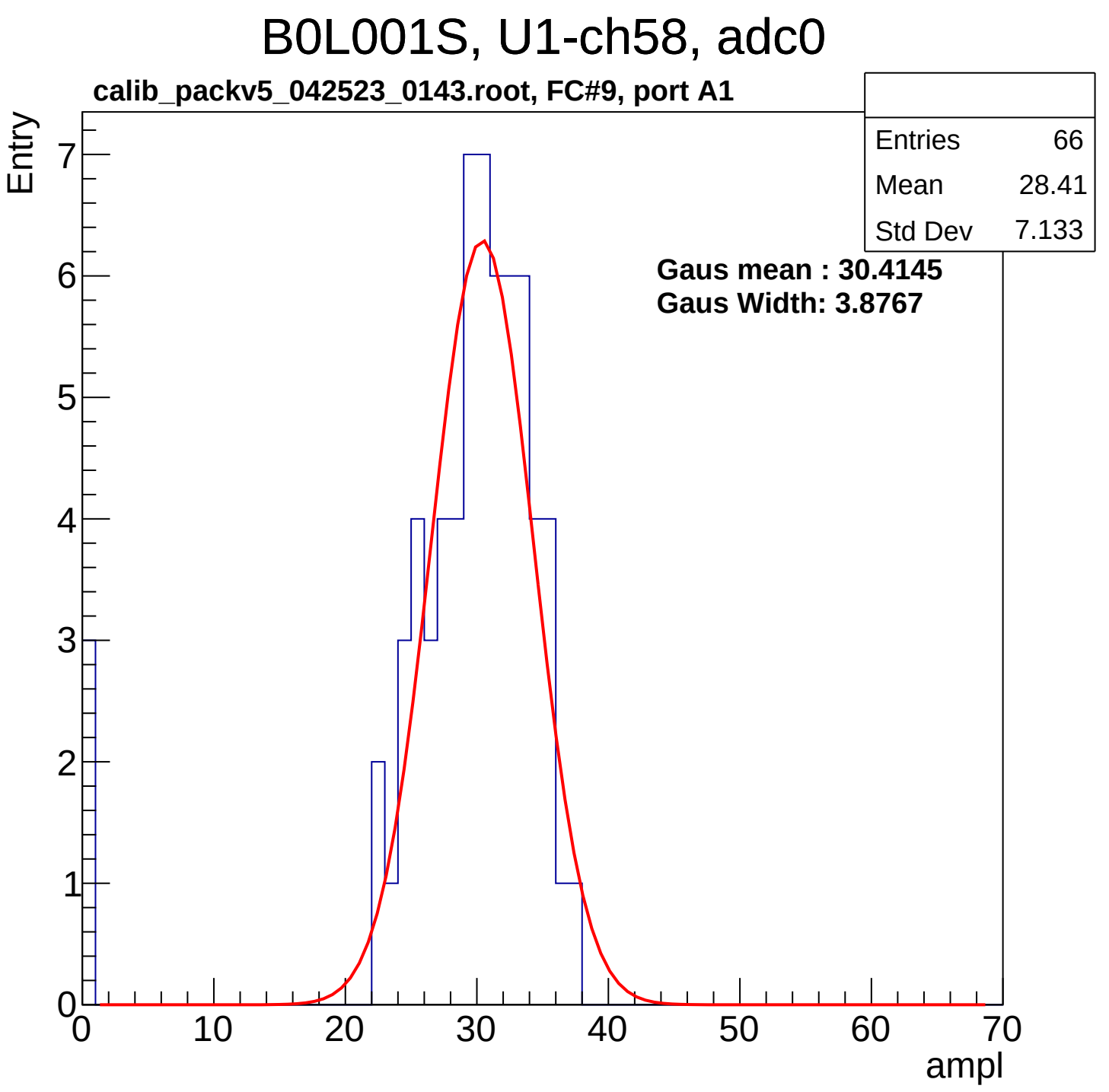
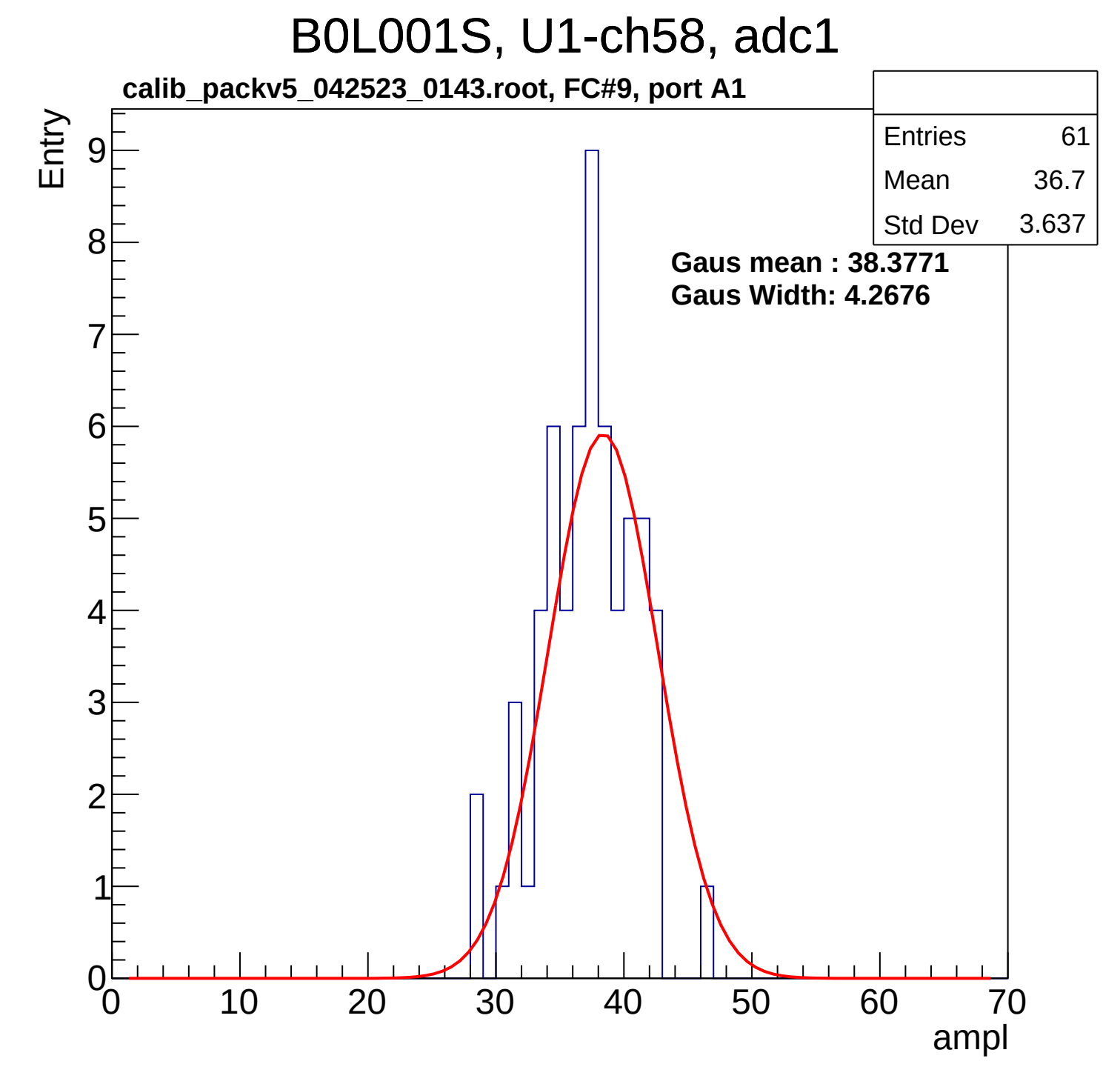

B0L001S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 28.41
Std Dev 7.133
Gaus mean : 30.4145
Gaus Width: 3.8767
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 36.7
Std Dev 3.637
Gaus mean : 38.3771
Gaus Width: 4.2676
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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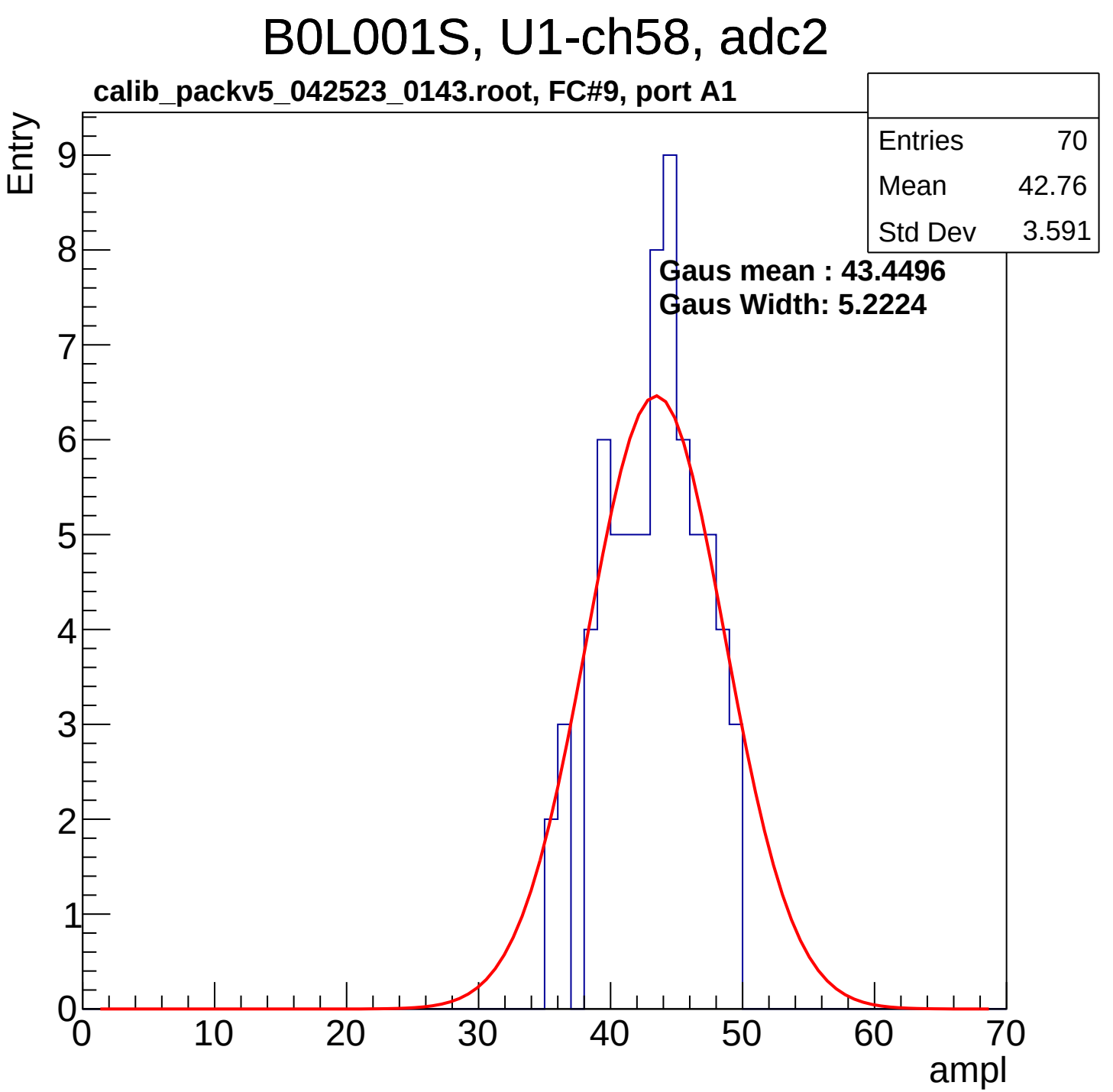
B0L001S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 42.76
Std Dev 3.591
Gaus mean : 43.4496
Gaus Width: 5.2224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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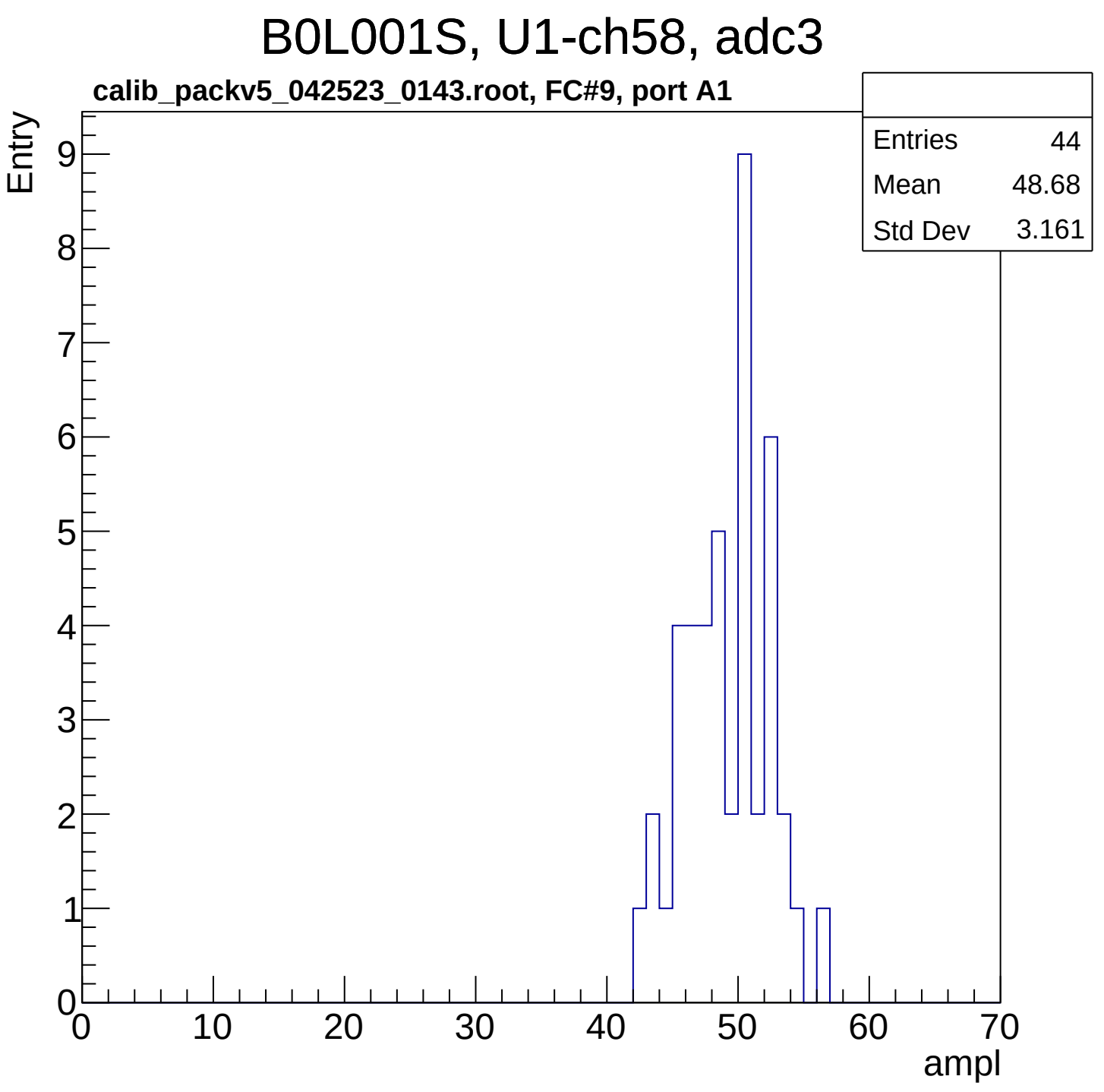
B0L001S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 48.68
Std Dev 3.161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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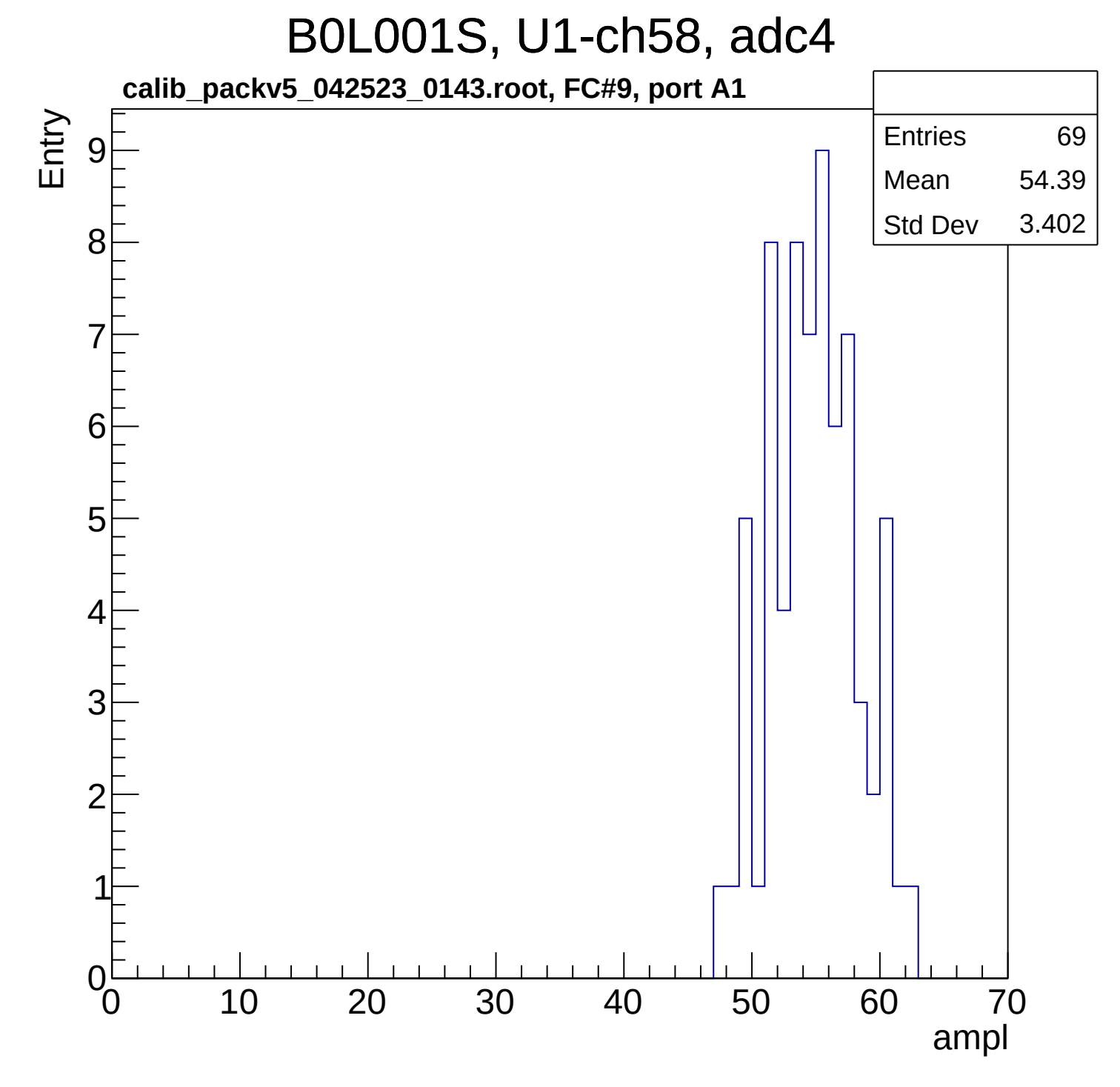
B0L001S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 54.39
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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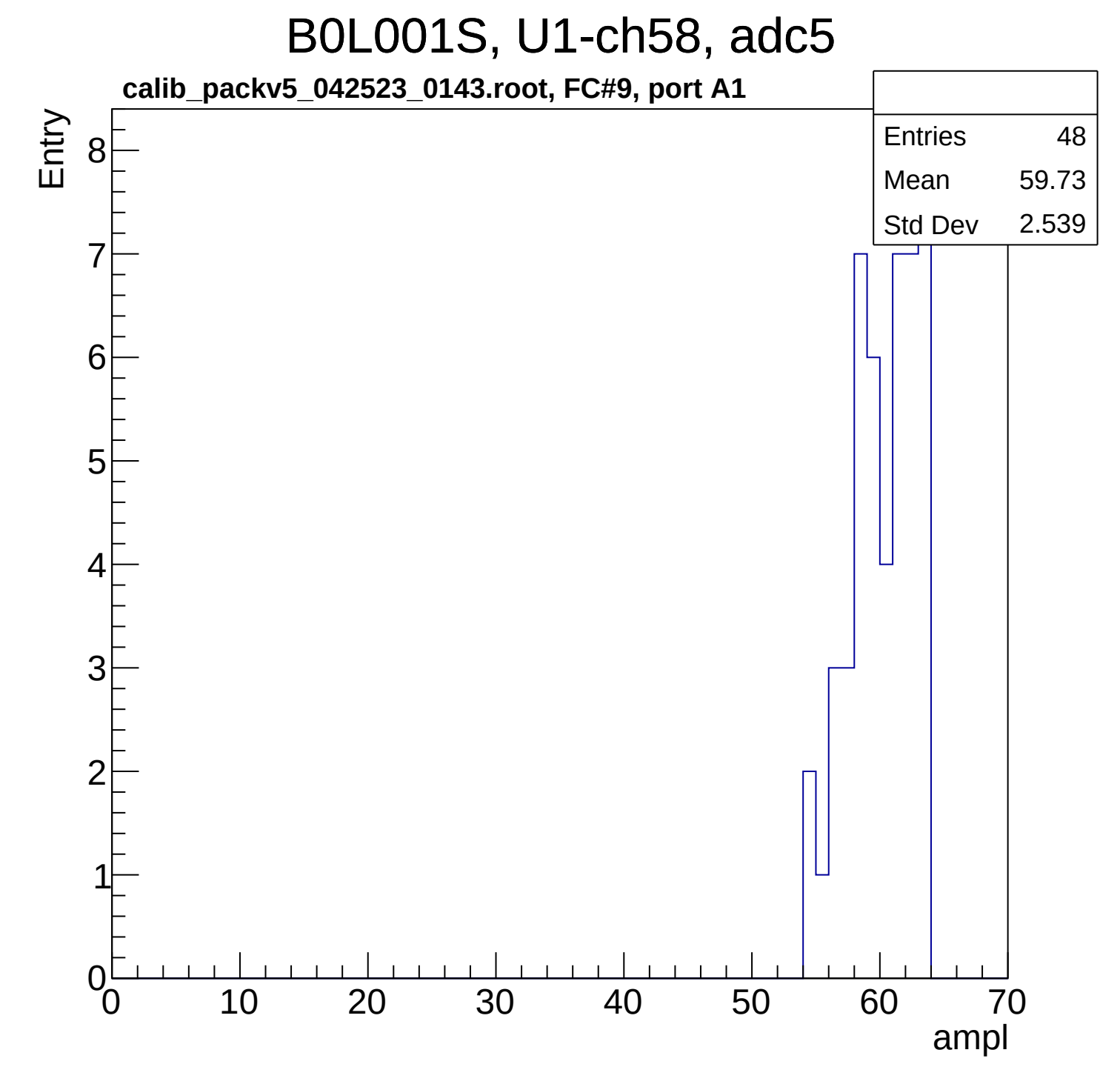
B0L001S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59.73
Std Dev 2.539
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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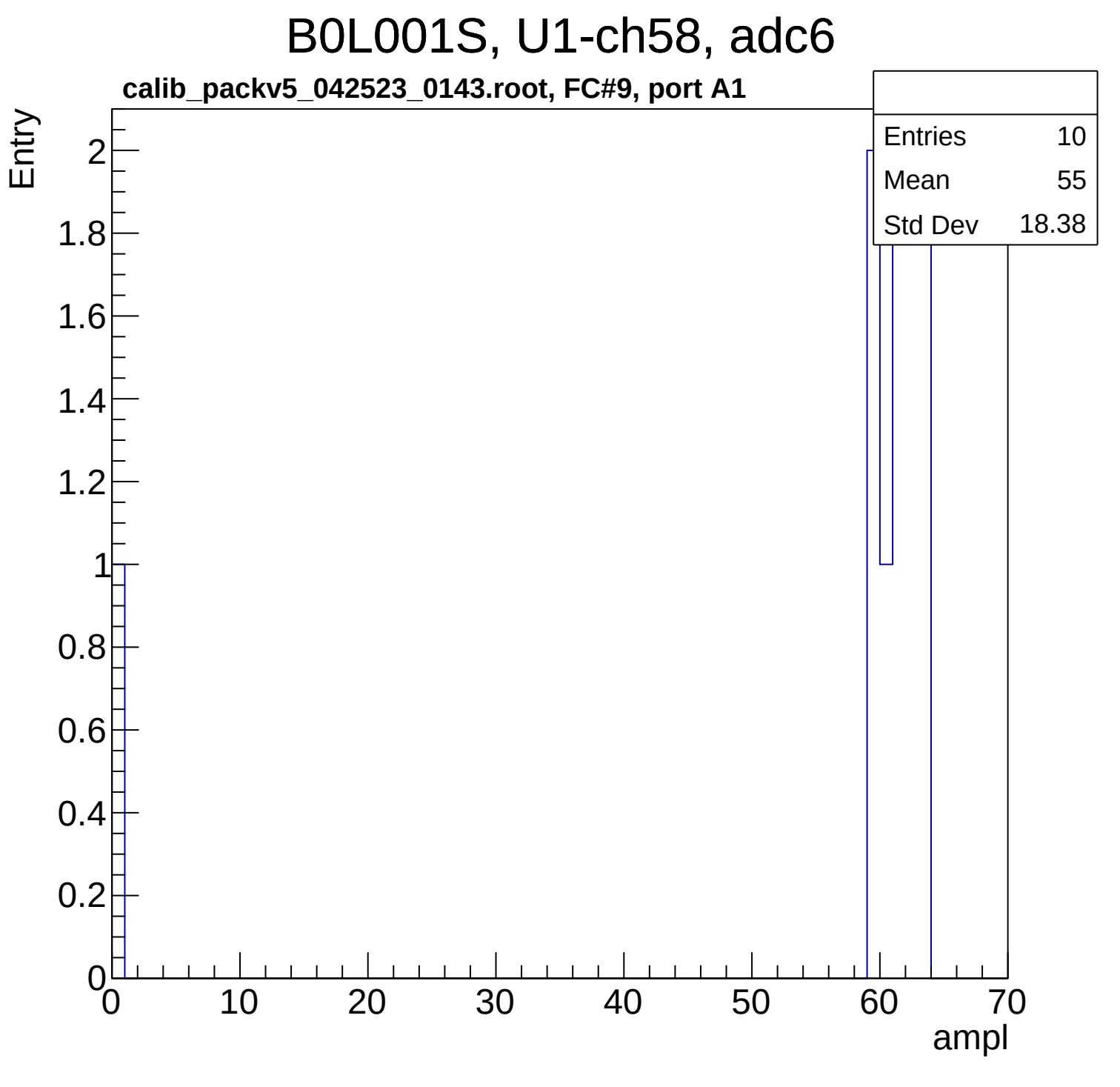
B0L001S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 55
Std Dev 18.38
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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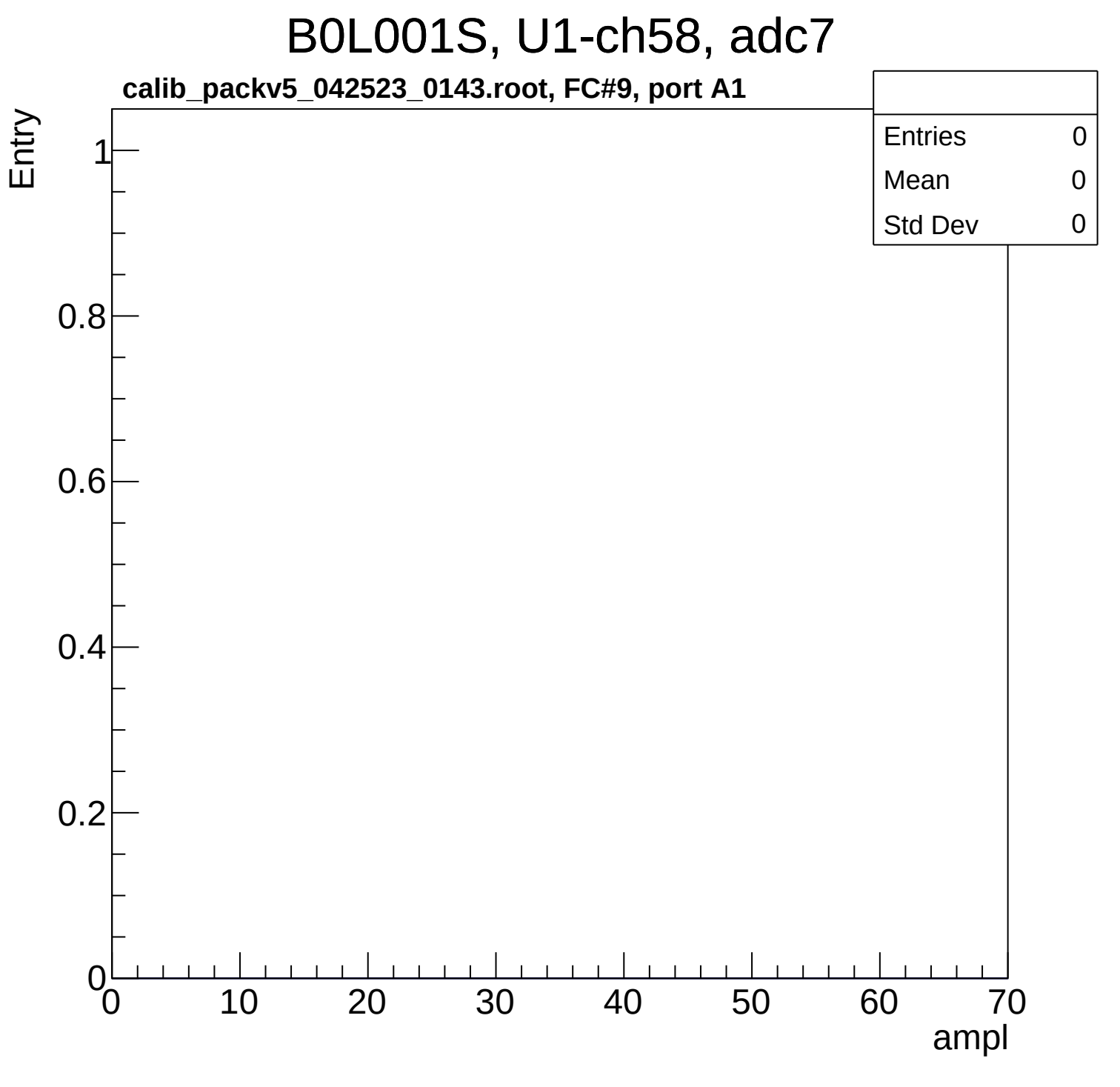
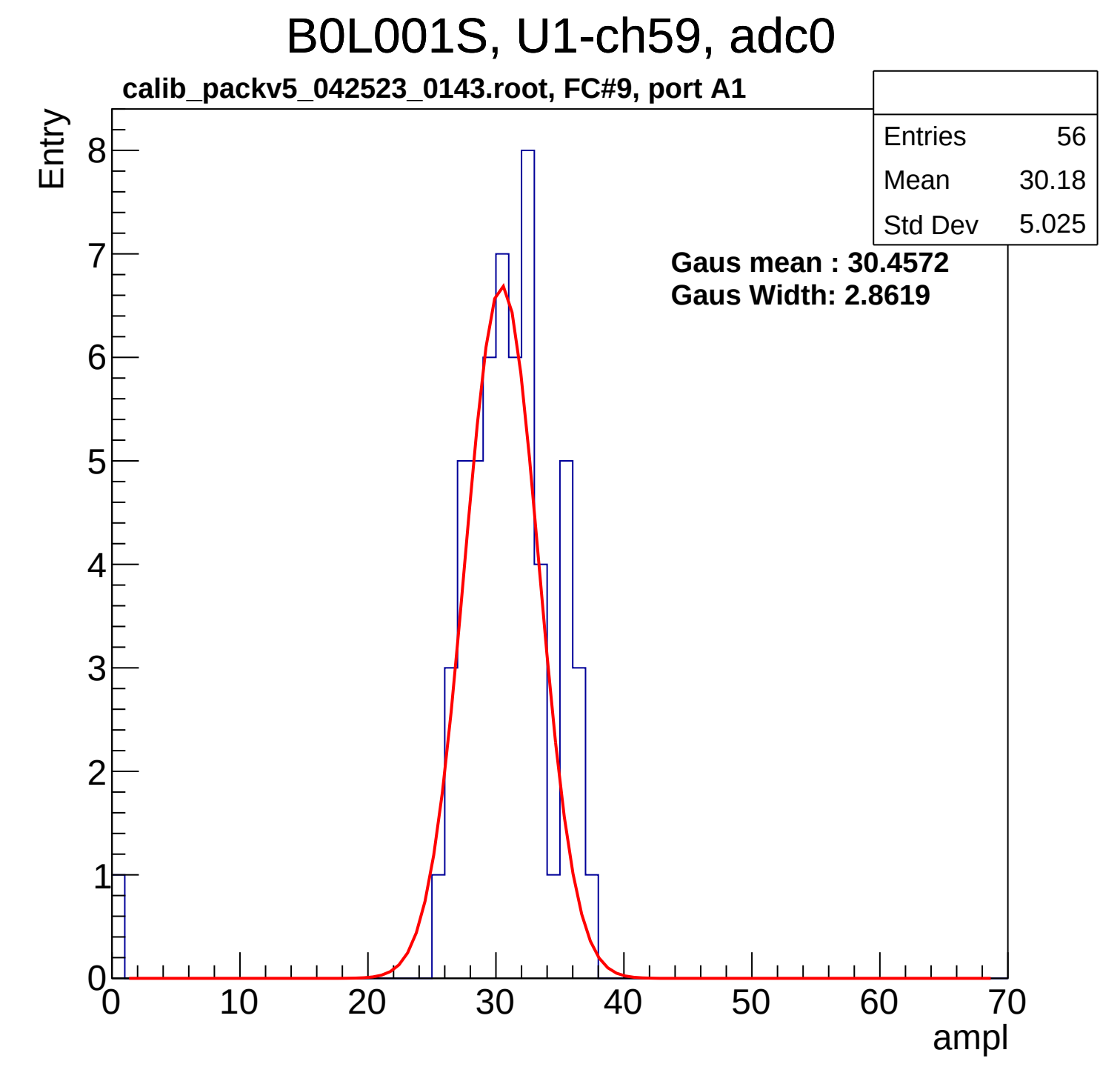
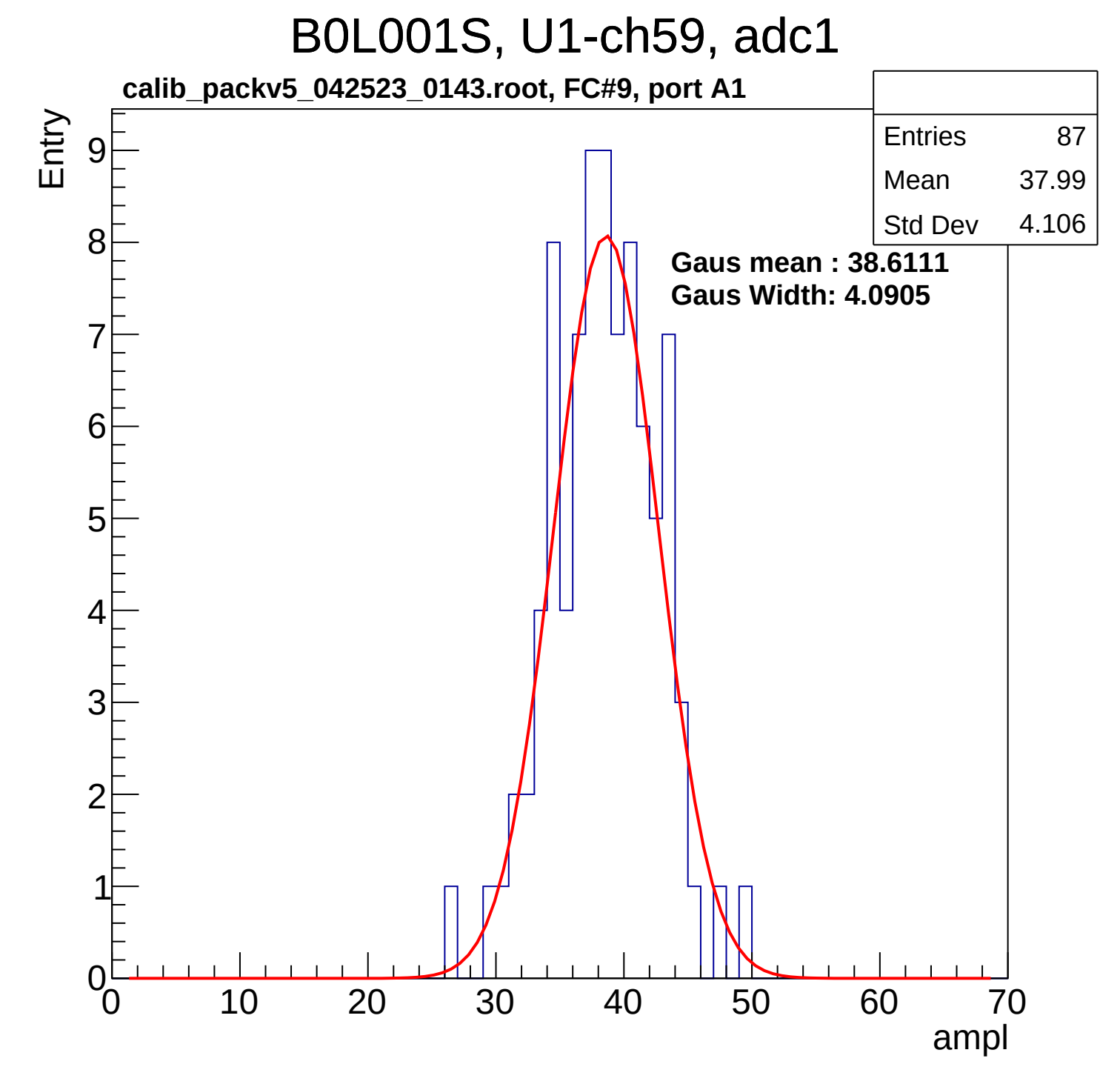

B0L001S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 30.18
Std Dev 5.025
Gaus mean : 30.4572
Gaus Width: 2.8619
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 87
Mean 37.99
Std Dev 4.106
Gaus mean : 38.6111
Gaus Width: 4.0905
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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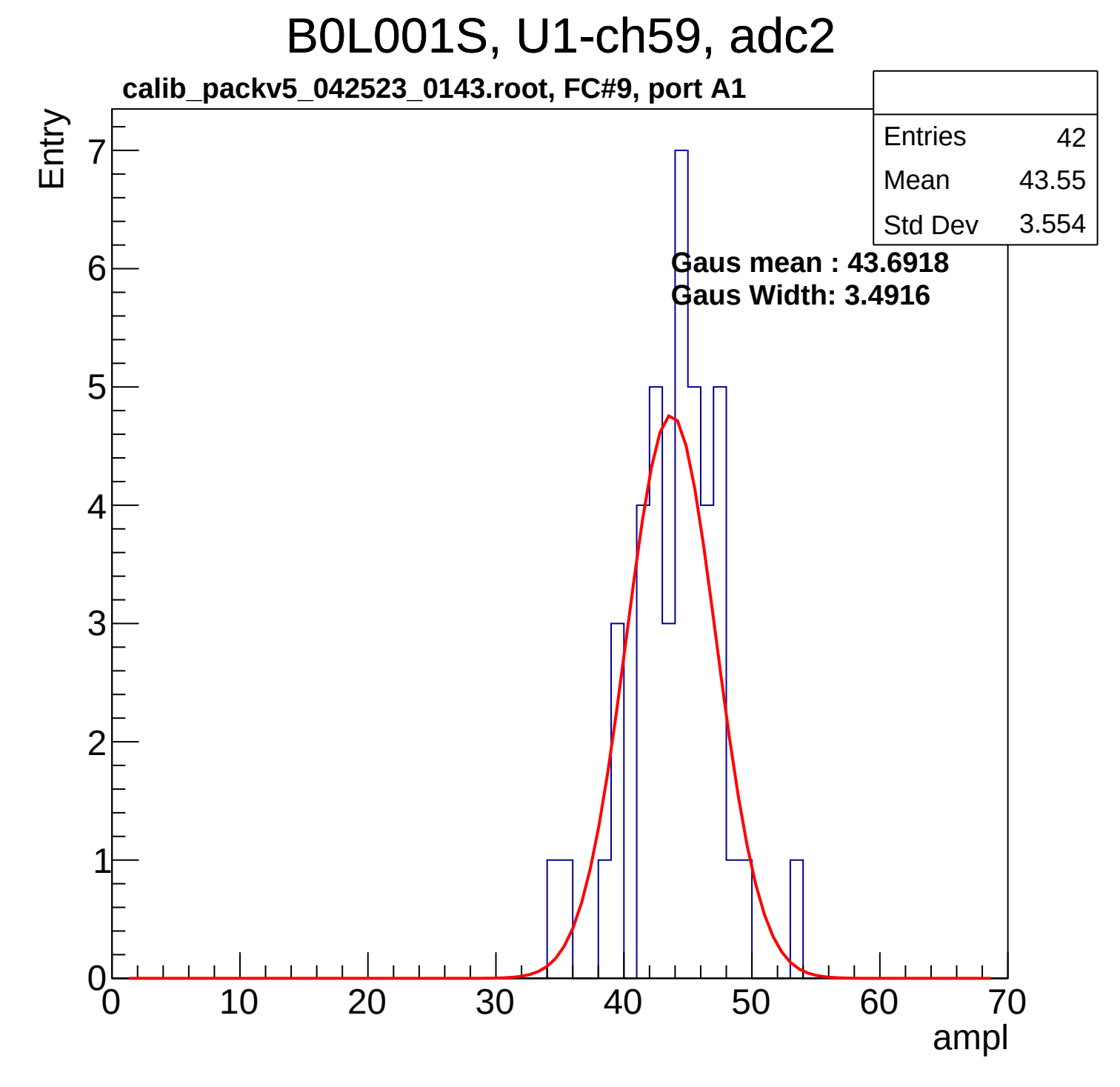
B0L001S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 43.55
Std Dev 3.554
Gaus mean : 43.6918
Gaus Width: 3.4916
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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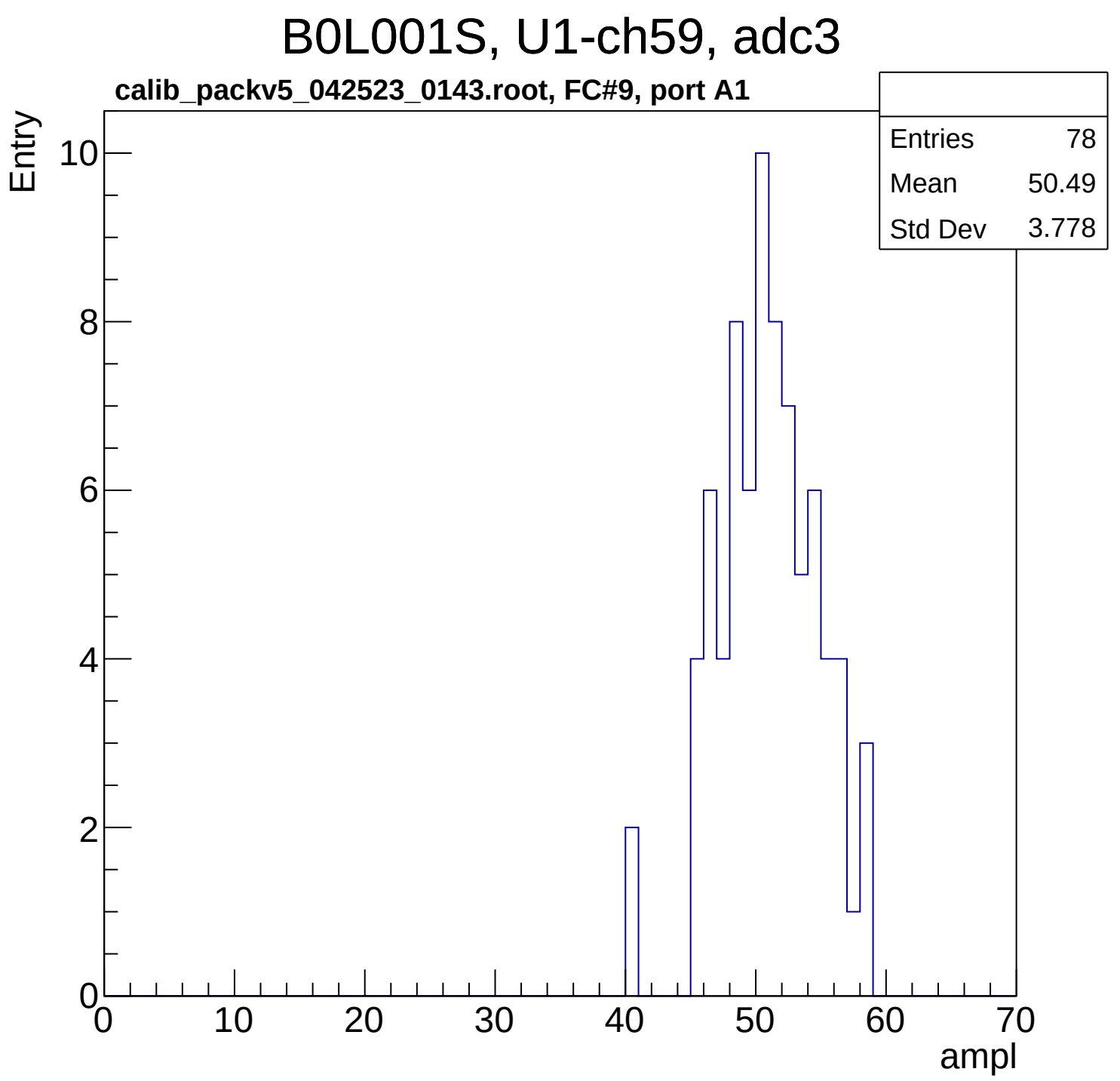
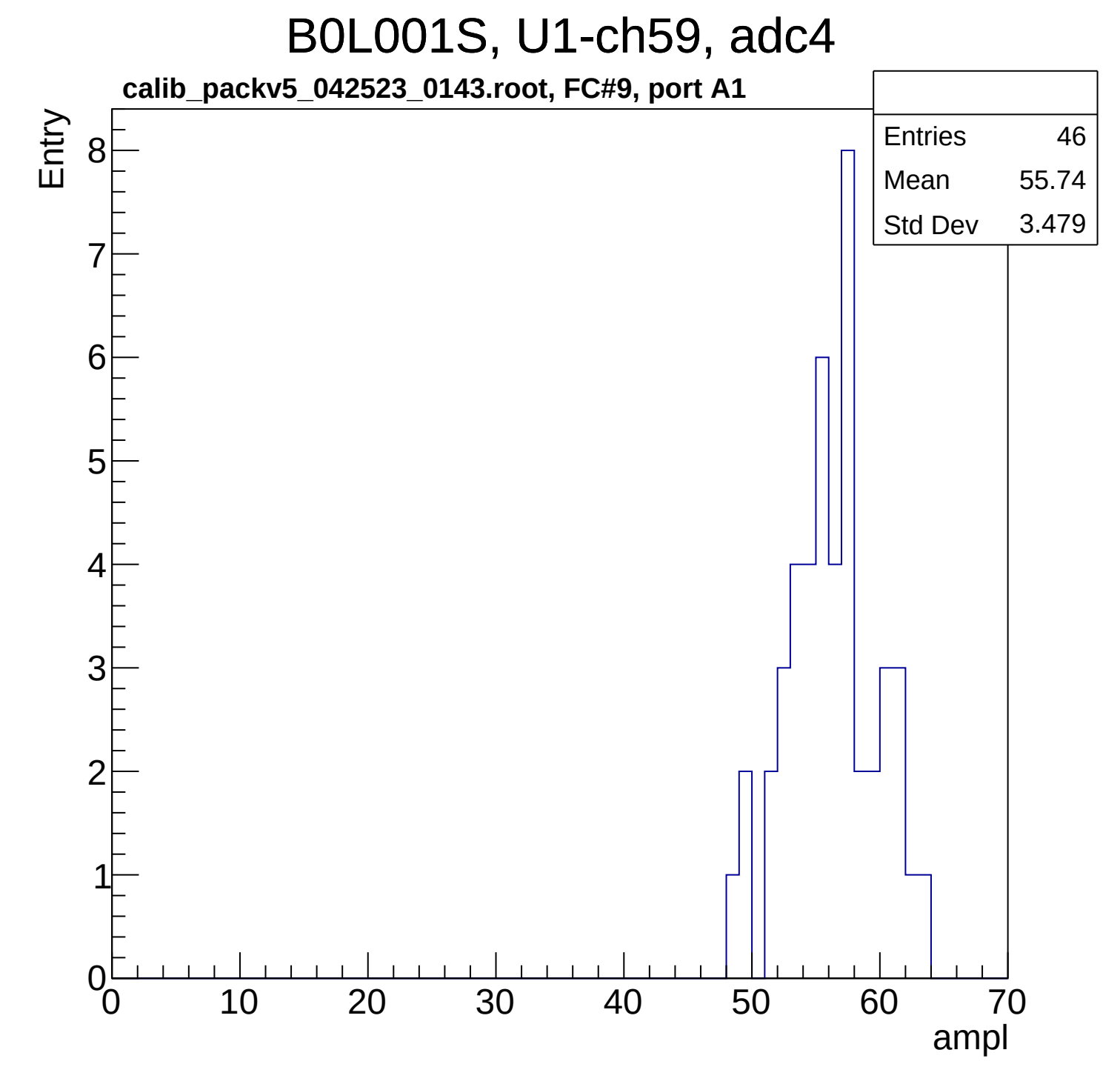

B0L001S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 50.49
Std Dev 3.778
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 55.74
Std Dev 3.479
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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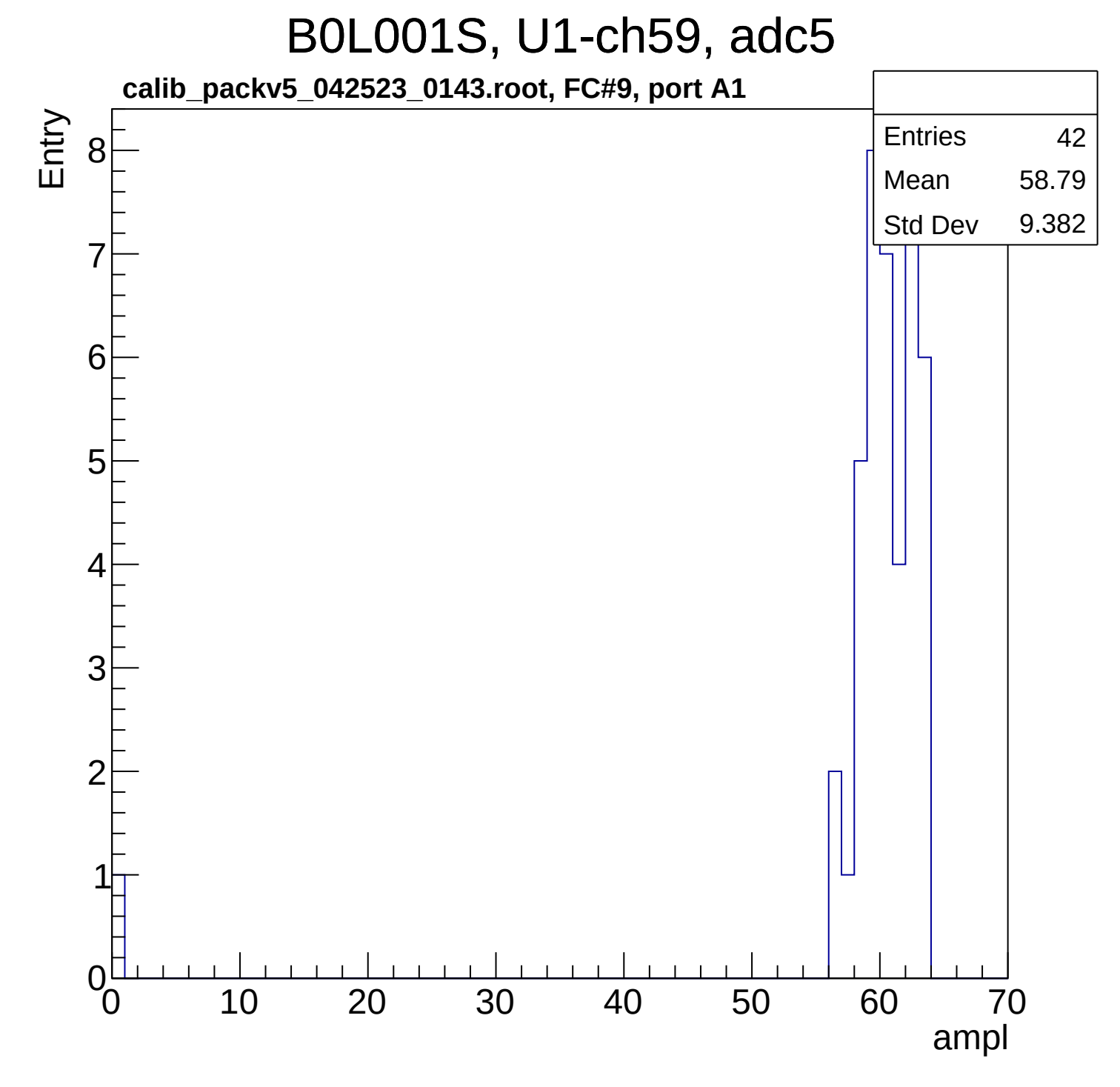
B0L001S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.79
Std Dev 9.382
Entry
ampl
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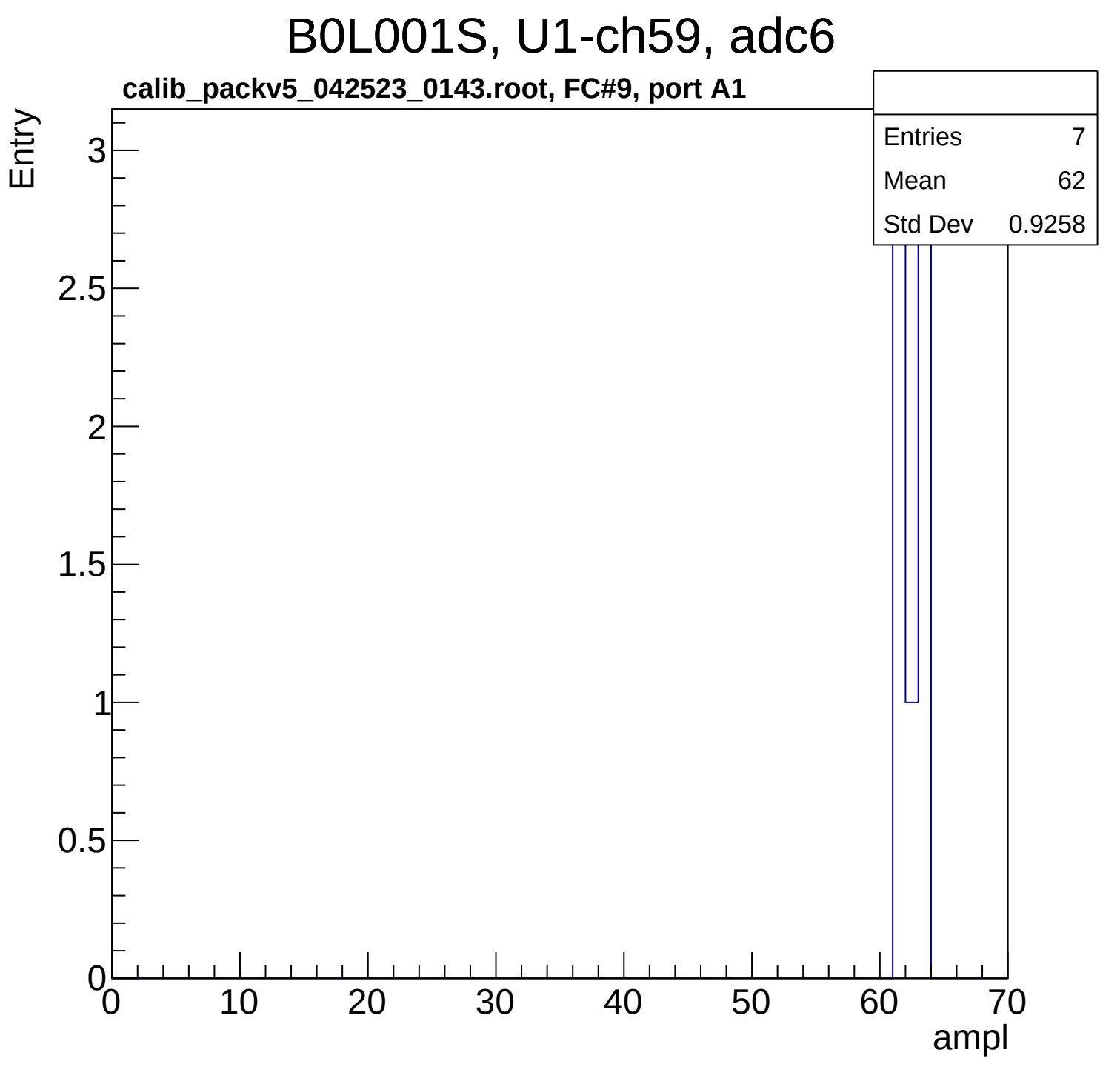
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 62
Std Dev 0.9258
Entry
ampl
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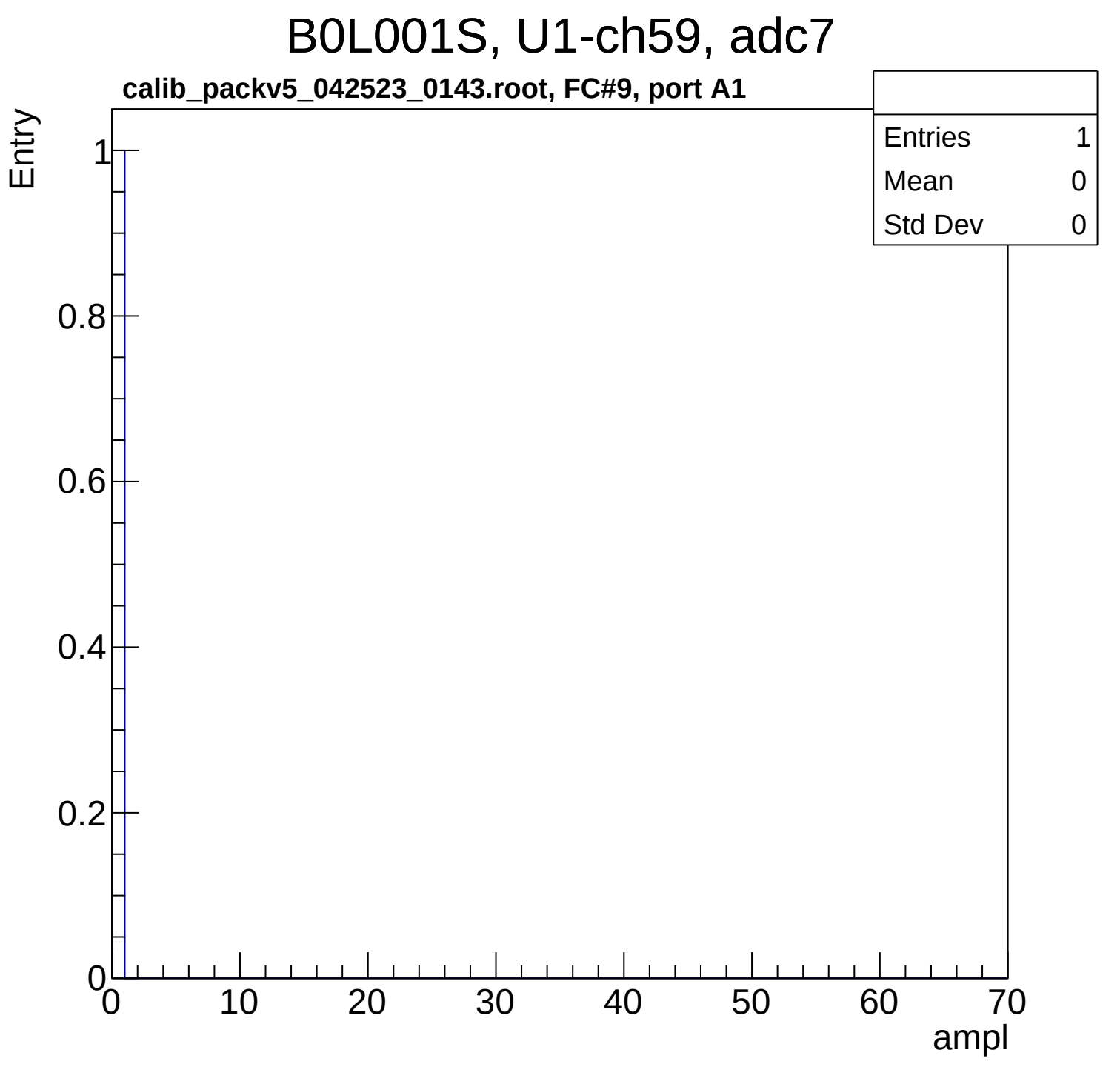
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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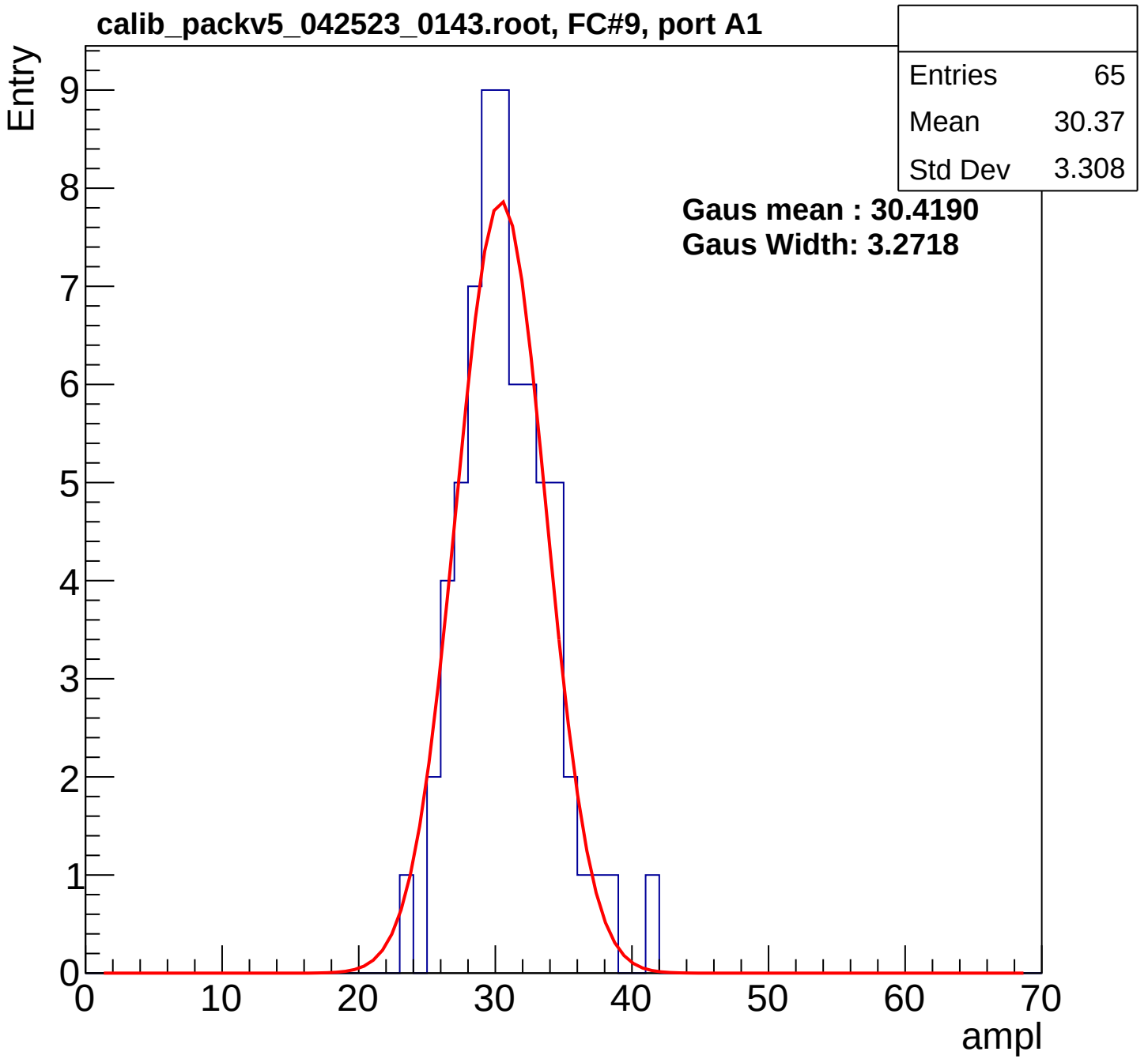
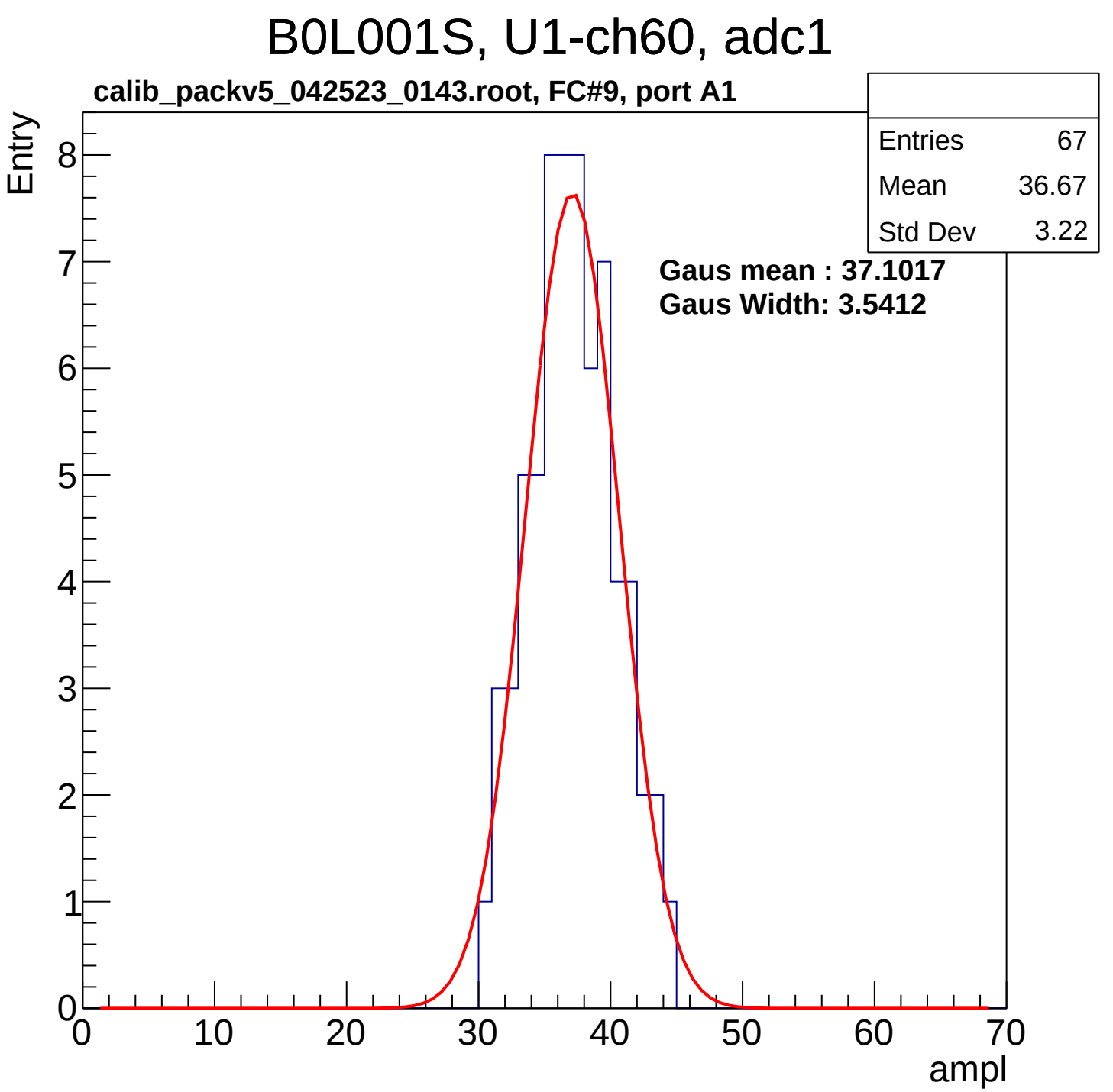
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 30.37
Std Dev 3.308
Gaus mean : 30.4190
Gaus Width: 3.2718
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 36.67
Std Dev 3.22
Gaus mean : 37.1017
Gaus Width: 3.5412
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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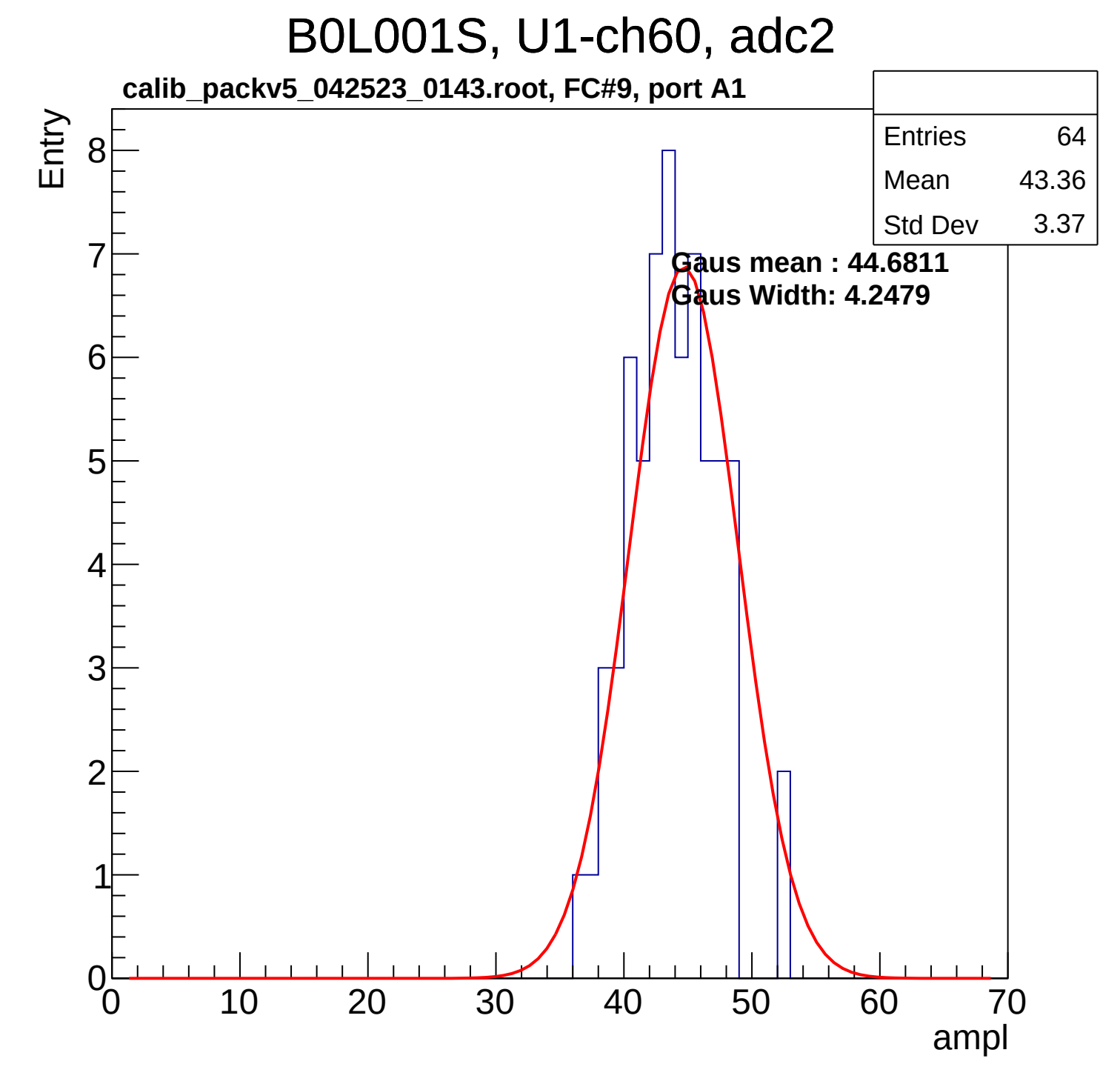
B0L001S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 43.36
Std Dev 3.37
Gaus mean : 44.6811
Gaus Width: 4.2479
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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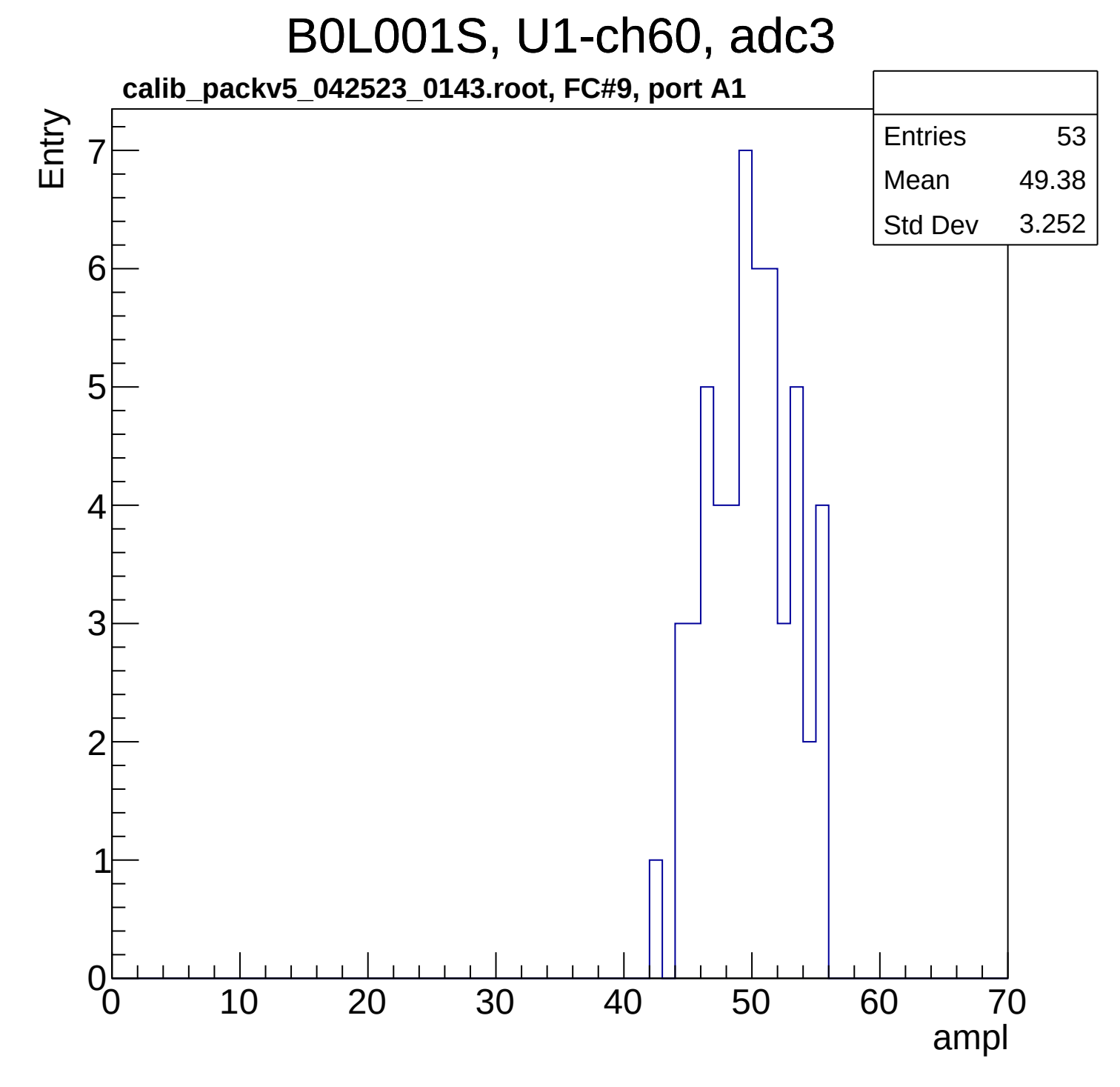
B0L001S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 49.38
Std Dev 3.252
Entry
ampl
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3
4
5
6
7
0
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20
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40
50
60
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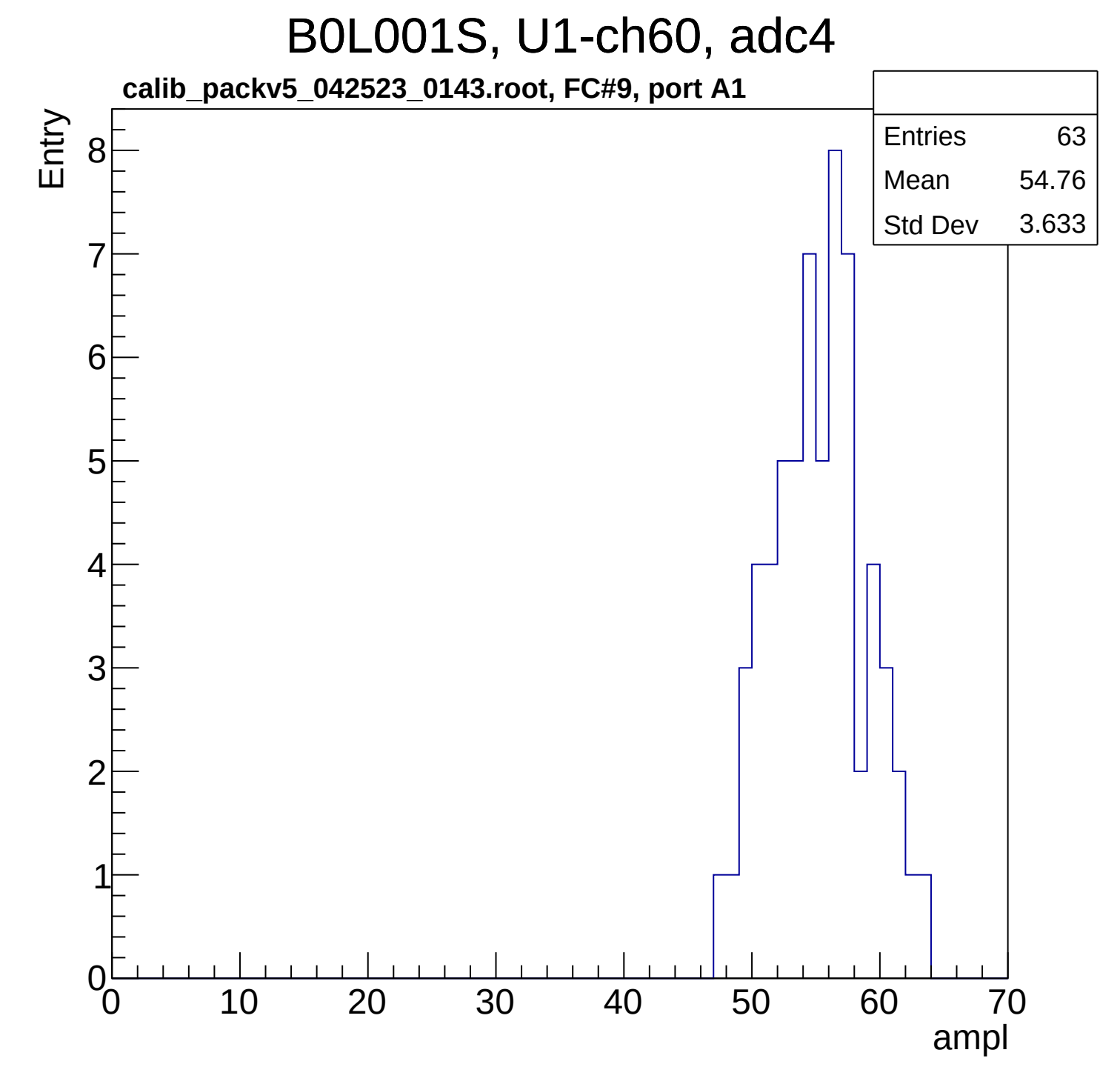
B0L001S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 54.76
Std Dev 3.633
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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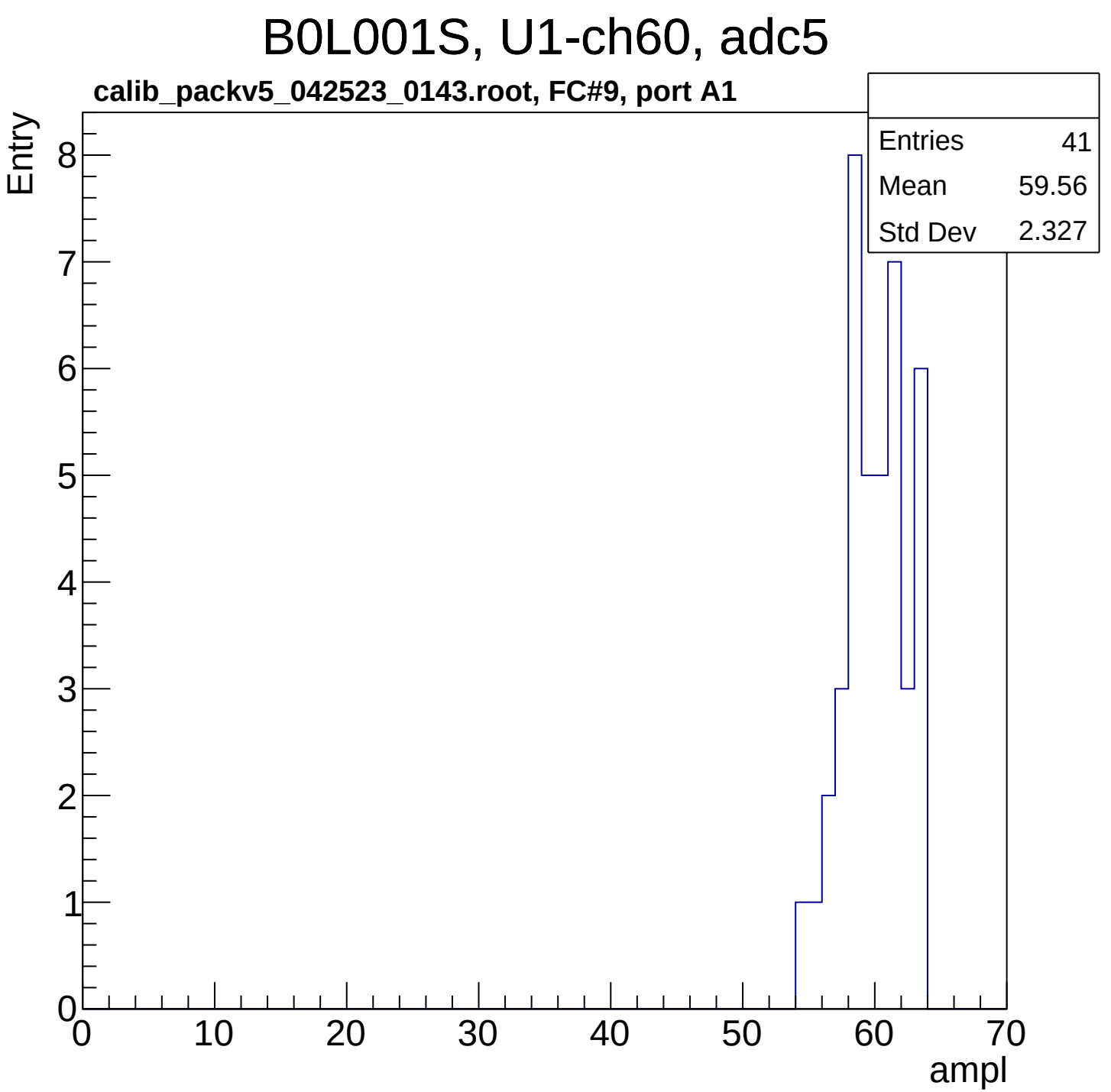
B0L001S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.56
Std Dev 2.327
Entry
ampl
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2
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4
5
6
7
8
0
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20
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40
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60
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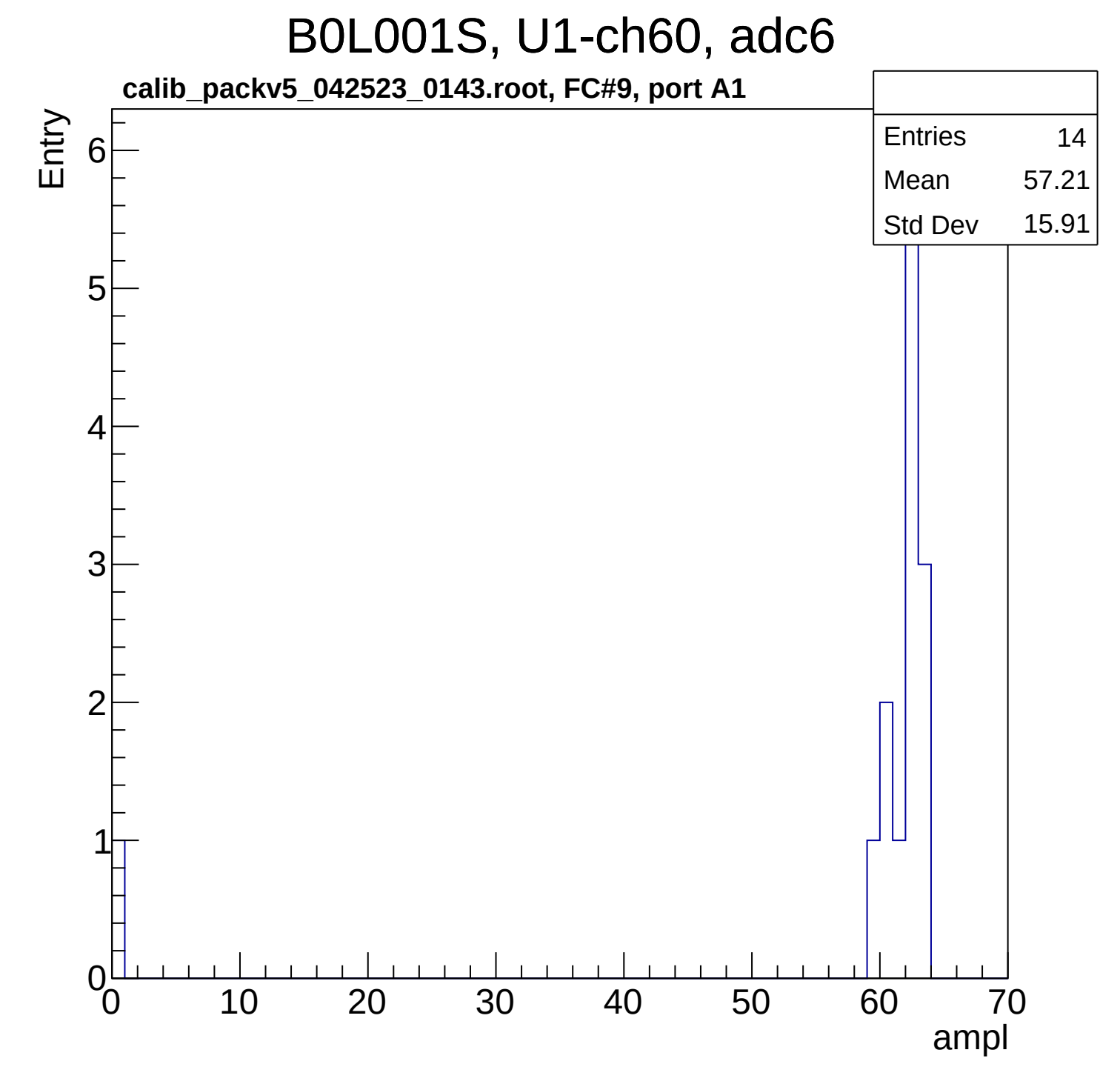
B0L001S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 14
Mean 57.21
Std Dev 15.91
Entry
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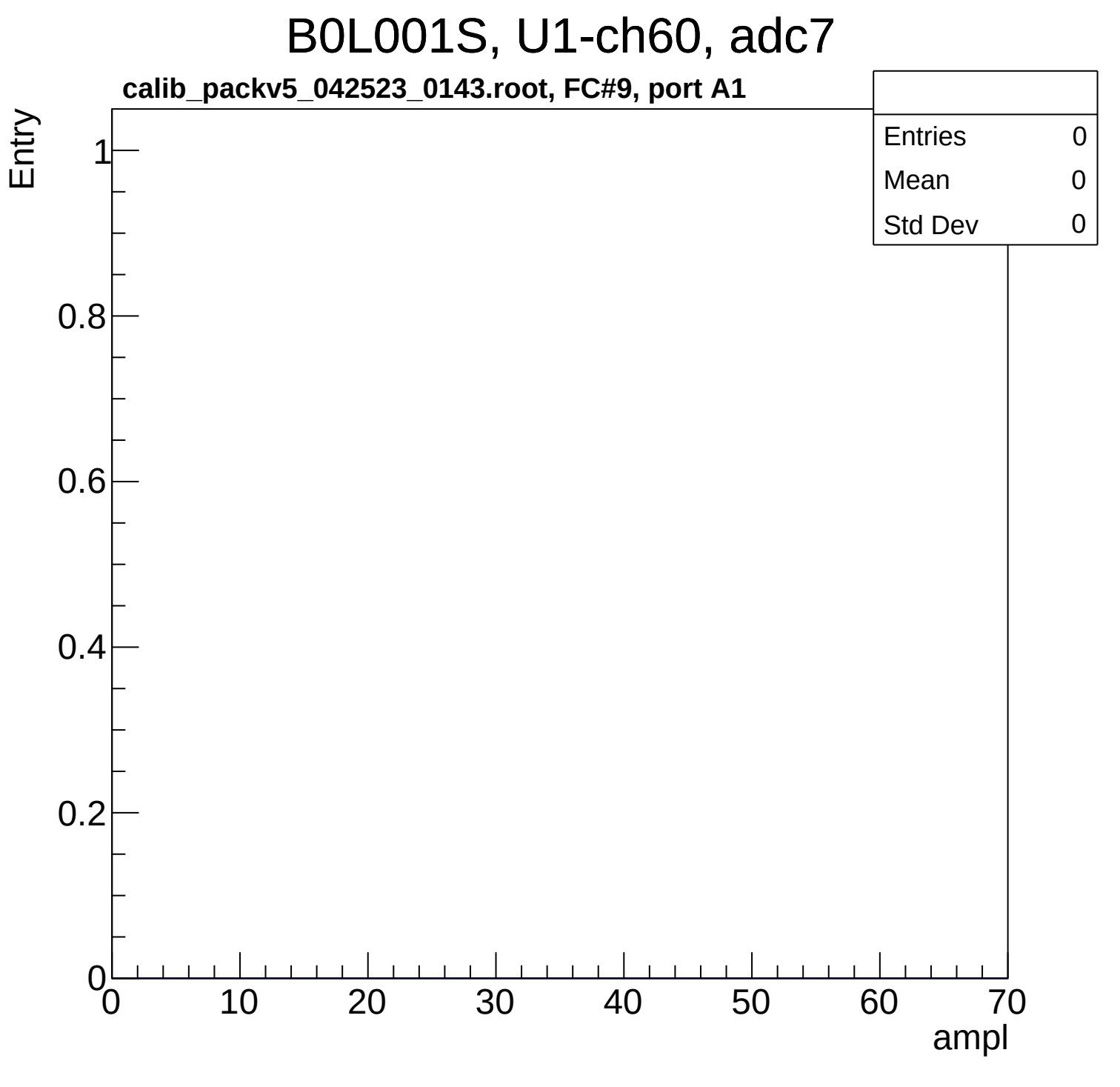
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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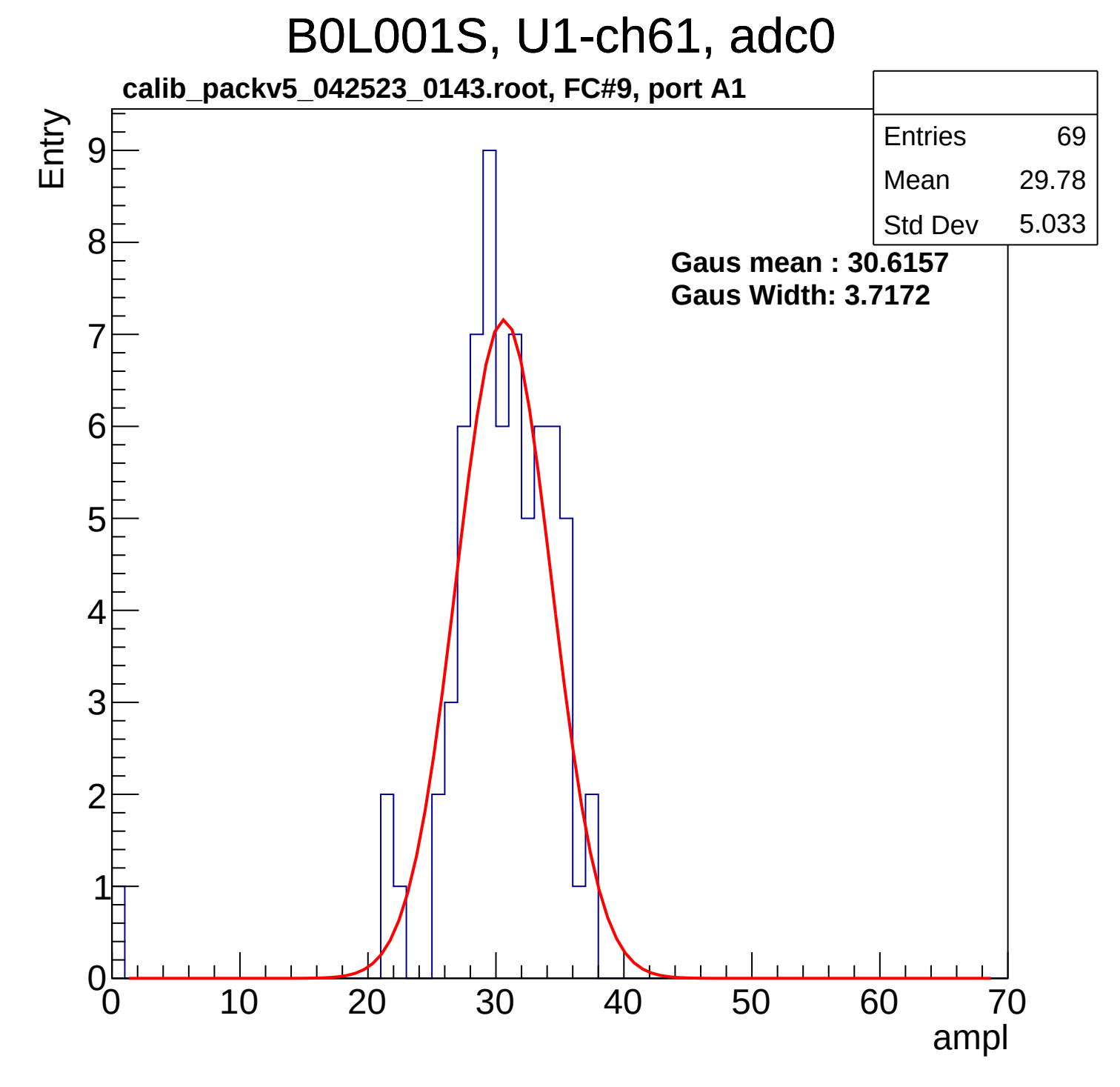
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.78
Std Dev 5.033
Gaus mean : 30.6157
Gaus Width: 3.7172
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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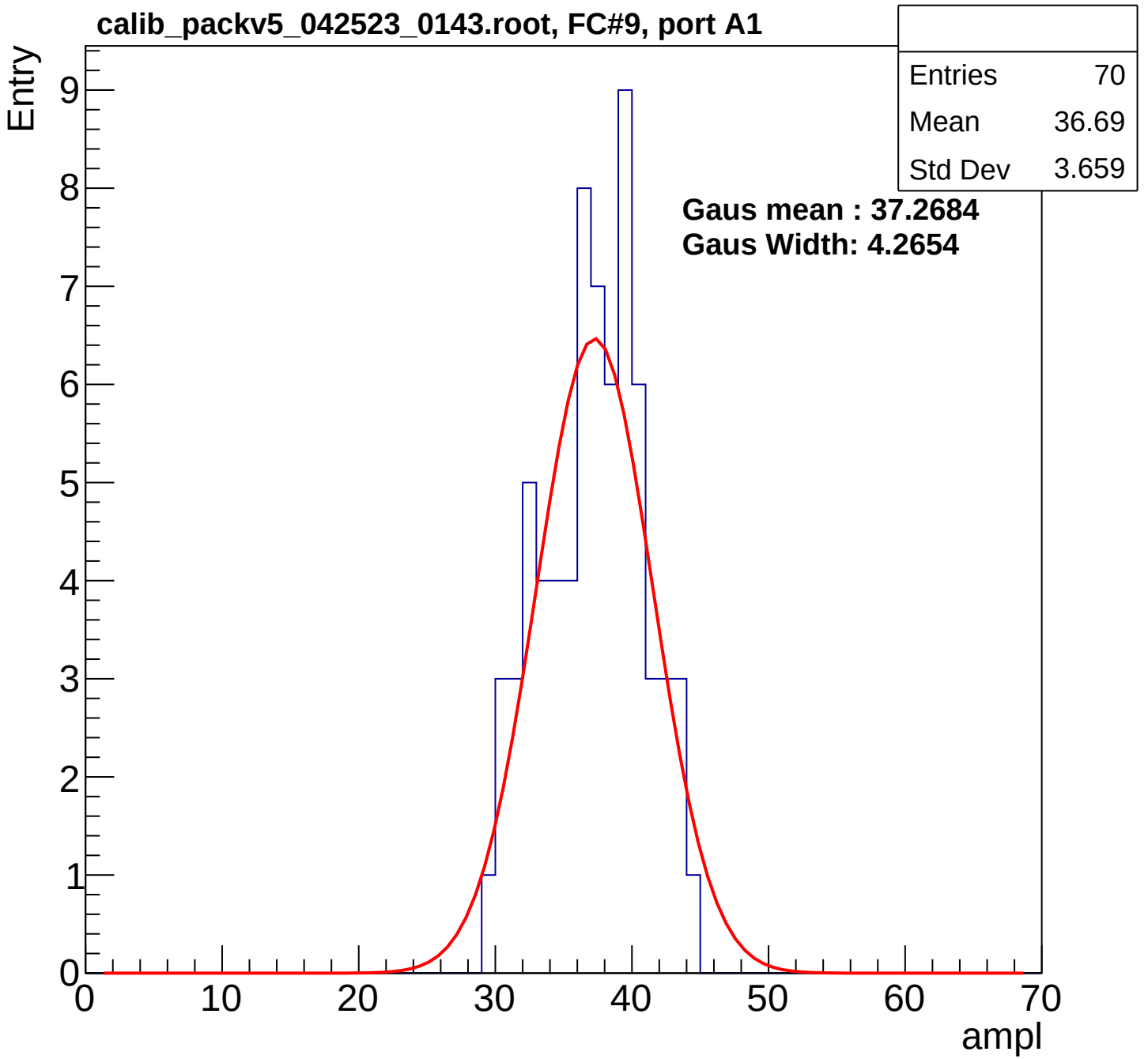
B0L001S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.69
Std Dev 3.659
Gaus mean : 37.2684
Gaus Width: 4.2654
Entry
ampl
0
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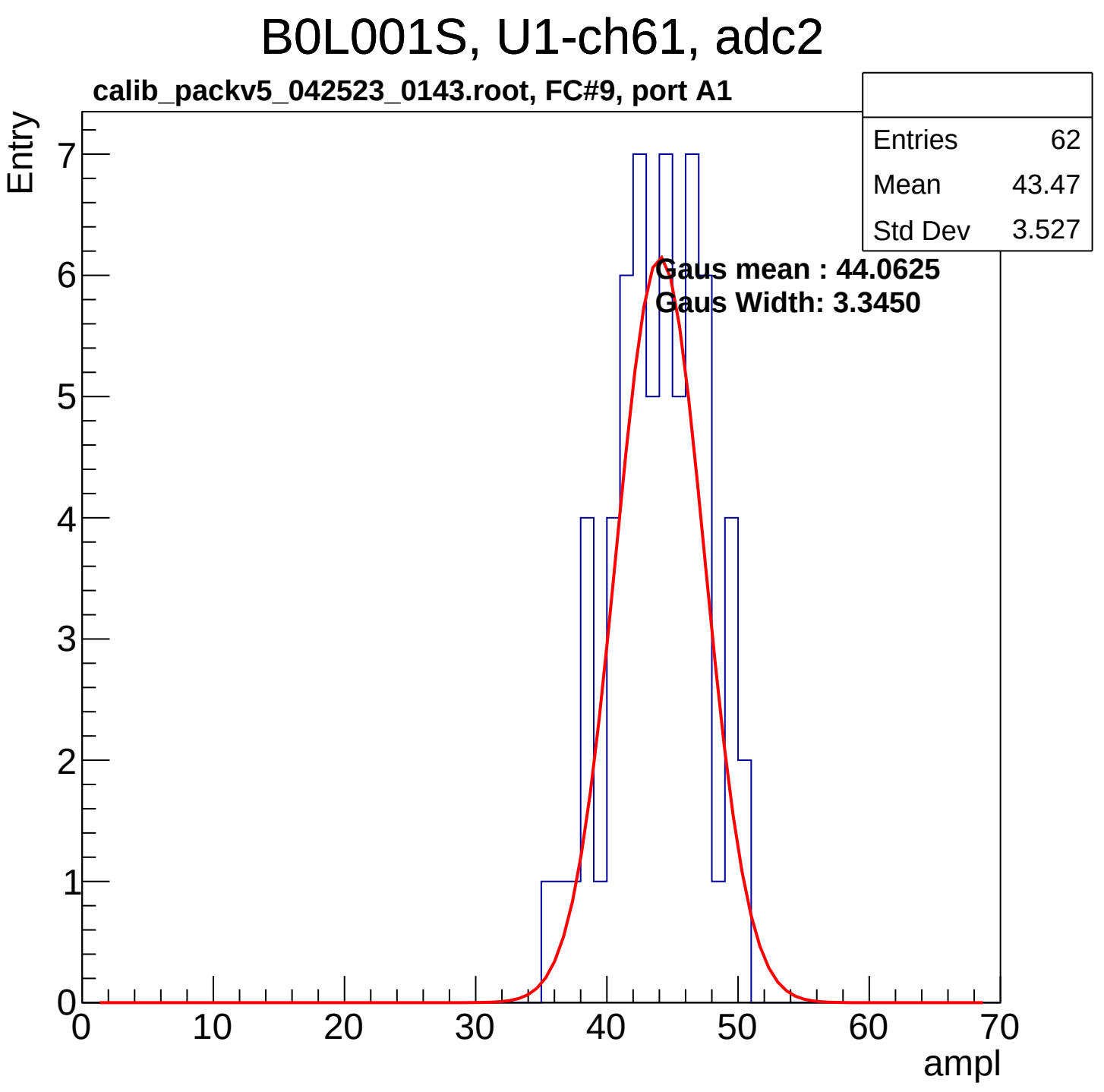
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 43.47
Std Dev 3.527
Gaus mean : 44.0625
Gaus Width: 3.3450
Entry
ampl
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1
2
3
4
5
6
7
0
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50
60
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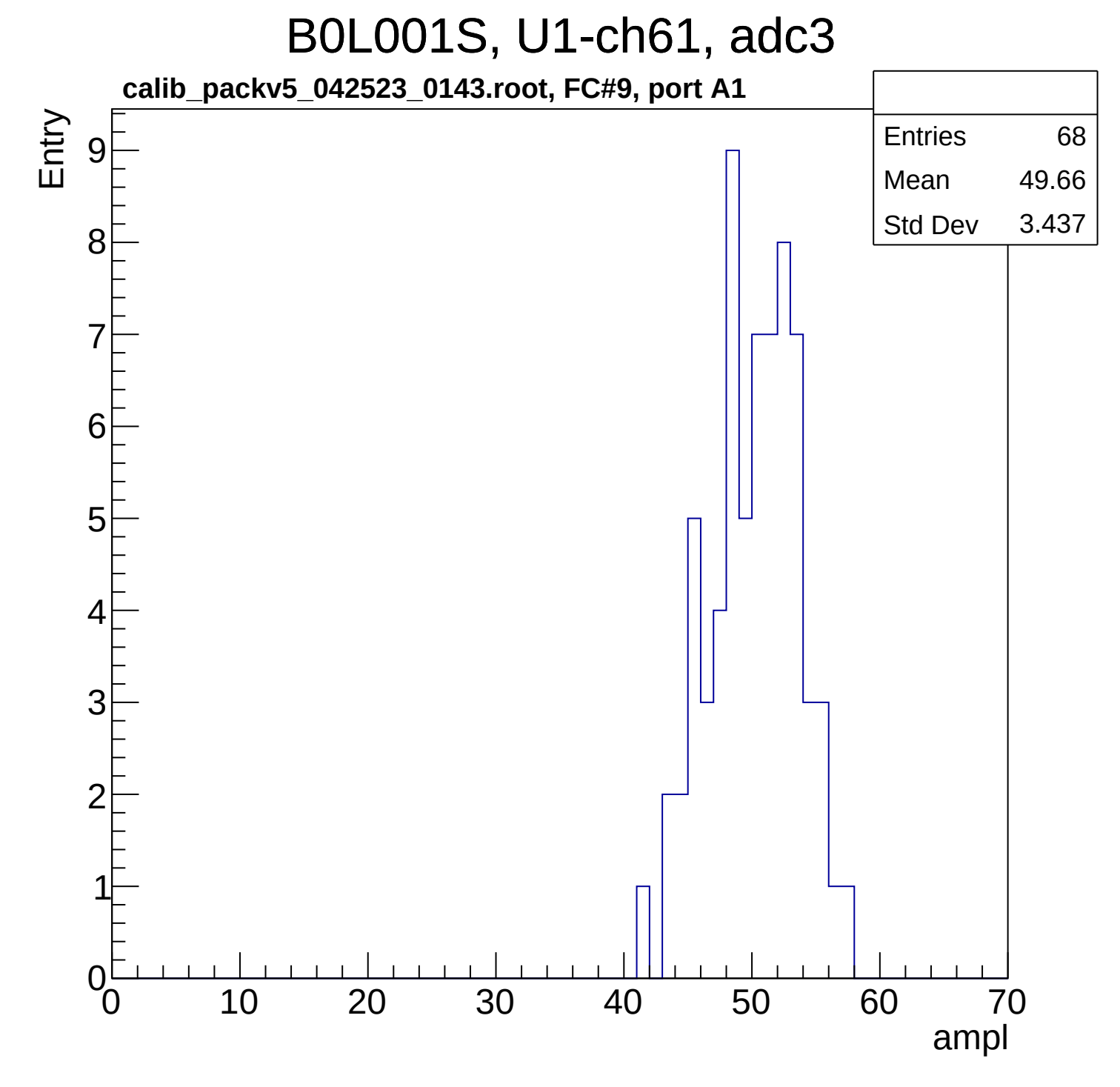
B0L001S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.66
Std Dev 3.437
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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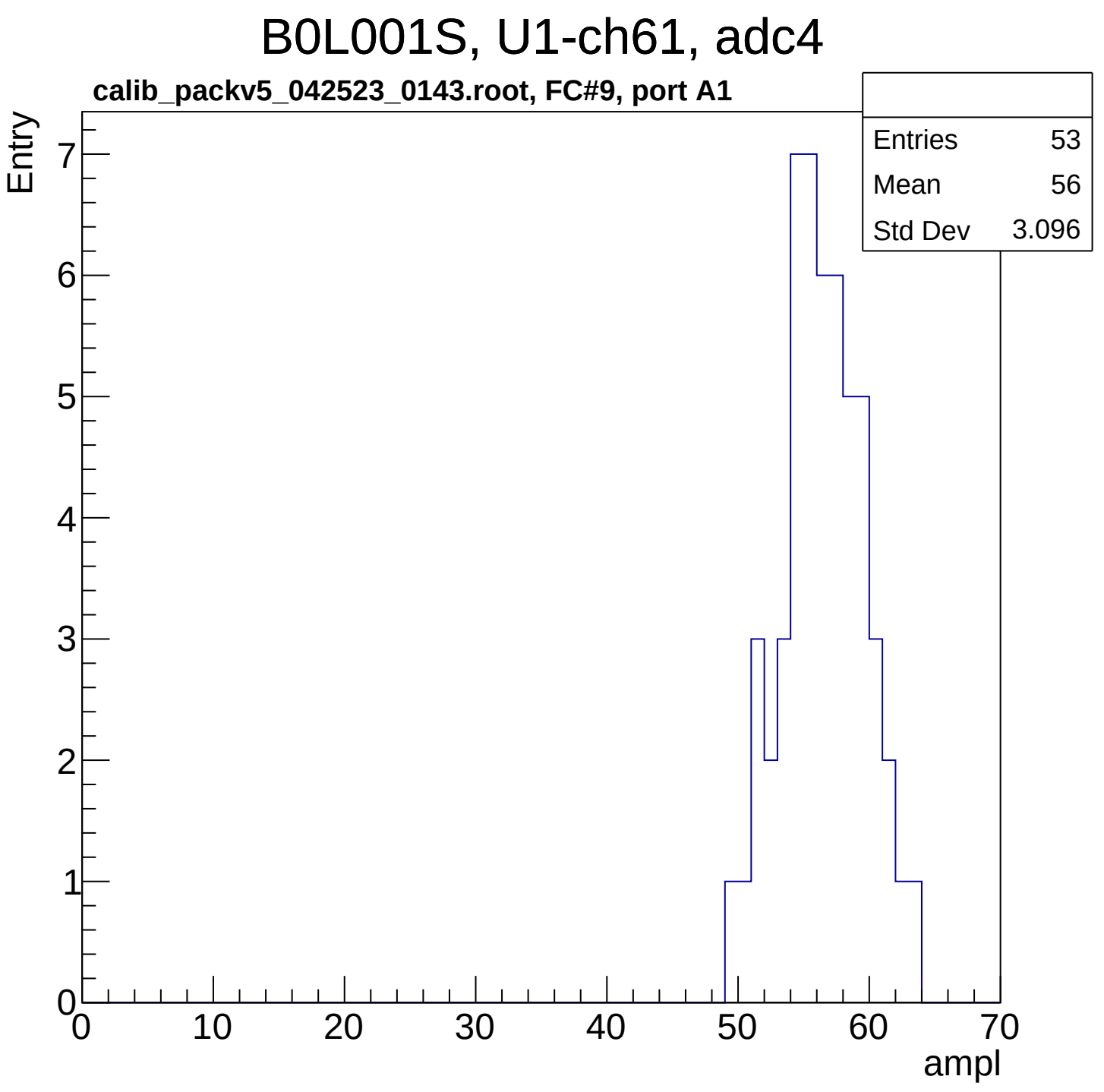
40
50
60
70


B0L001S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56
Std Dev 3.096
Entry
0
1
2
3
4
5
6
7
0
10
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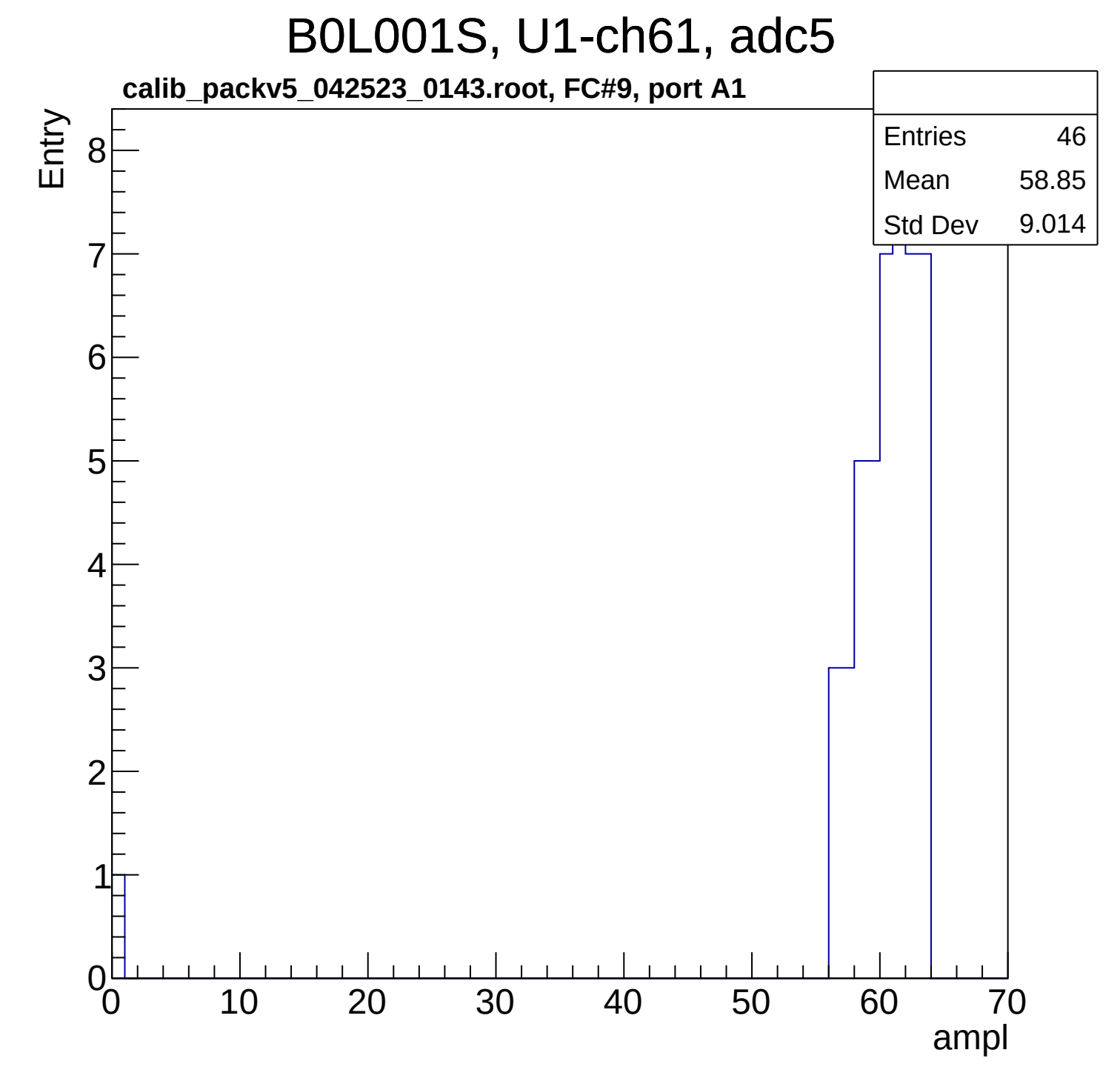
50
60
70
ampl


B0L001S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.85
Std Dev 9.014
Entry
ampl
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1
2
3
4
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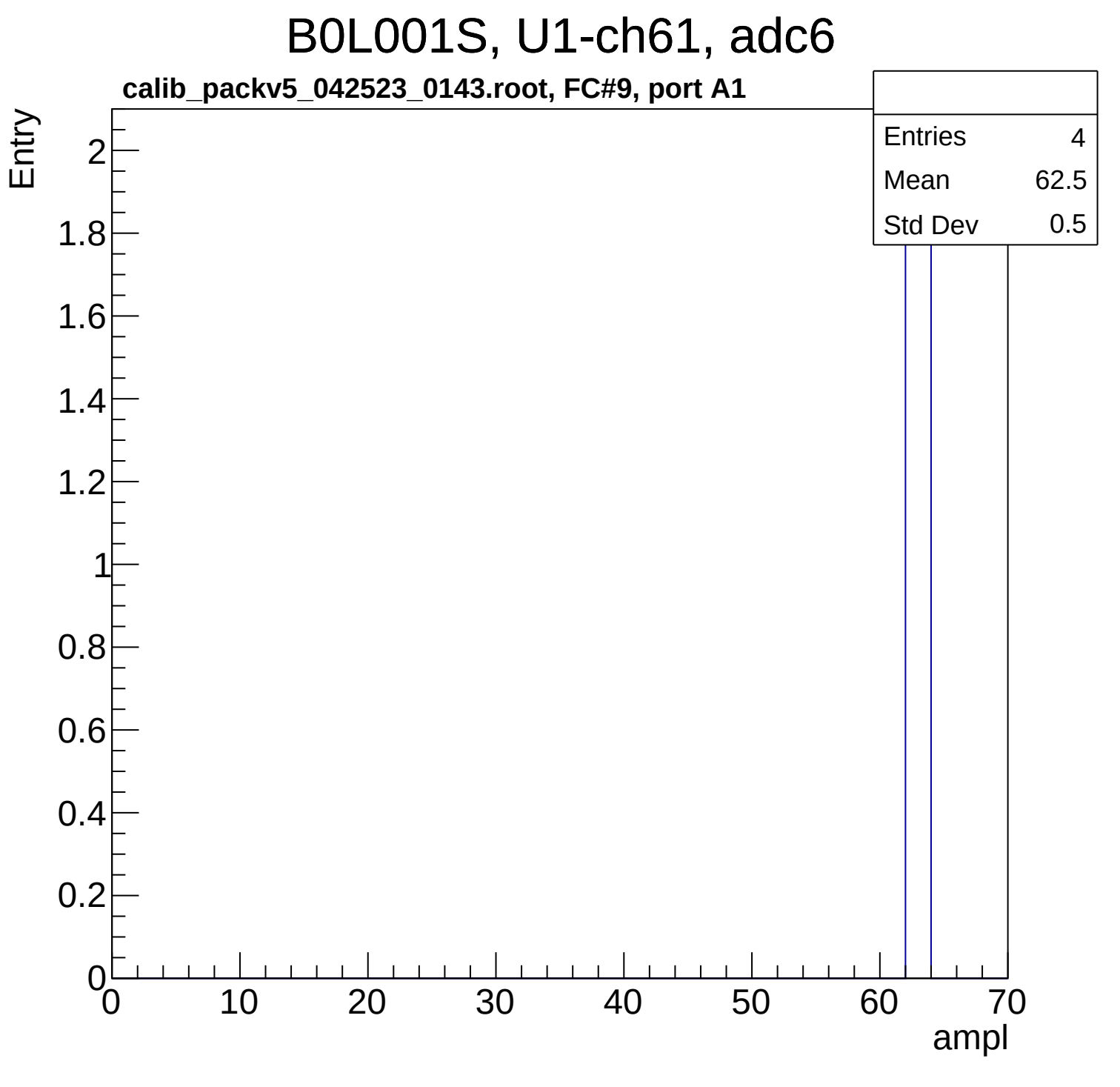
B0L001S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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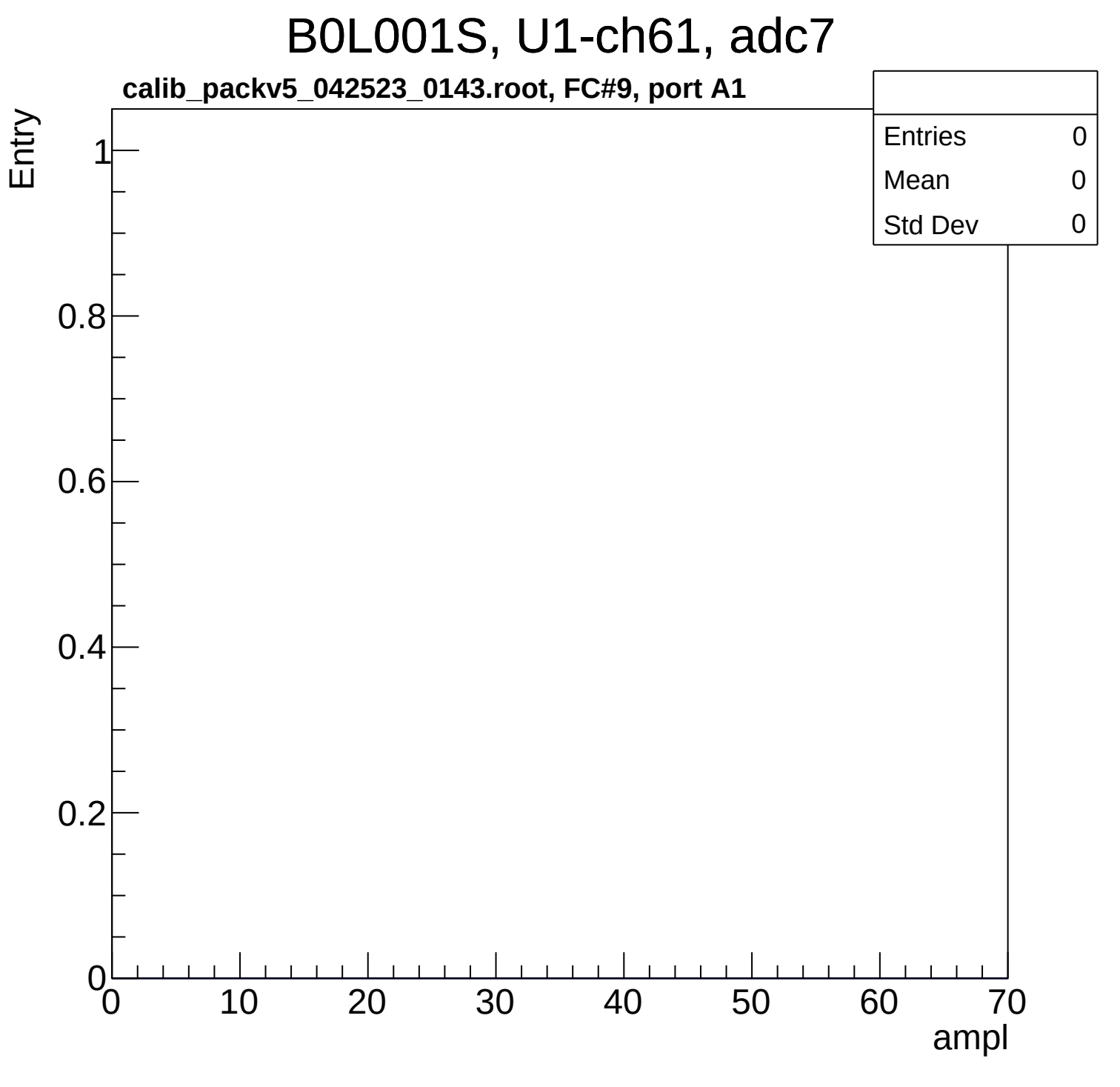
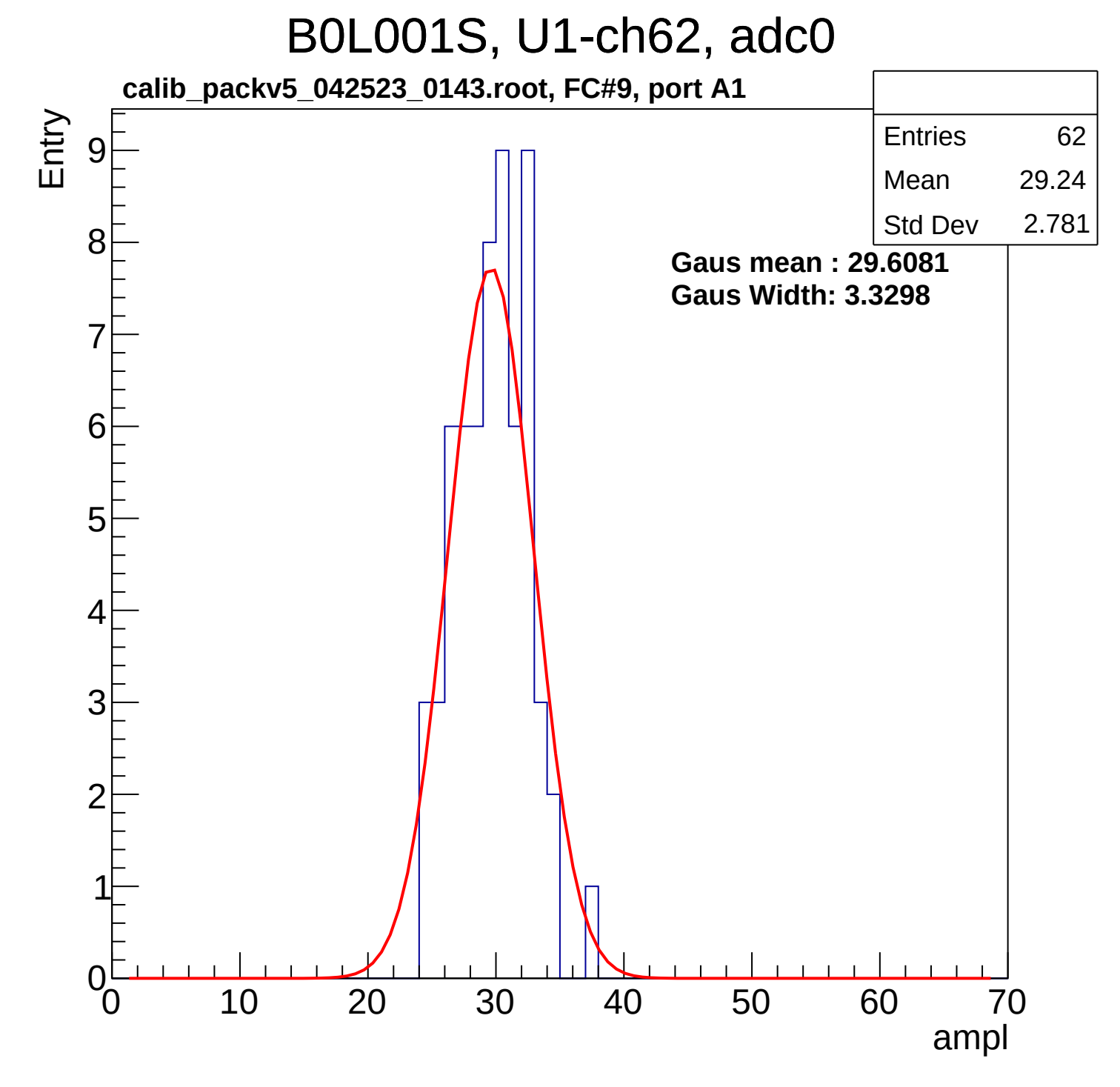

B0L001S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 29.24
Std Dev 2.781
Gaus mean : 29.6081
Gaus Width: 3.3298
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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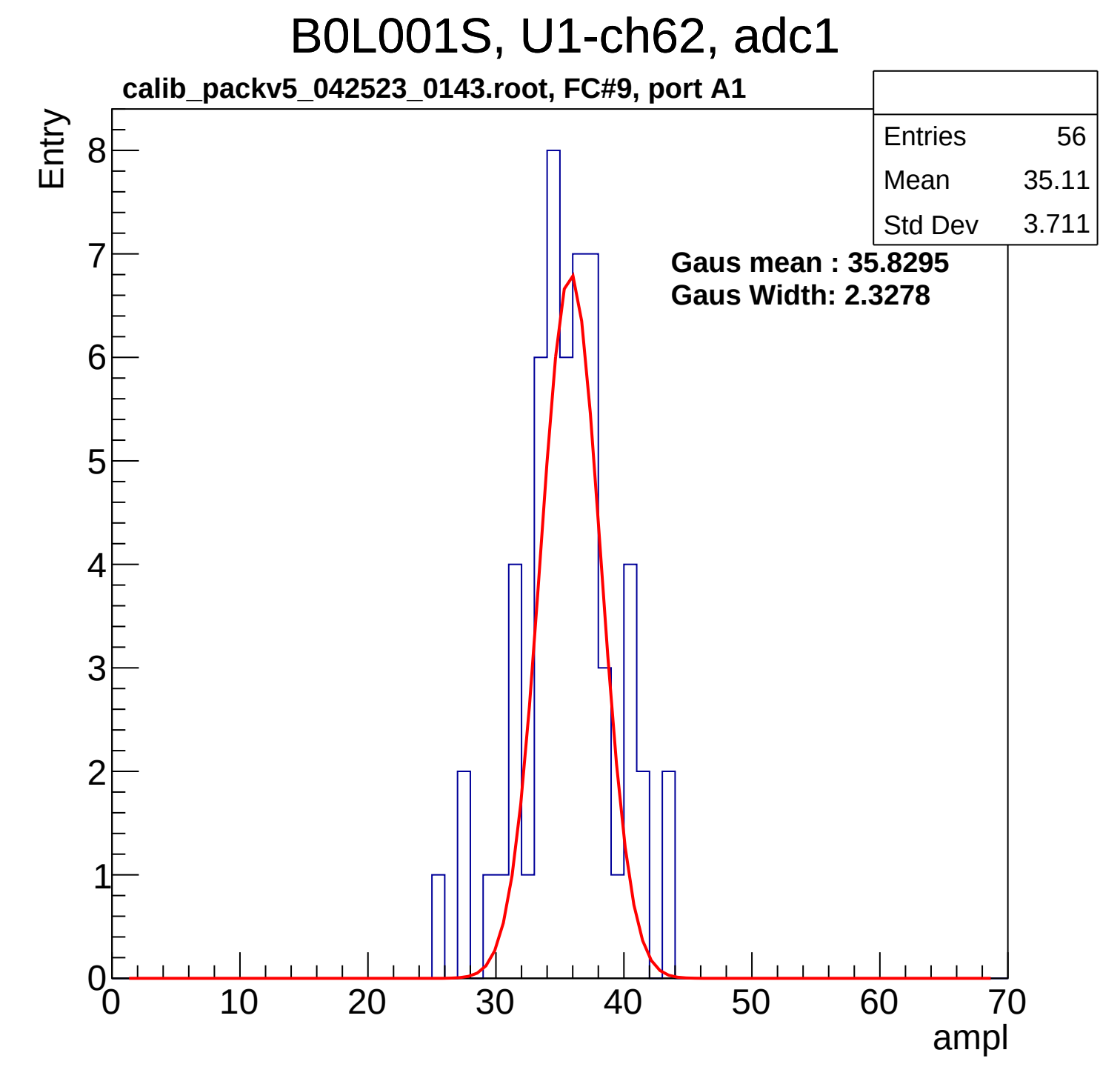
B0L001S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 35.11
Std Dev 3.711
Gaus mean : 35.8295
Gaus Width: 2.3278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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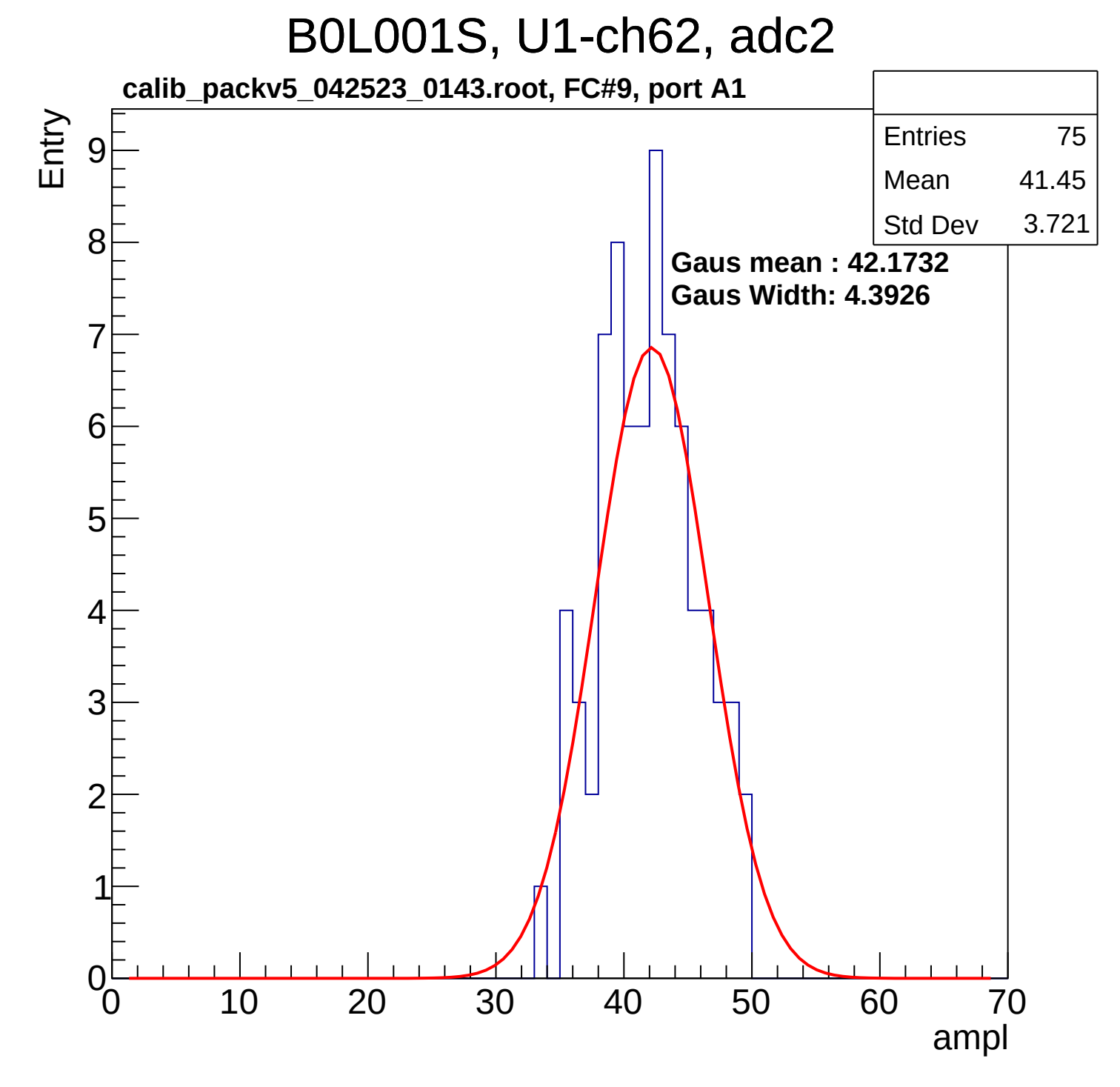
B0L001S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 41.45
Std Dev 3.721
Gaus mean : 42.1732
Gaus Width: 4.3926
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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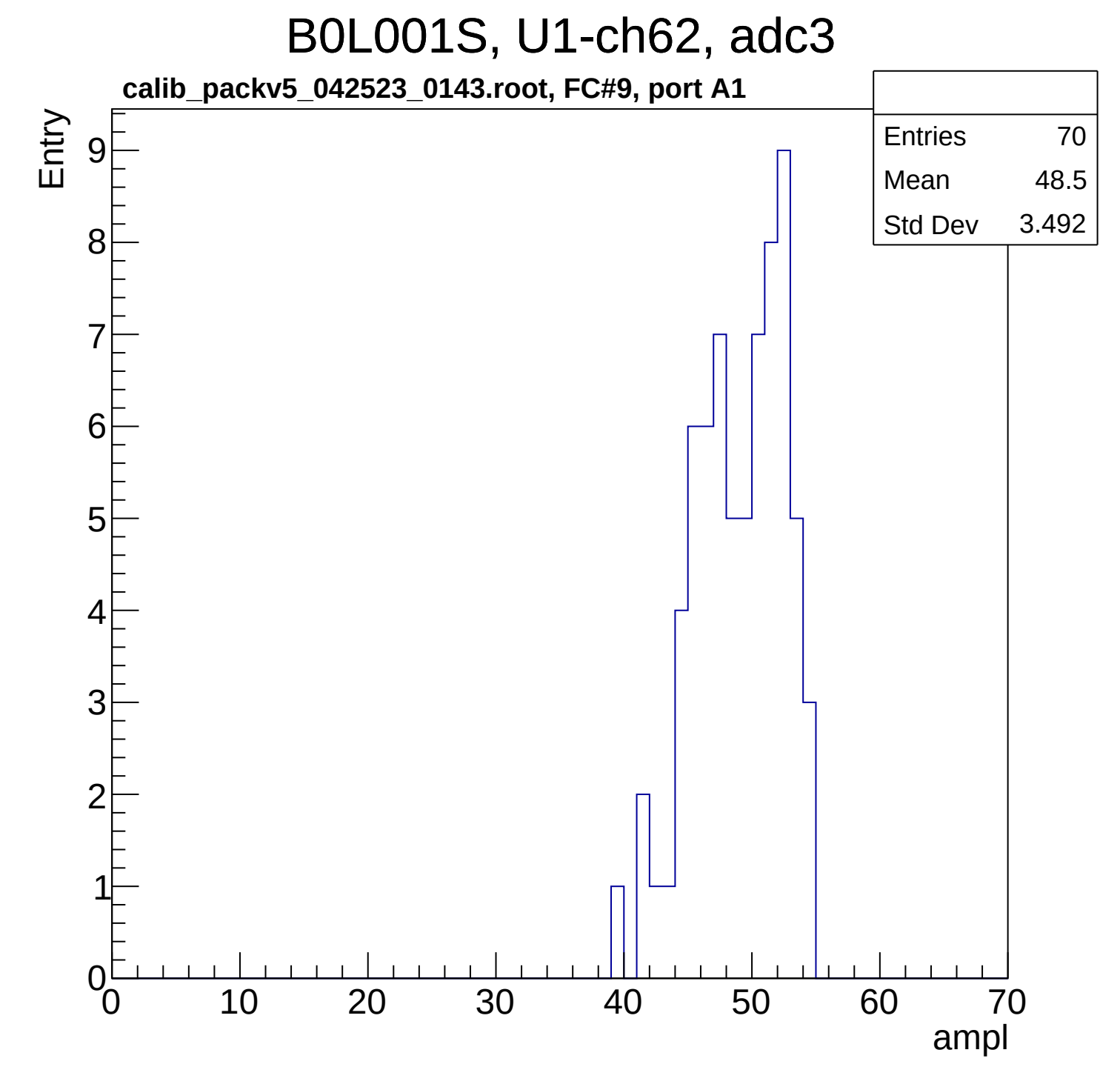
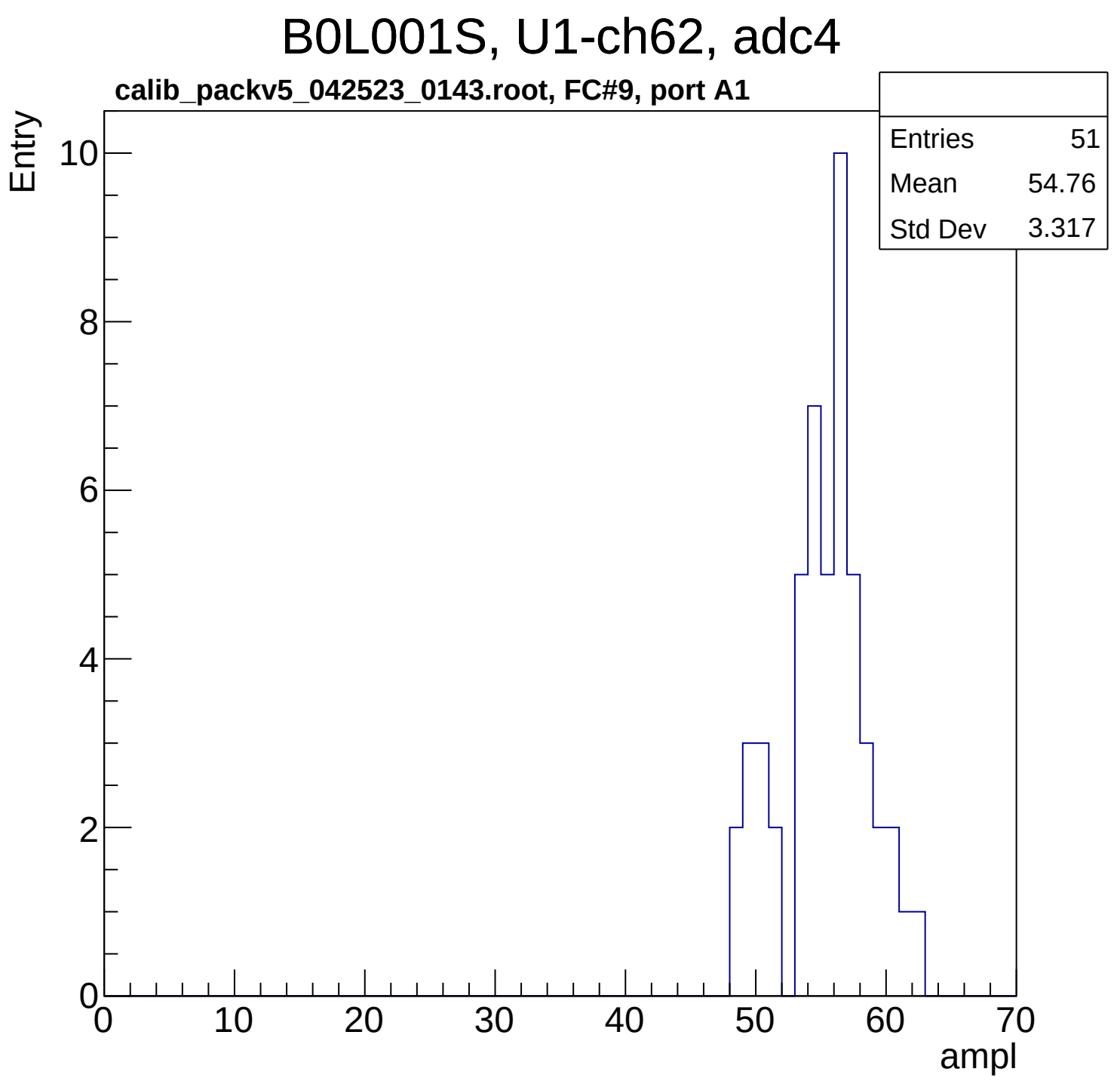
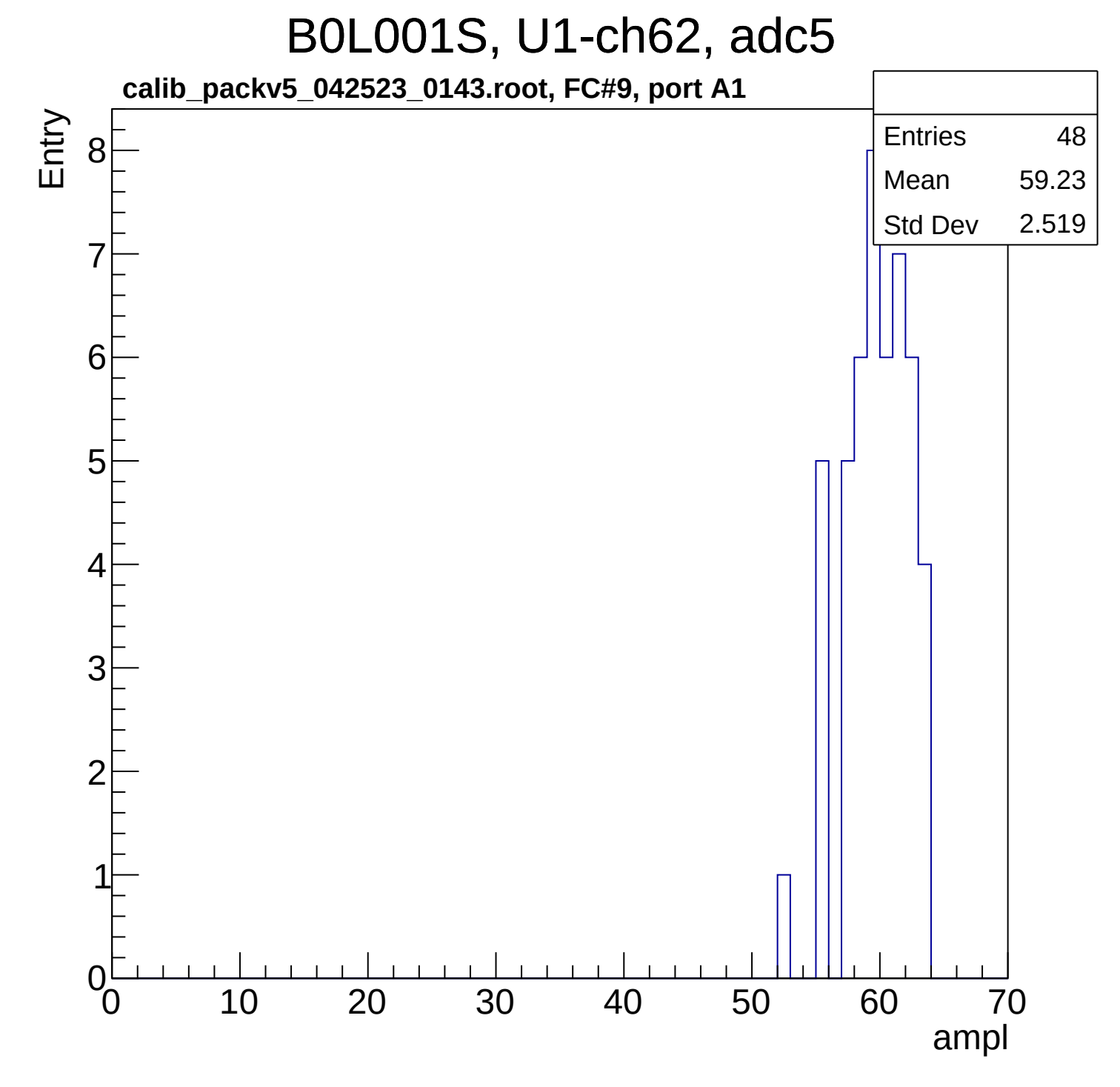

B0L001S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 48.5
Std Dev 3.492
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 54.76
Std Dev 3.317
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 59.23
Std Dev 2.519
Entry
ampl
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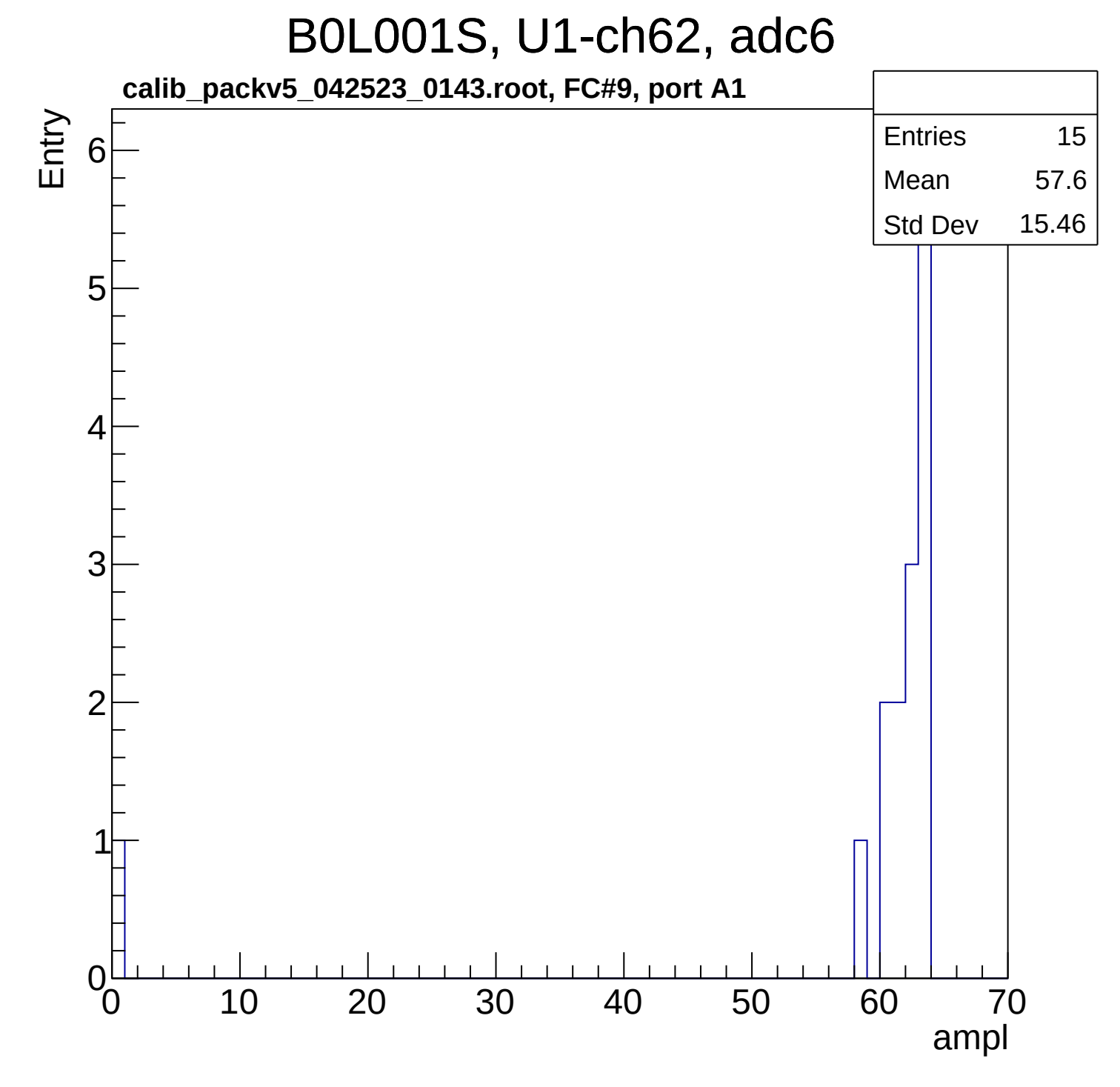
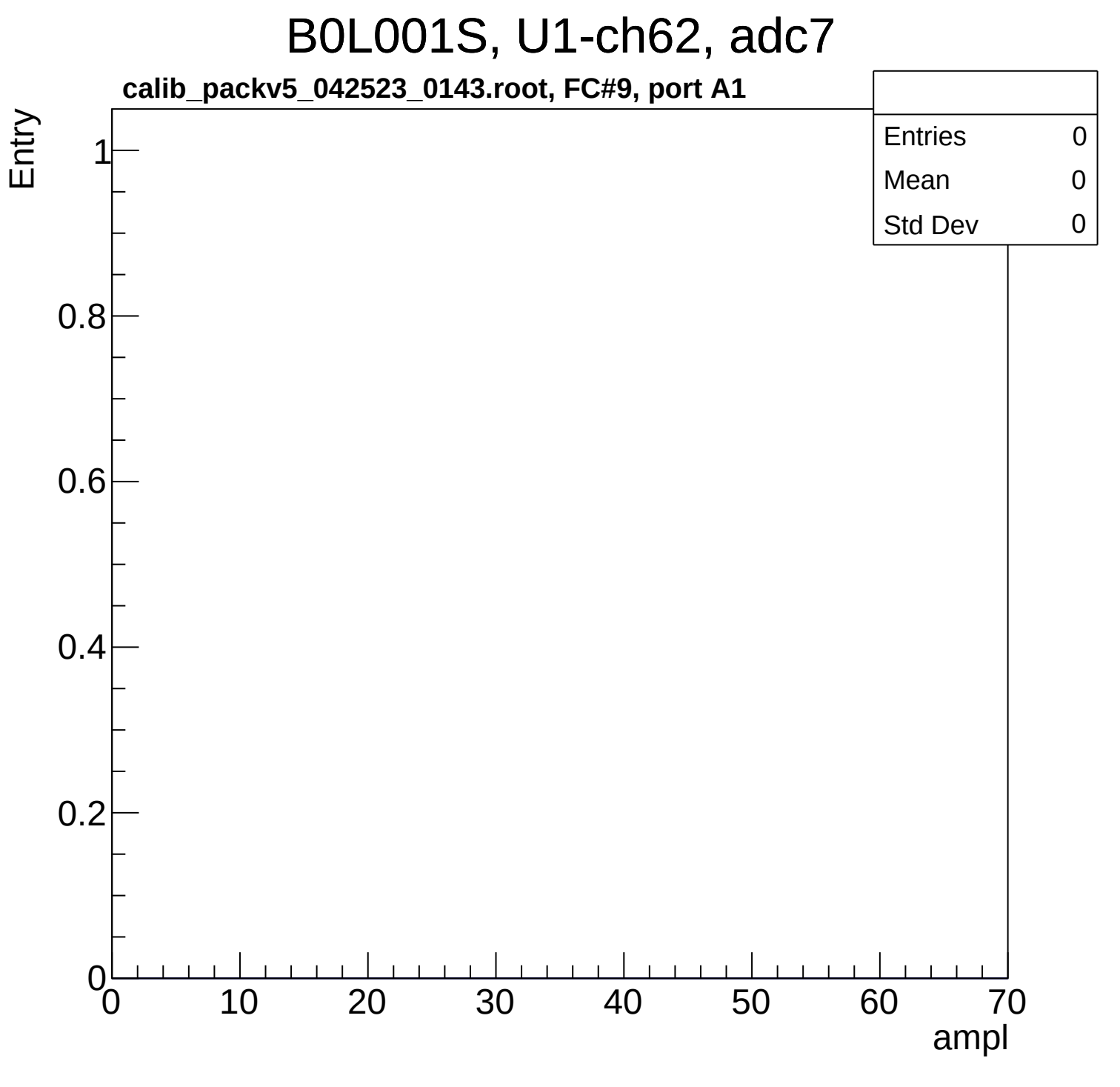
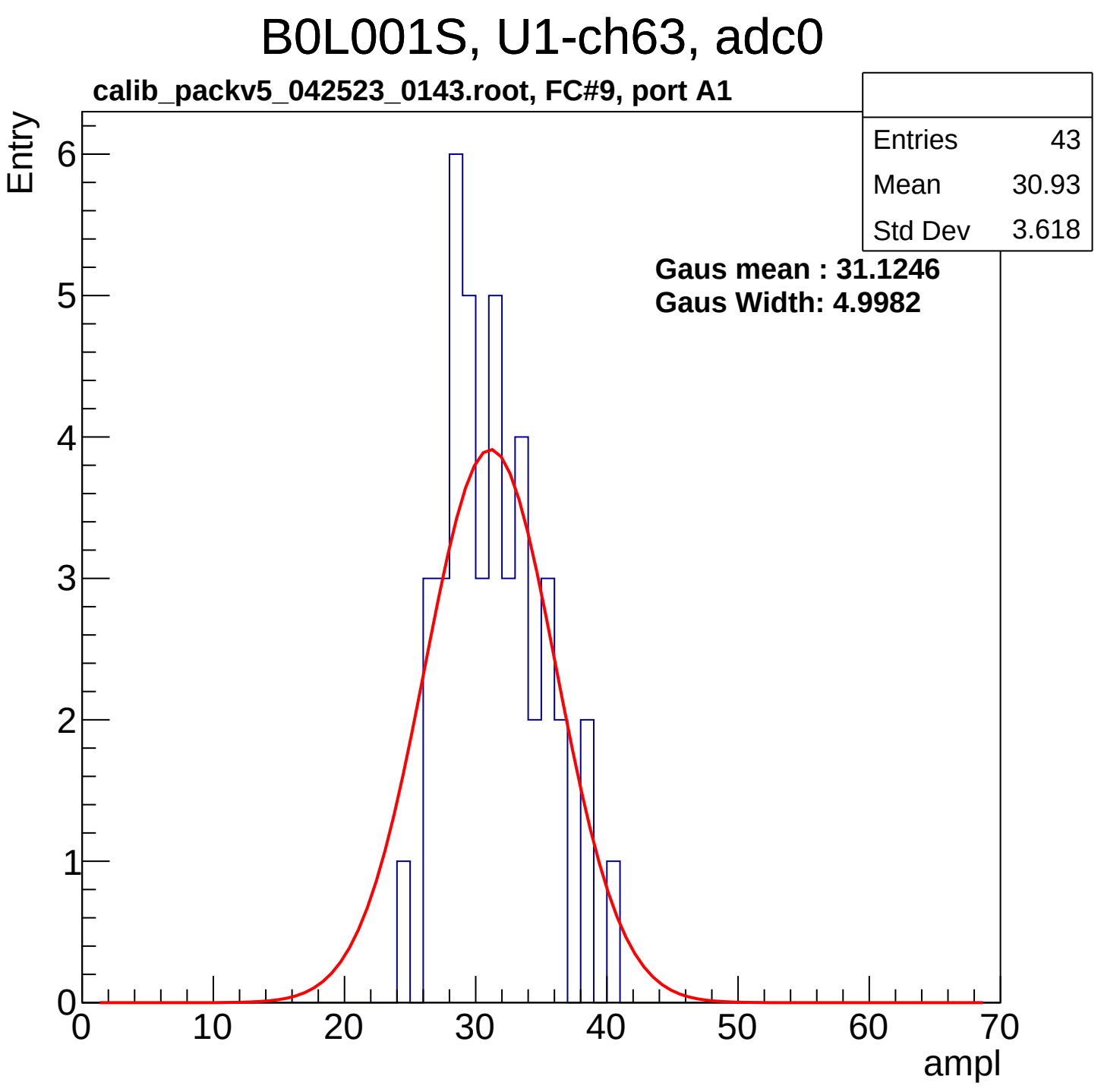
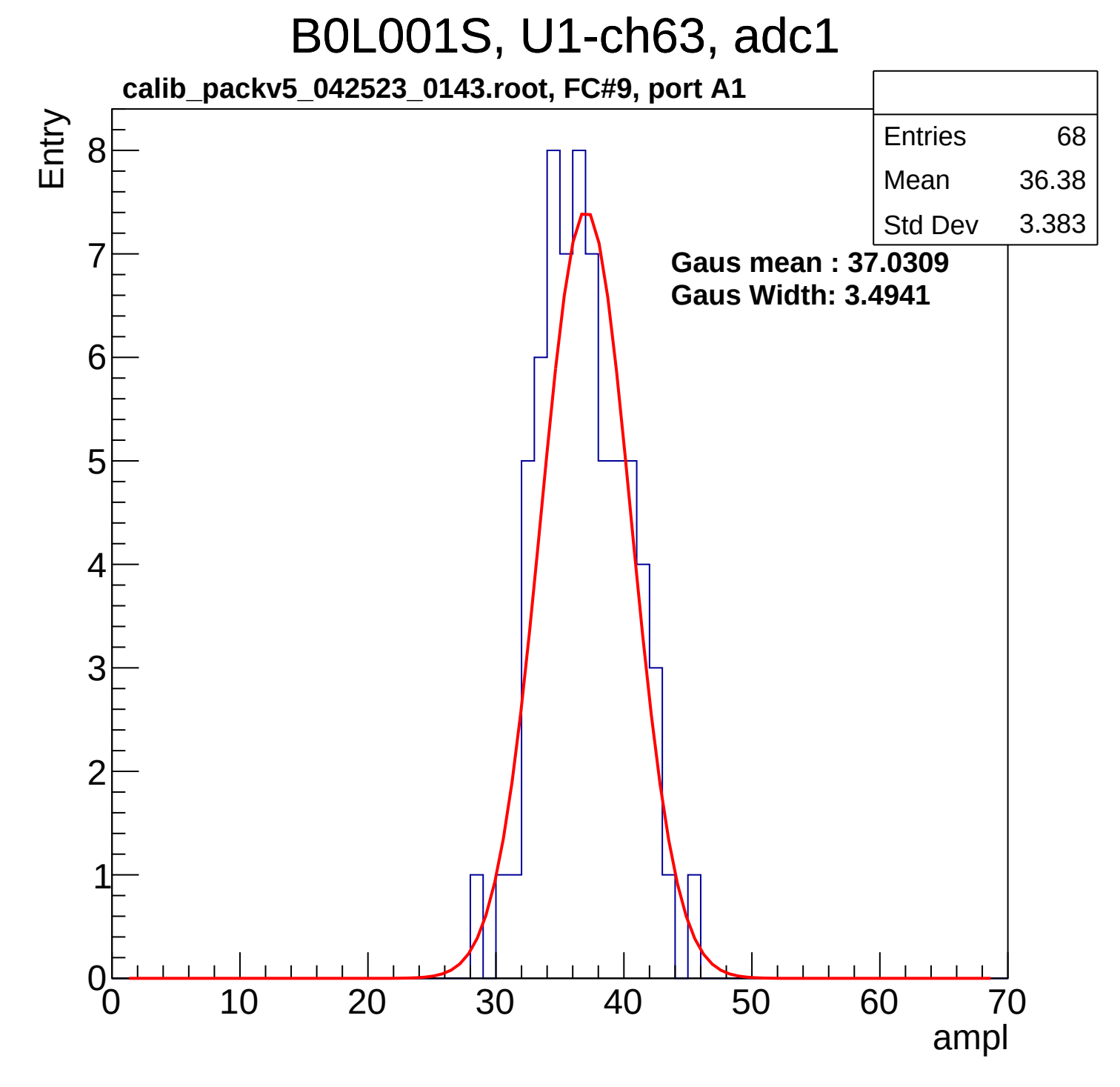
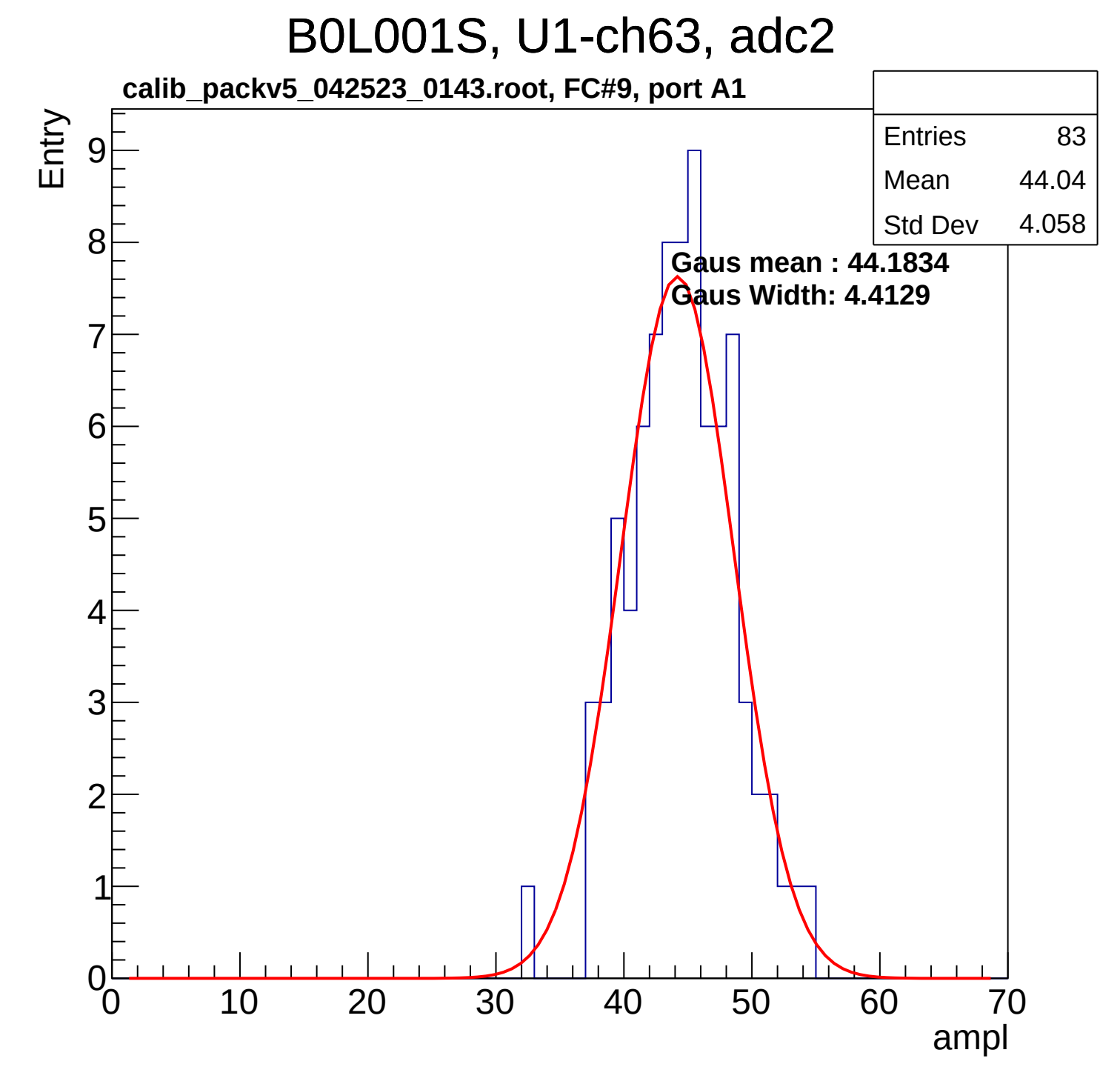
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 57.6
Std Dev 15.46
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 30.93
Std Dev 3.618
Gaus mean : 31.1246
Gaus Width: 4.9982
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 36.38
Std Dev 3.383
Gaus mean : 37.0309
Gaus Width: 3.4941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 44.04
Std Dev 4.058
Gaus mean : 44.1834
Gaus Width: 4.4129
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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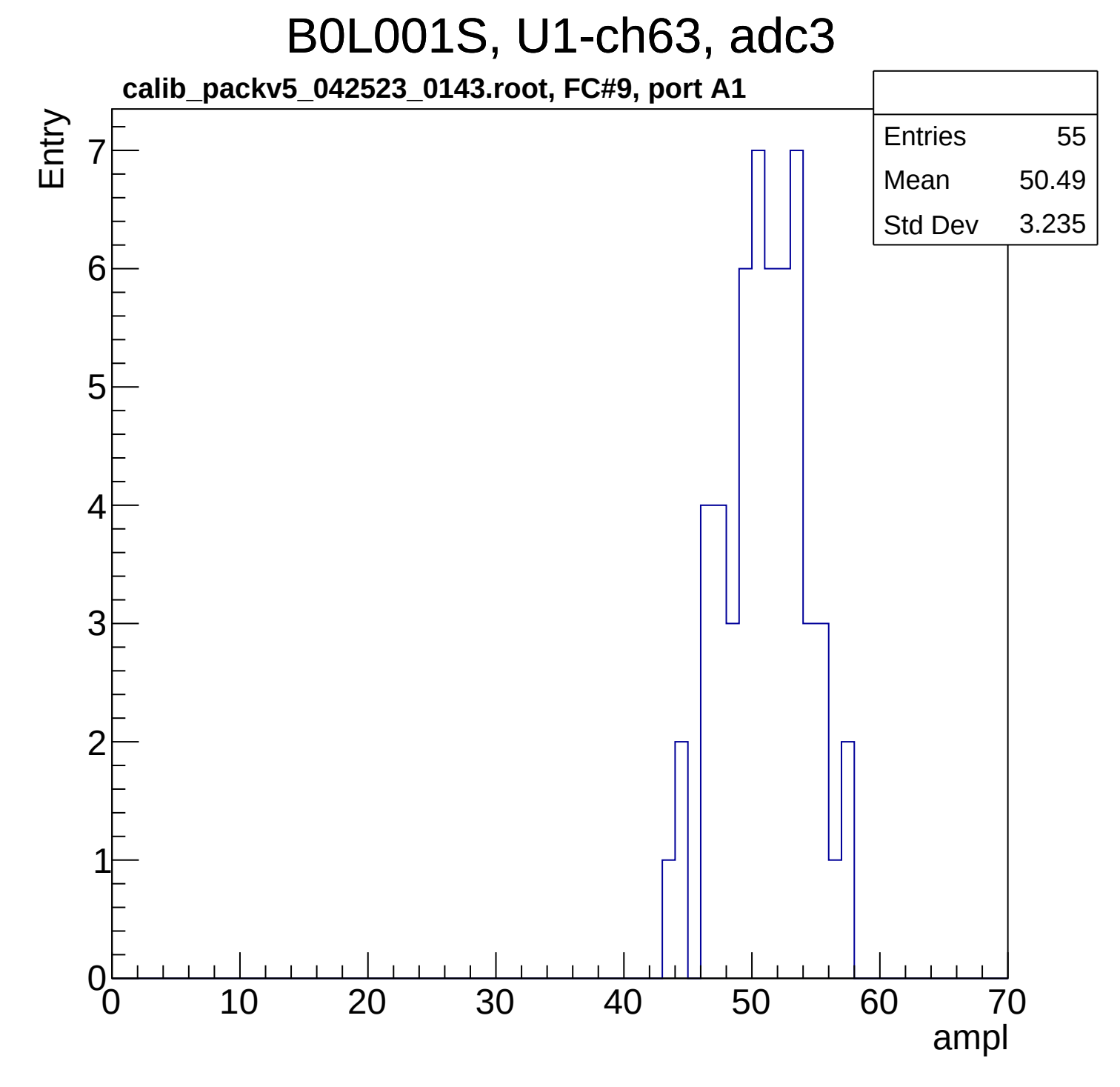
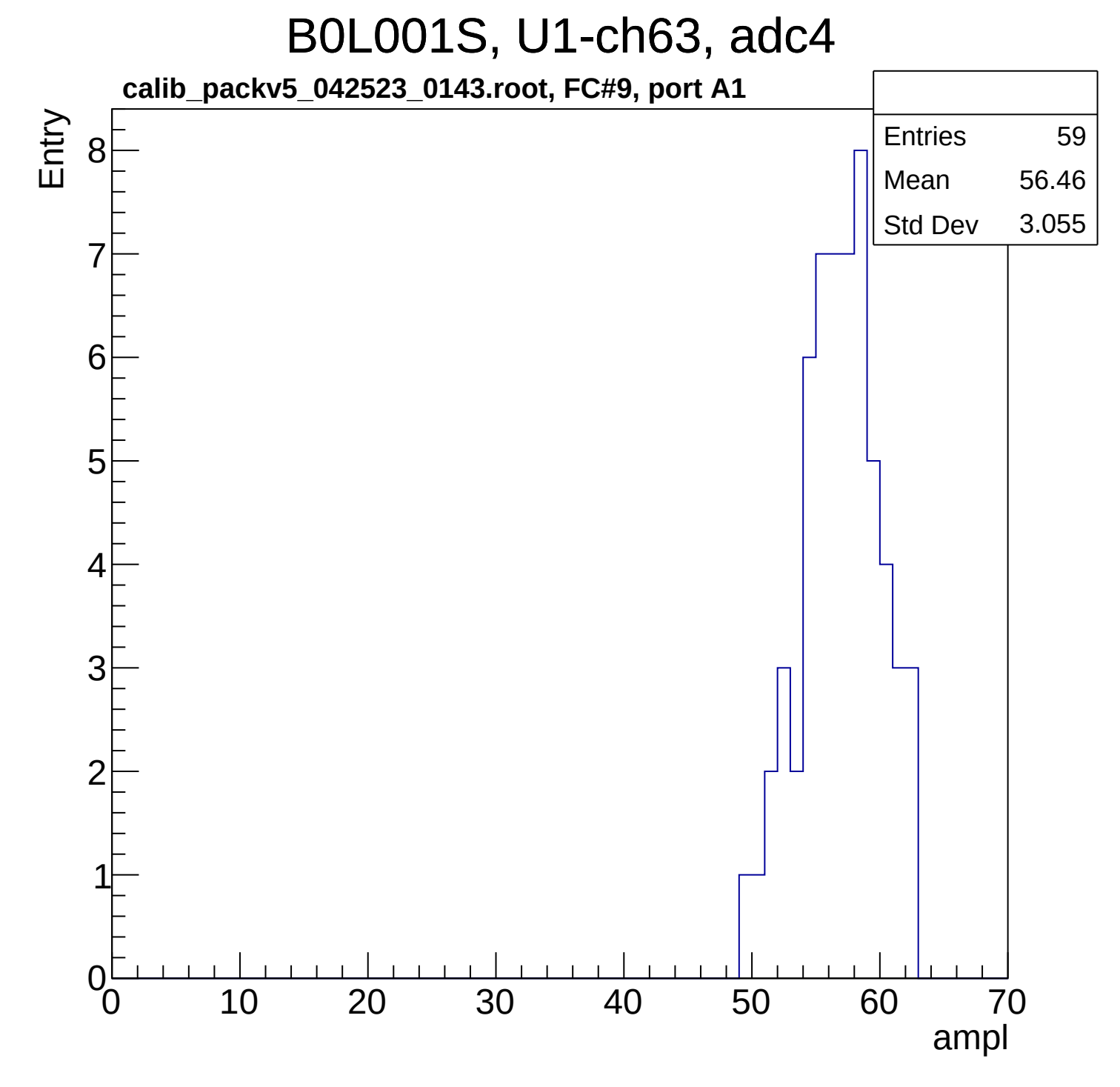

B0L001S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 50.49
Std Dev 3.235
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 56.46
Std Dev 3.055
Entry
ampl
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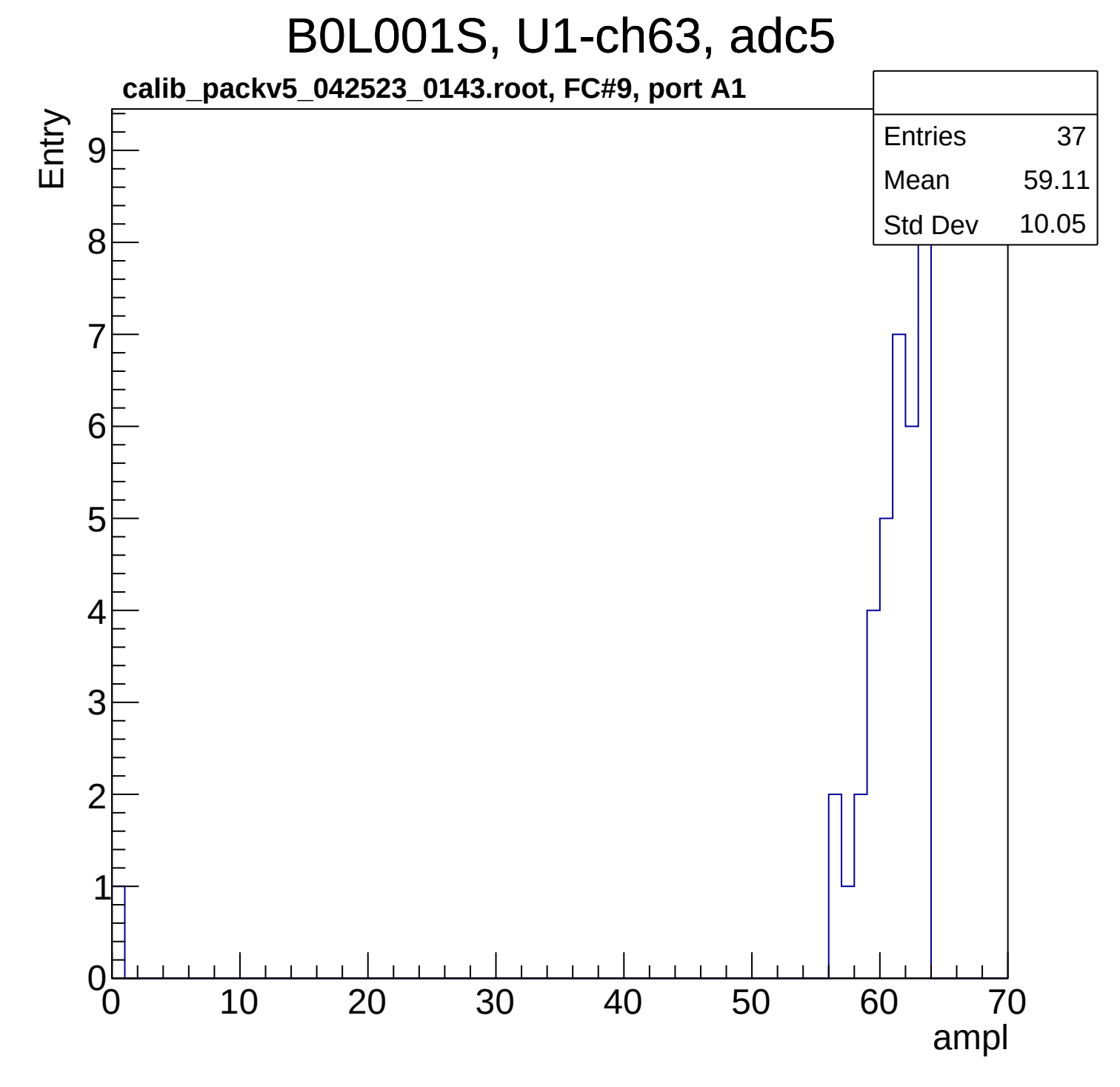
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 37
Mean 59.11
Std Dev 10.05
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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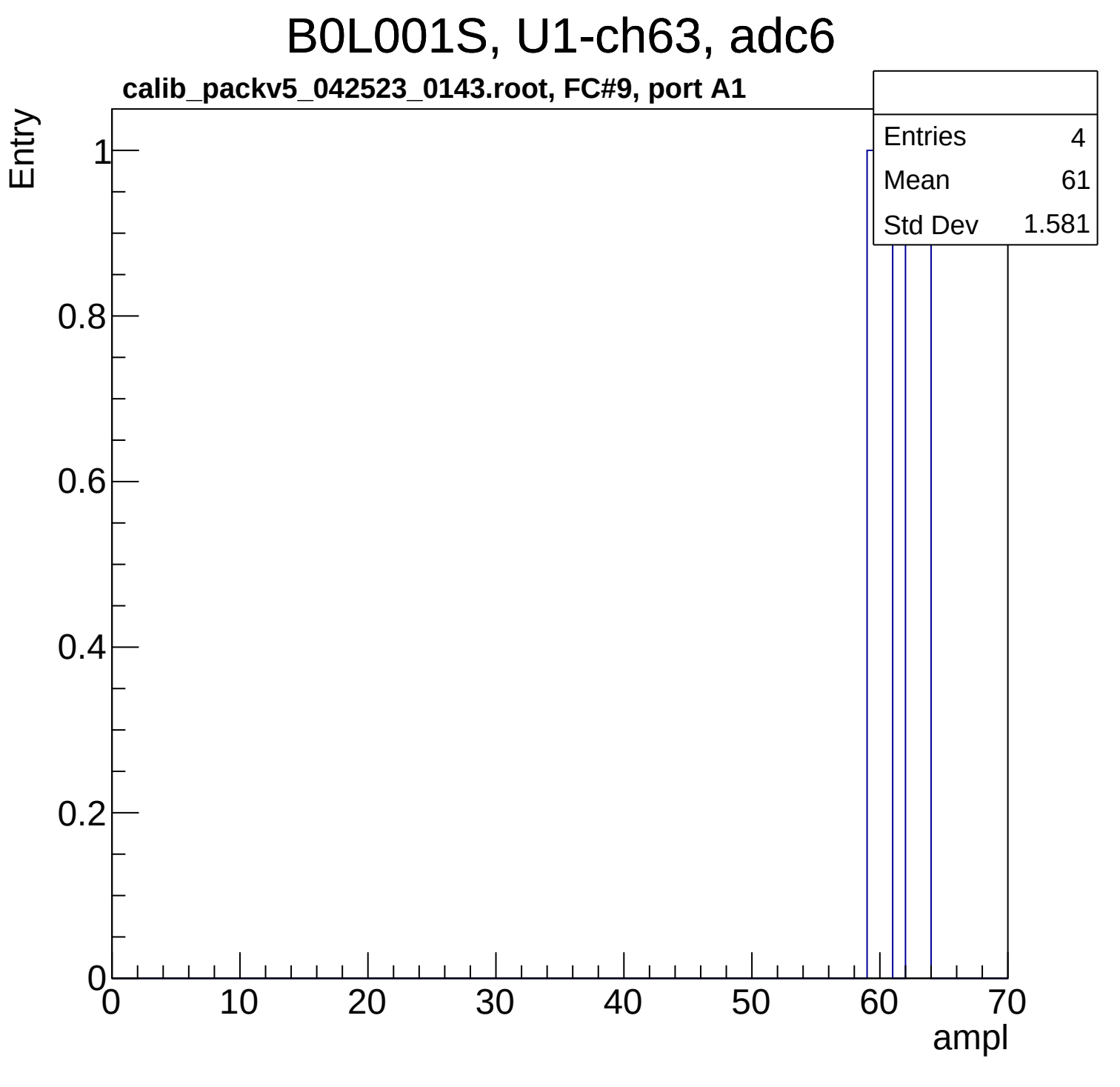
B0L001S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61
Std Dev 1.581
Entry
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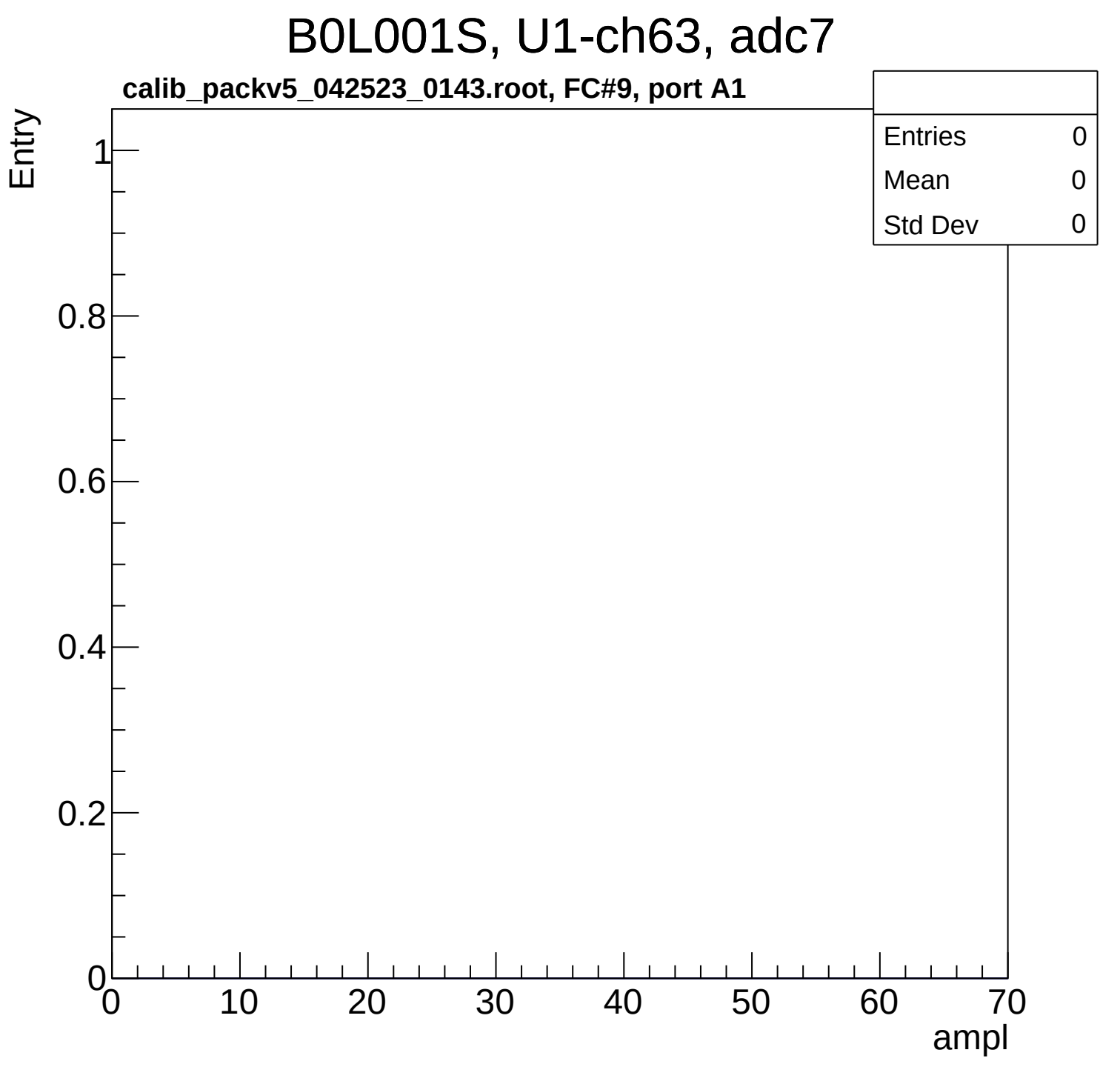
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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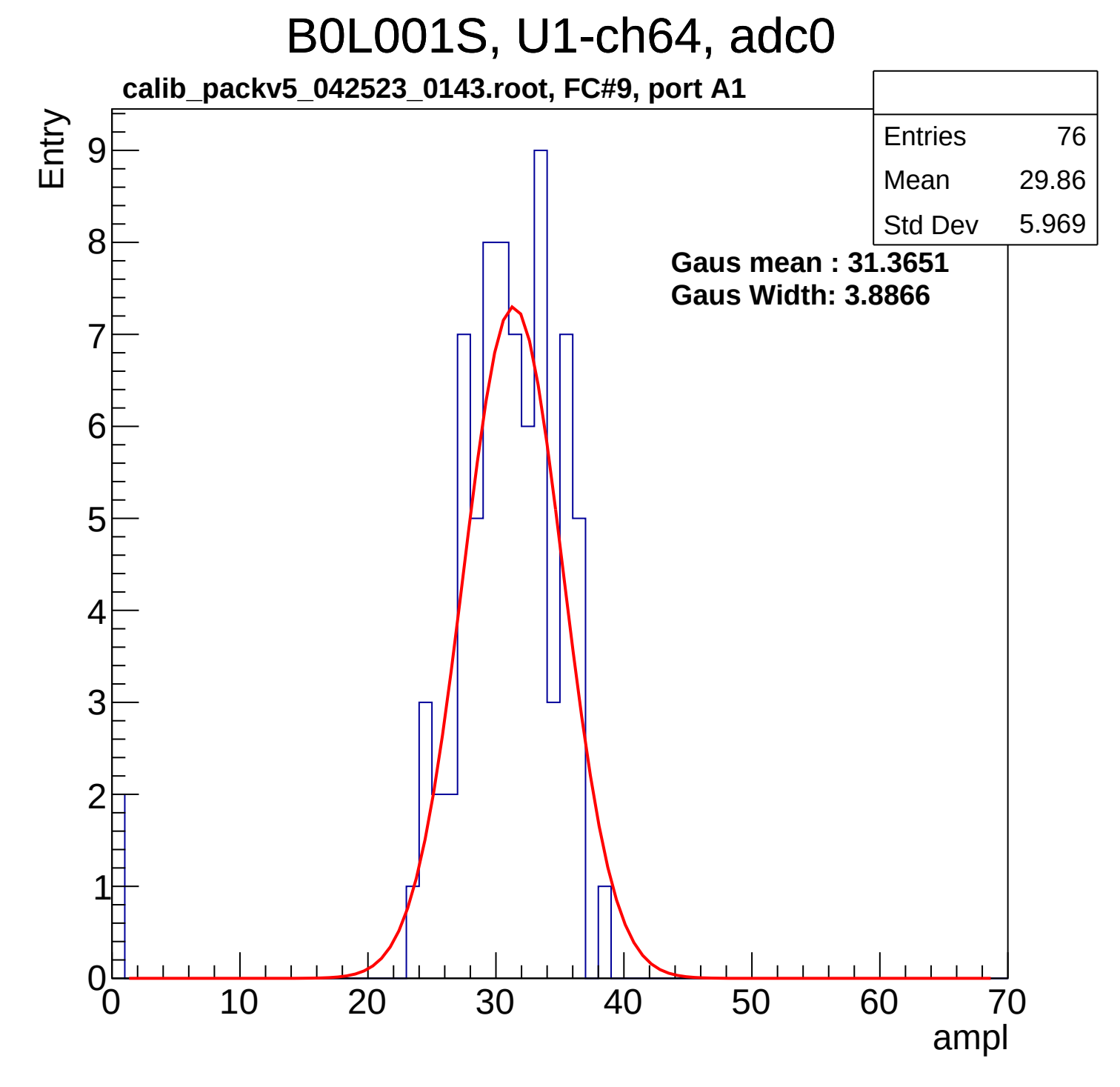
50
60
70
ampl


B0L001S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 29.86
Std Dev 5.969
Gaus mean : 31.3651
Gaus Width: 3.8866
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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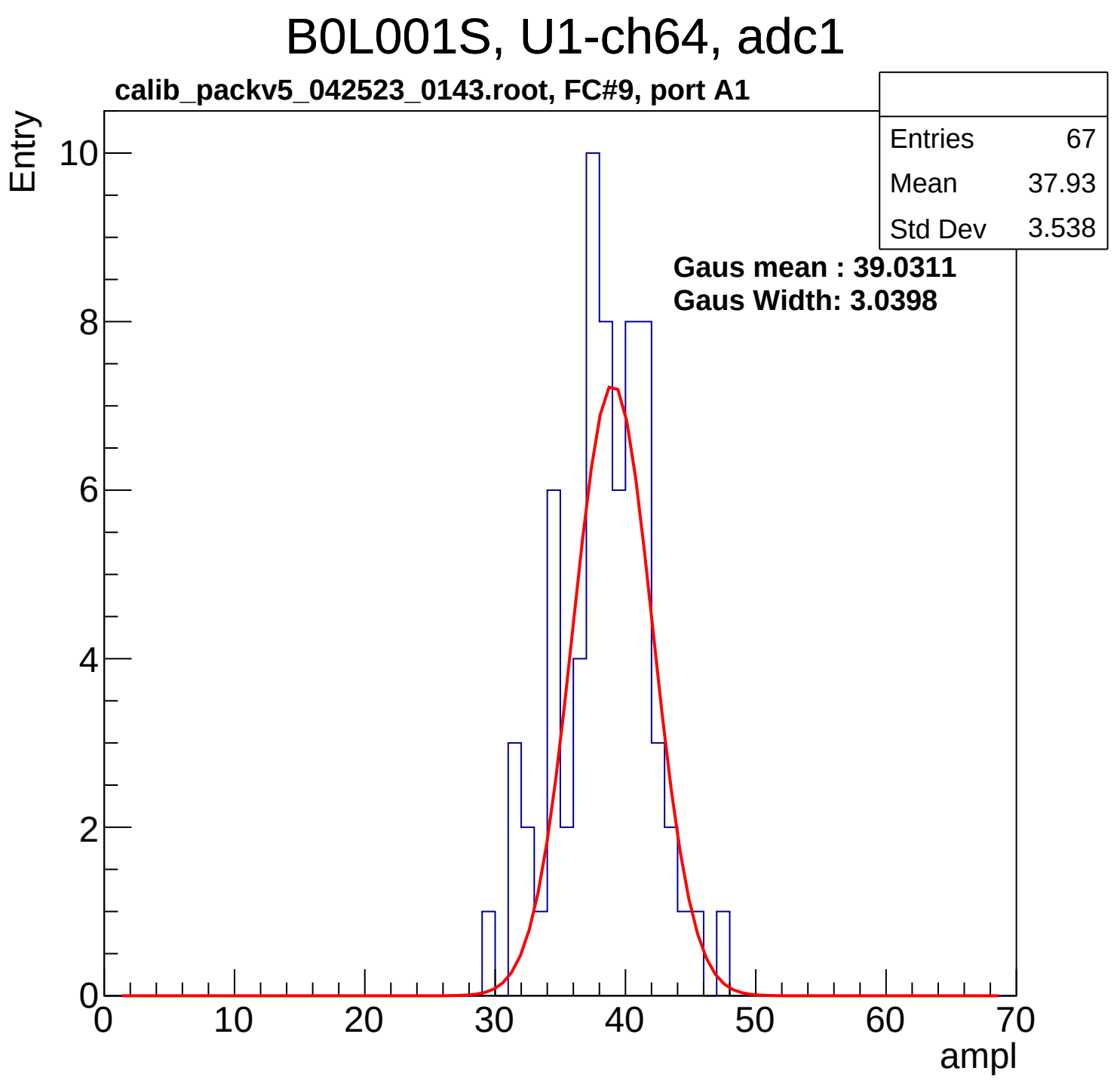
B0L001S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.93
Std Dev 3.538
Gaus mean : 39.0311
Gaus Width: 3.0398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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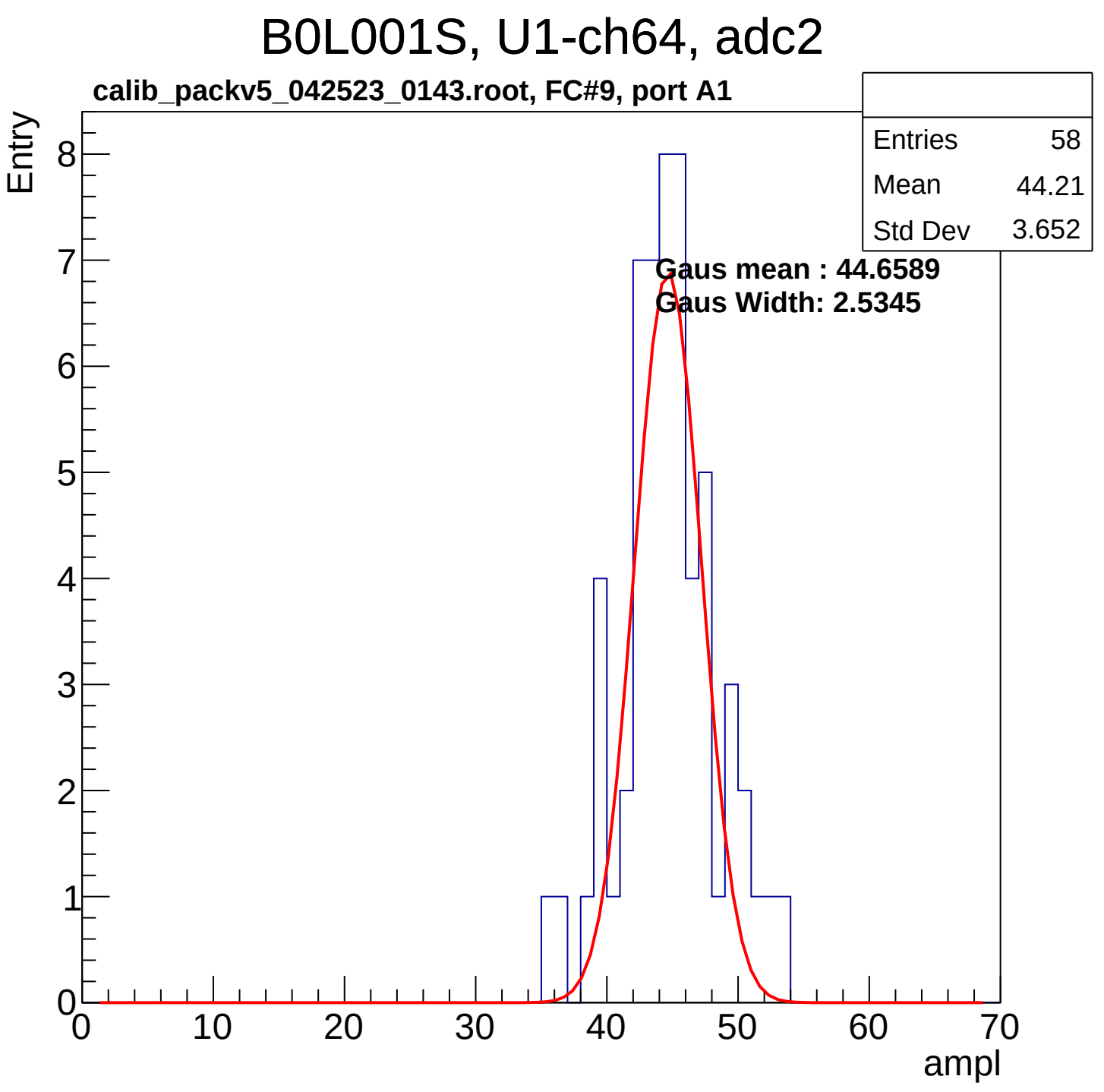
B0L001S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 44.21
Std Dev 3.652
Gaus mean : 44.6589
Gaus Width: 2.5345
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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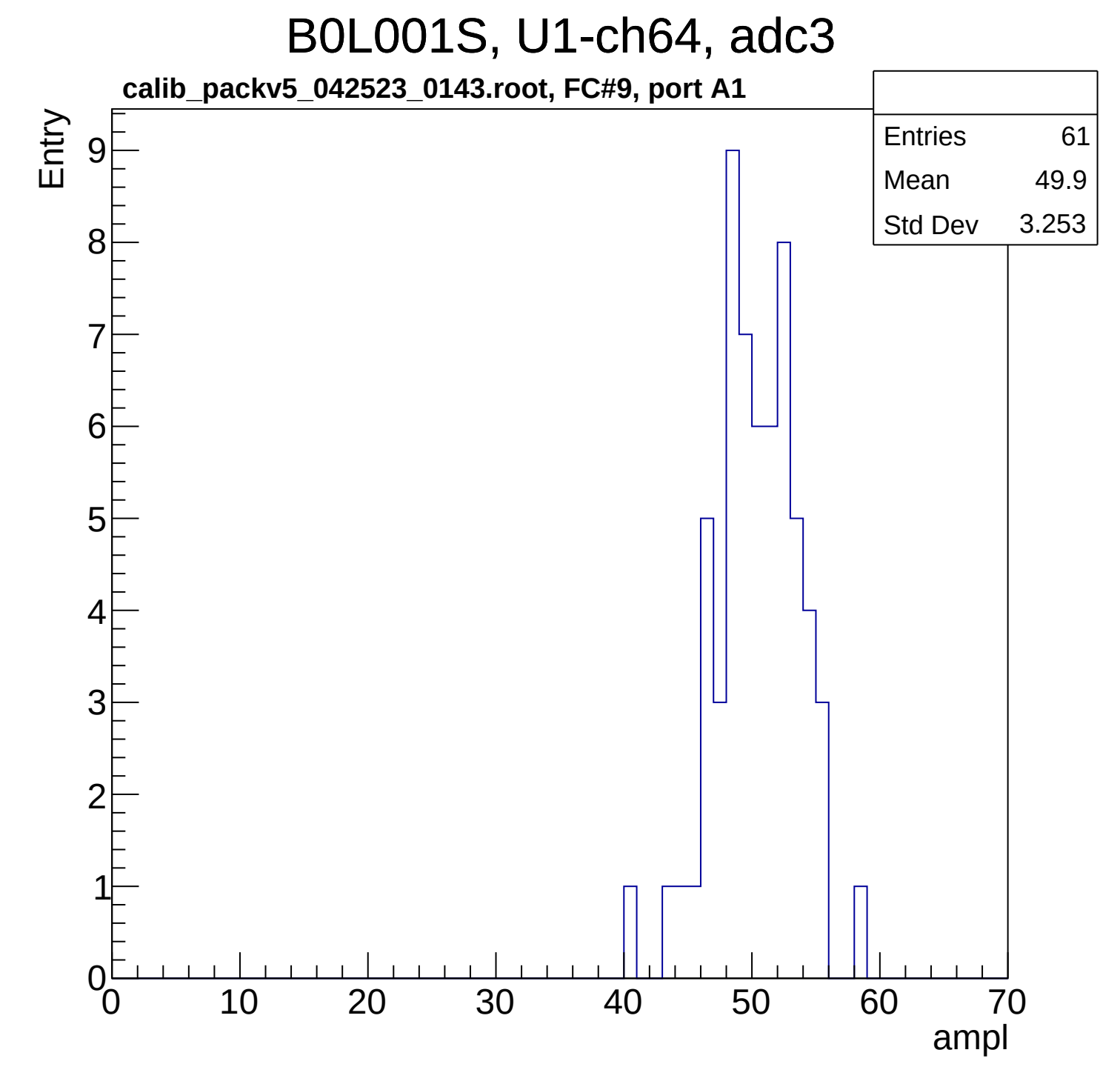
40
50
60
70


B0L001S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 49.9
Std Dev 3.253
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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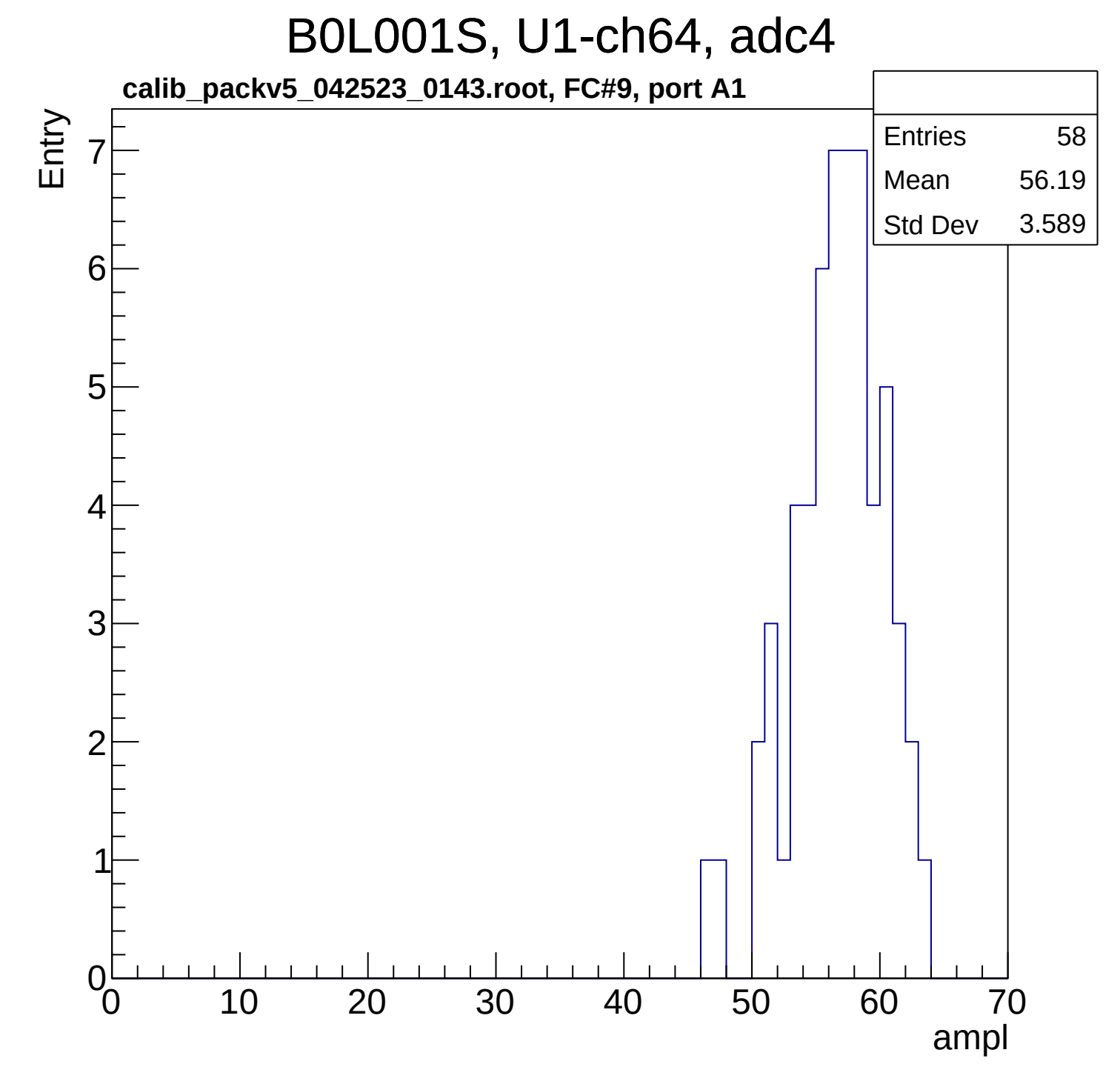
40
50
60
70


B0L001S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.19
Std Dev 3.589
Entry
ampl
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5
6
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0
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40
50
60
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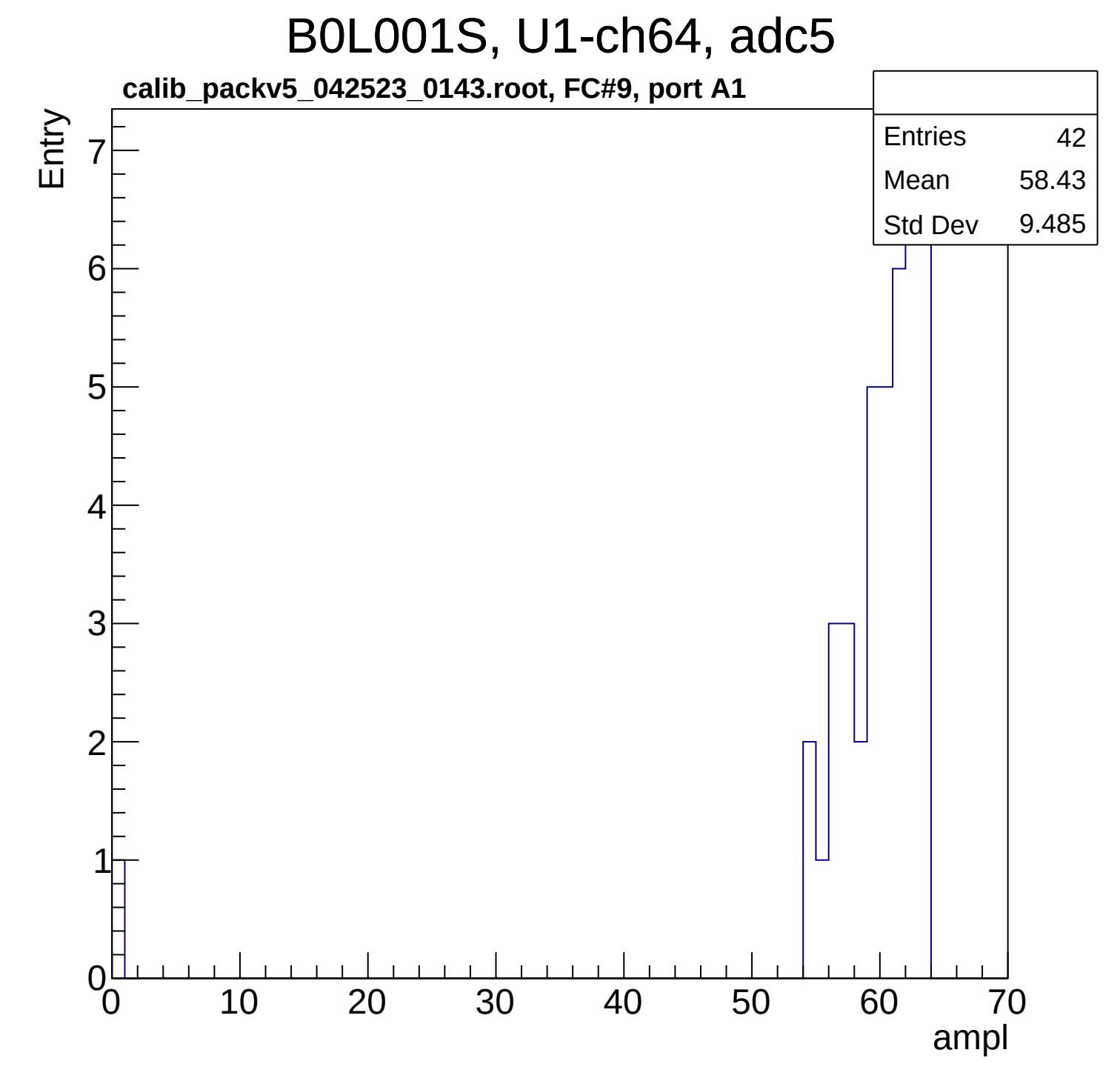
B0L001S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.43
Std Dev 9.485
Entry
ampl
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3
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0
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40
50
60
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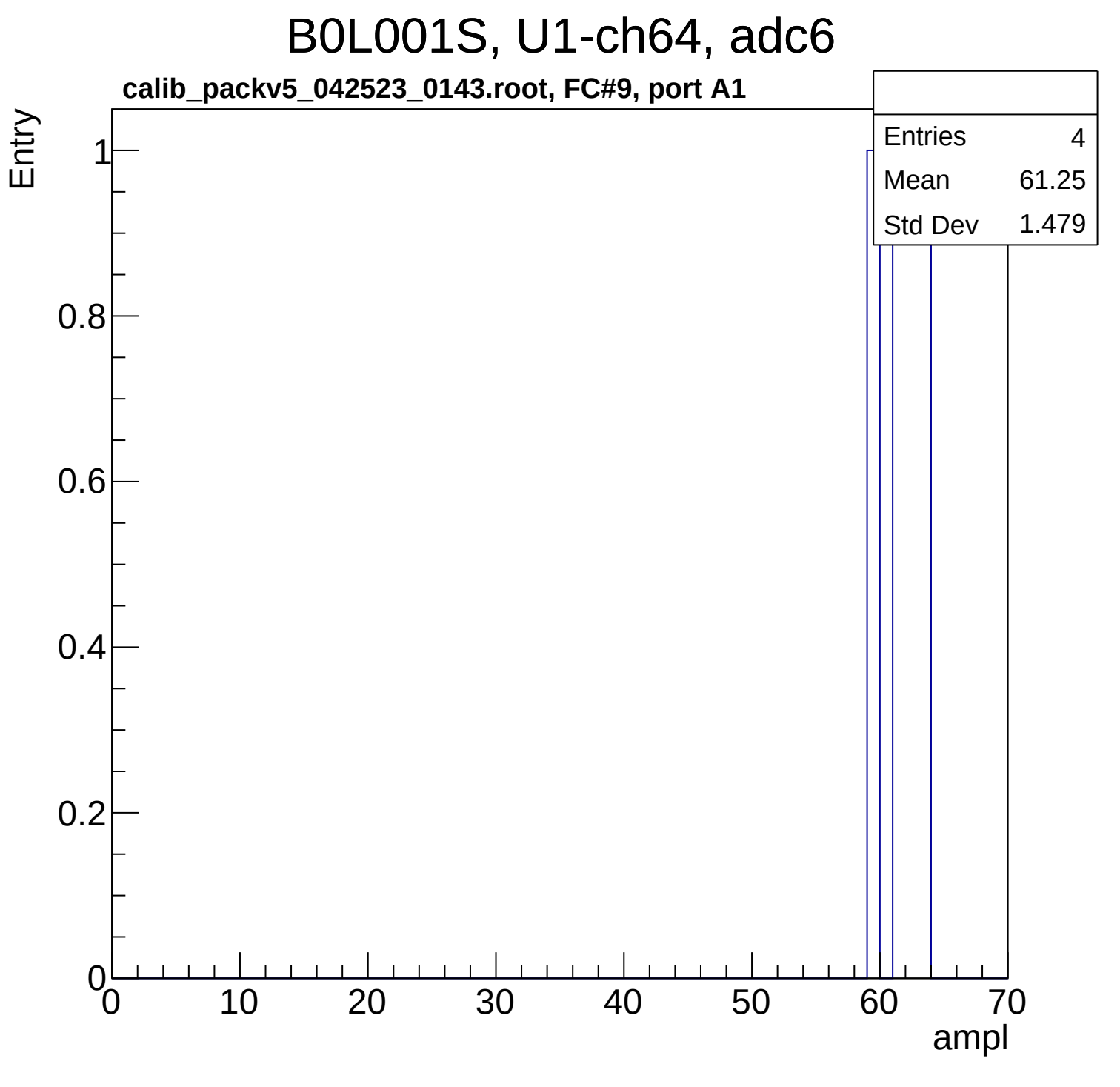
B0L001S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
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0.6
0.4
0.2
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0
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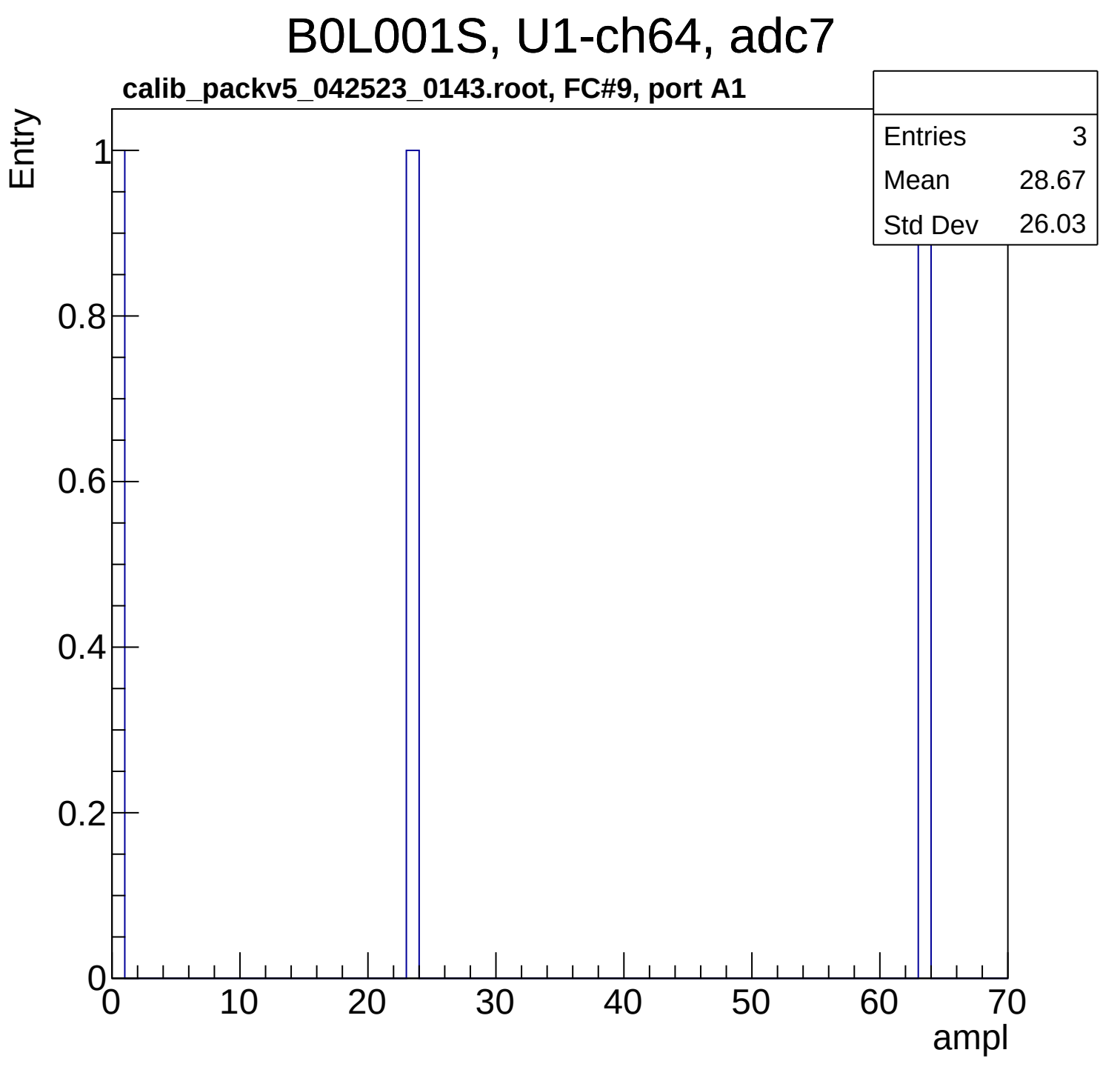
40
50
60
70
ampl


B0L001S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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0.6
0.8
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0
10
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40
50
60
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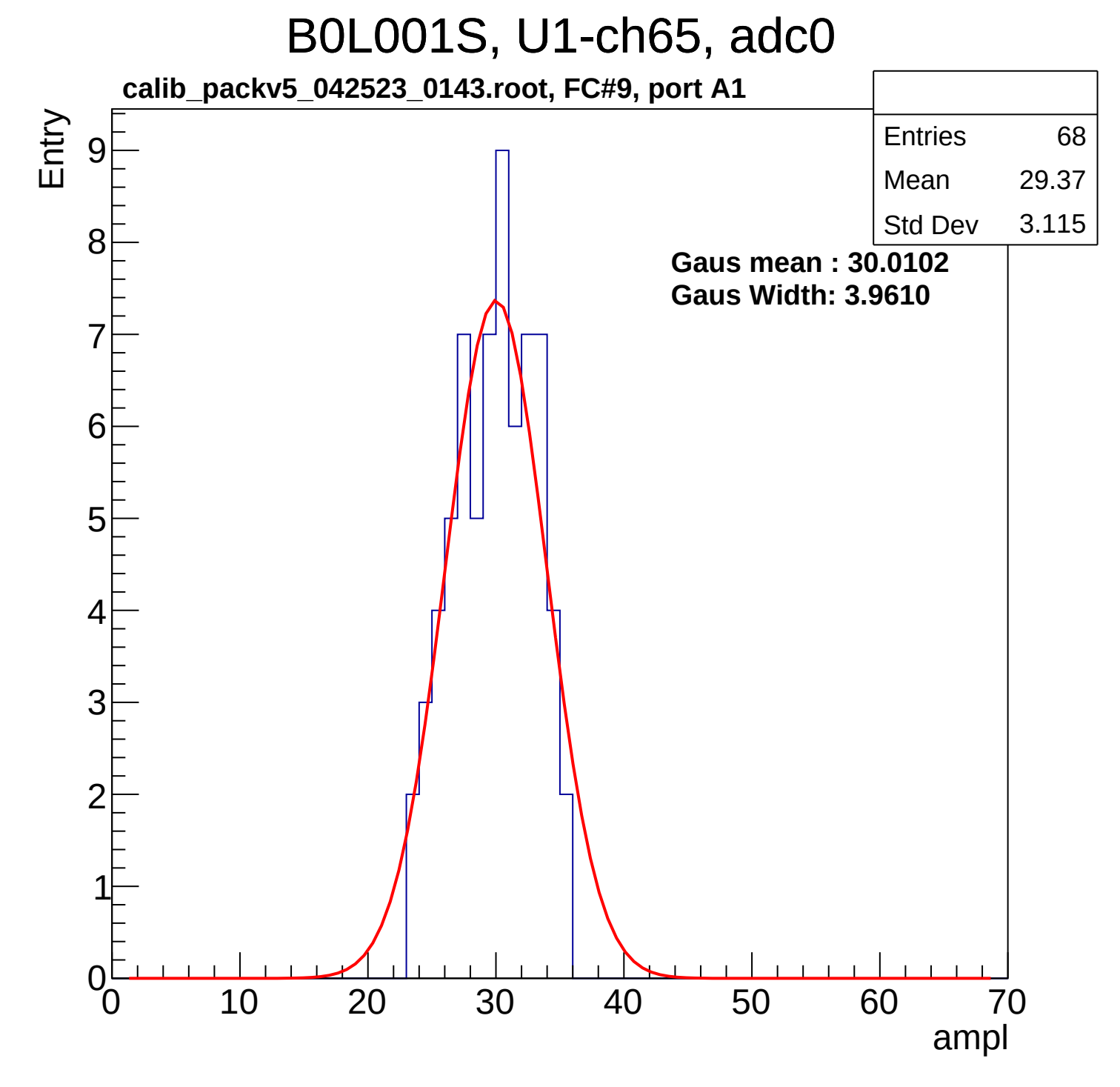
B0L001S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 29.37
Std Dev 3.115
Gaus mean : 30.0102
Gaus Width: 3.9610
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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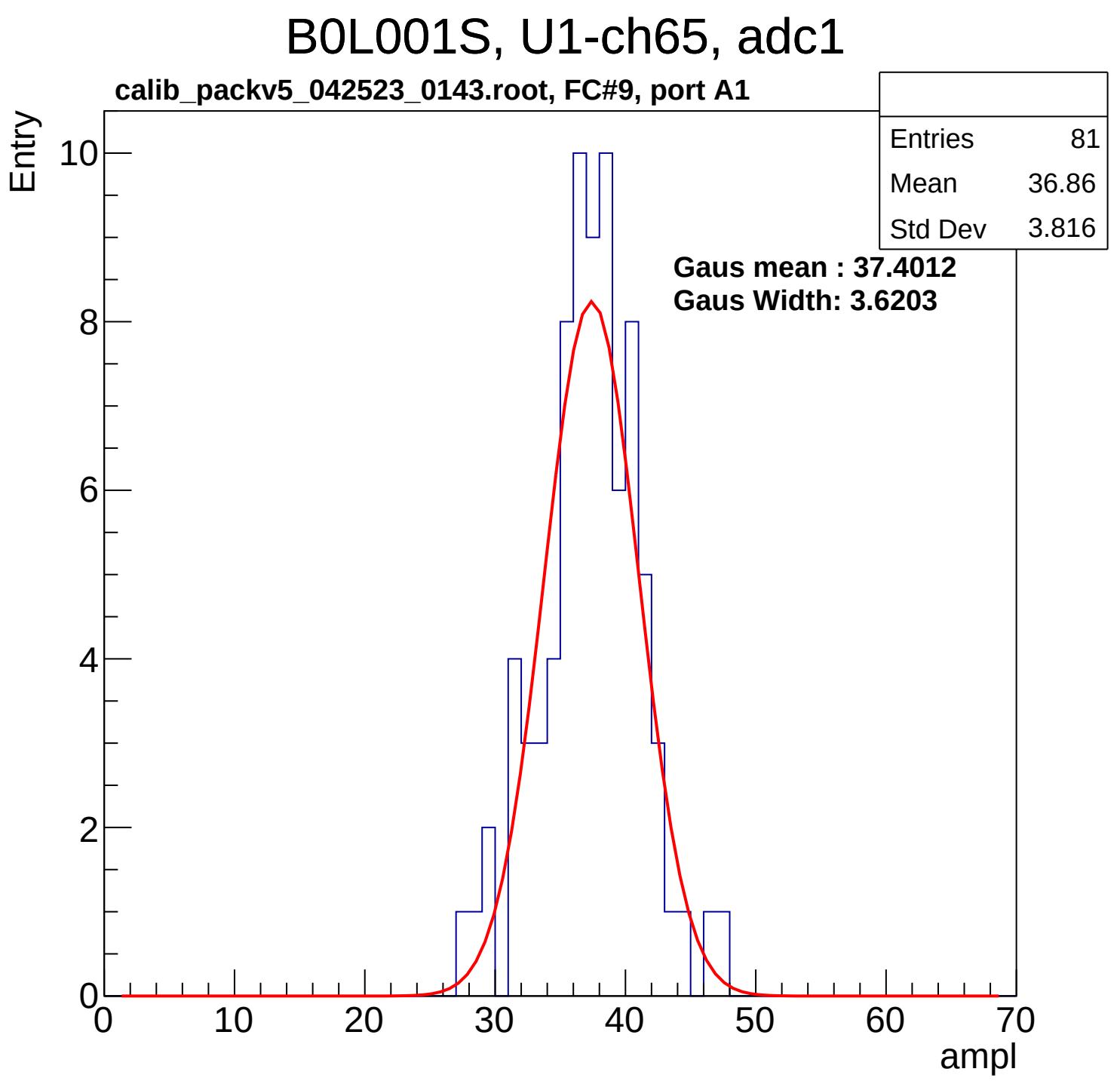
B0L001S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 36.86
Std Dev 3.816
Gaus mean : 37.4012
Gaus Width: 3.6203
Entry
ampl
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6
8
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0
10
20
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40
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60
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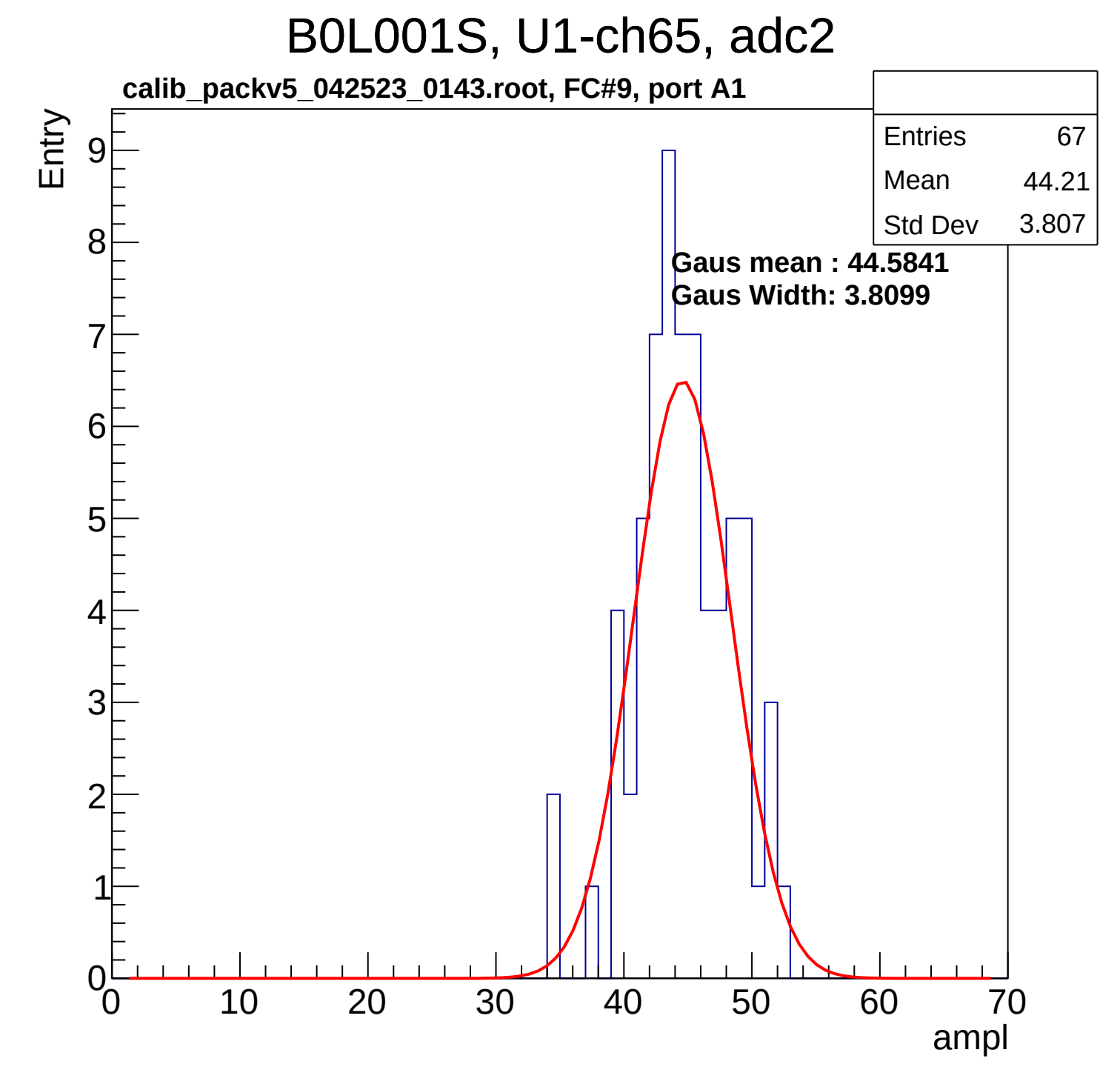
B0L001S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.21
Std Dev 3.807
Gaus mean : 44.5841
Gaus Width: 3.8099
Entry
ampl
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2
3
4
5
6
7
8
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0
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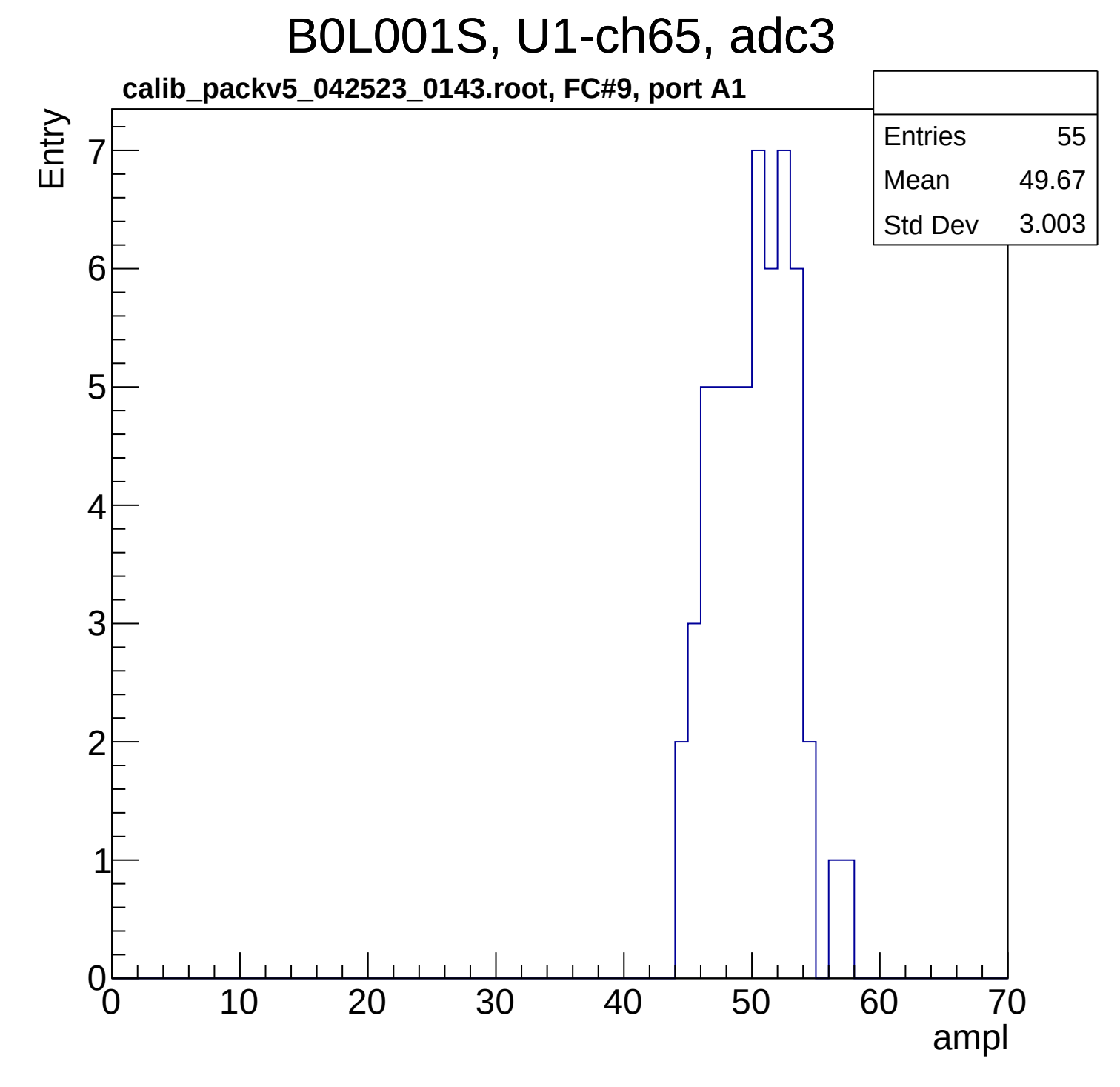
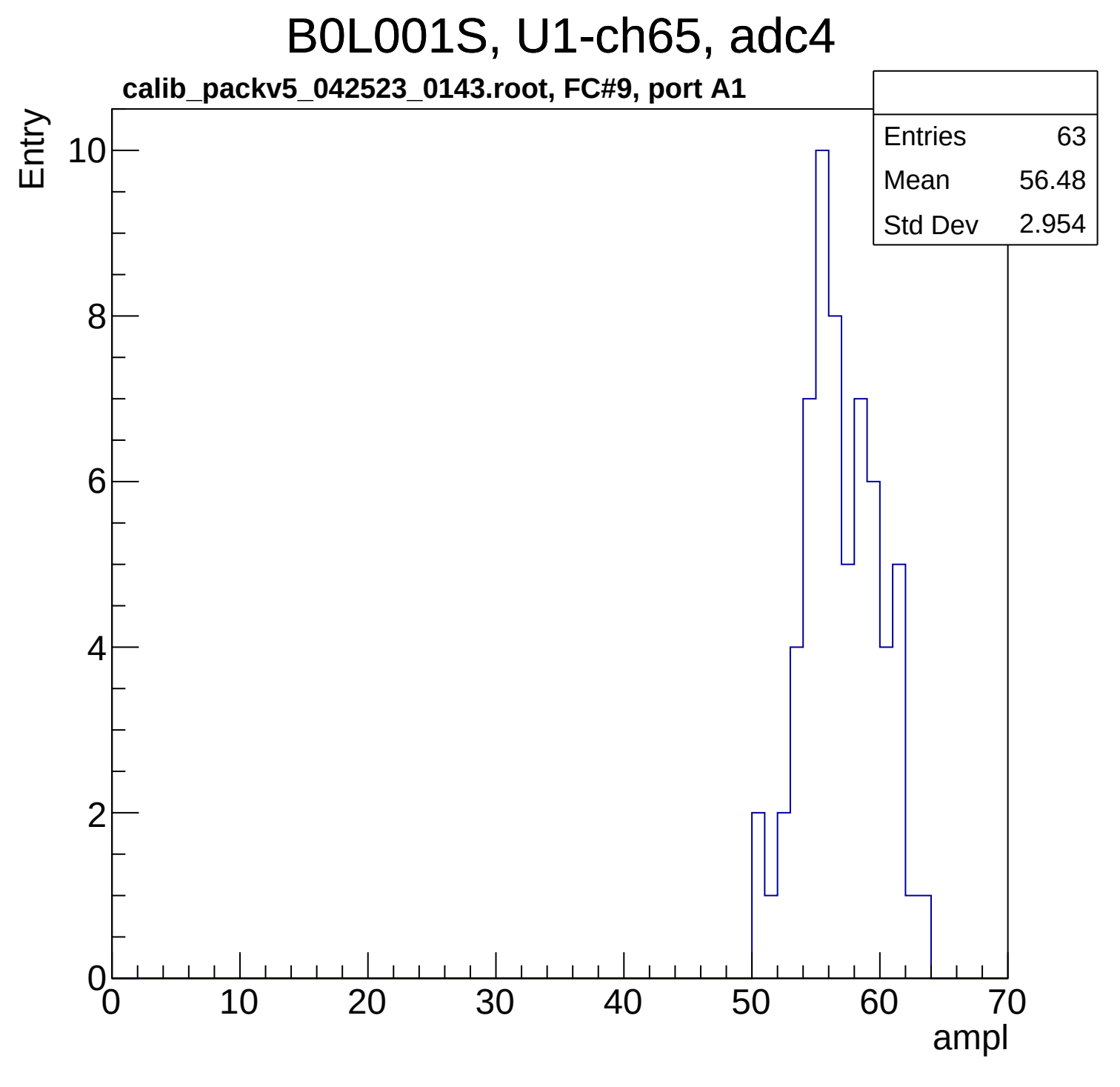
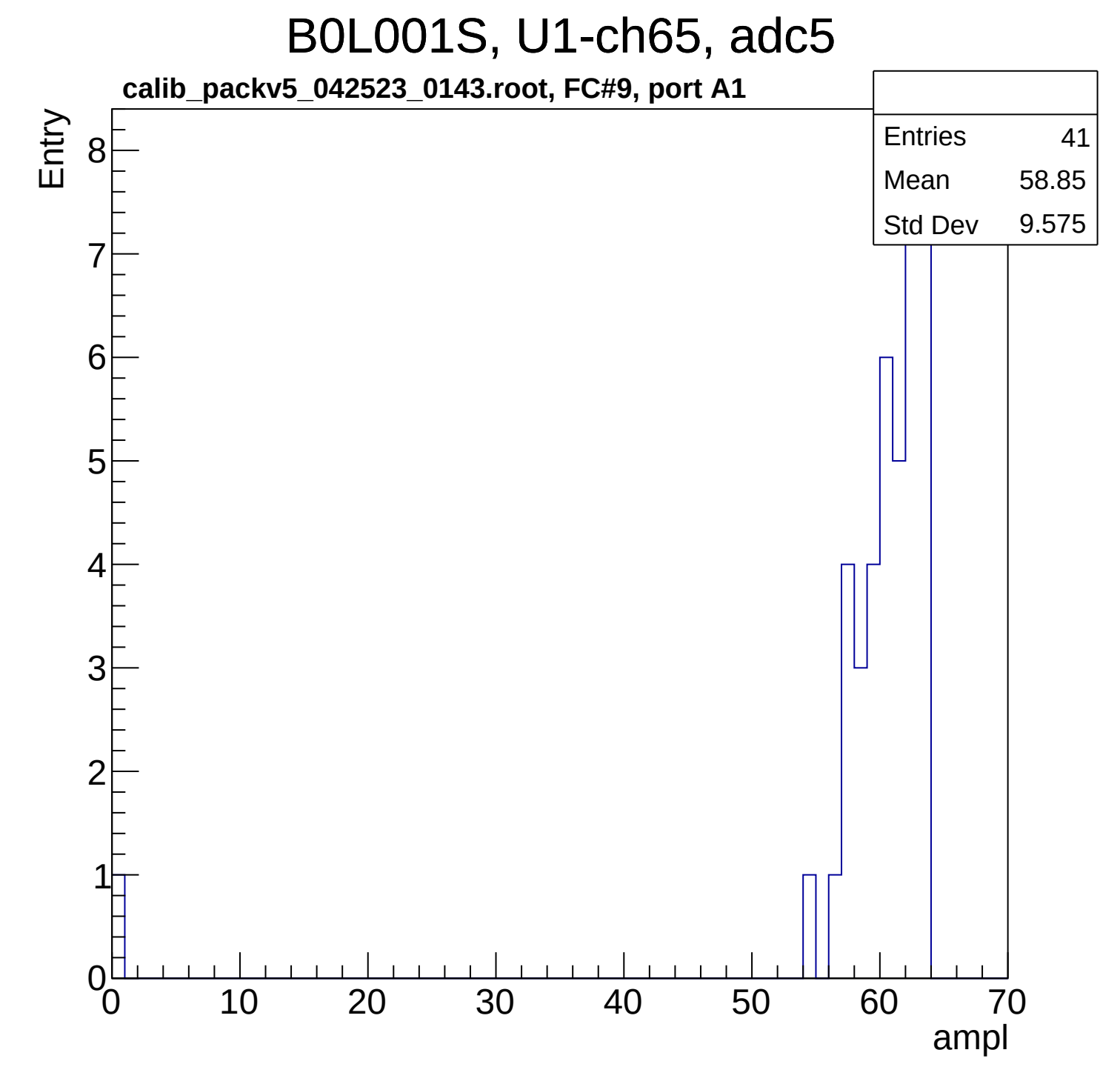
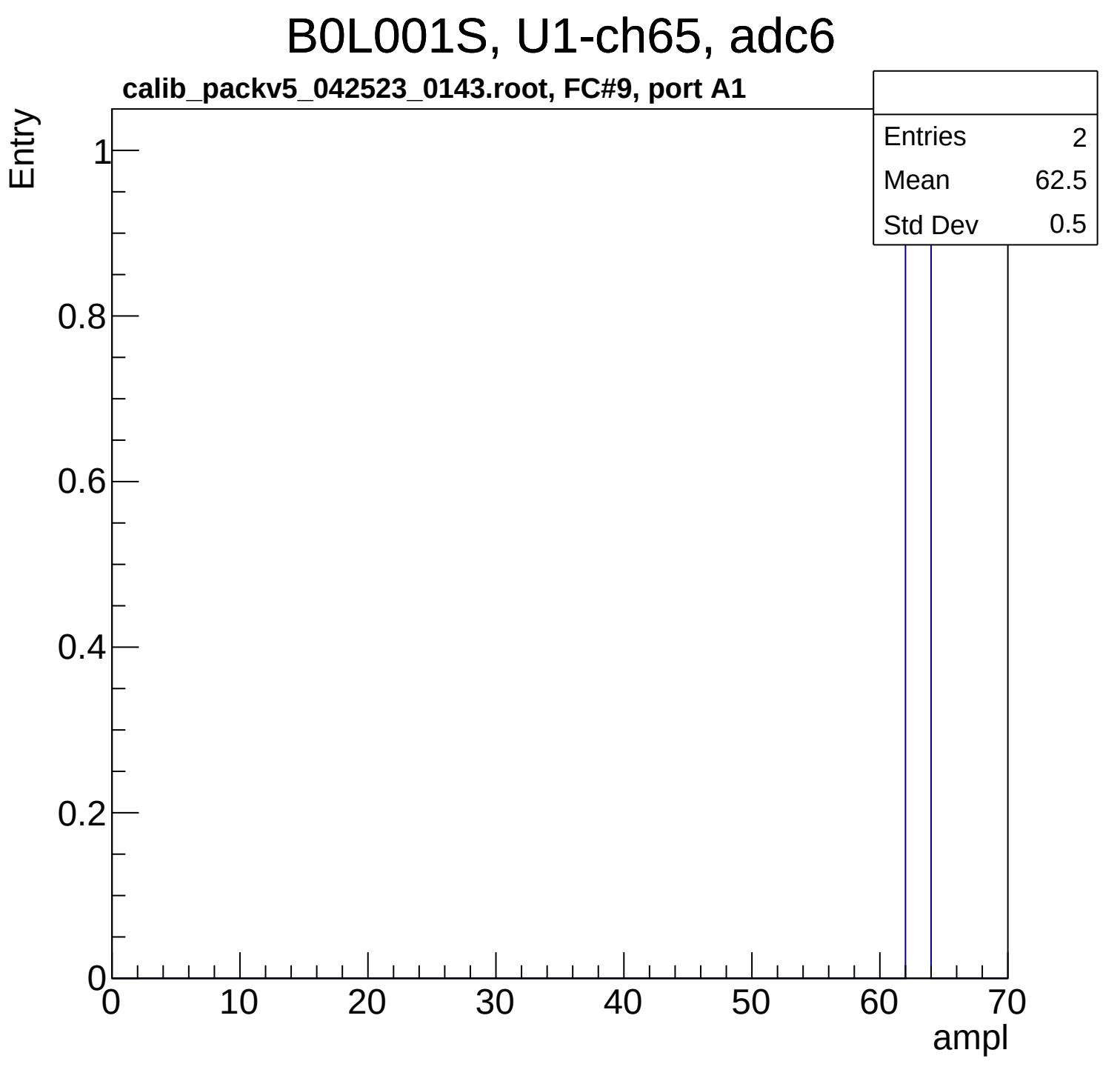
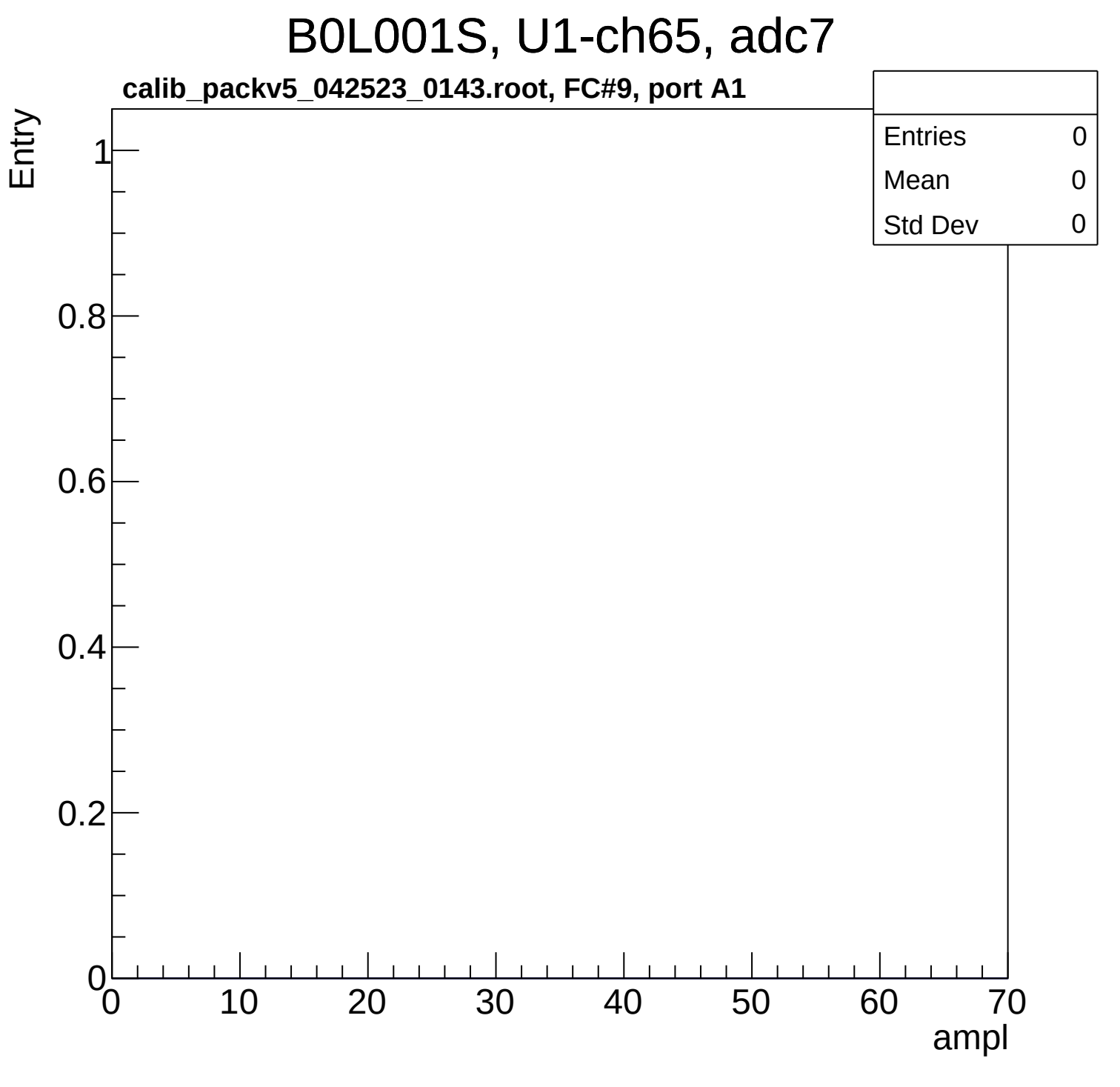
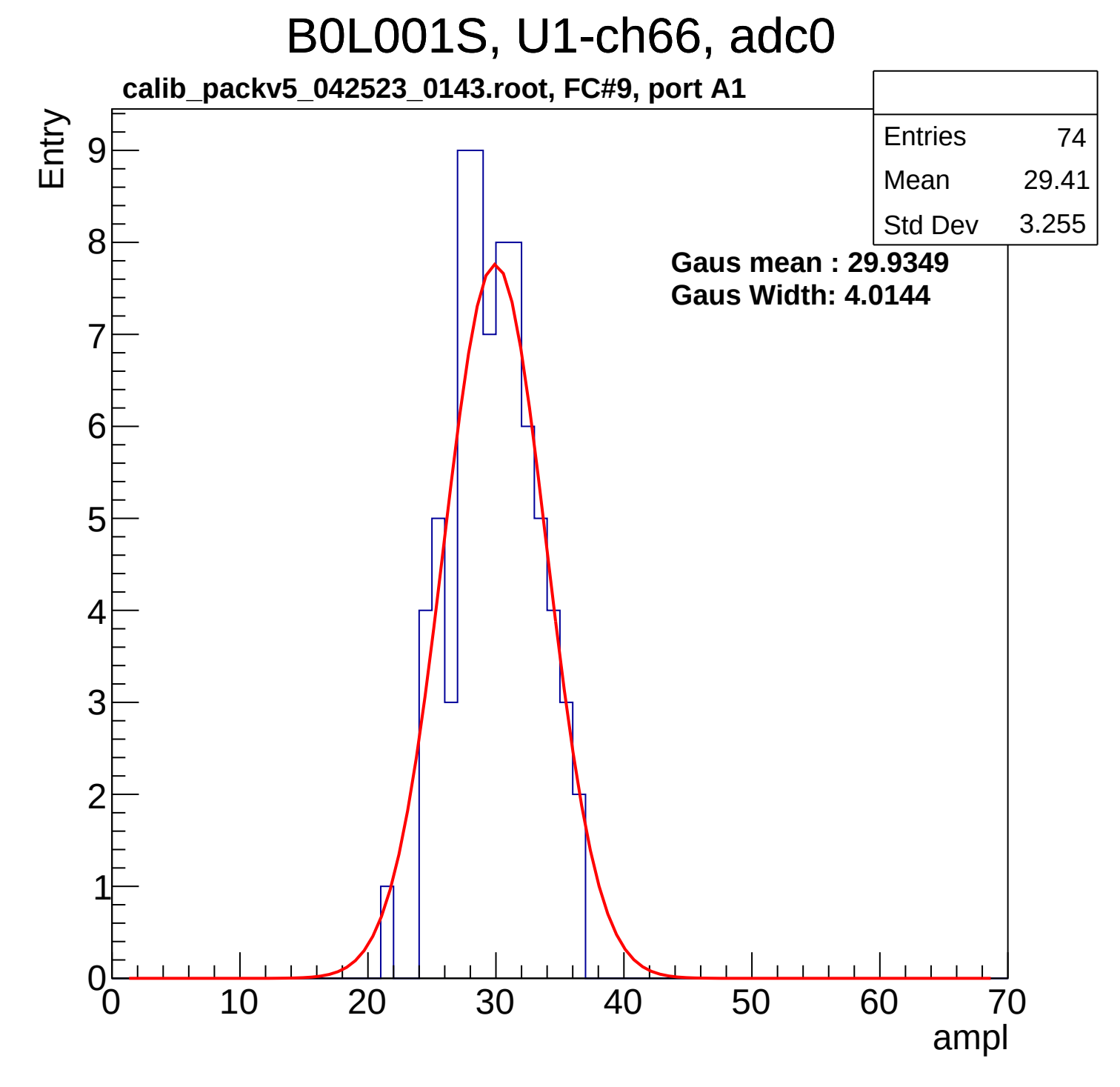

B0L001S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.67
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

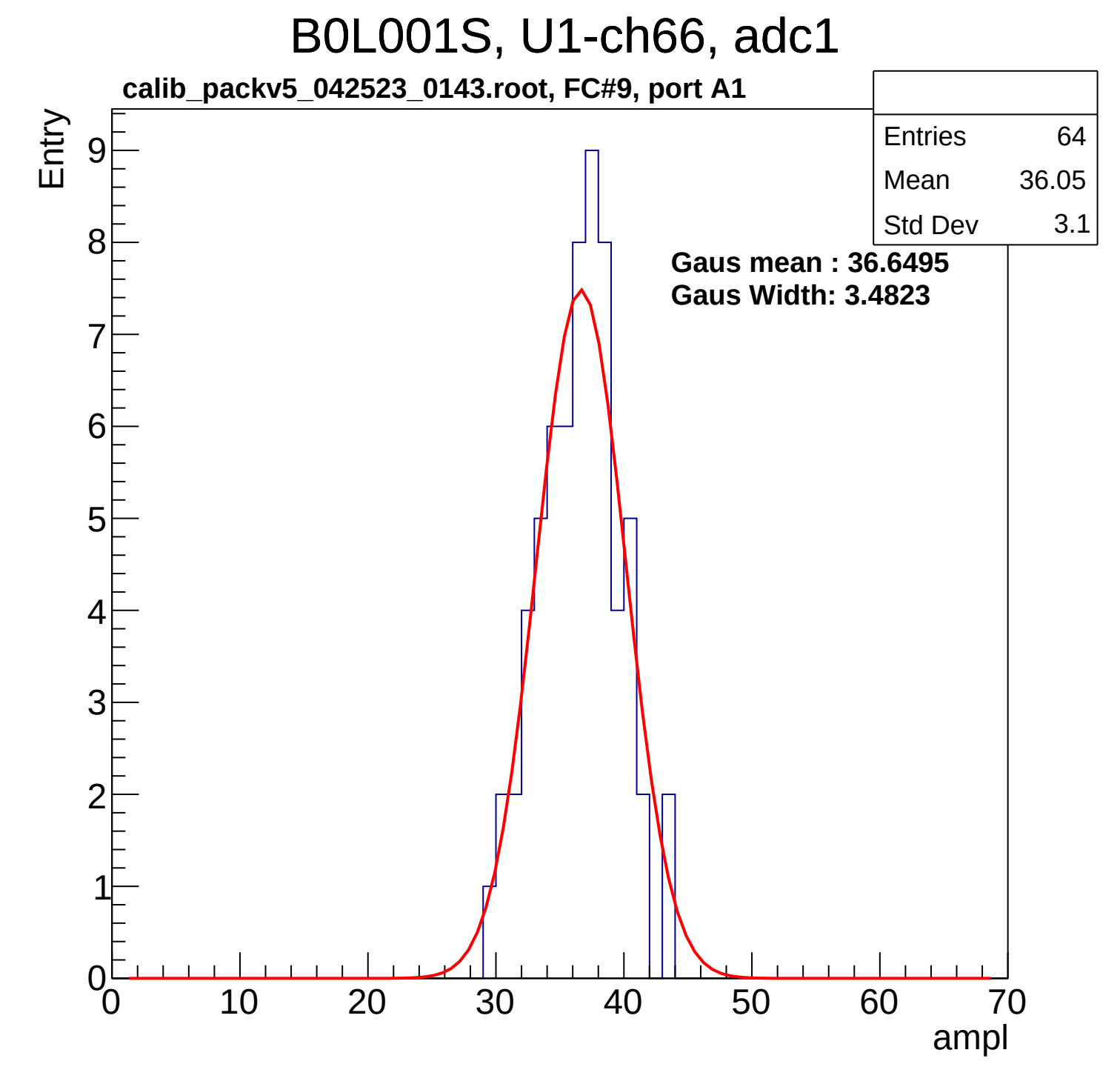

B0L001S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.48
Std Dev 2.954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

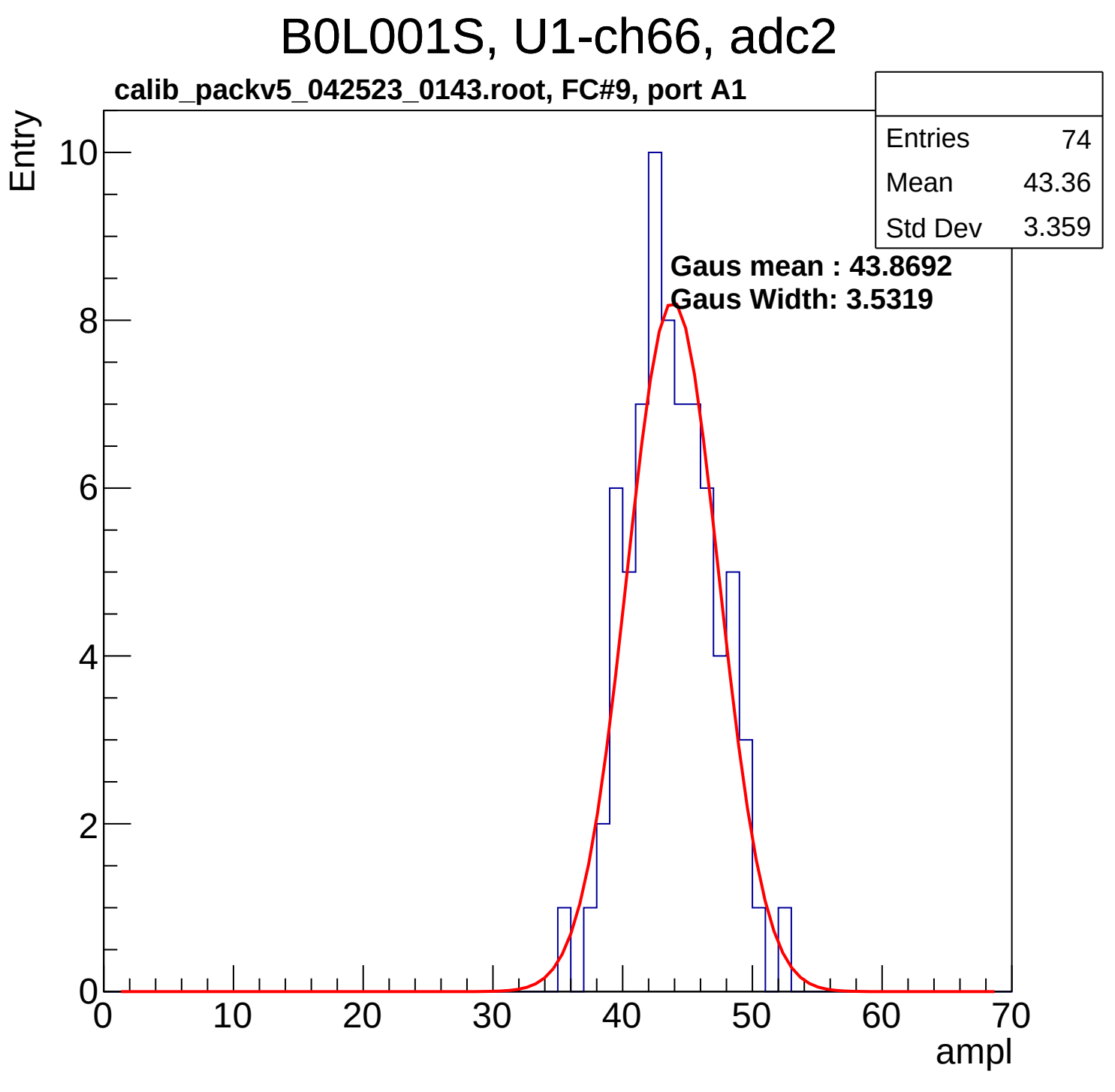

B0L001S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 58.85
Std Dev 9.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

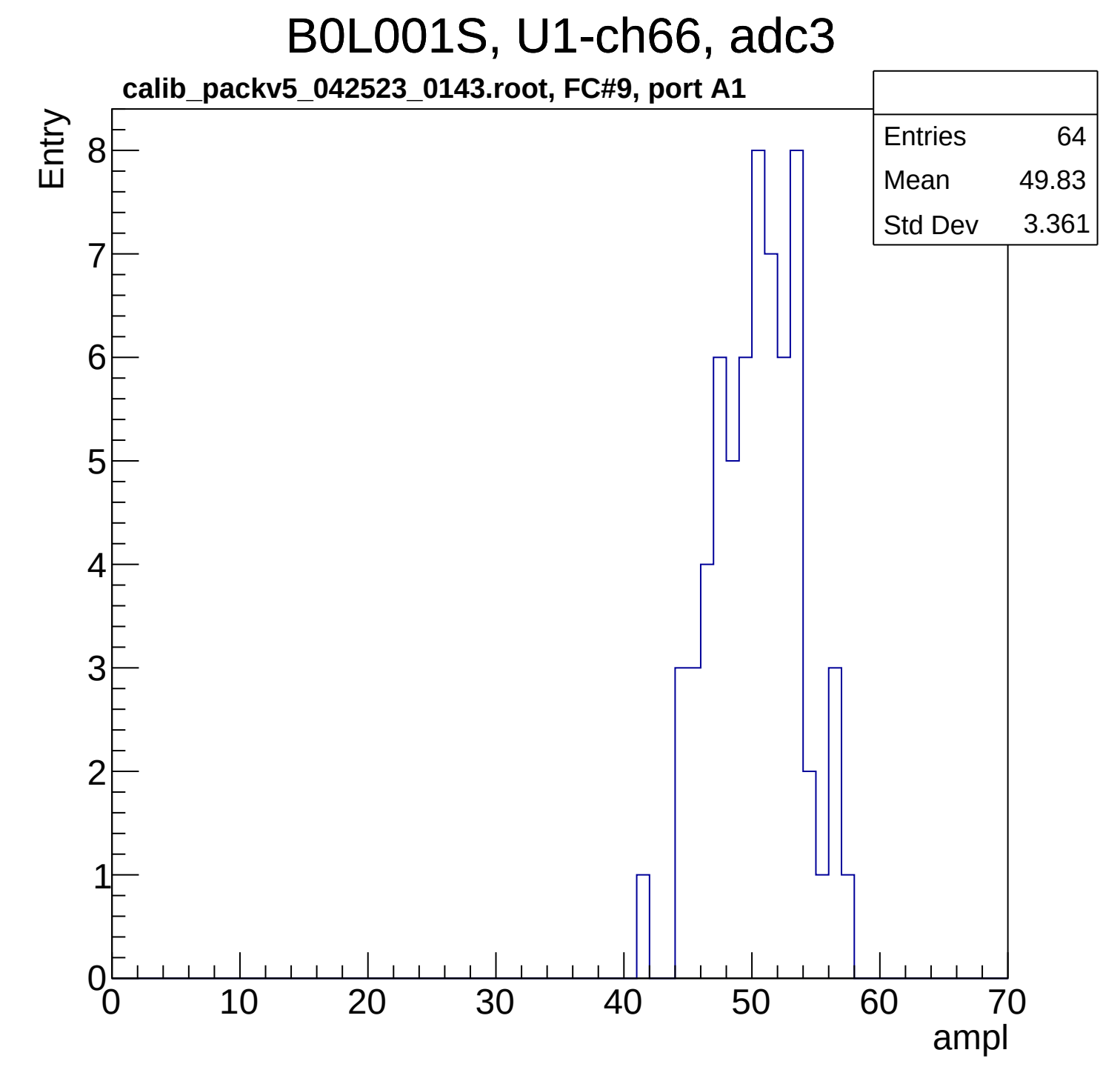

B0L001S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

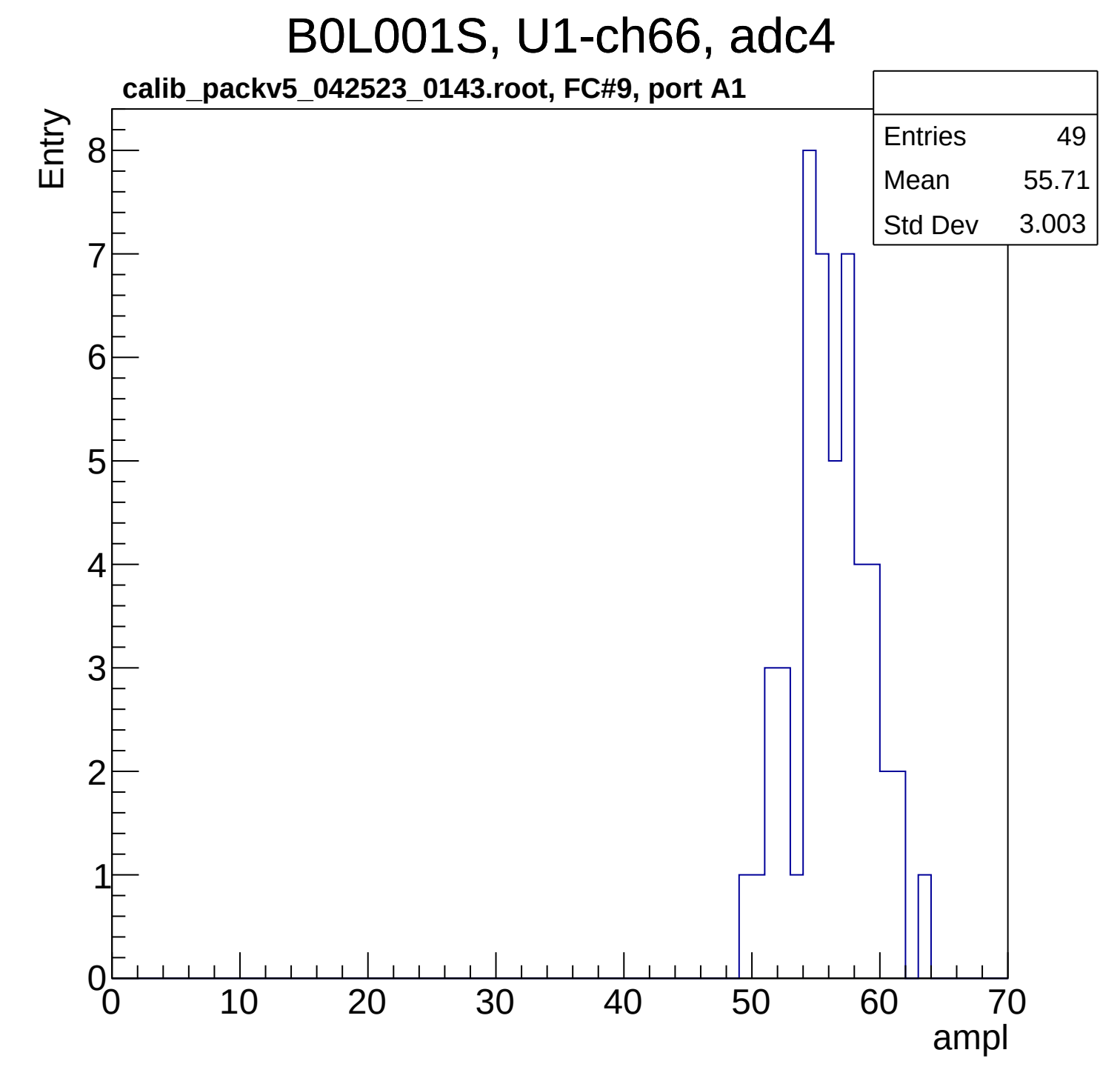

B0L001S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

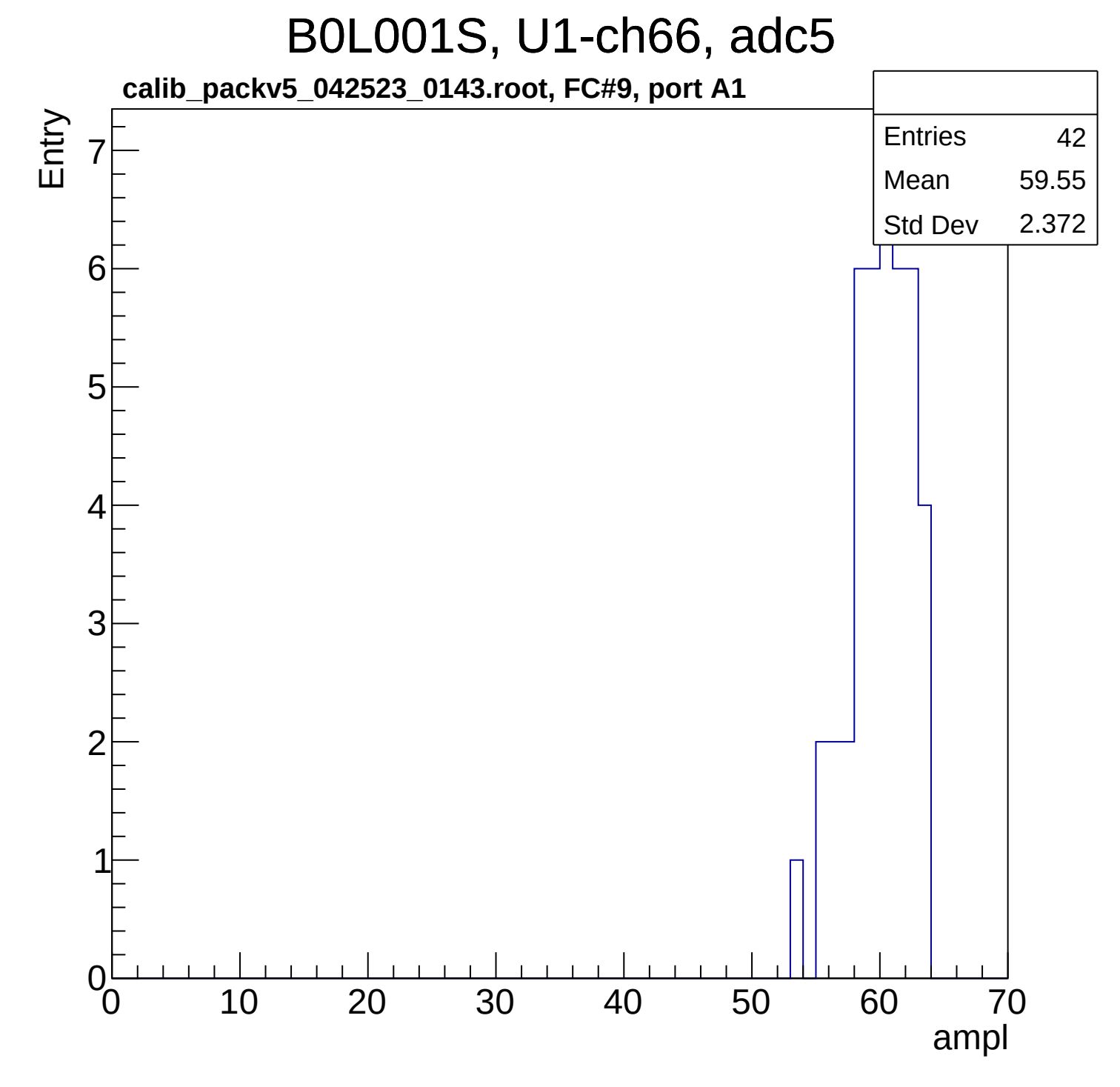

B0L001S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 29.41
Std Dev 3.255
Gaus mean : 29.9349
Gaus Width: 4.0144
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

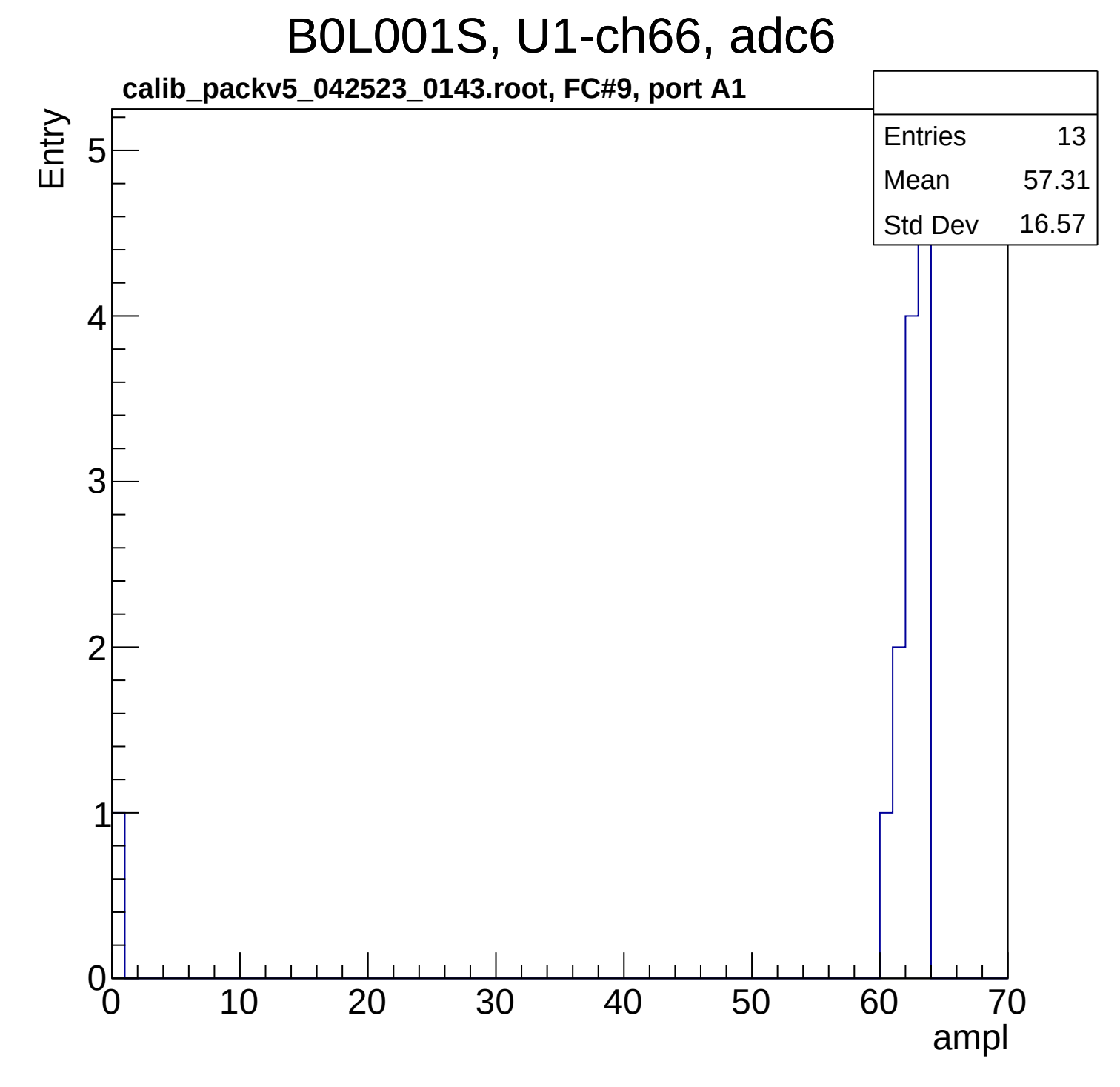

B0L001S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.05
Std Dev 3.1
Gaus mean : 36.6495
Gaus Width: 3.4823
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

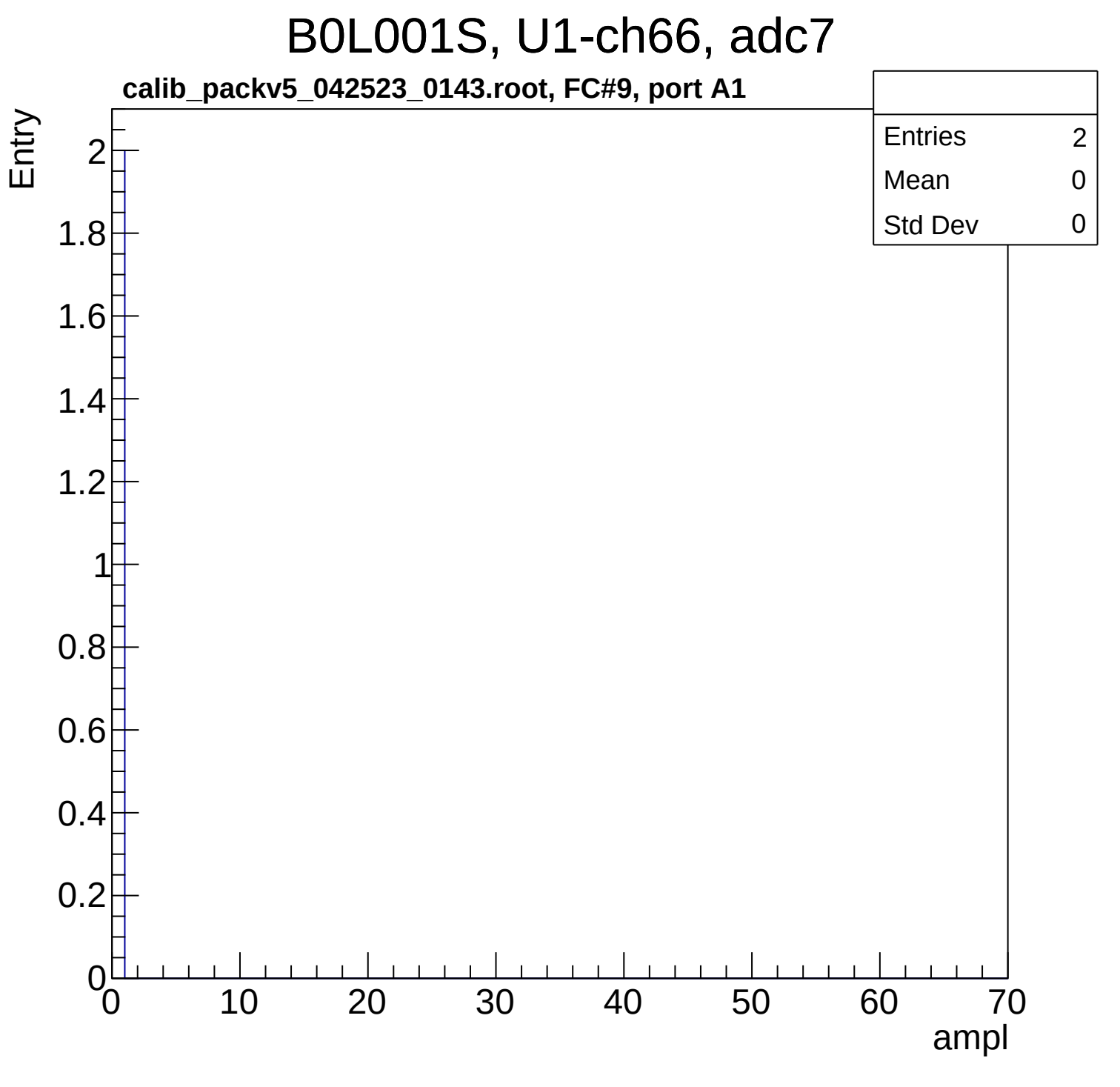

B0L001S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.36
Std Dev 3.359
Gaus mean : 43.8692
Gaus Width: 3.5319
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

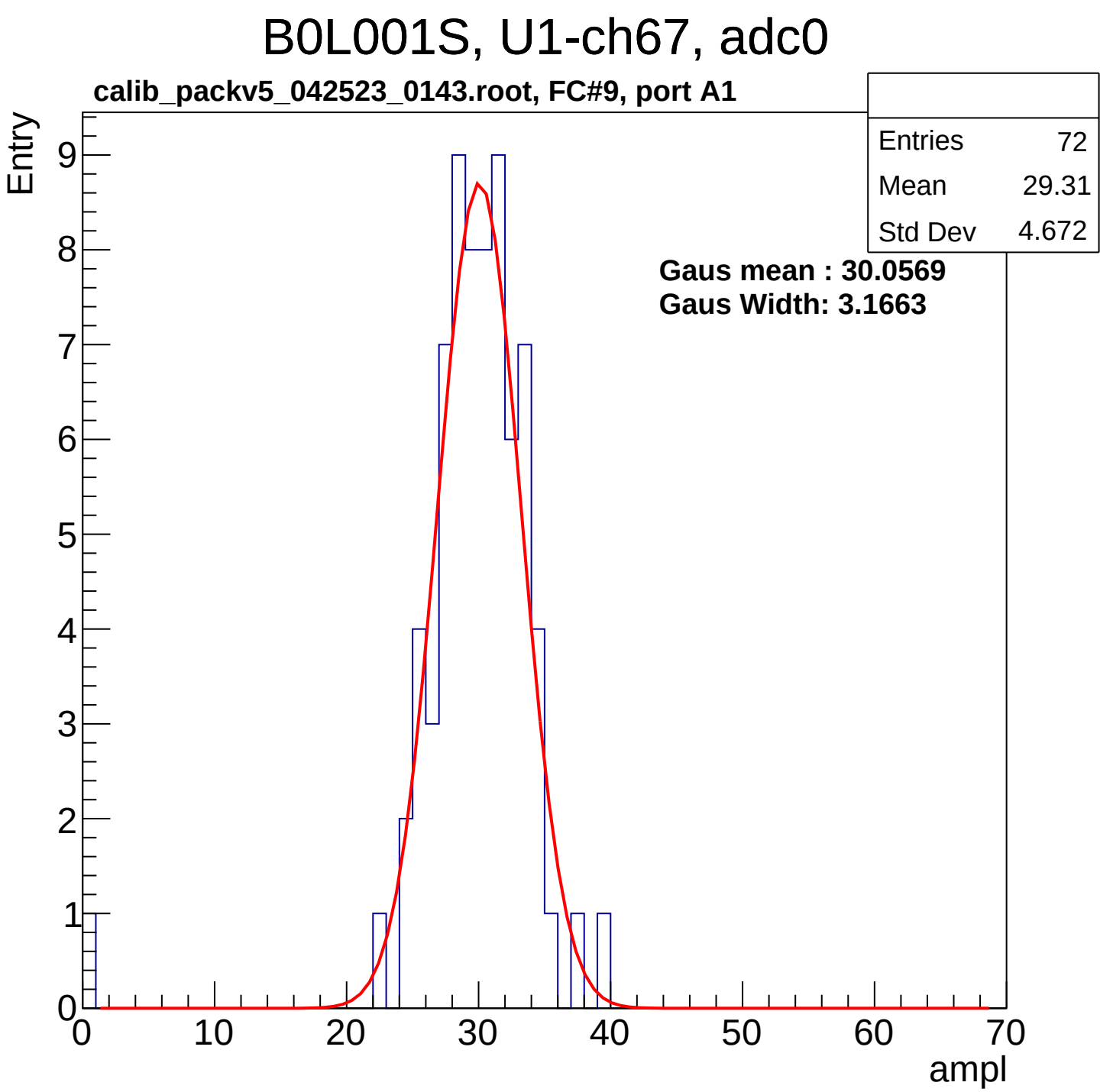

B0L001S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.83
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

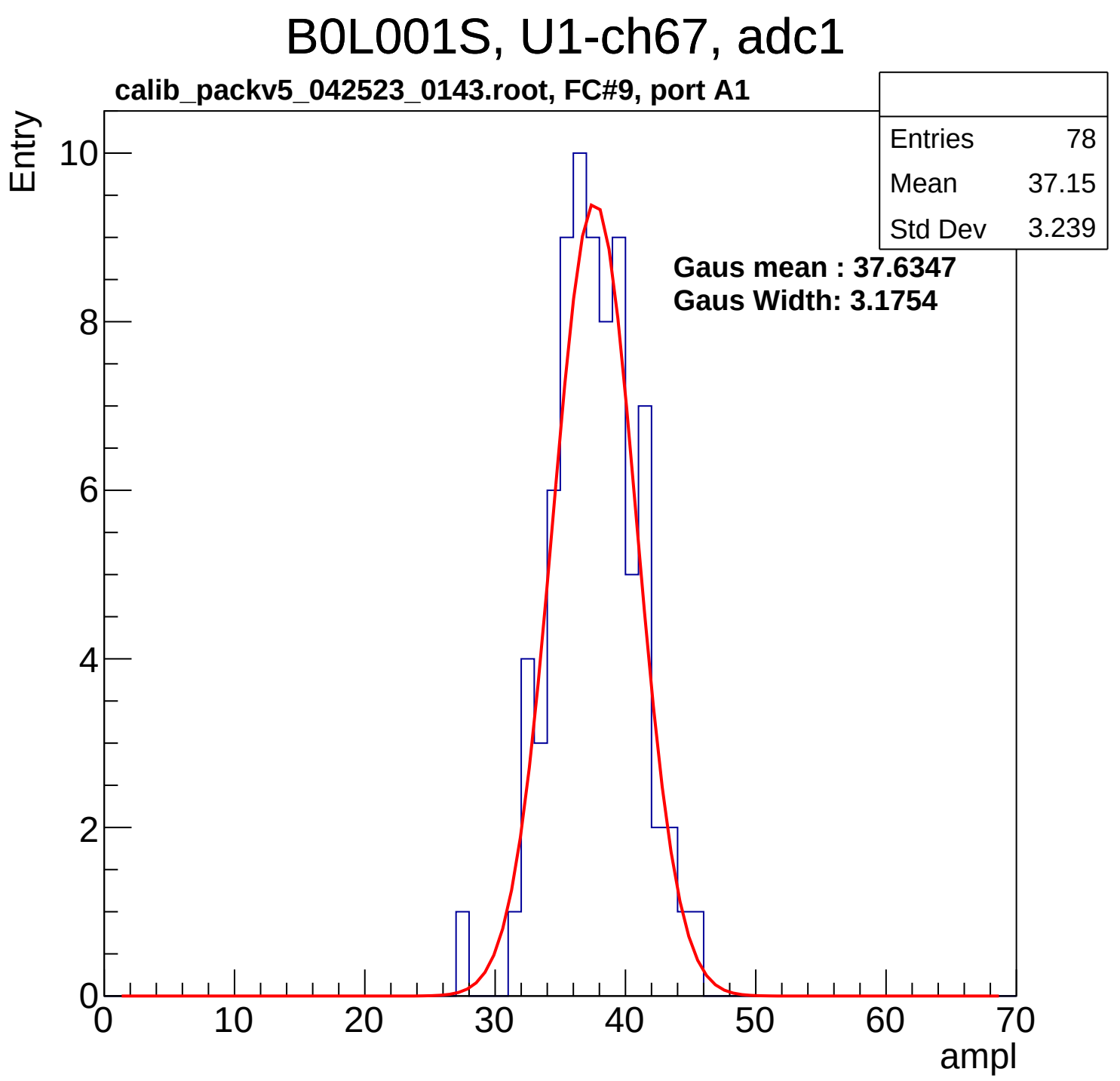

B0L001S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 55.71
Std Dev 3.003
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

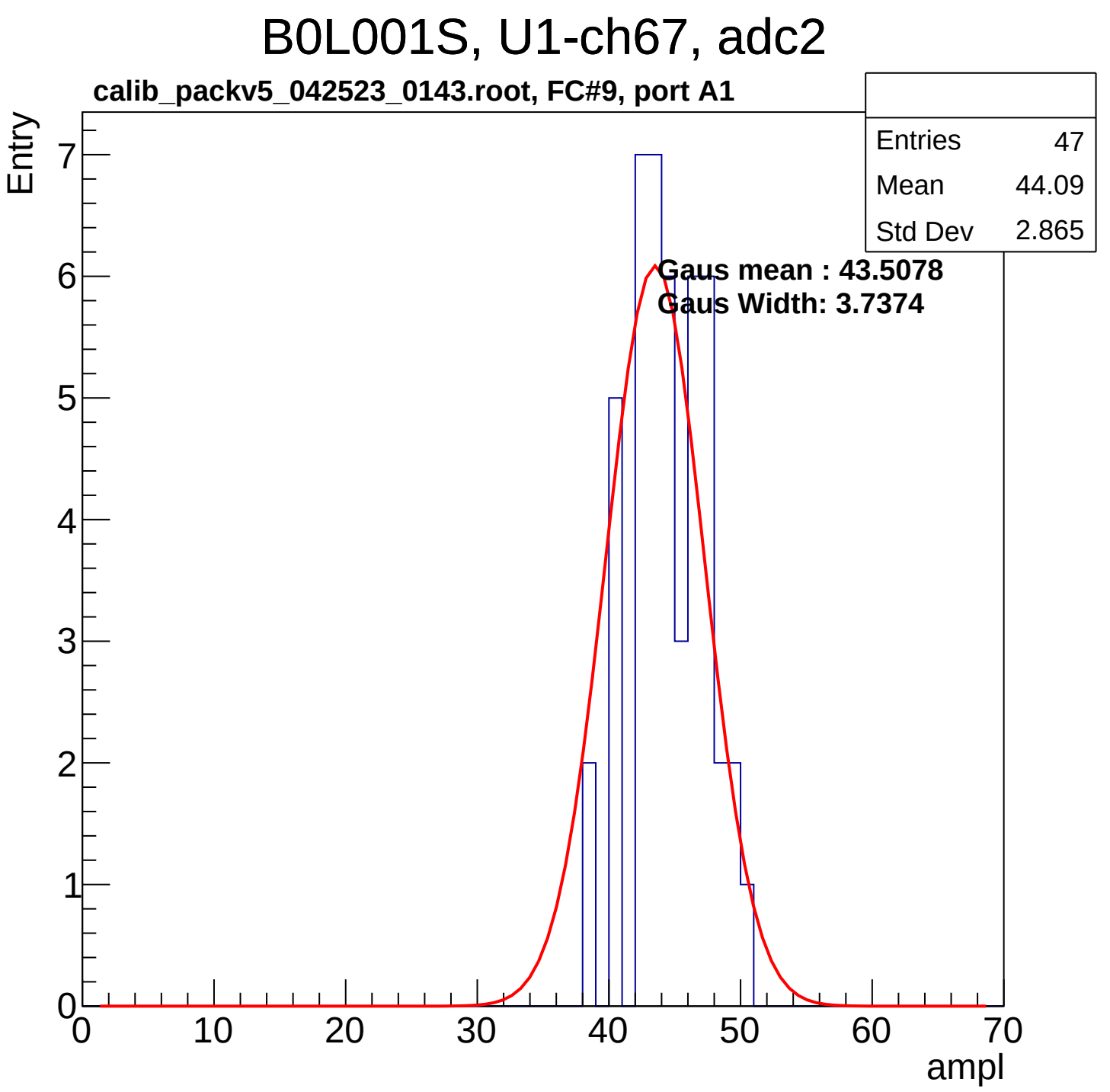

B0L001S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.55
Std Dev 2.372
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

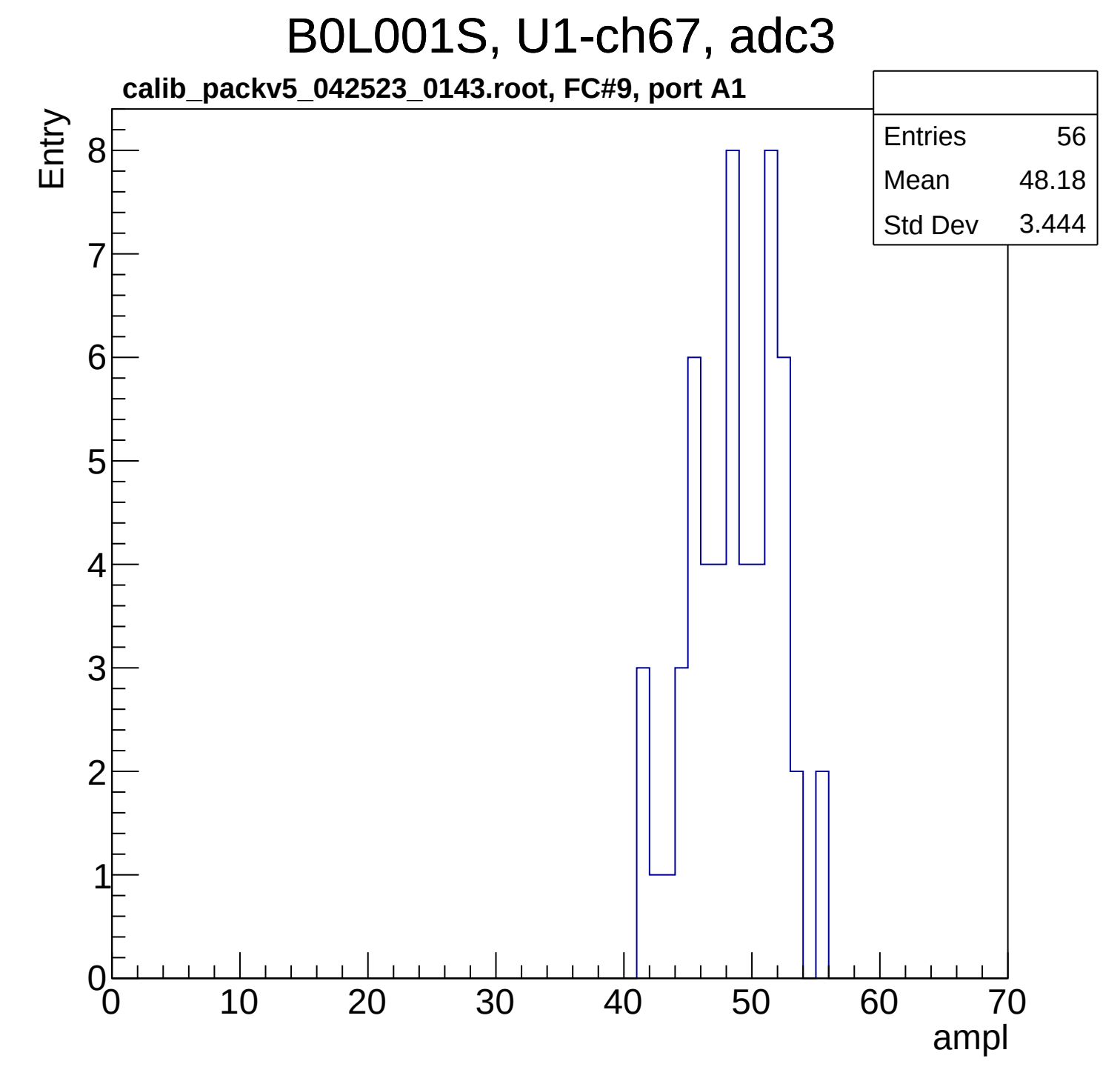

B0L001S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 57.31
Std Dev 16.57
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

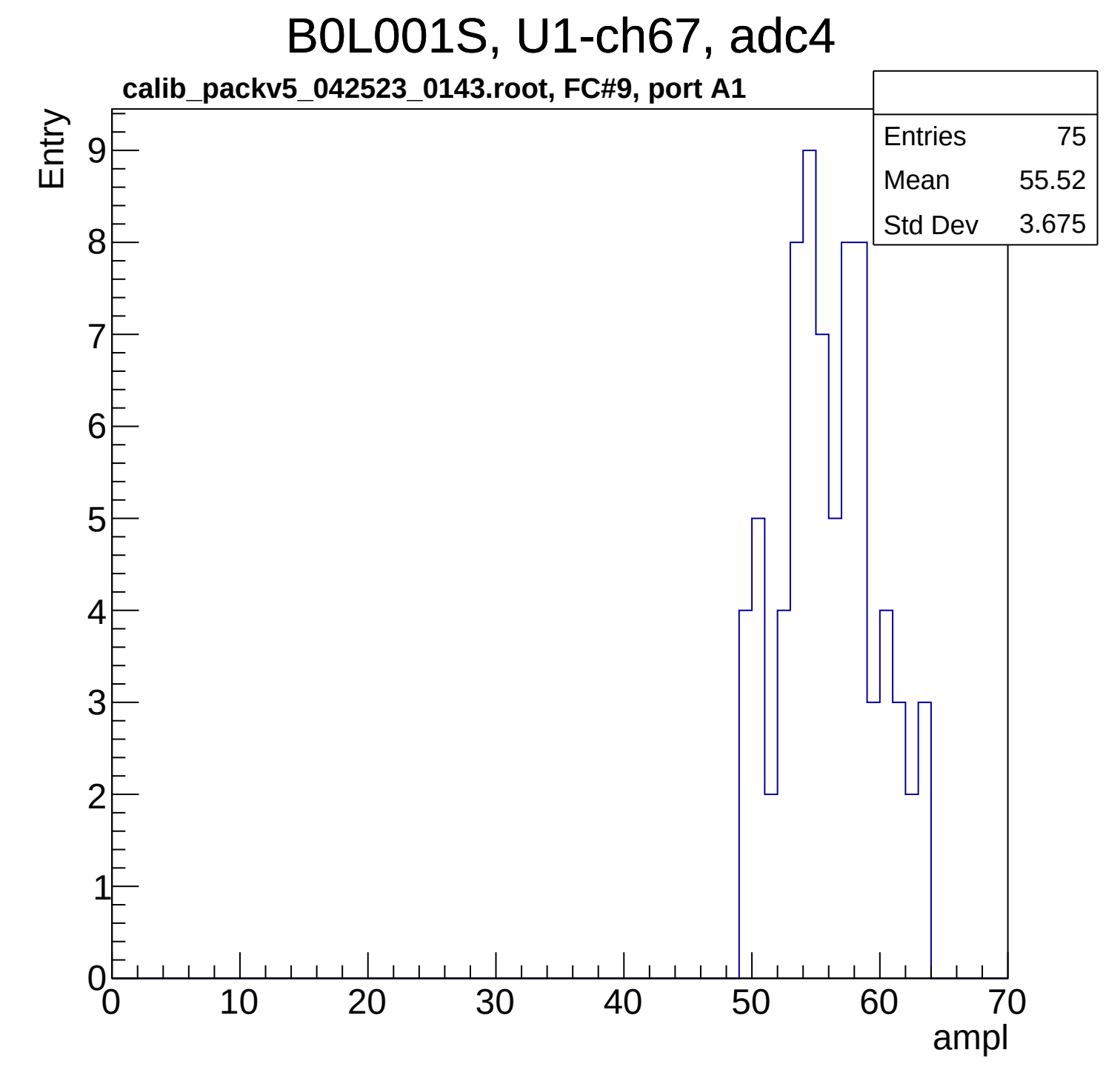

B0L001S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

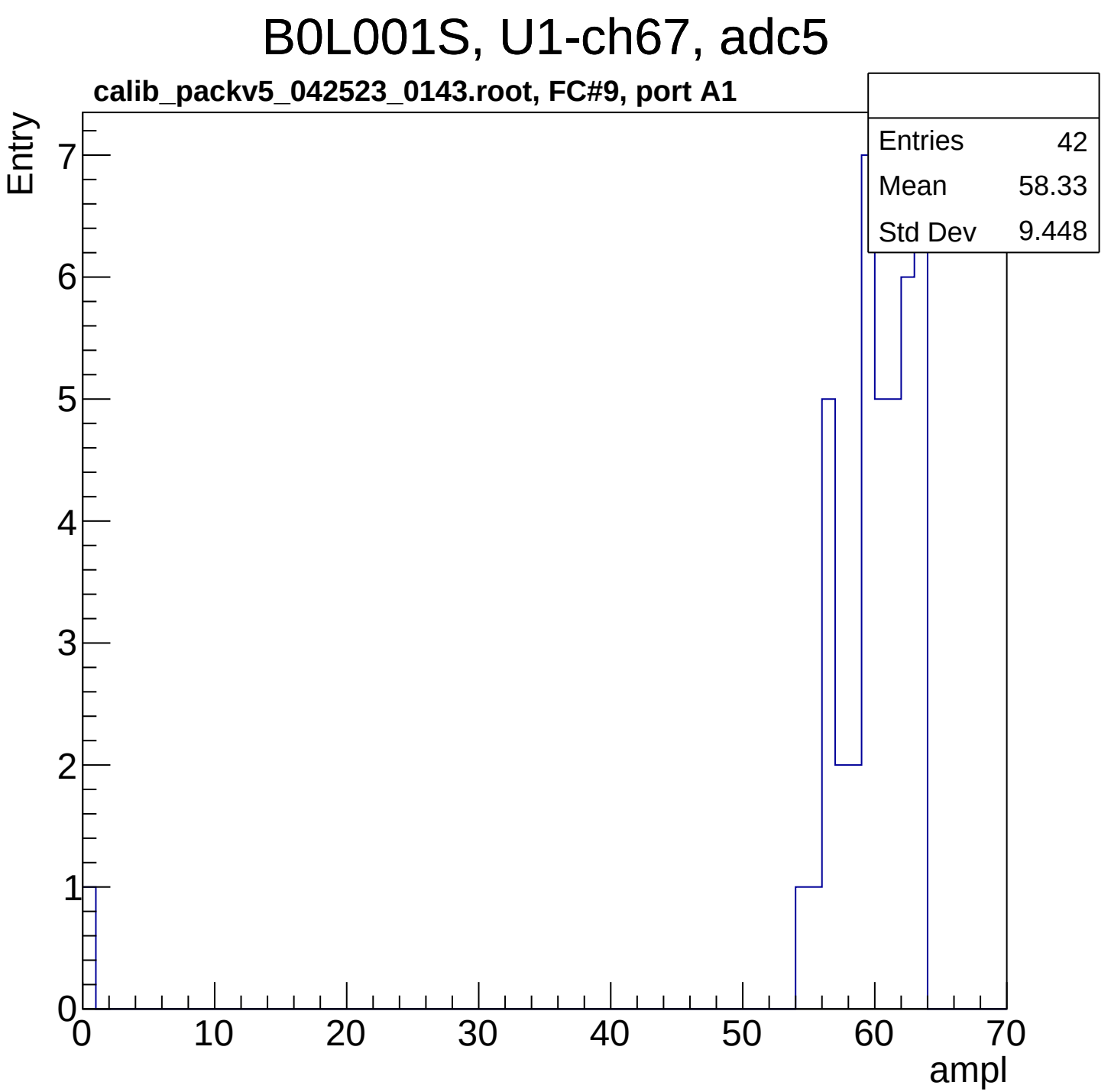

B0L001S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 29.31
Std Dev 4.672
Gaus mean : 30.0569
Gaus Width: 3.1663
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

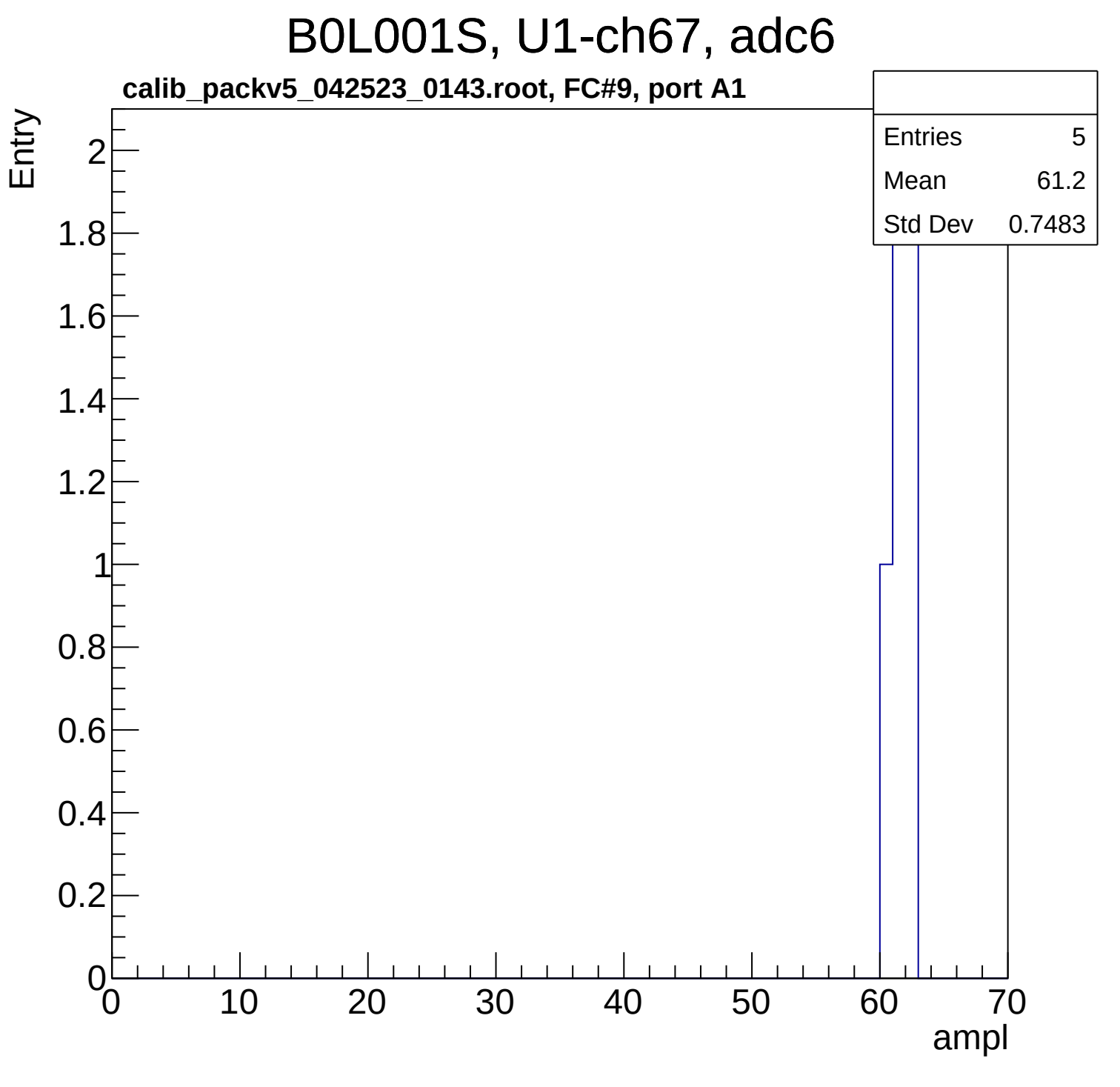

B0L001S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37.15
Std Dev 3.239
Gaus mean : 37.6347
Gaus Width: 3.1754
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

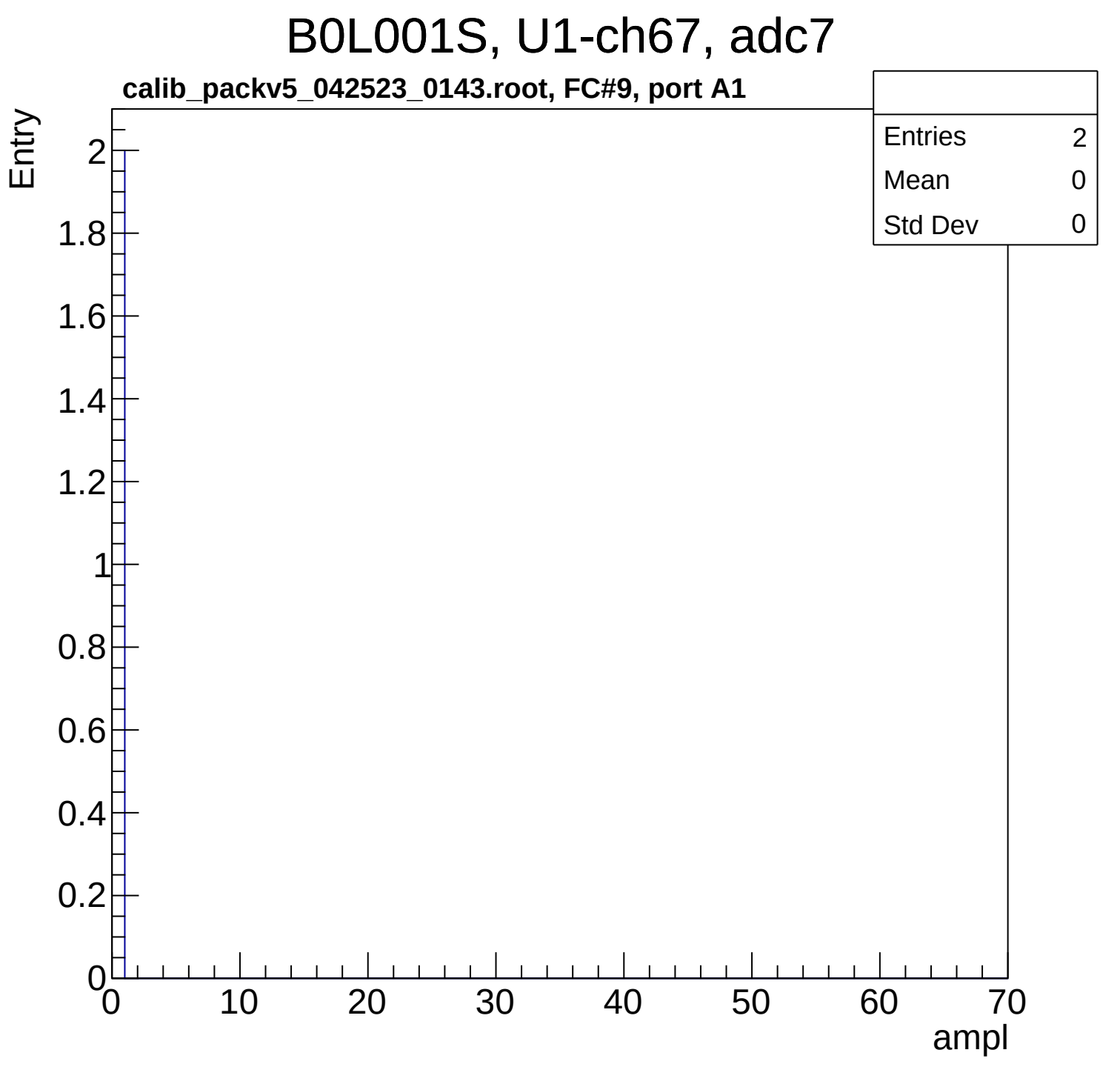

B0L001S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 44.09
Std Dev 2.865
Gaus mean : 43.5078
Gaus Width: 3.7374
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

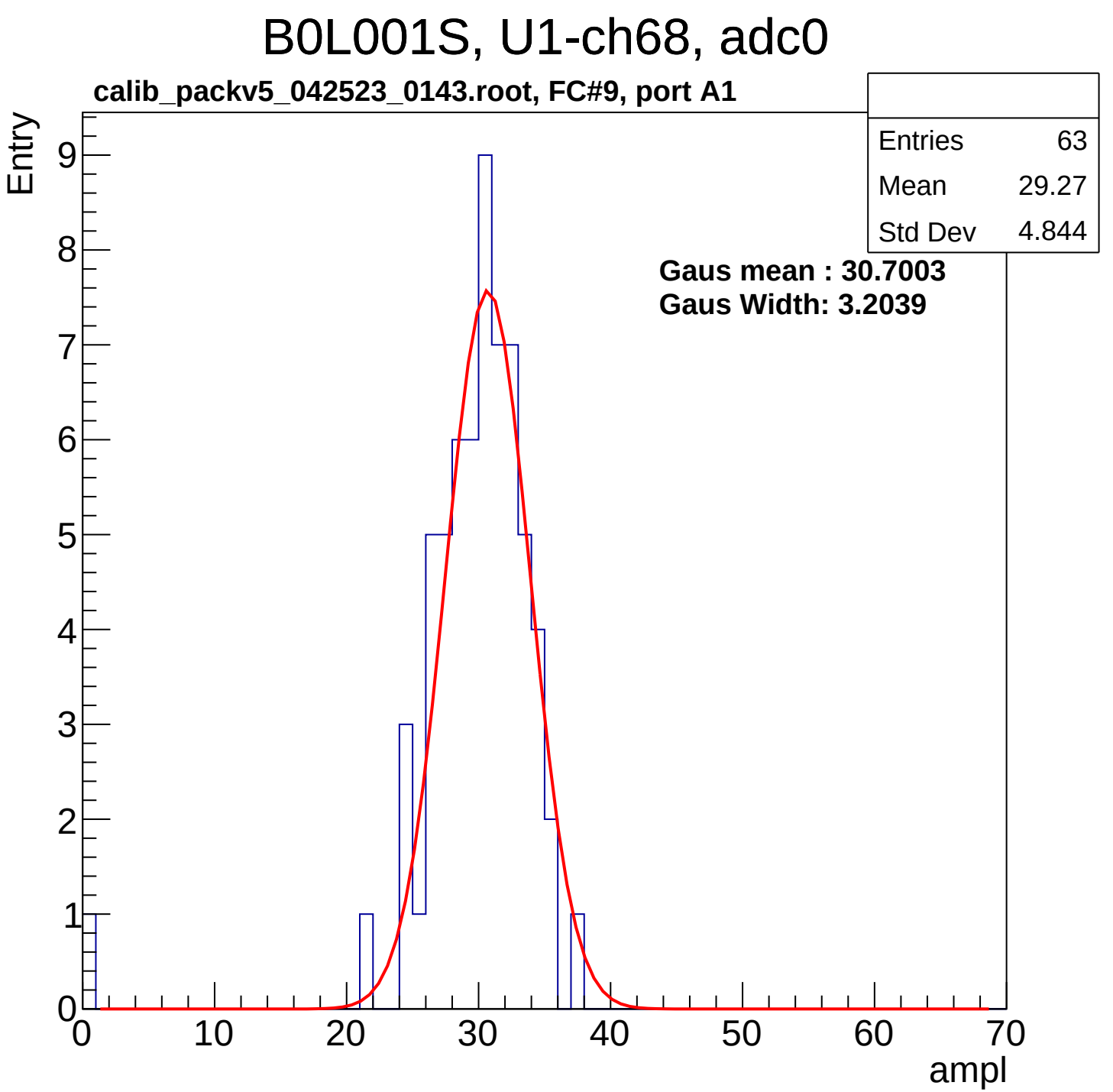

B0L001S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 48.18
Std Dev 3.444
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

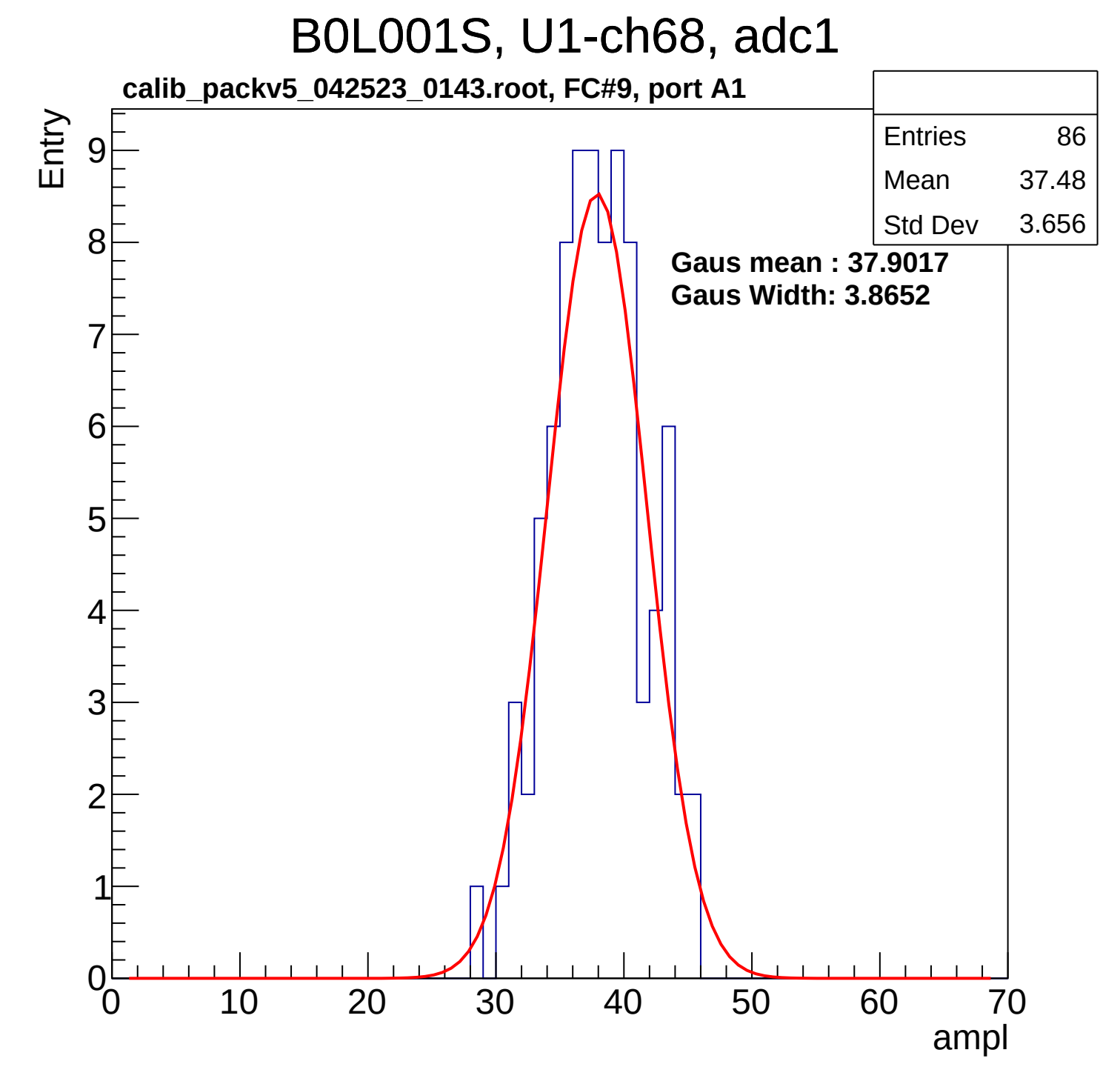

B0L001S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 55.52
Std Dev 3.675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

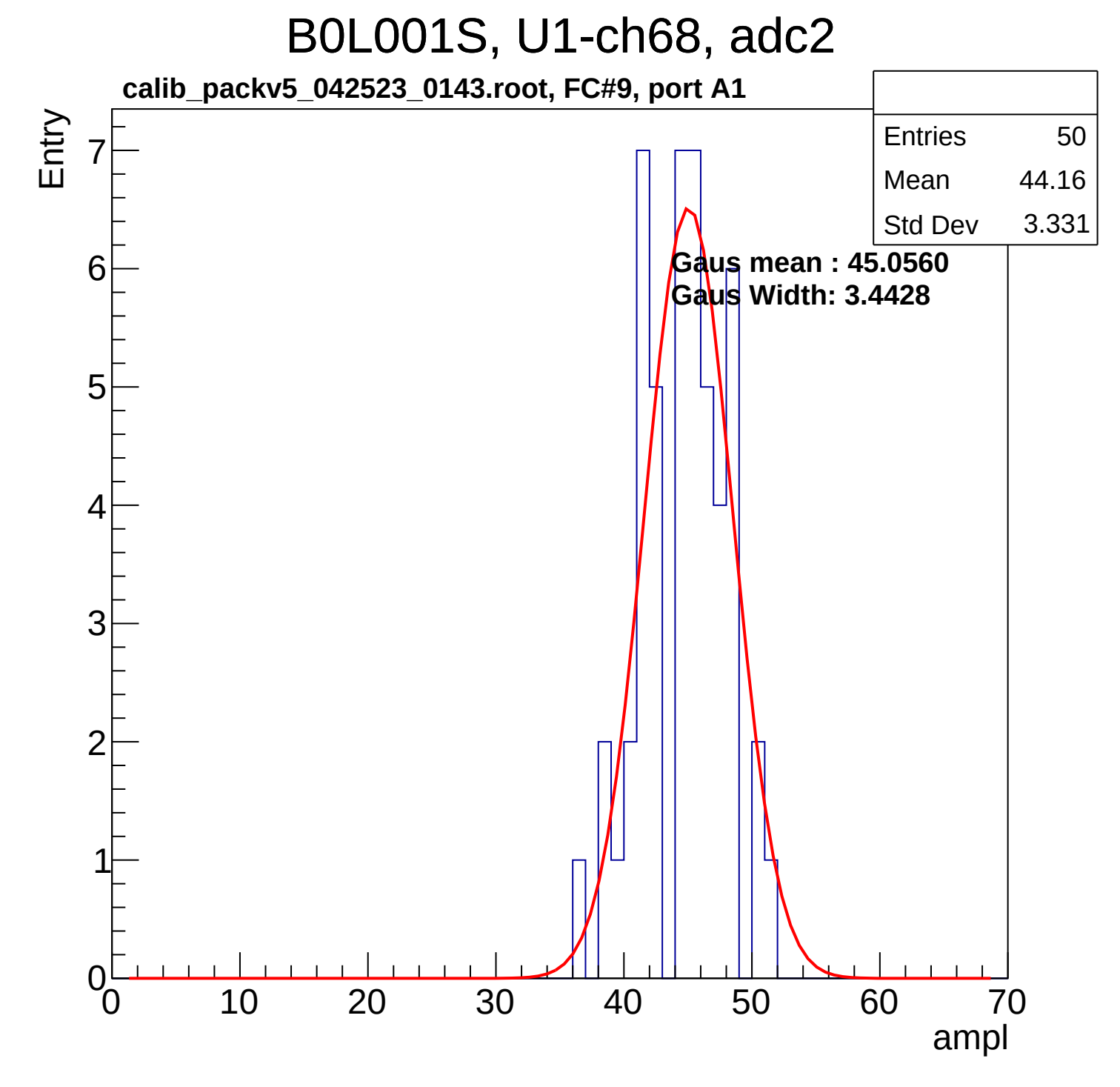

B0L001S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 58.33
Std Dev 9.448
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

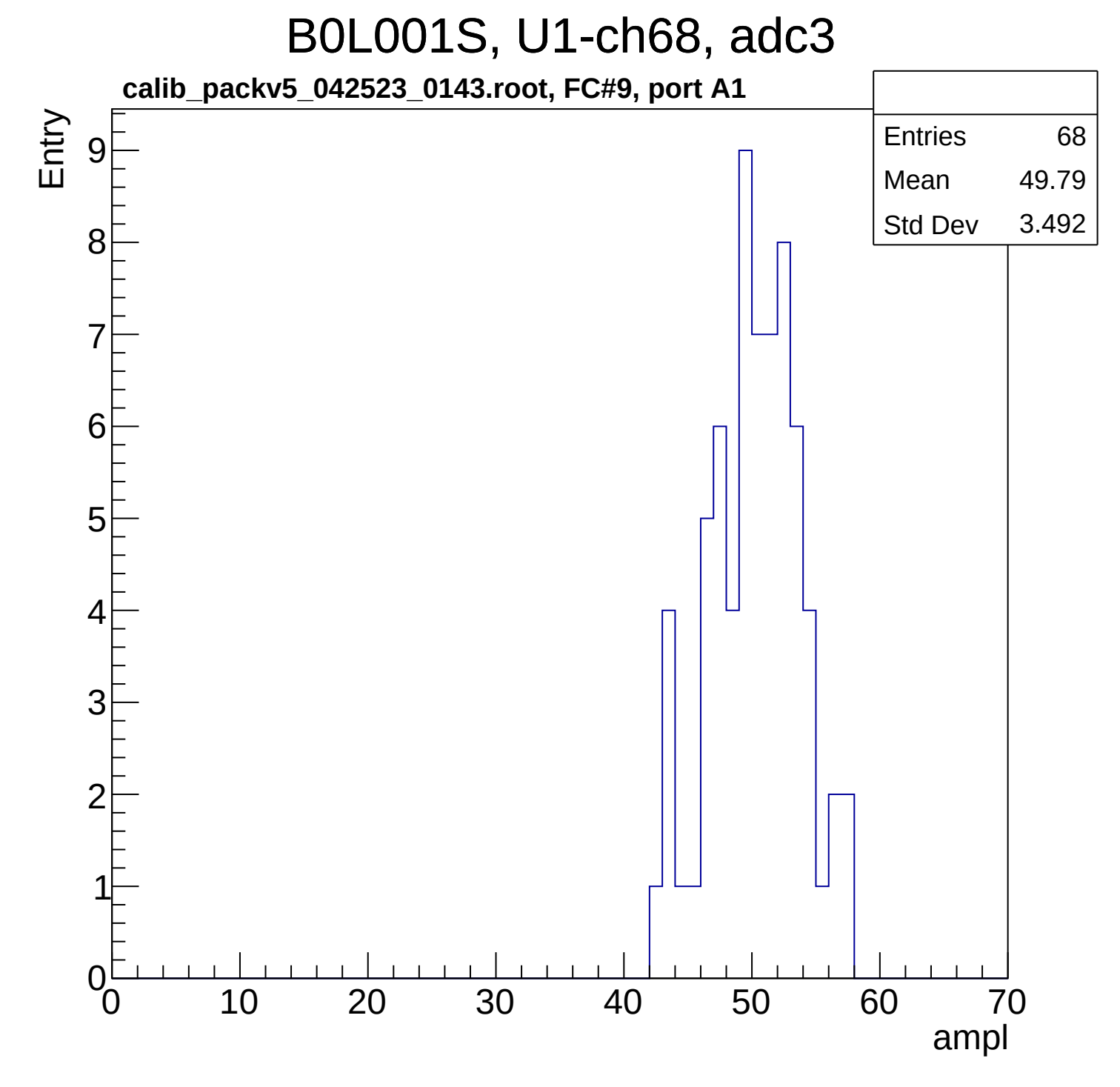

B0L001S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.27
Std Dev 4.844
Gaus mean : 30.7003
Gaus Width: 3.2039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 37.48
Std Dev 3.656
Gaus mean : 37.9017
Gaus Width: 3.8652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 44.16
Std Dev 3.331
Gaus mean : 45.0560
Gaus Width: 3.4428
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.79
Std Dev 3.492
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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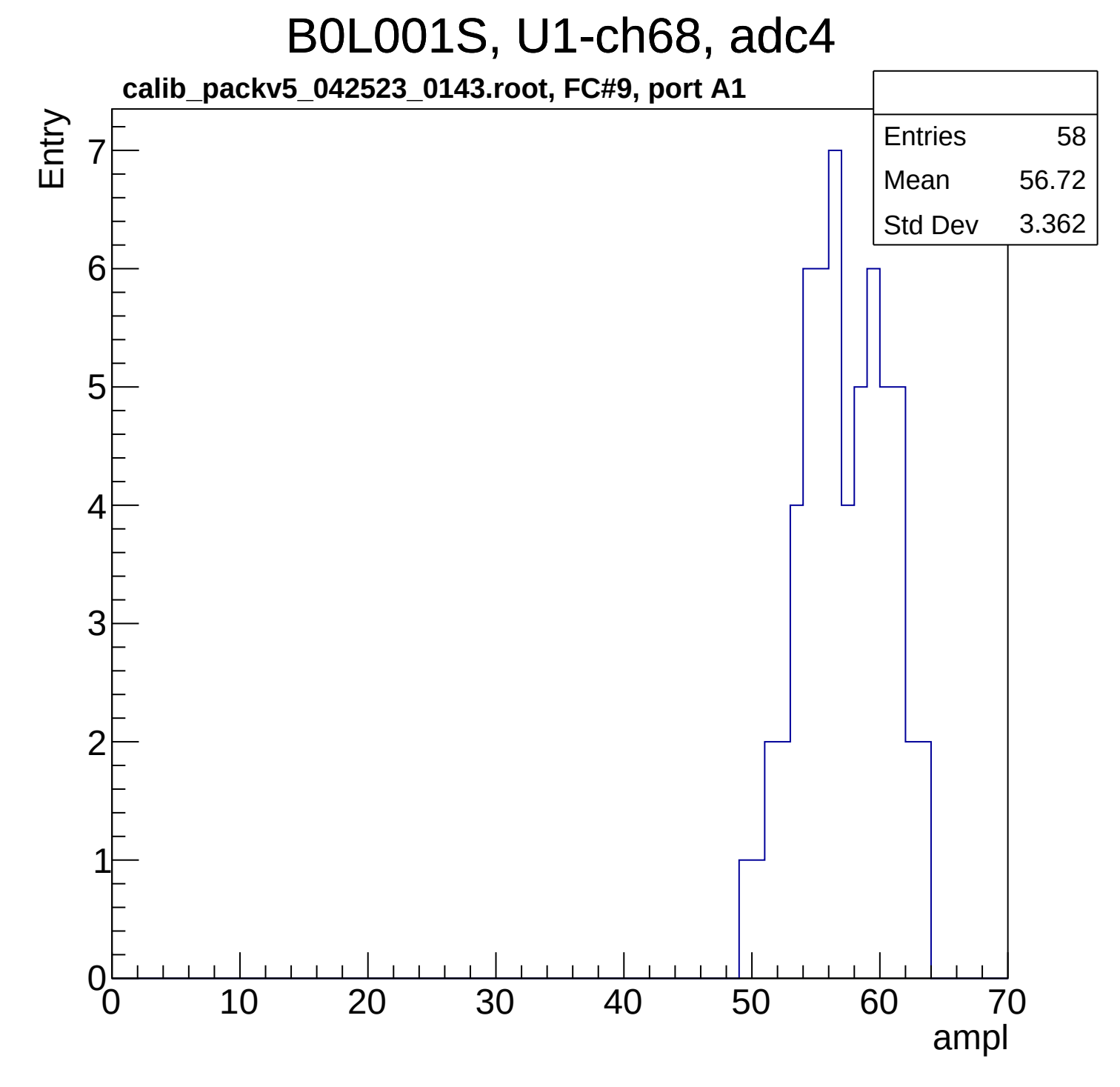
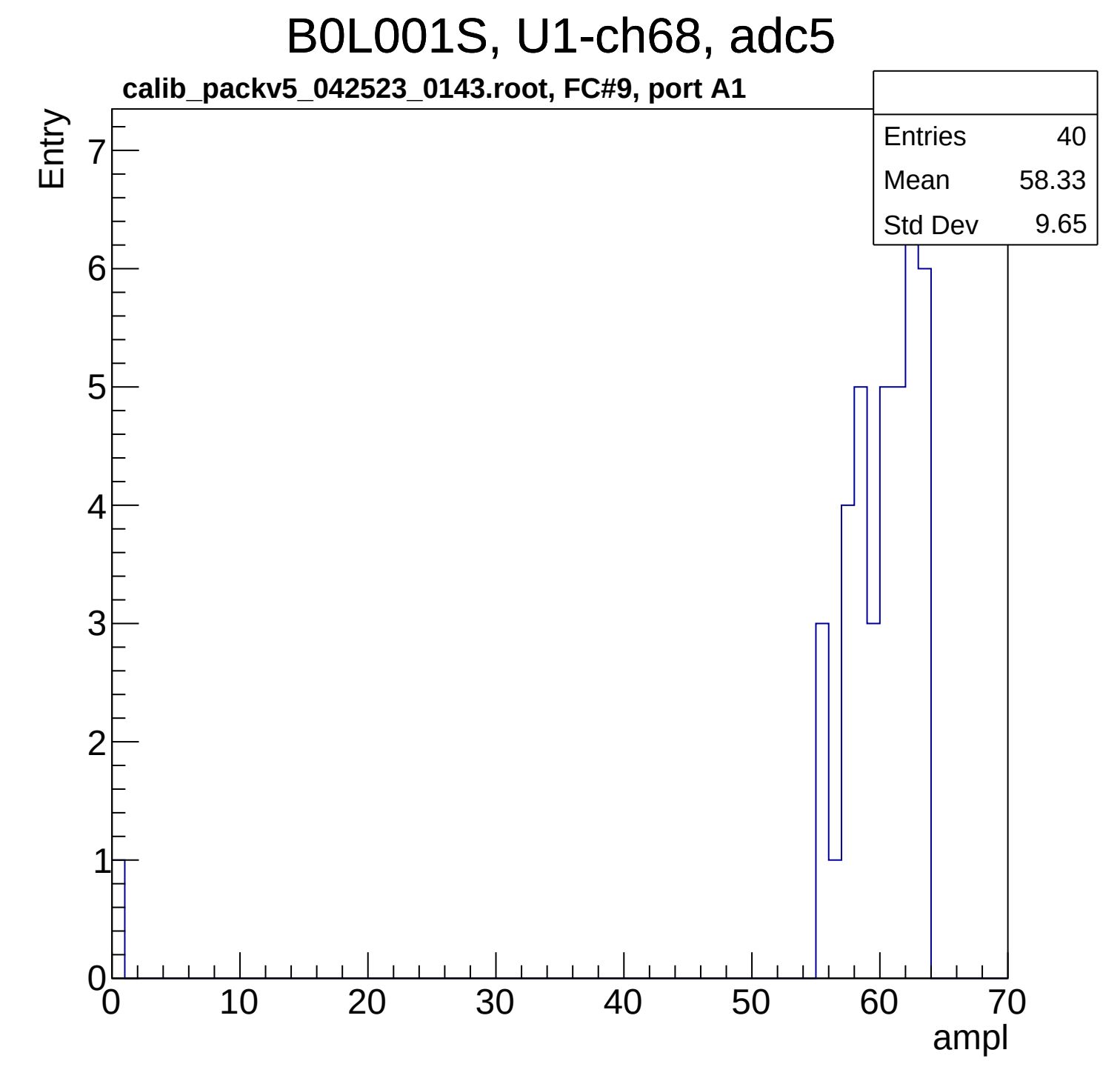
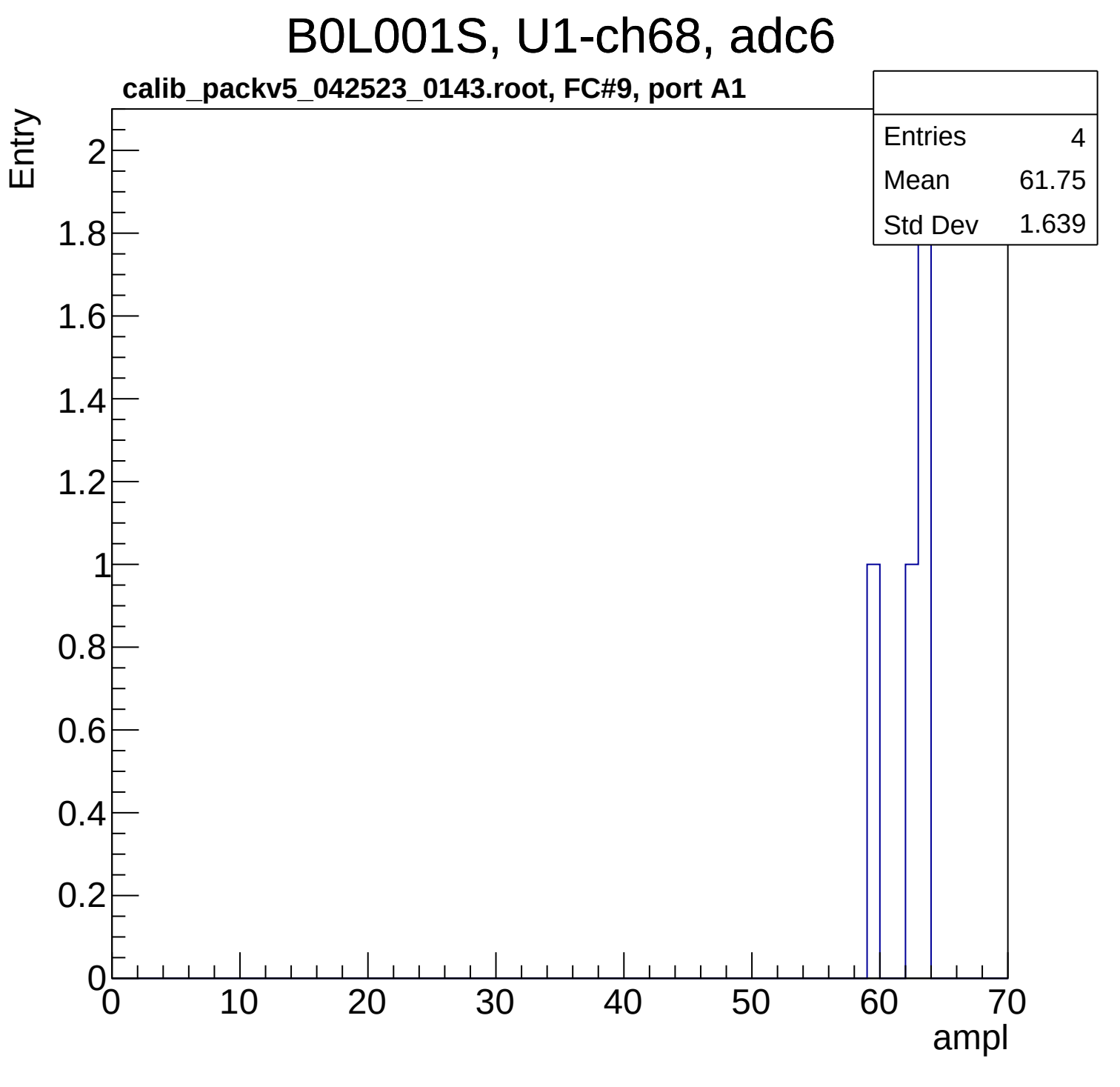
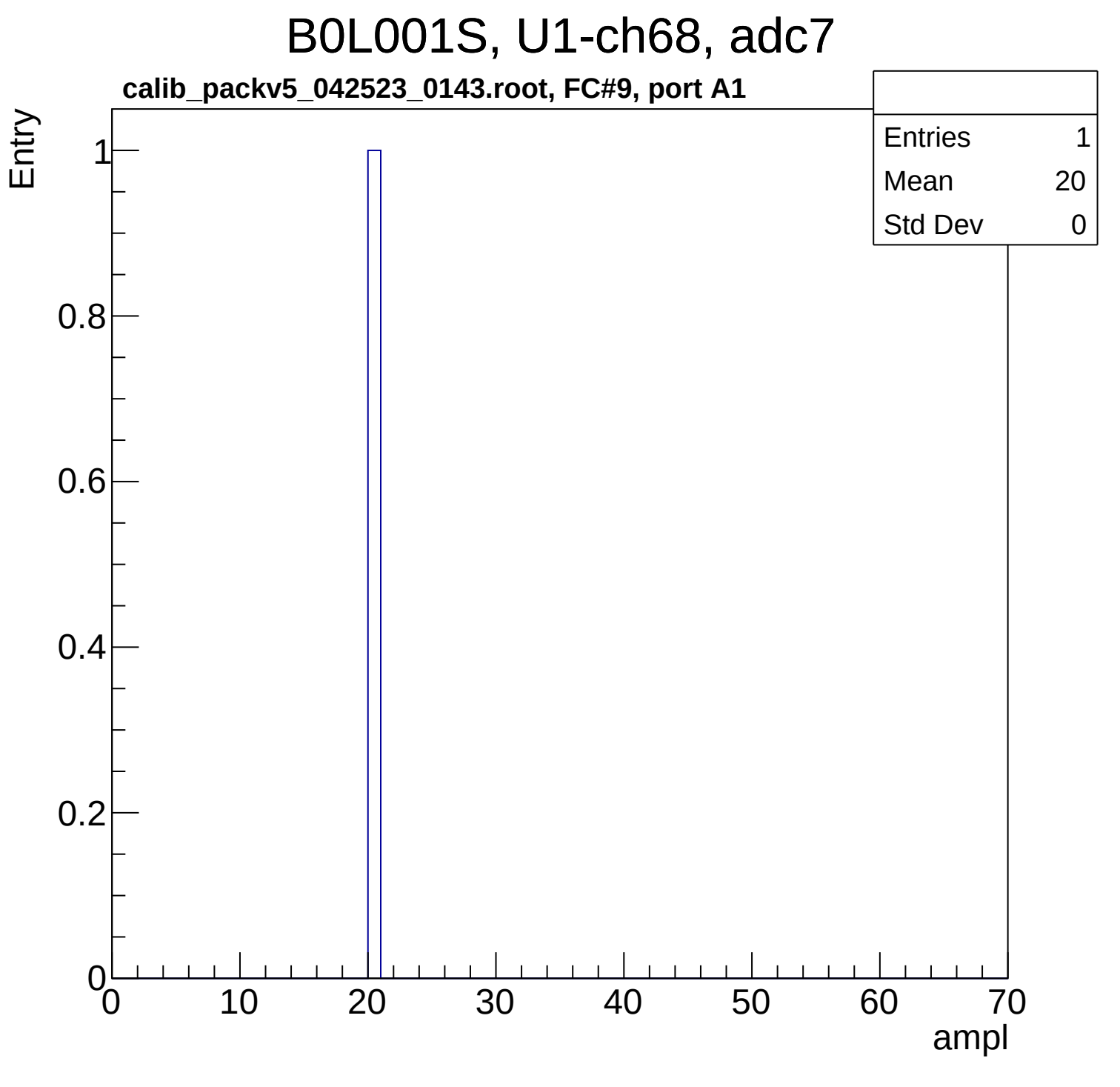

B0L001S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.72
Std Dev 3.362
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.33
Std Dev 9.65
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 20
Std Dev 0
Entry
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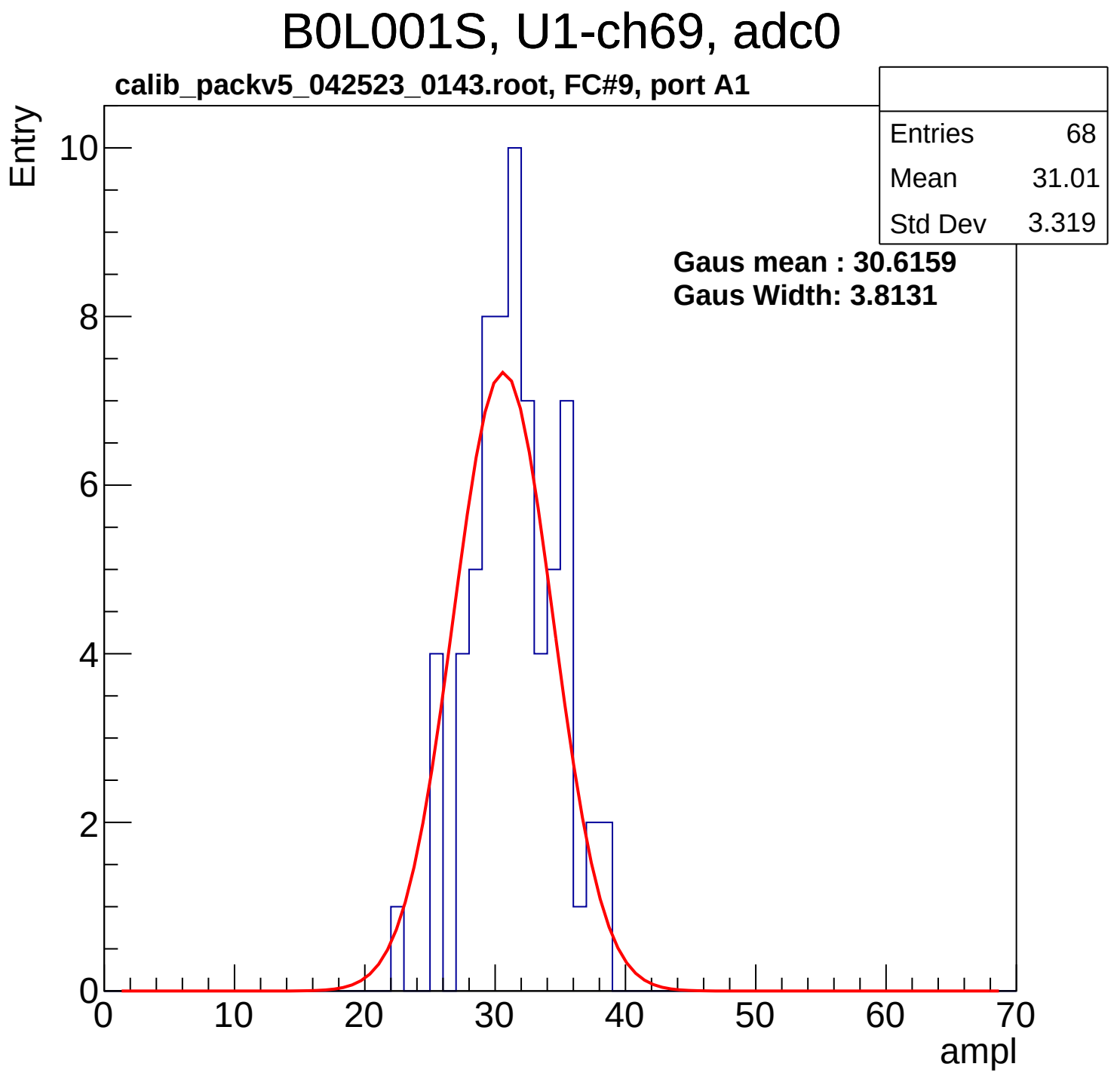
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 31.01
Std Dev 3.319
Gaus mean : 30.6159
Gaus Width: 3.8131
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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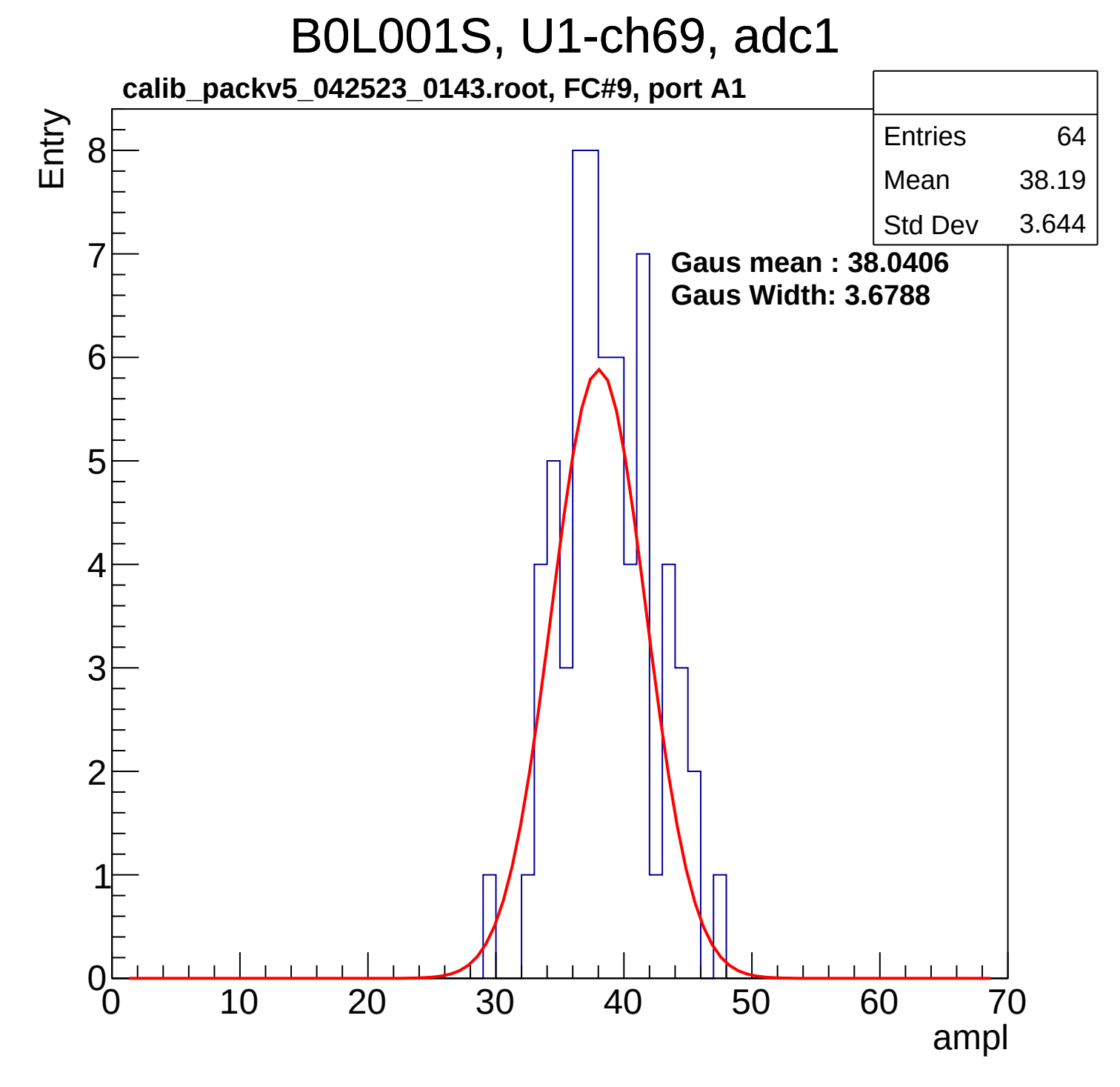
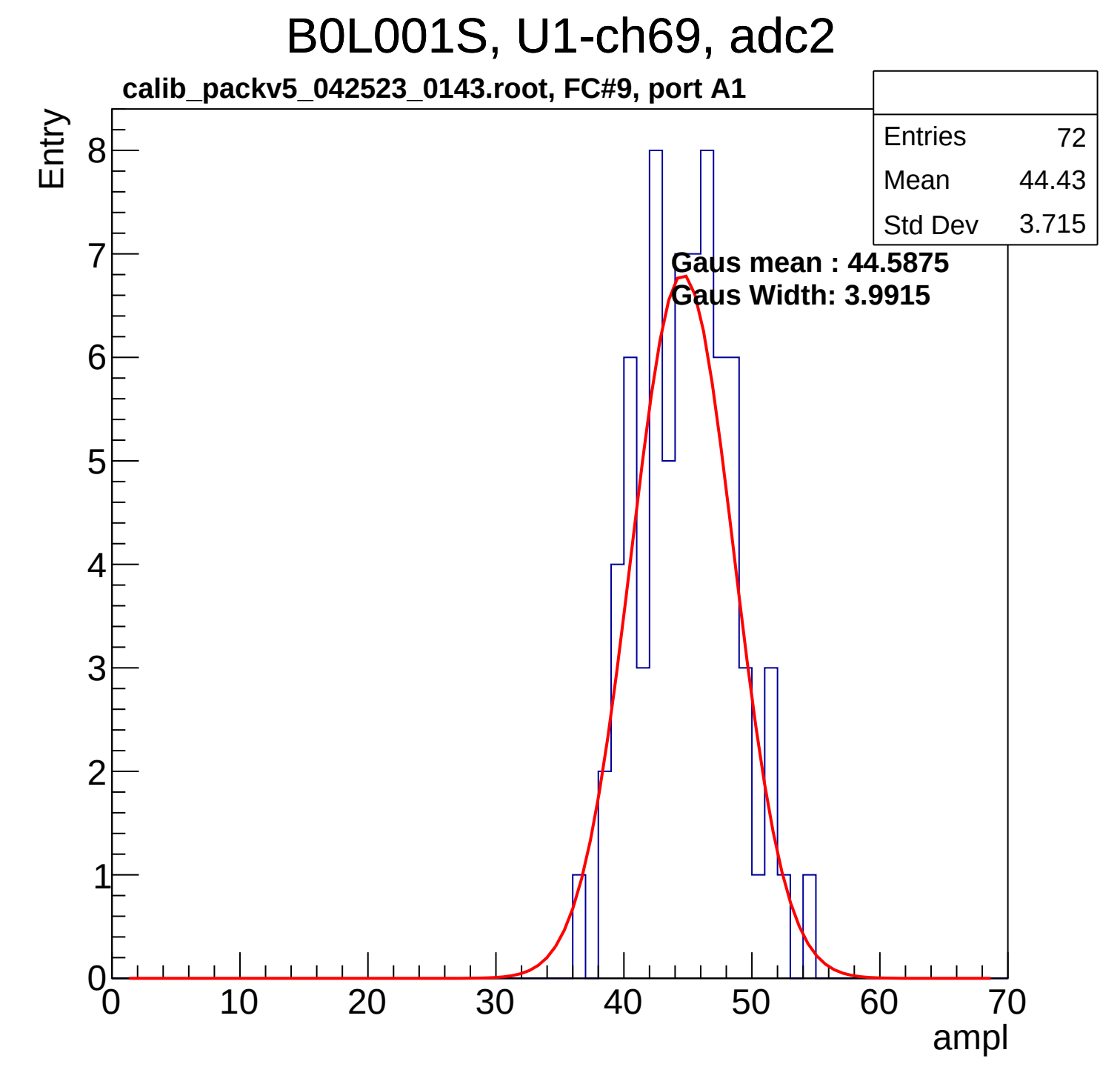
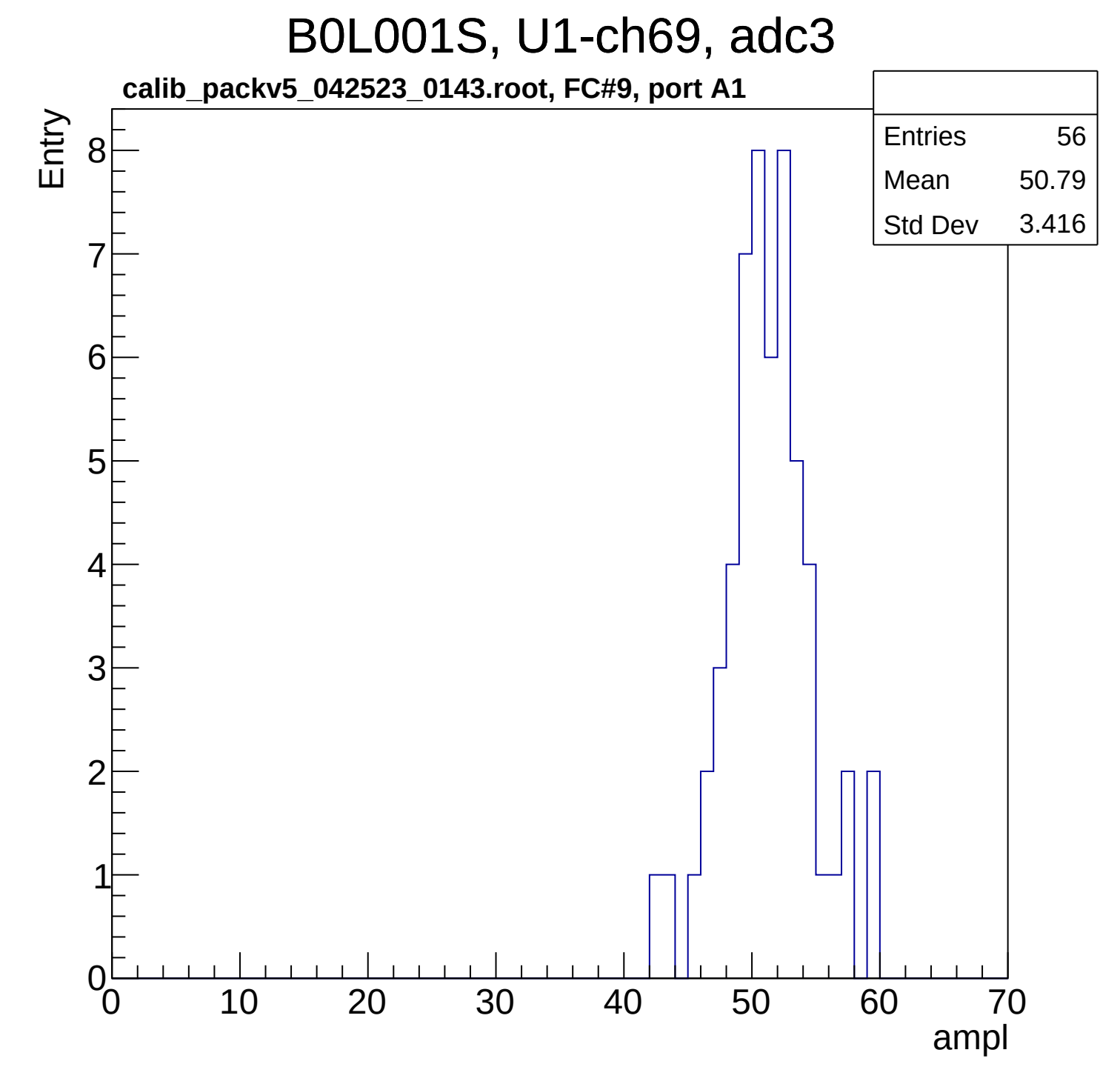
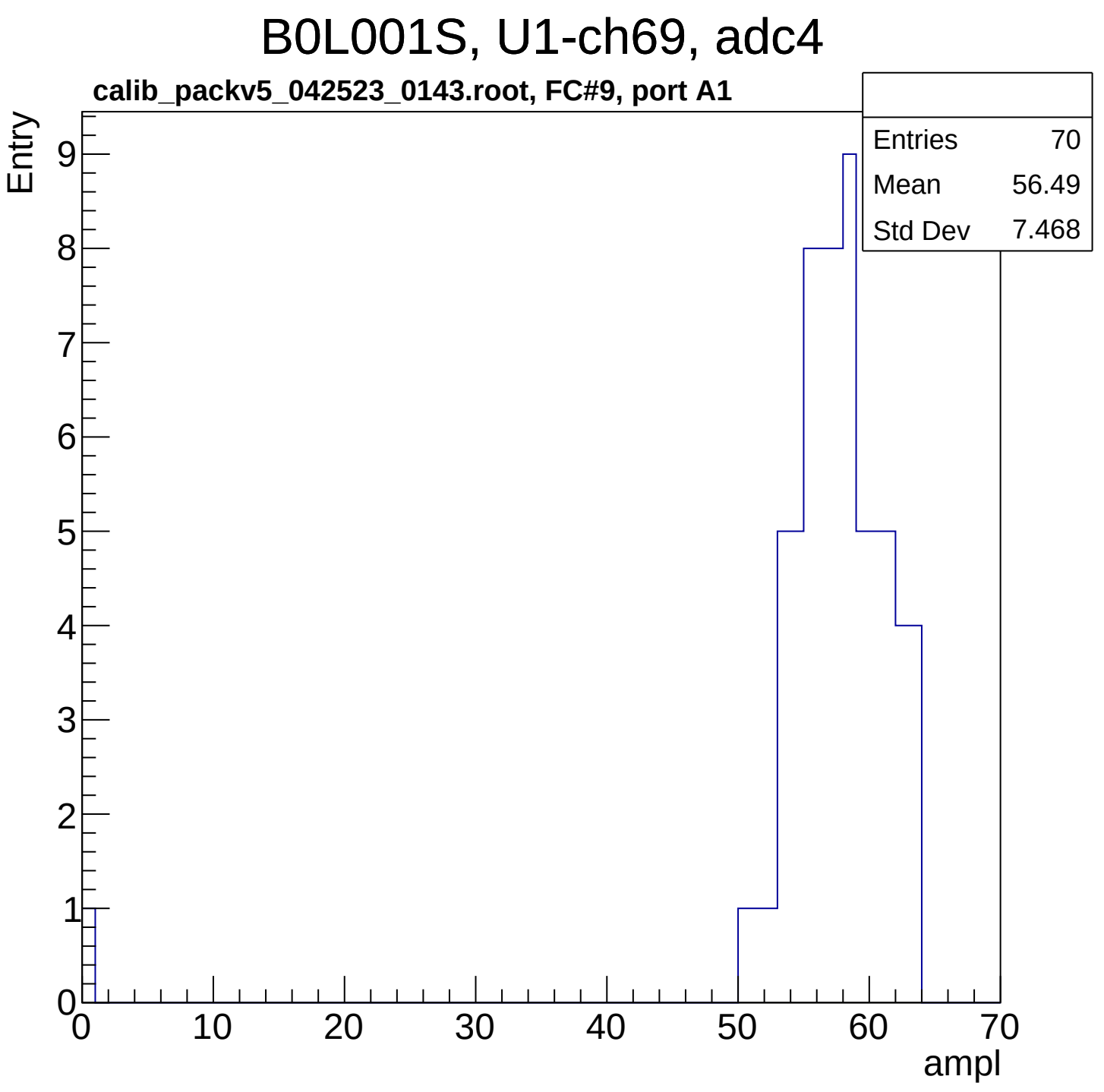
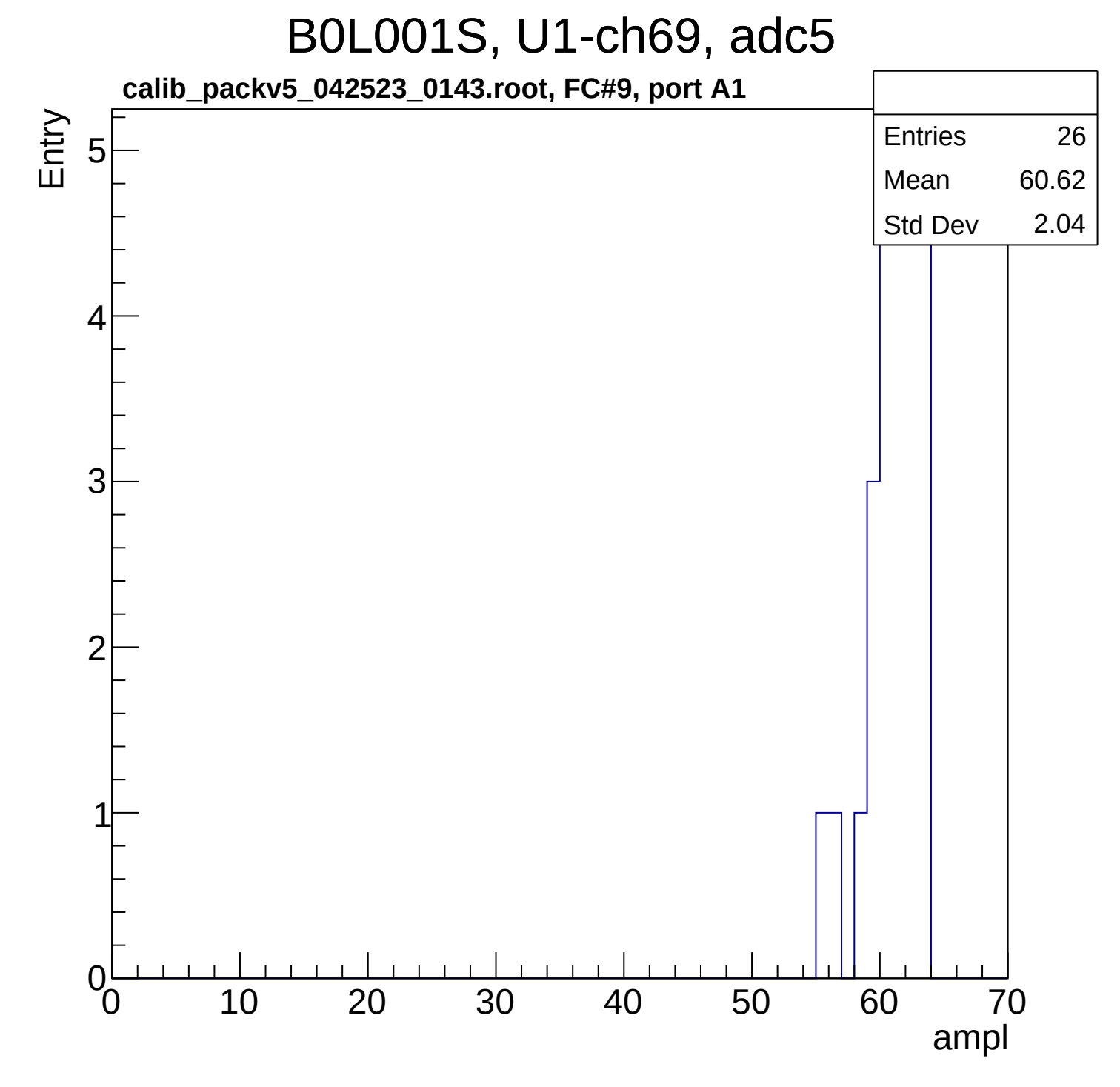
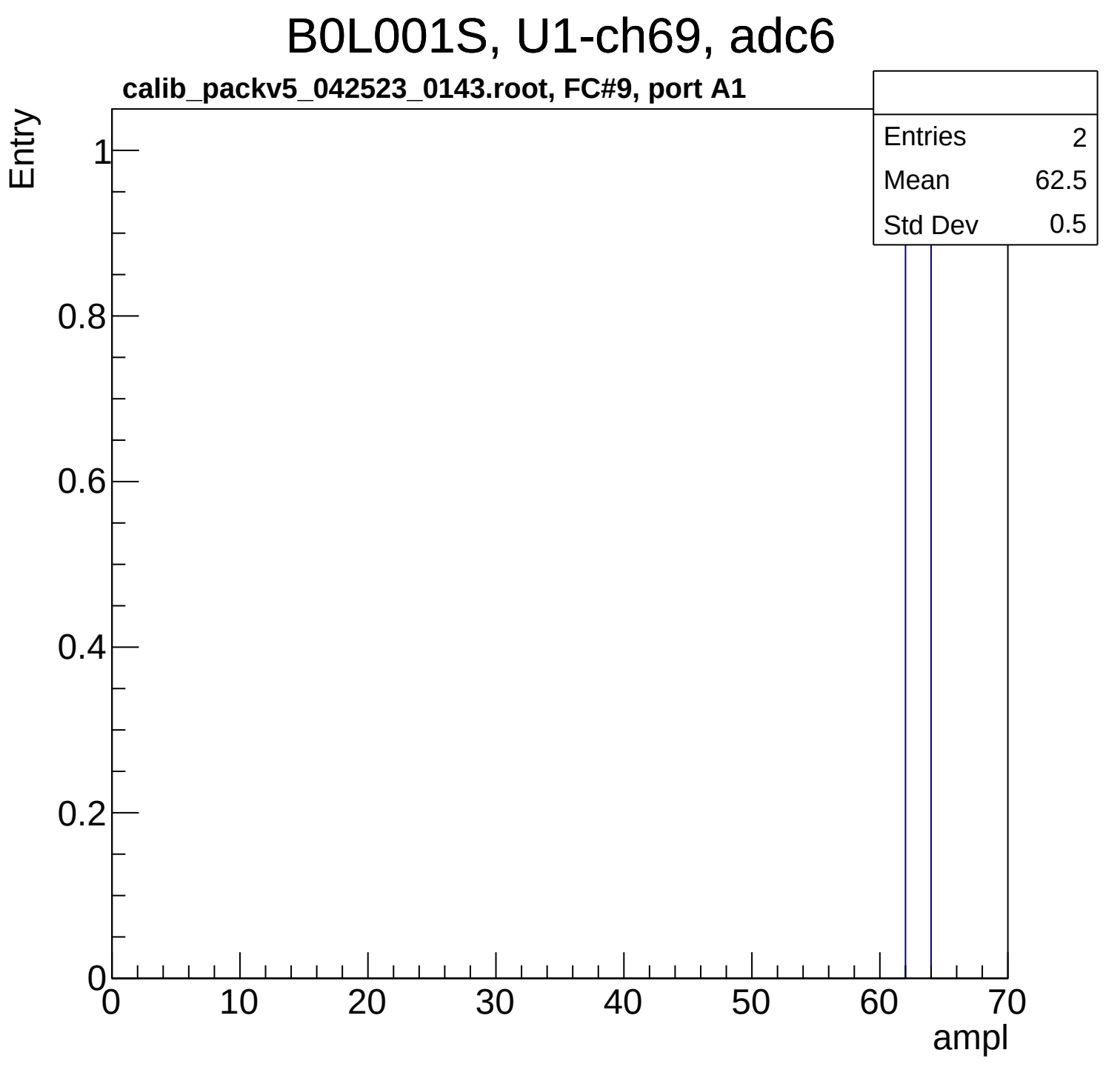

B0L001S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 38.19
Std Dev 3.644
Gaus mean : 38.0406
Gaus Width: 3.6788
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

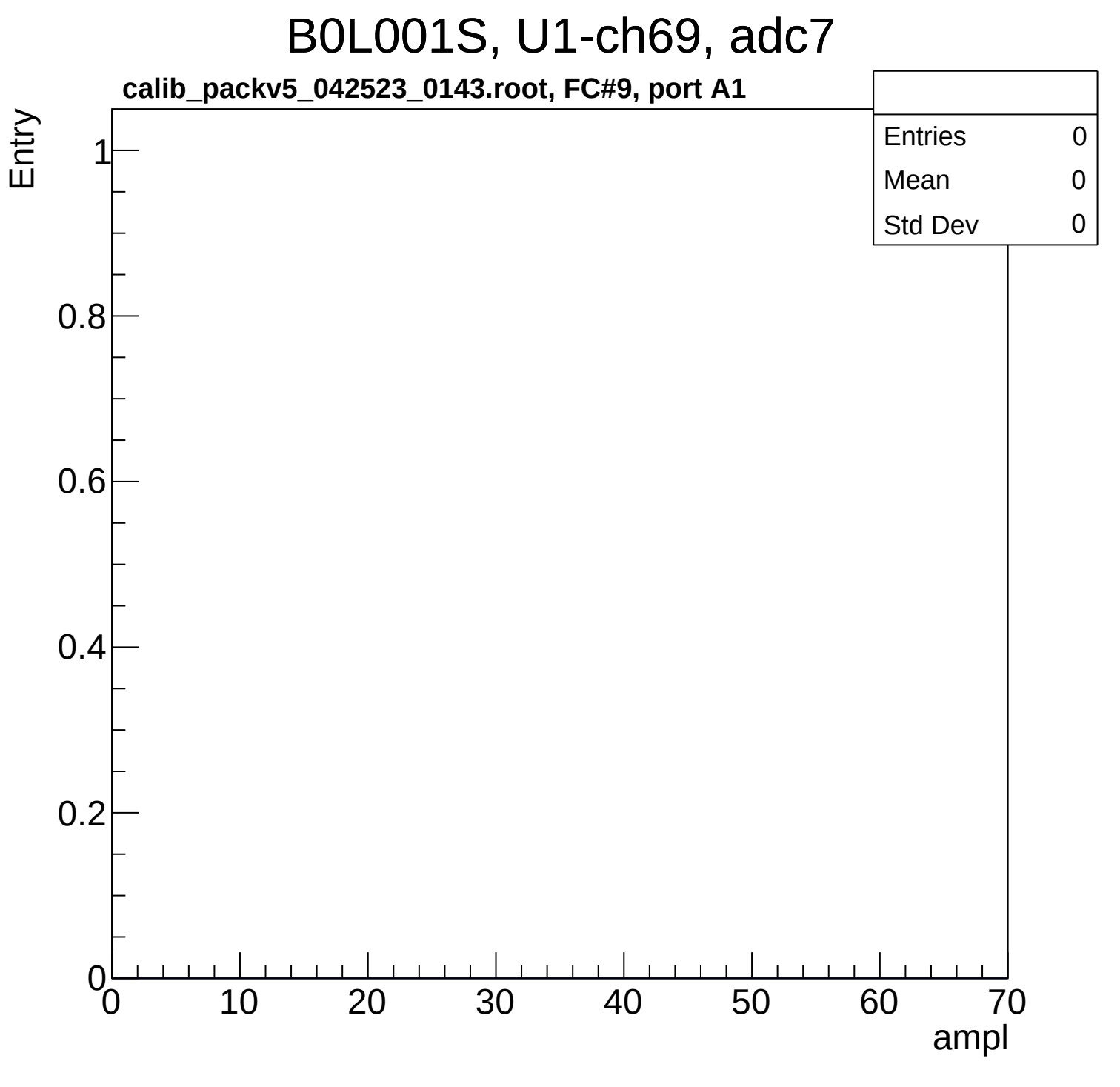

B0L001S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 44.43
Std Dev 3.715
Gaus mean : 44.5875
Gaus Width: 3.9915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

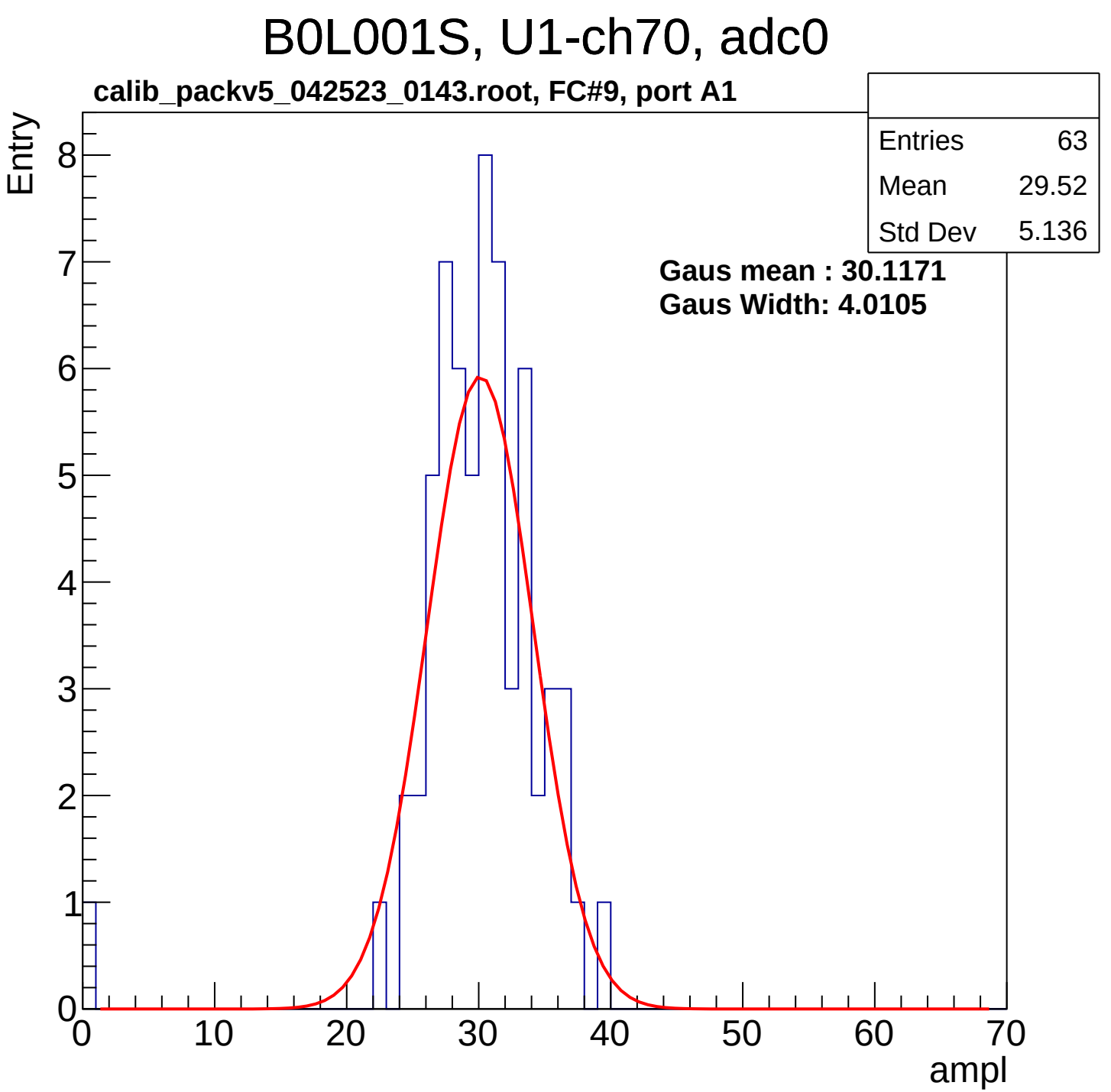

B0L001S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 50.79
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

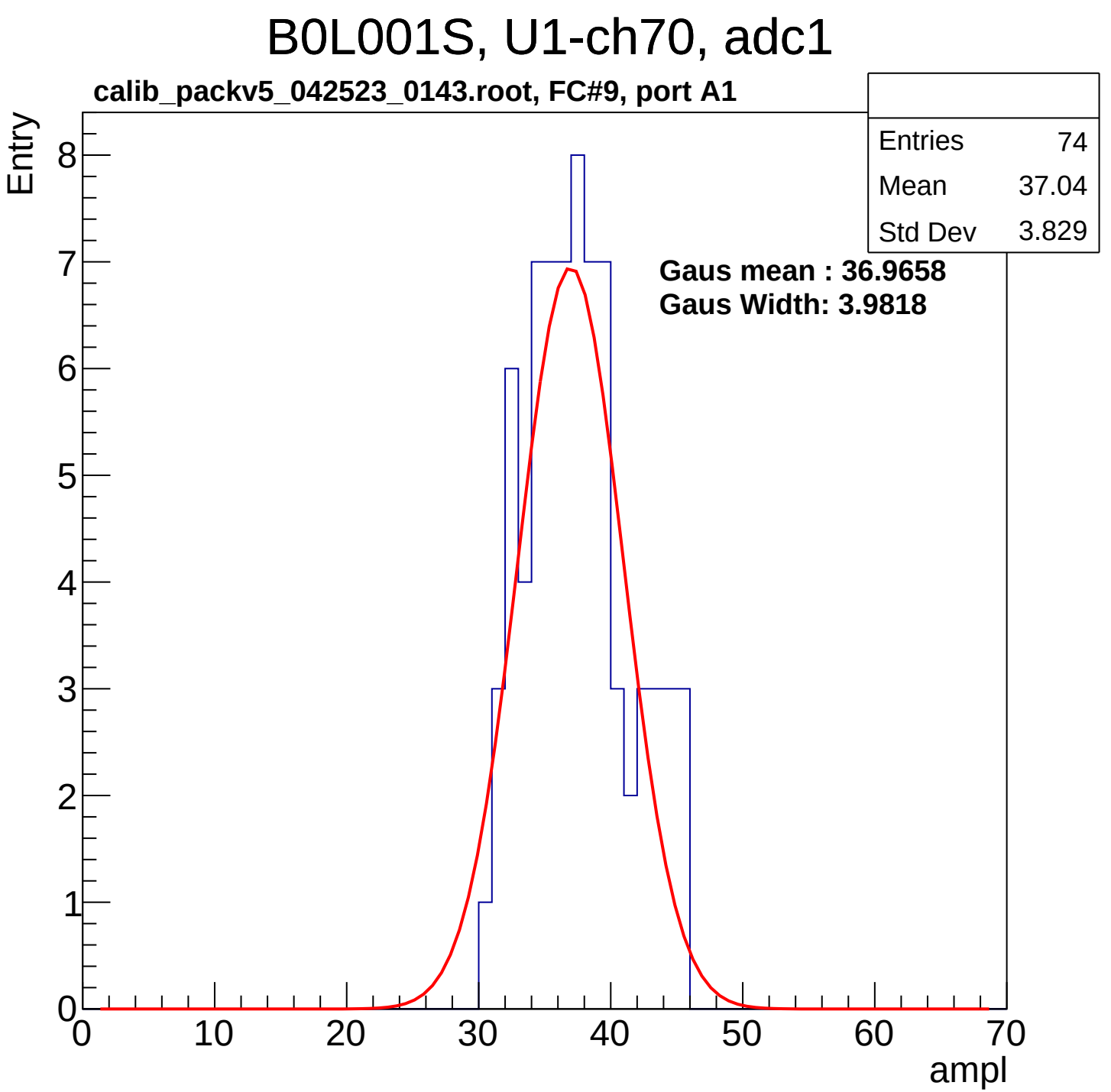

B0L001S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 56.49
Std Dev 7.468
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

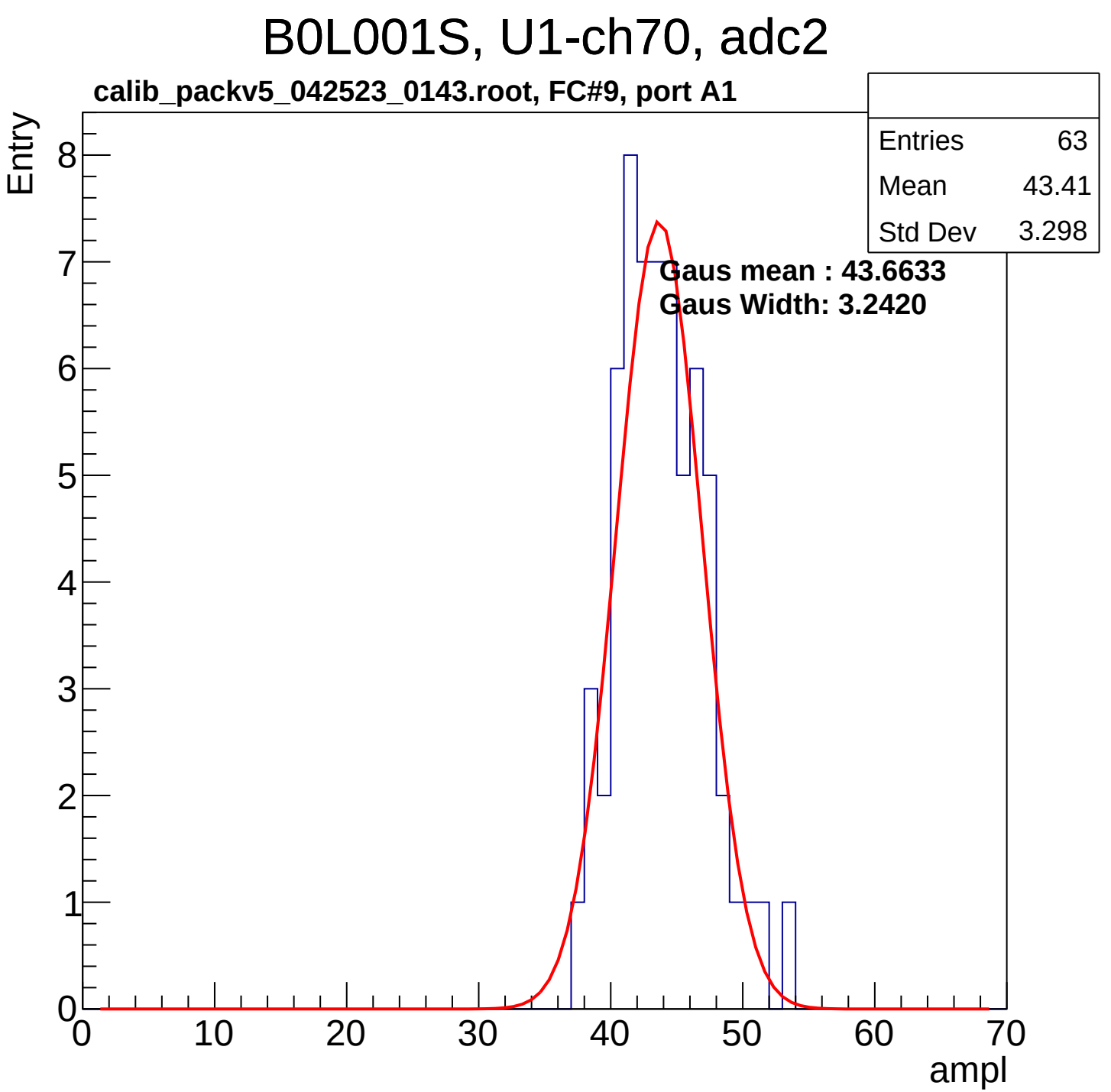

B0L001S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 26
Mean 60.62
Std Dev 2.04
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

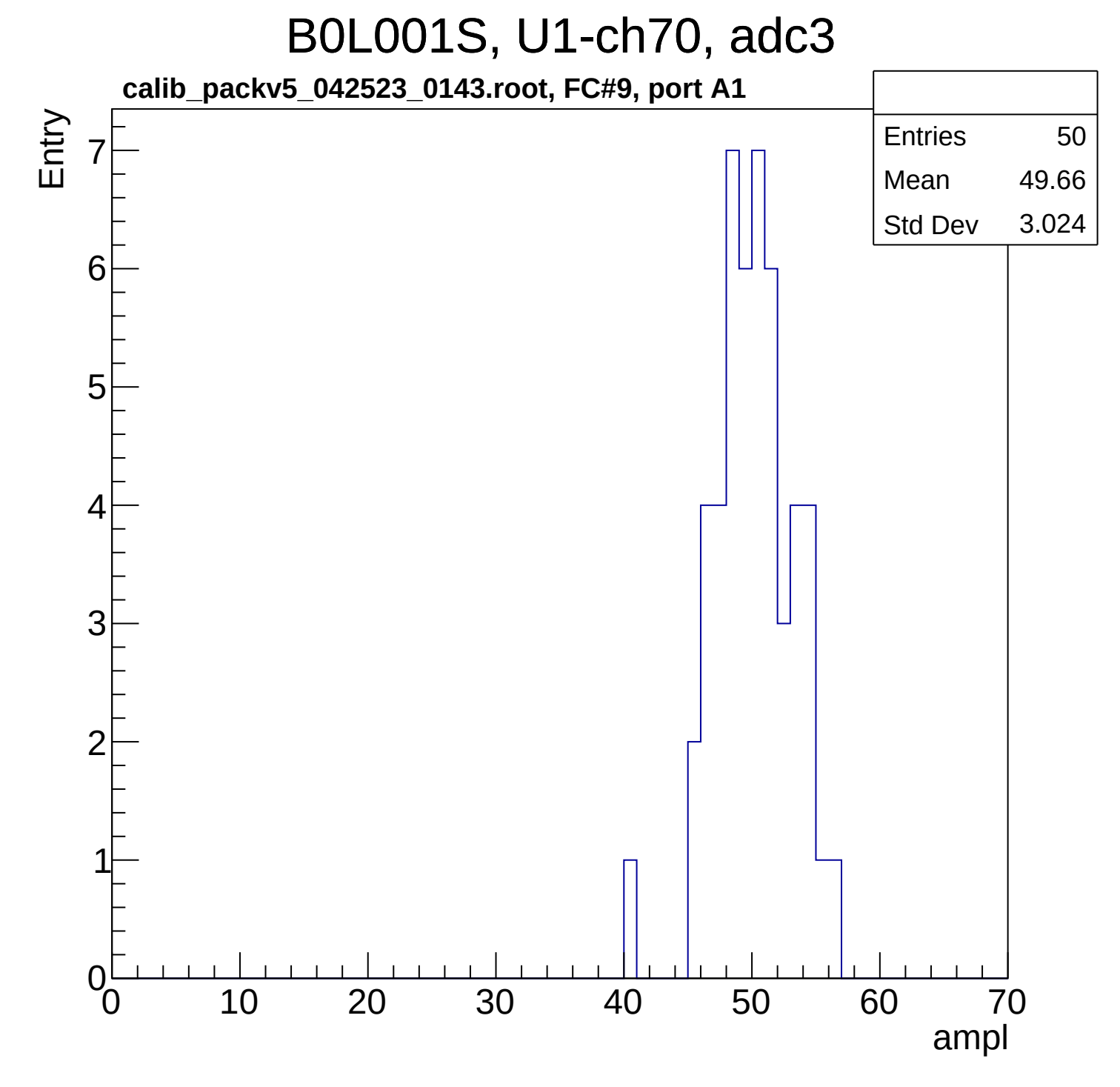

B0L001S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

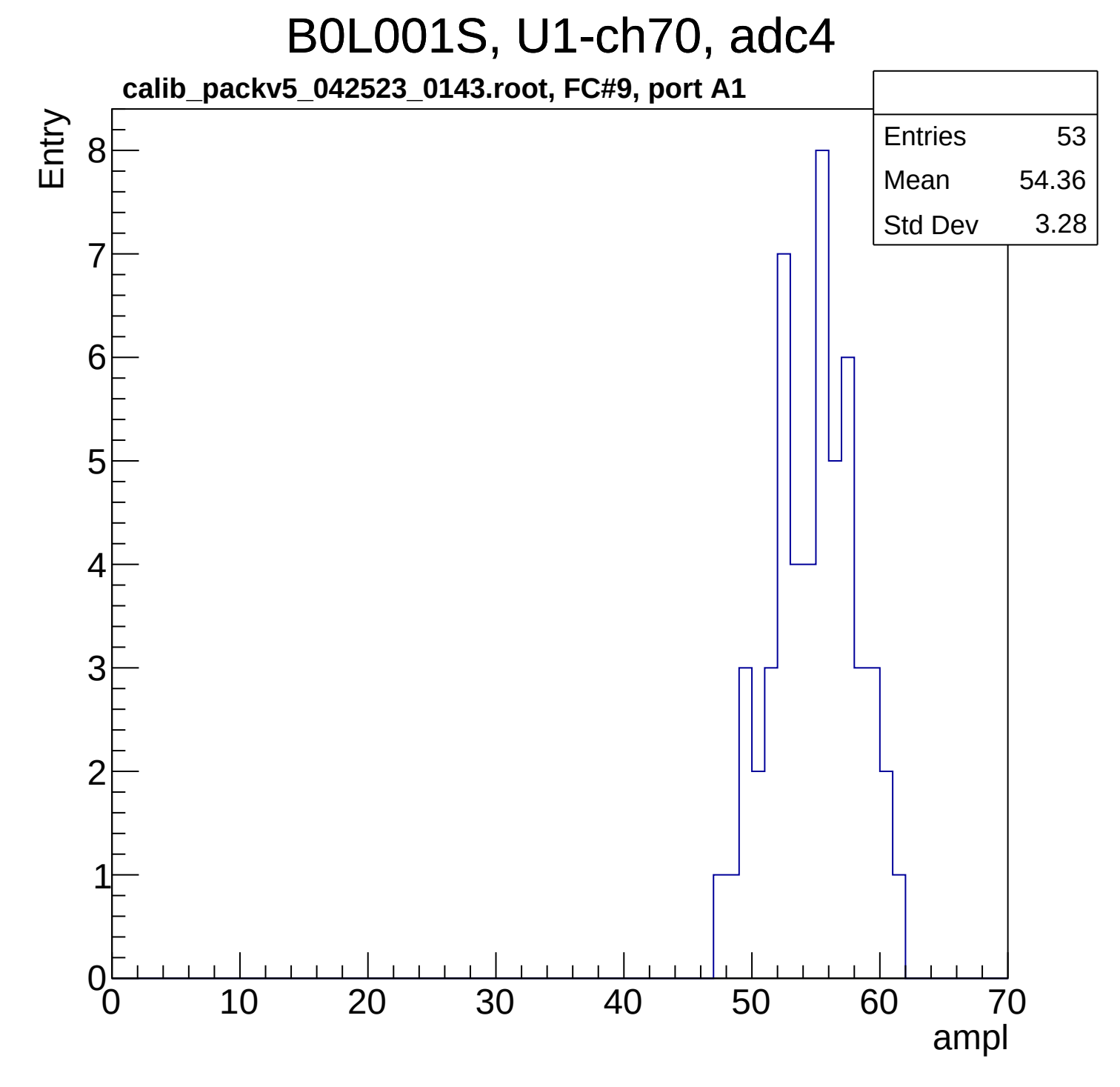

B0L001S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

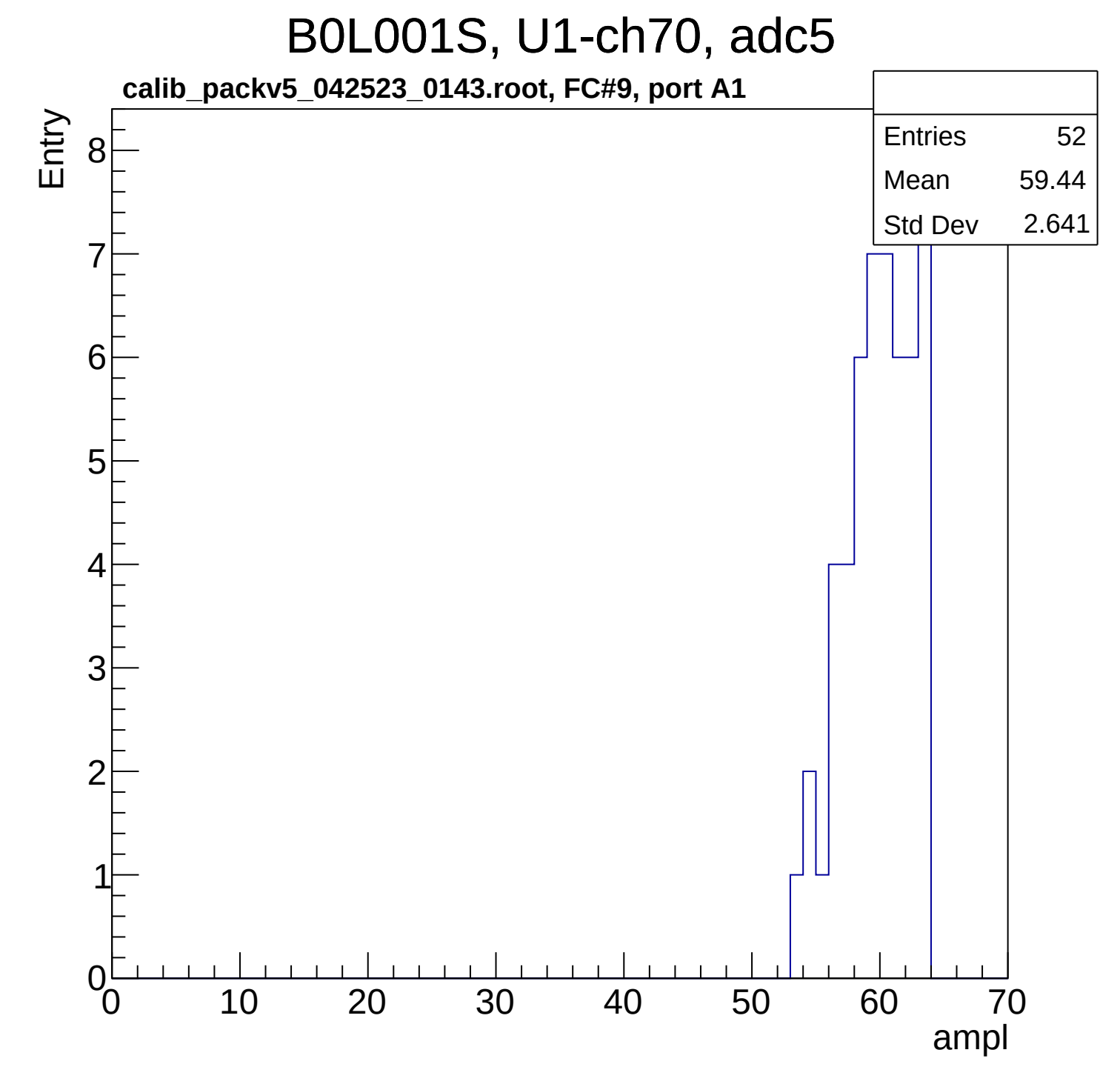

B0L001S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.52
Std Dev 5.136
Gaus mean : 30.1171
Gaus Width: 4.0105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.04
Std Dev 3.829
Gaus mean : 36.9658
Gaus Width: 3.9818
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.41
Std Dev 3.298
Gaus mean : 43.6633
Gaus Width: 3.2420
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 49.66
Std Dev 3.024
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 54.36
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 59.44
Std Dev 2.641
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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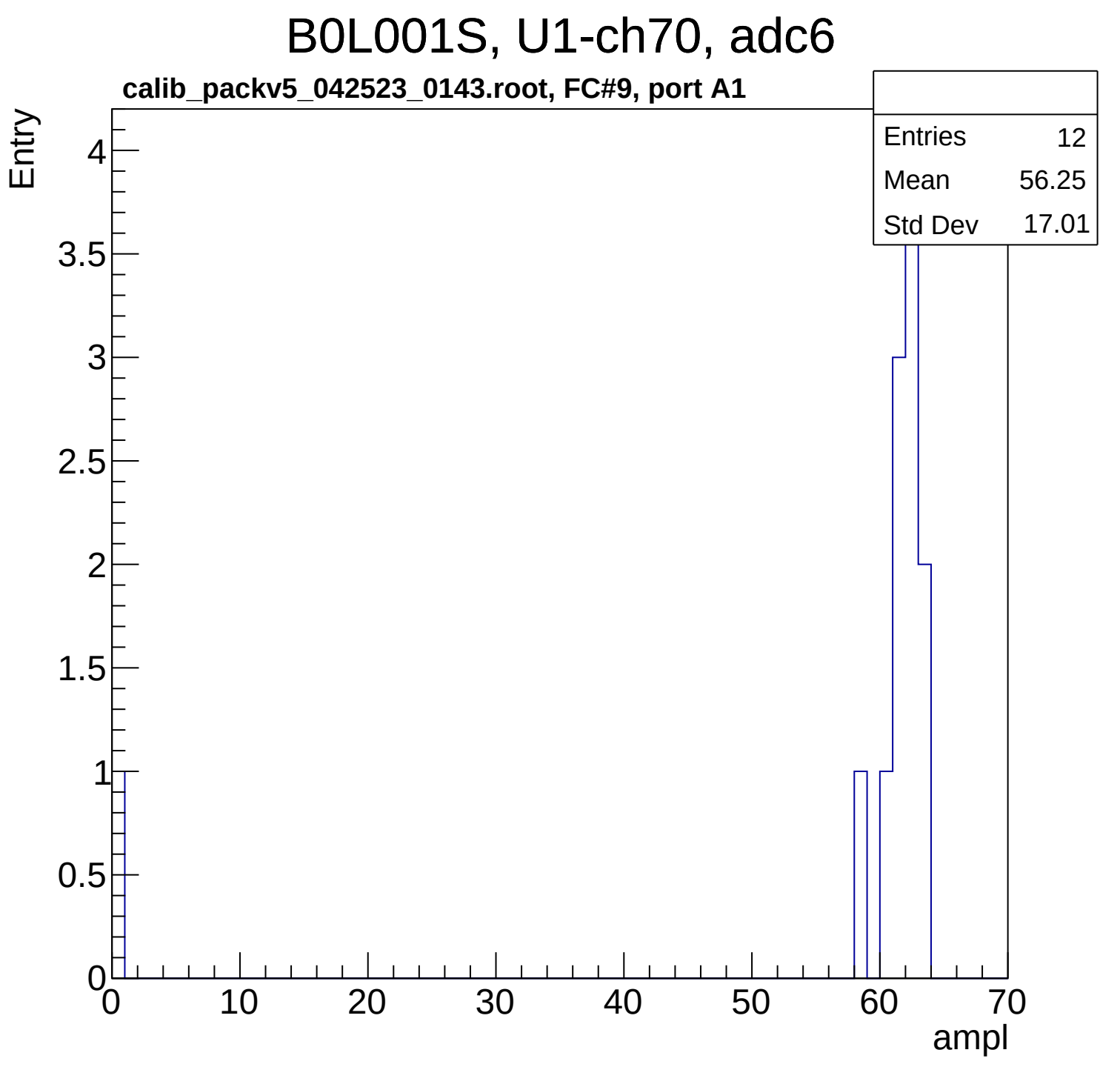
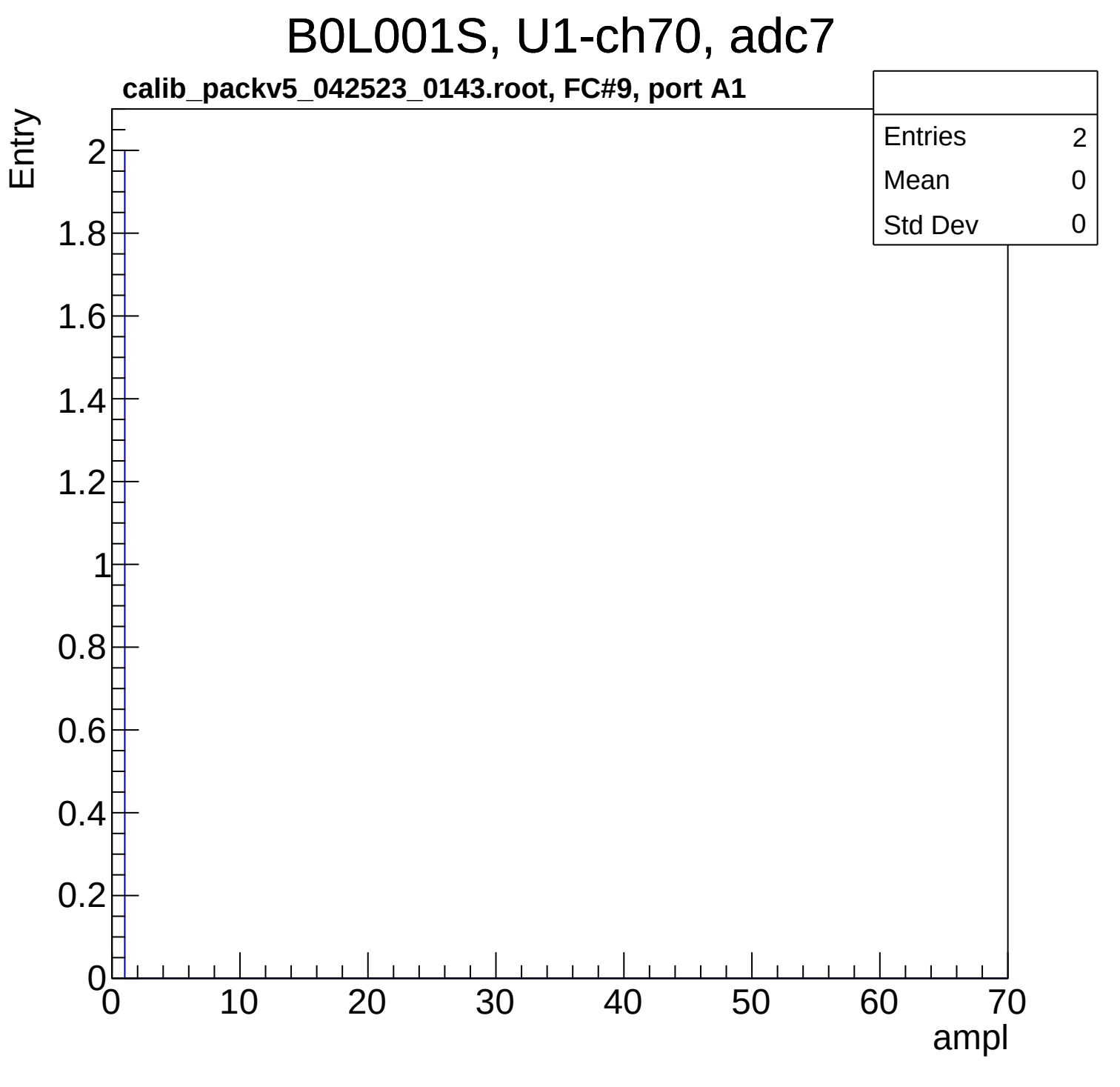
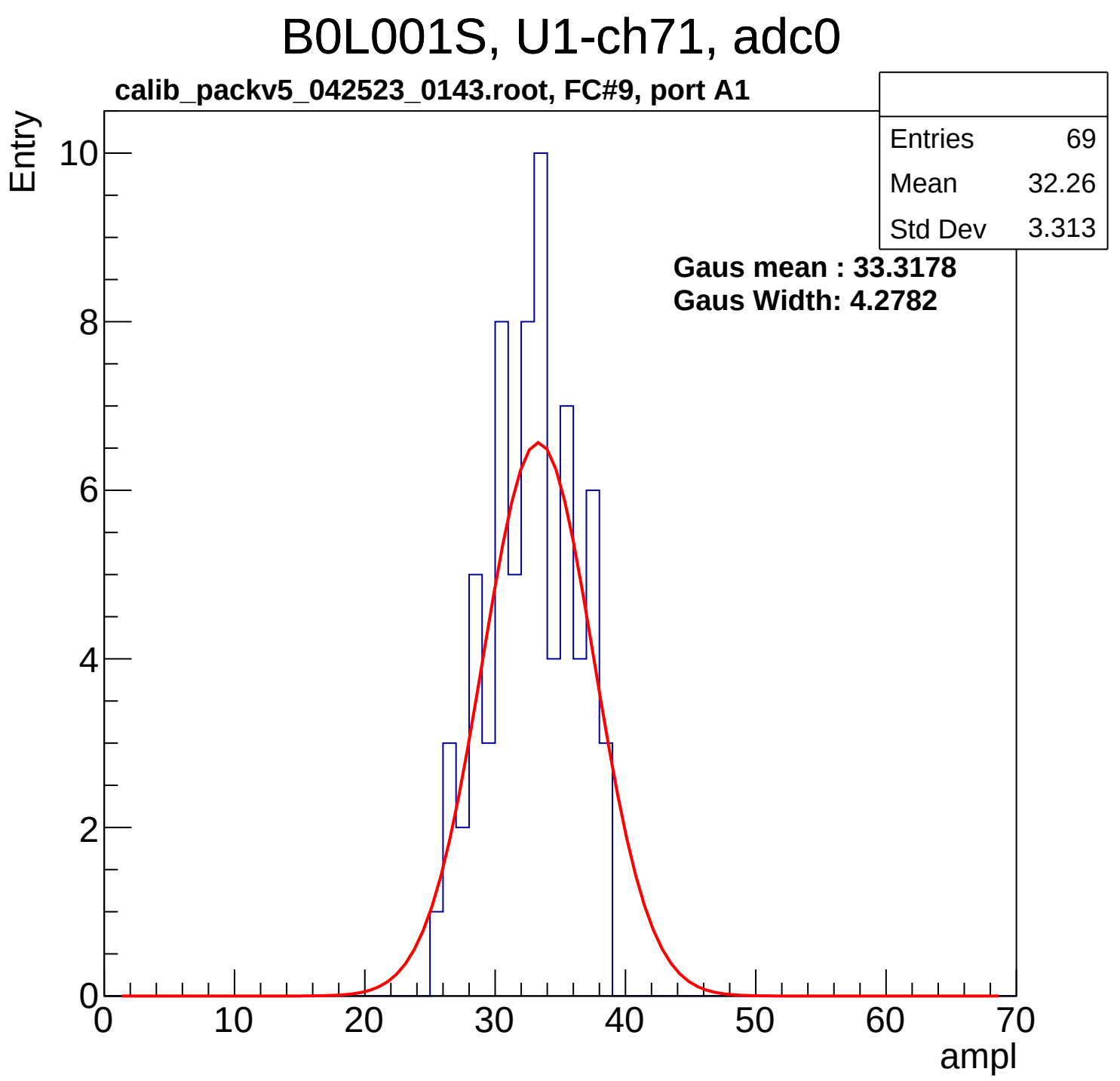
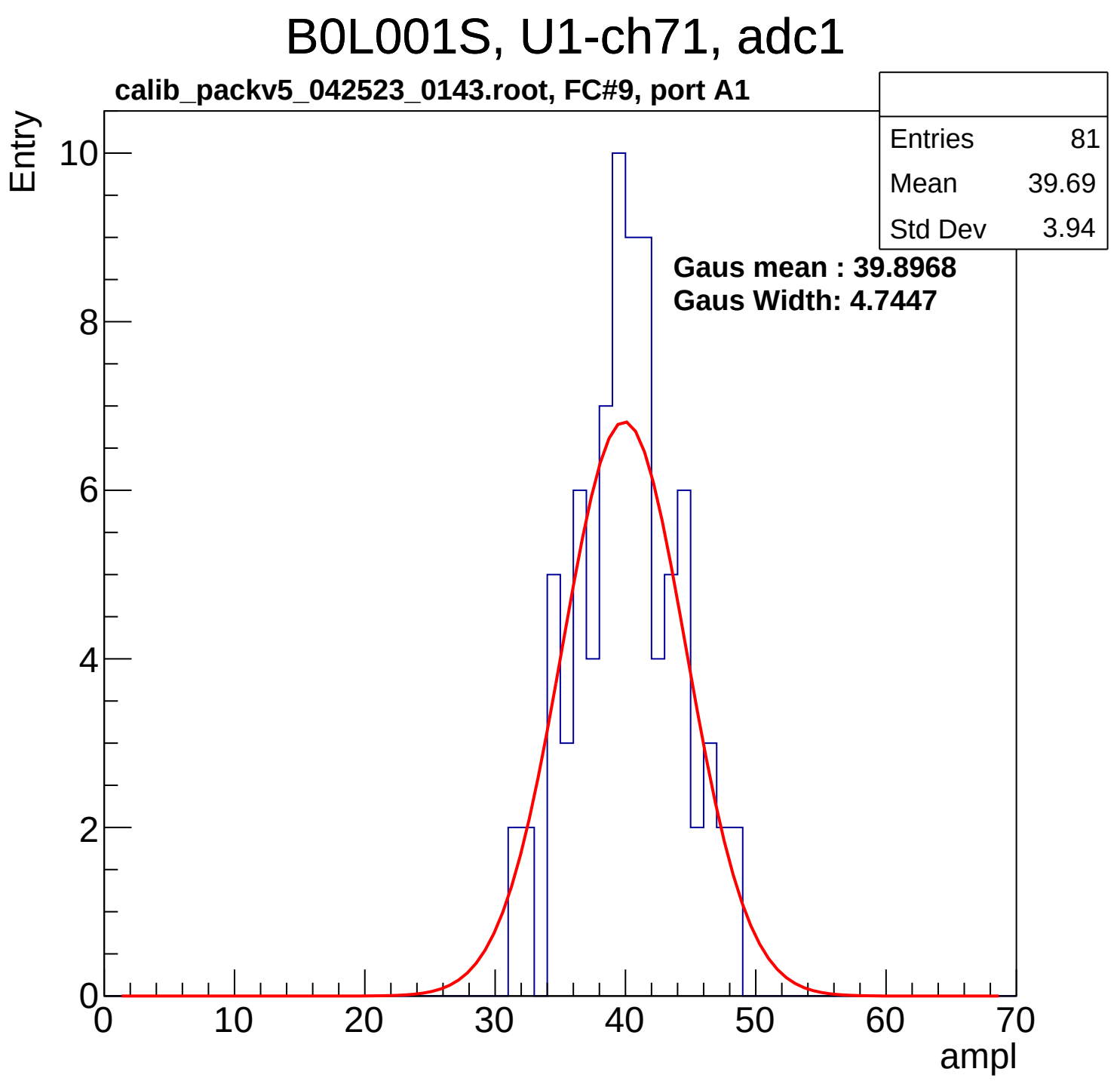
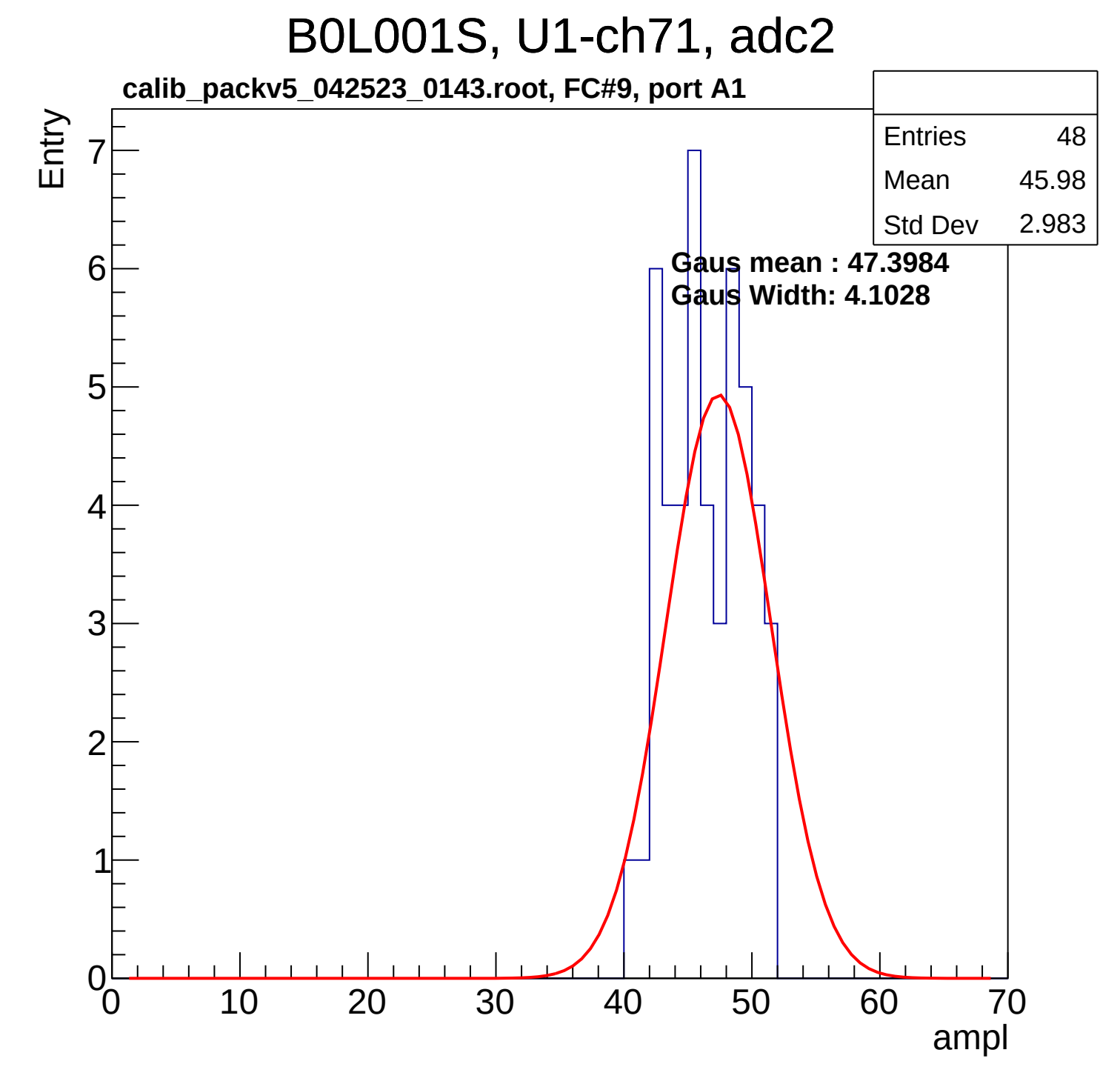
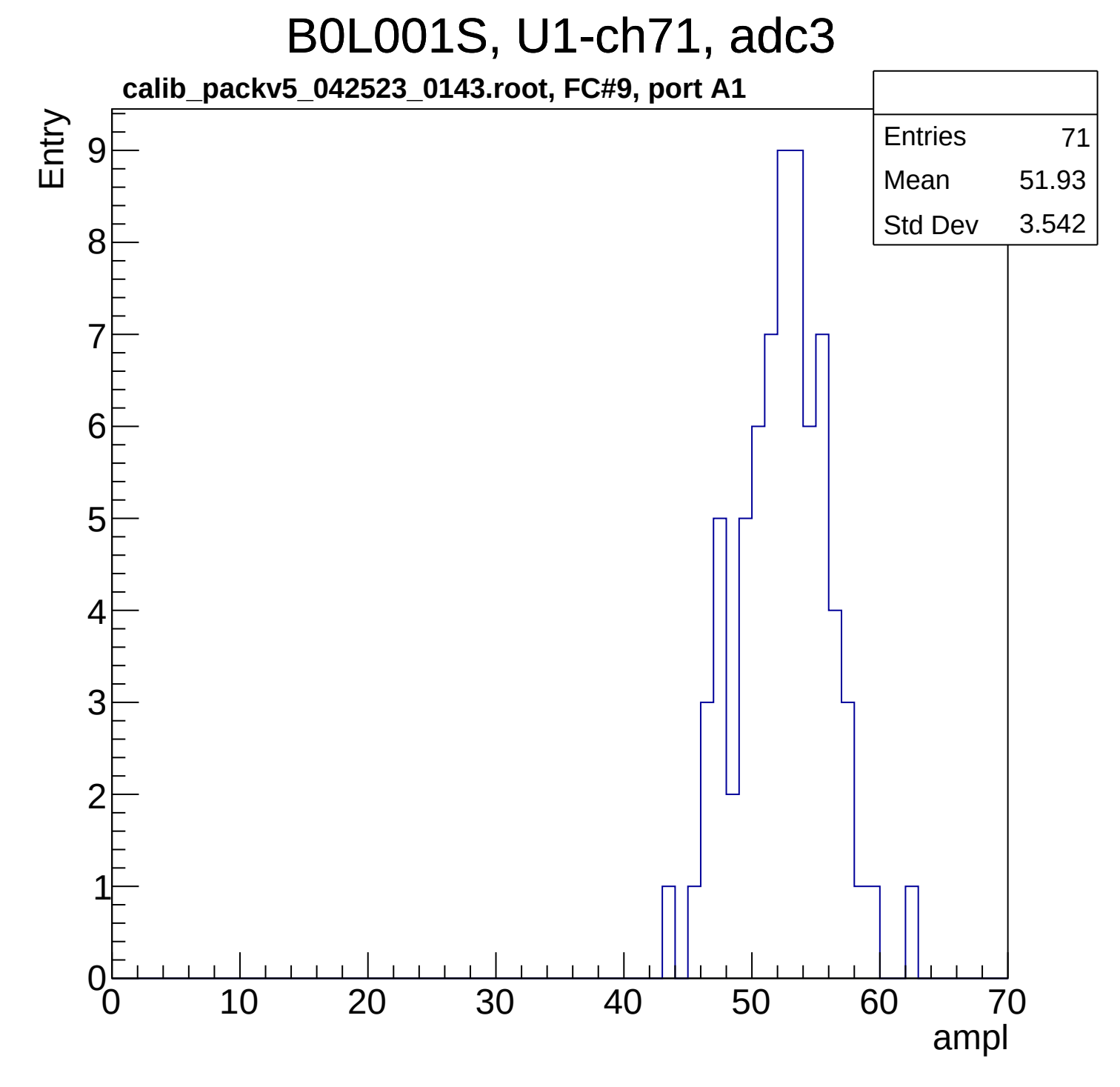

B0L001S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 56.25
Std Dev 17.01
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 32.26
Std Dev 3.313
Gaus mean : 33.3178
Gaus Width: 4.2782
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 81
Mean 39.69
Std Dev 3.94
Gaus mean : 39.8968
Gaus Width: 4.7447
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 45.98
Std Dev 2.983
Gaus mean : 47.3984
Gaus Width: 4.1028
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.93
Std Dev 3.542
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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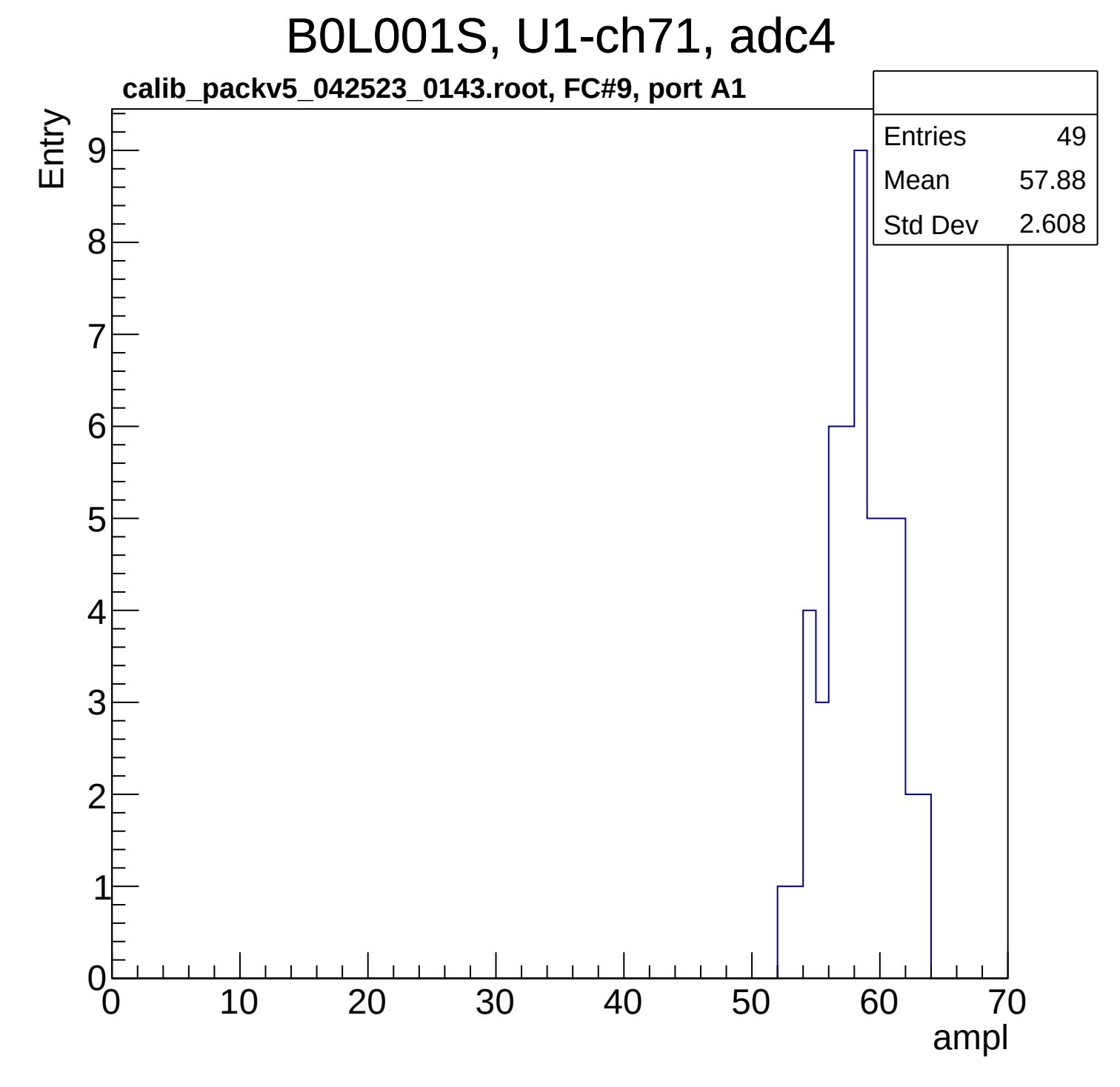
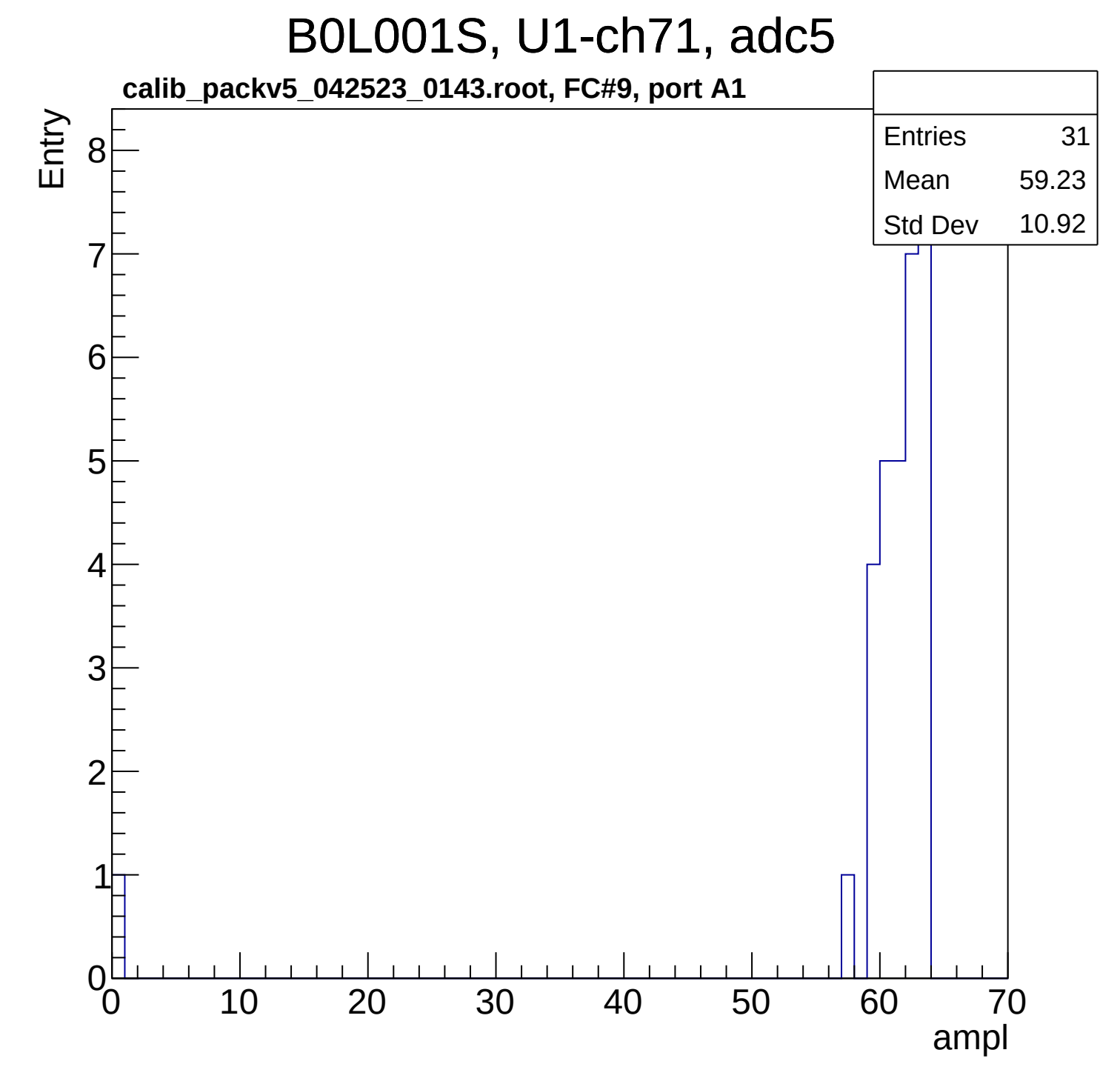
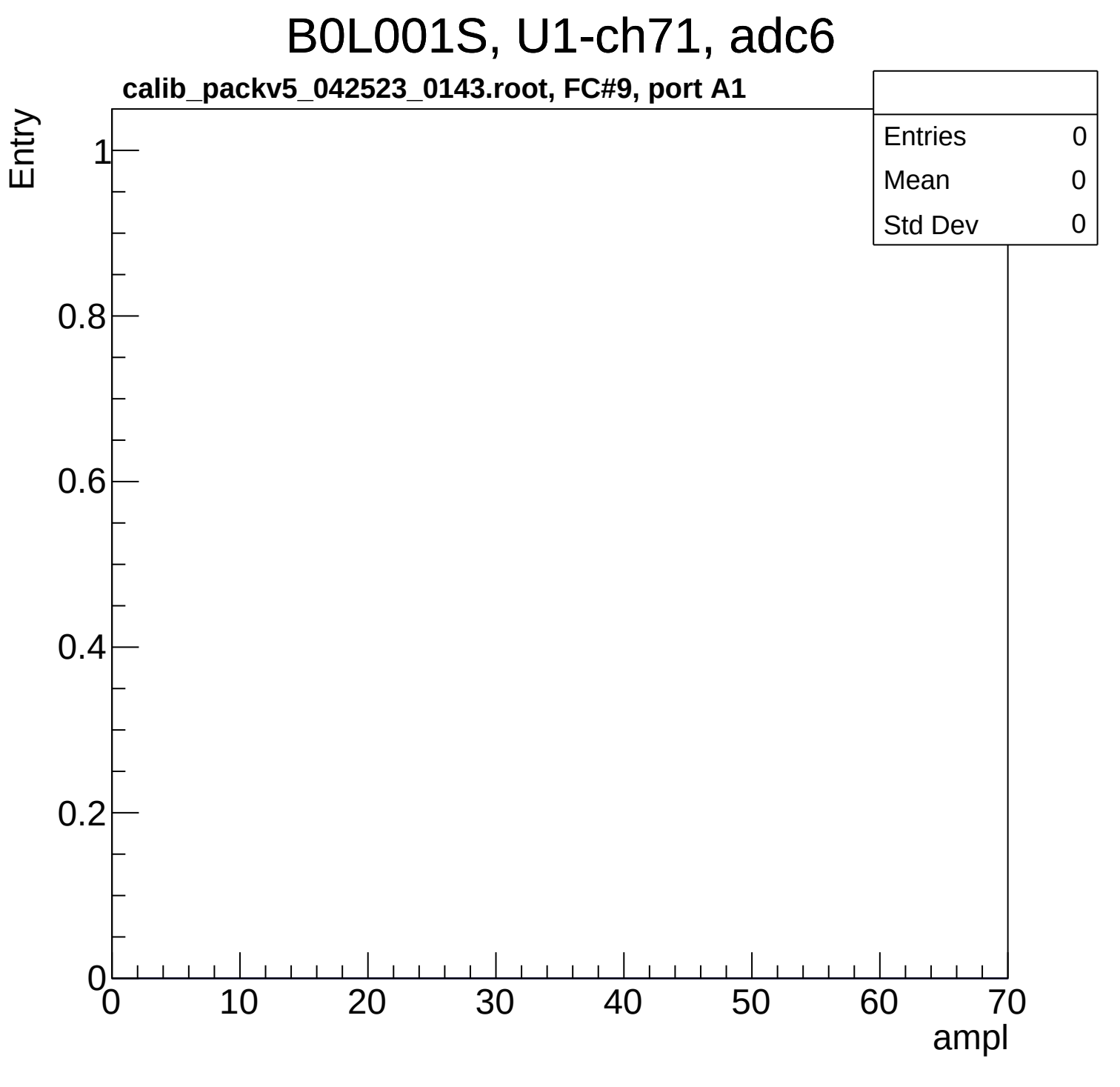
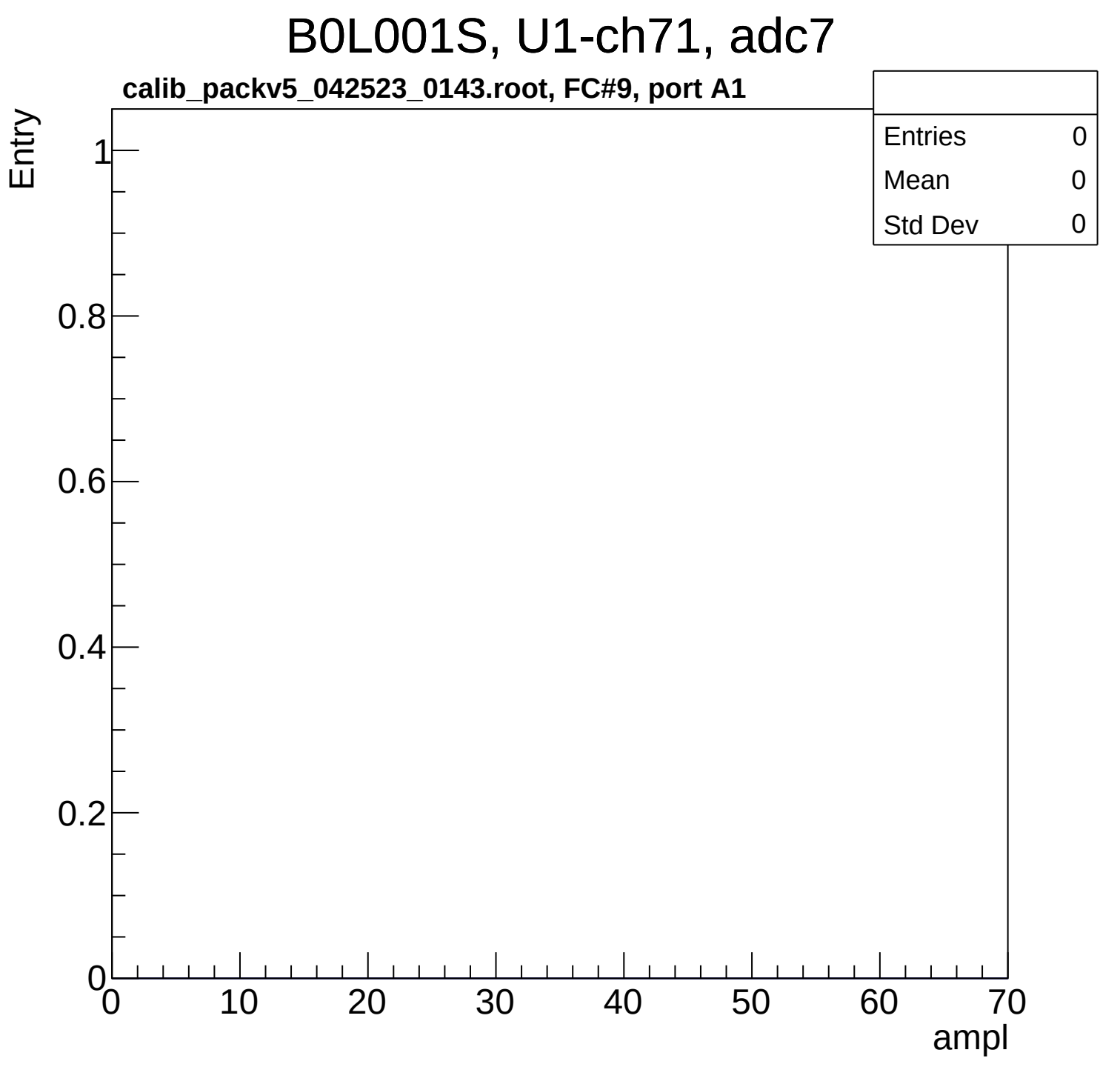
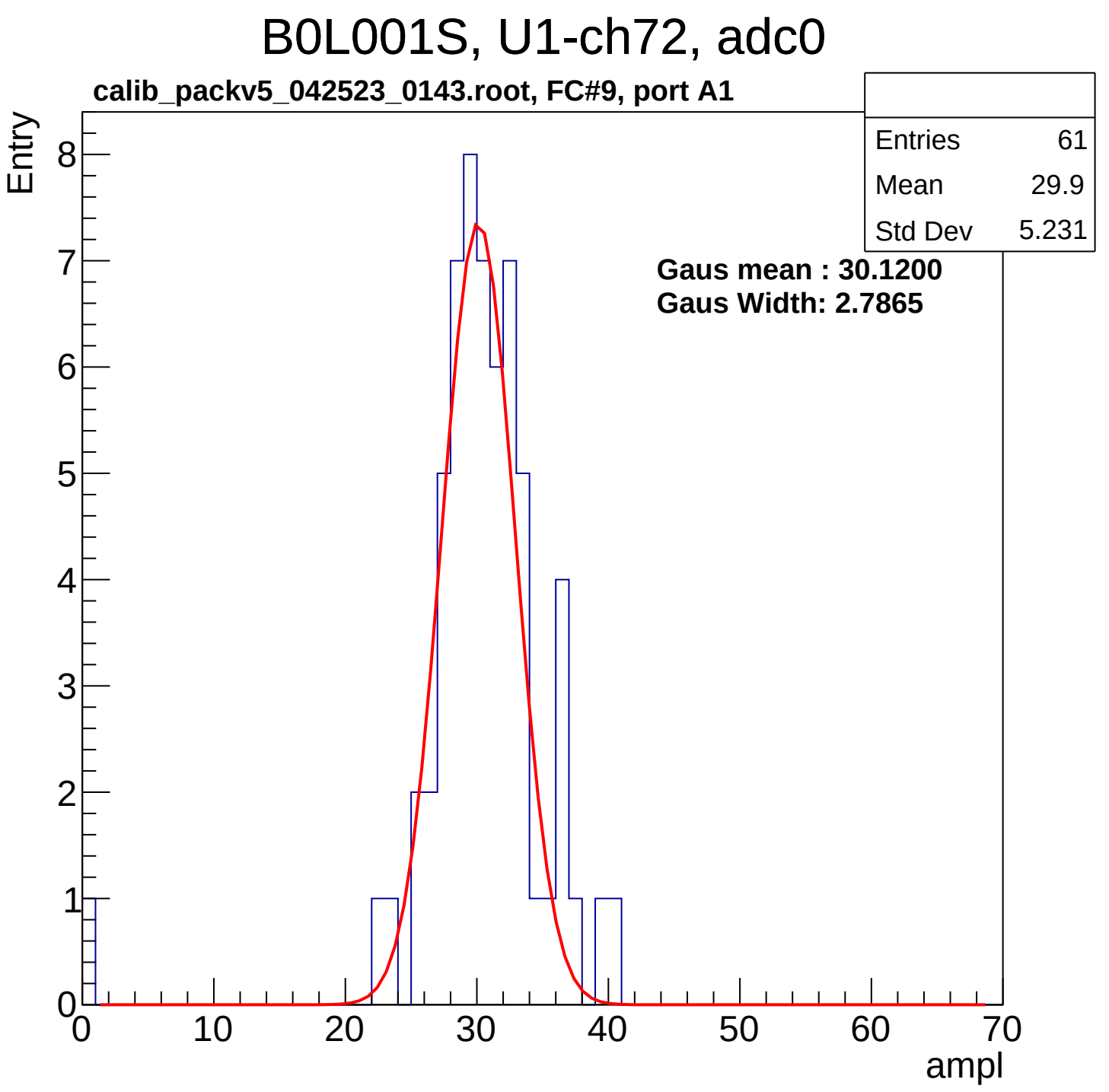
40
50
60
70

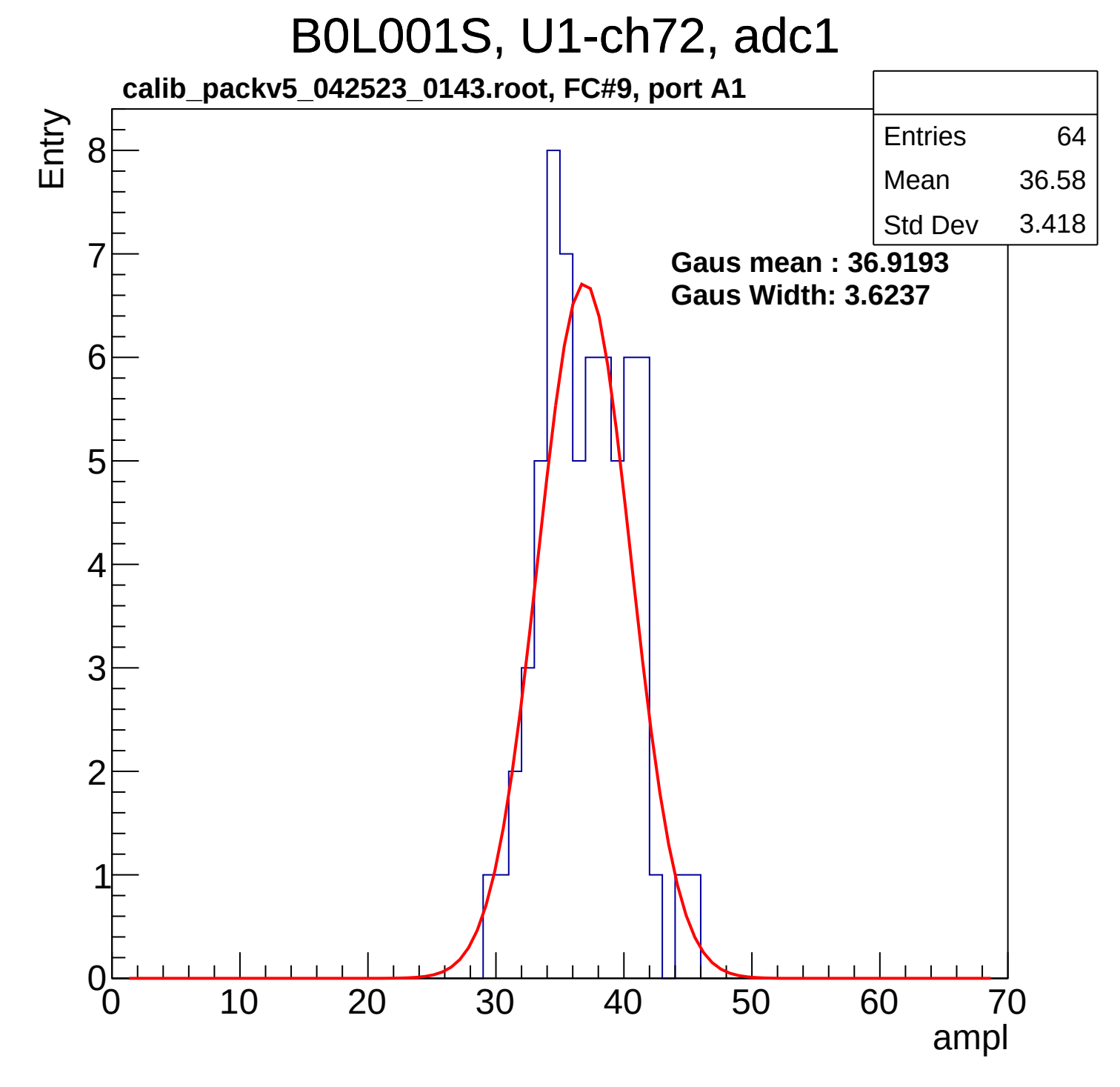

B0L001S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 57.88
Std Dev 2.608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

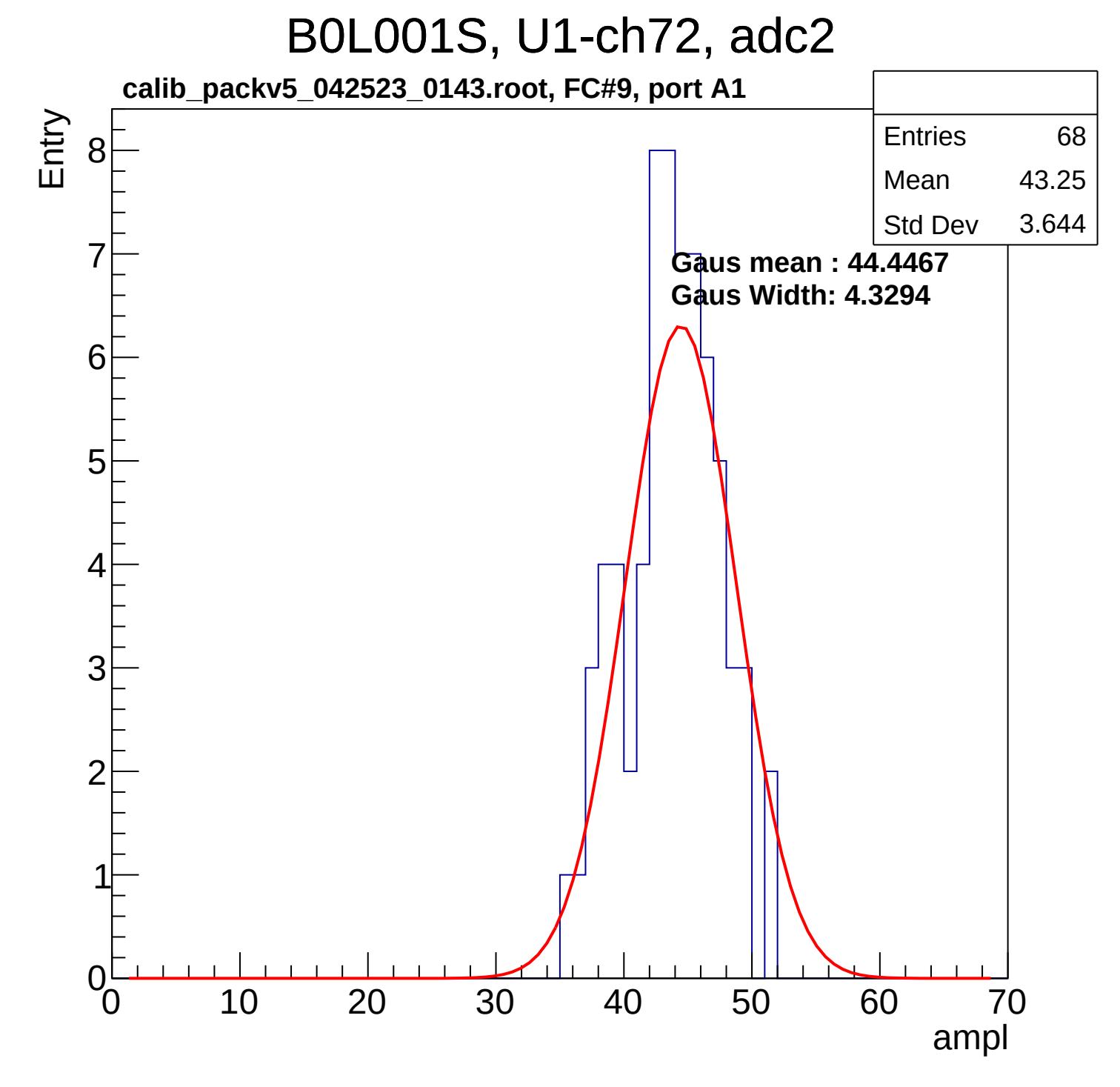

B0L001S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 59.23
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

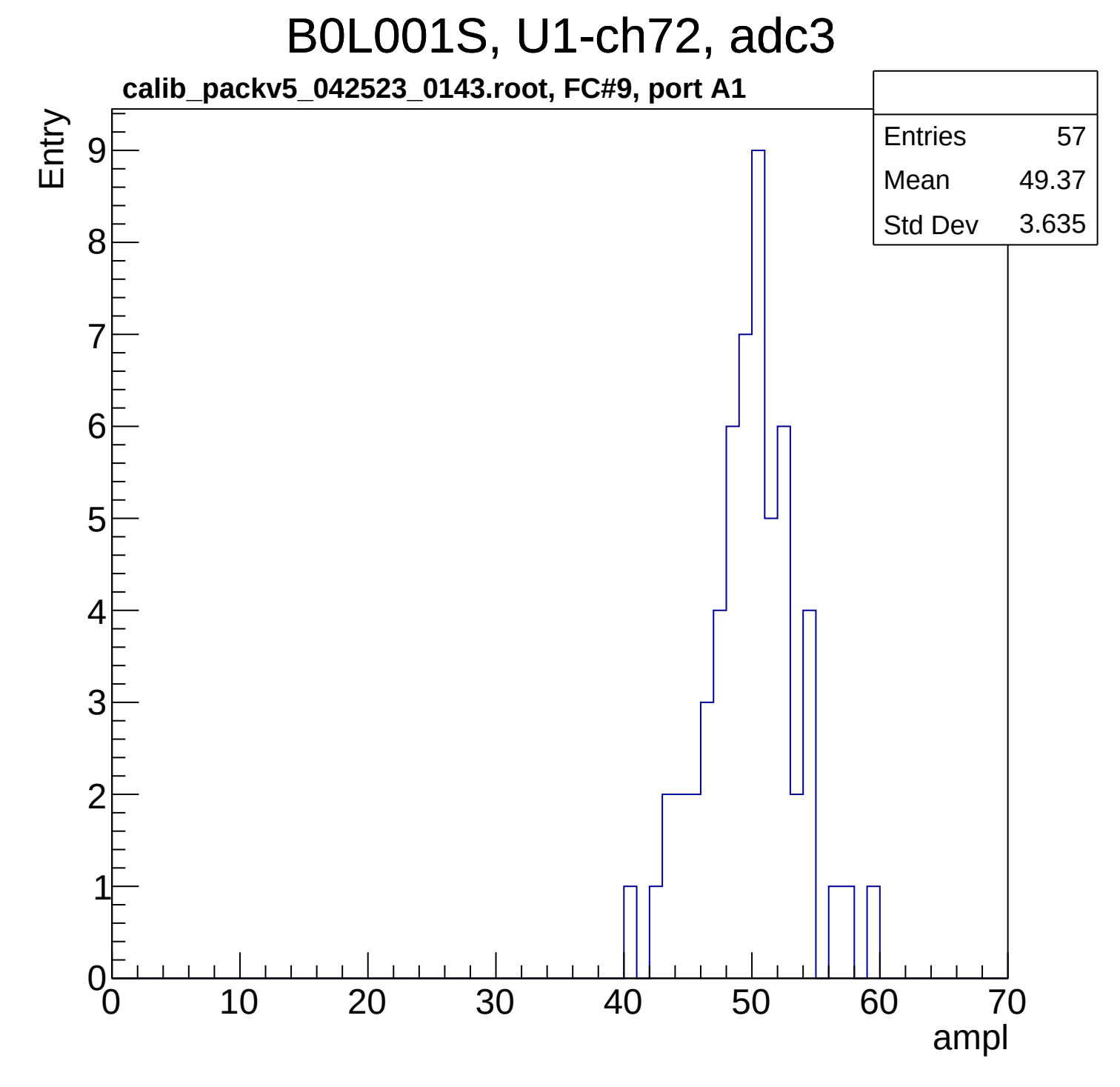

B0L001S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

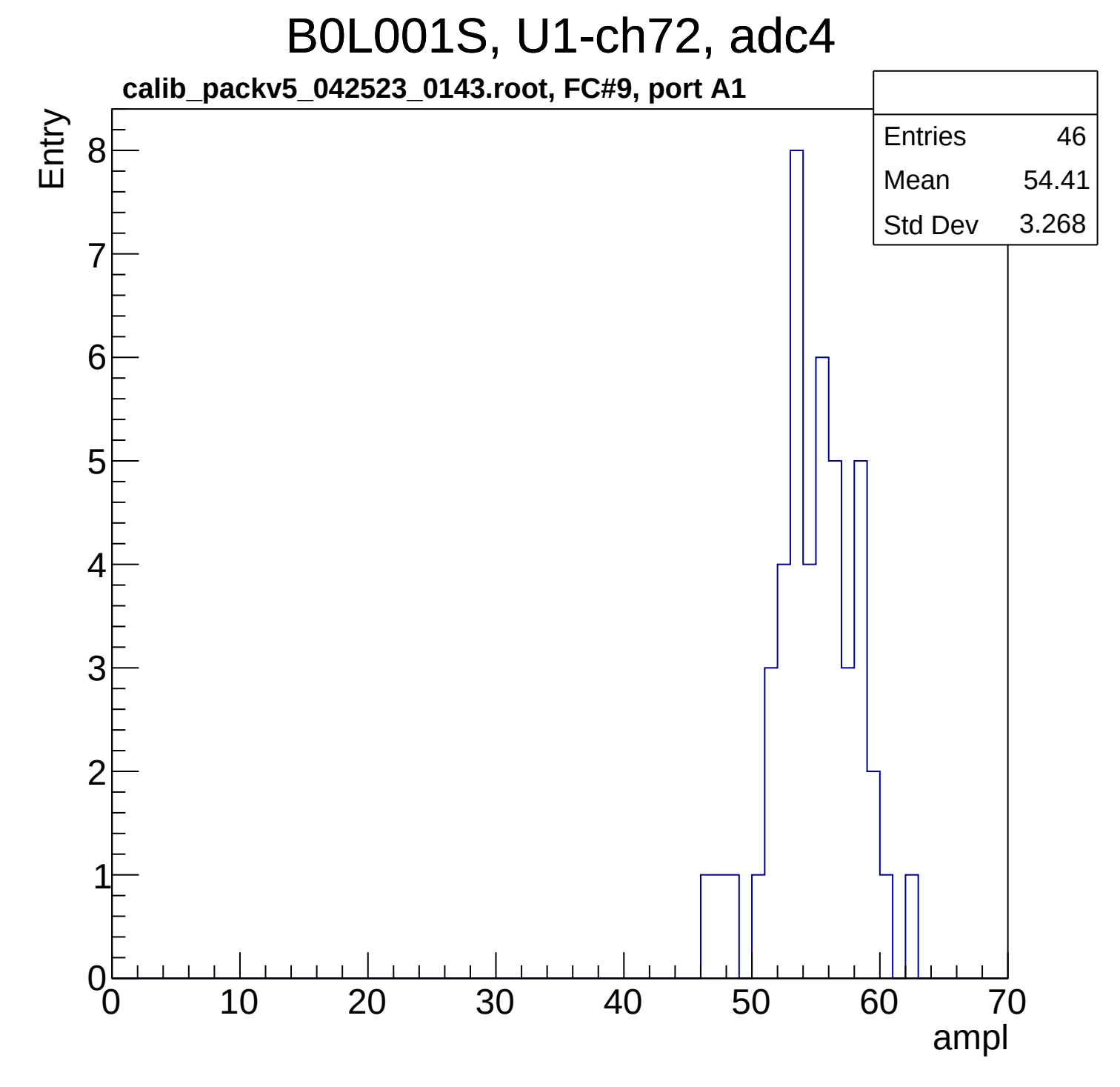

B0L001S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

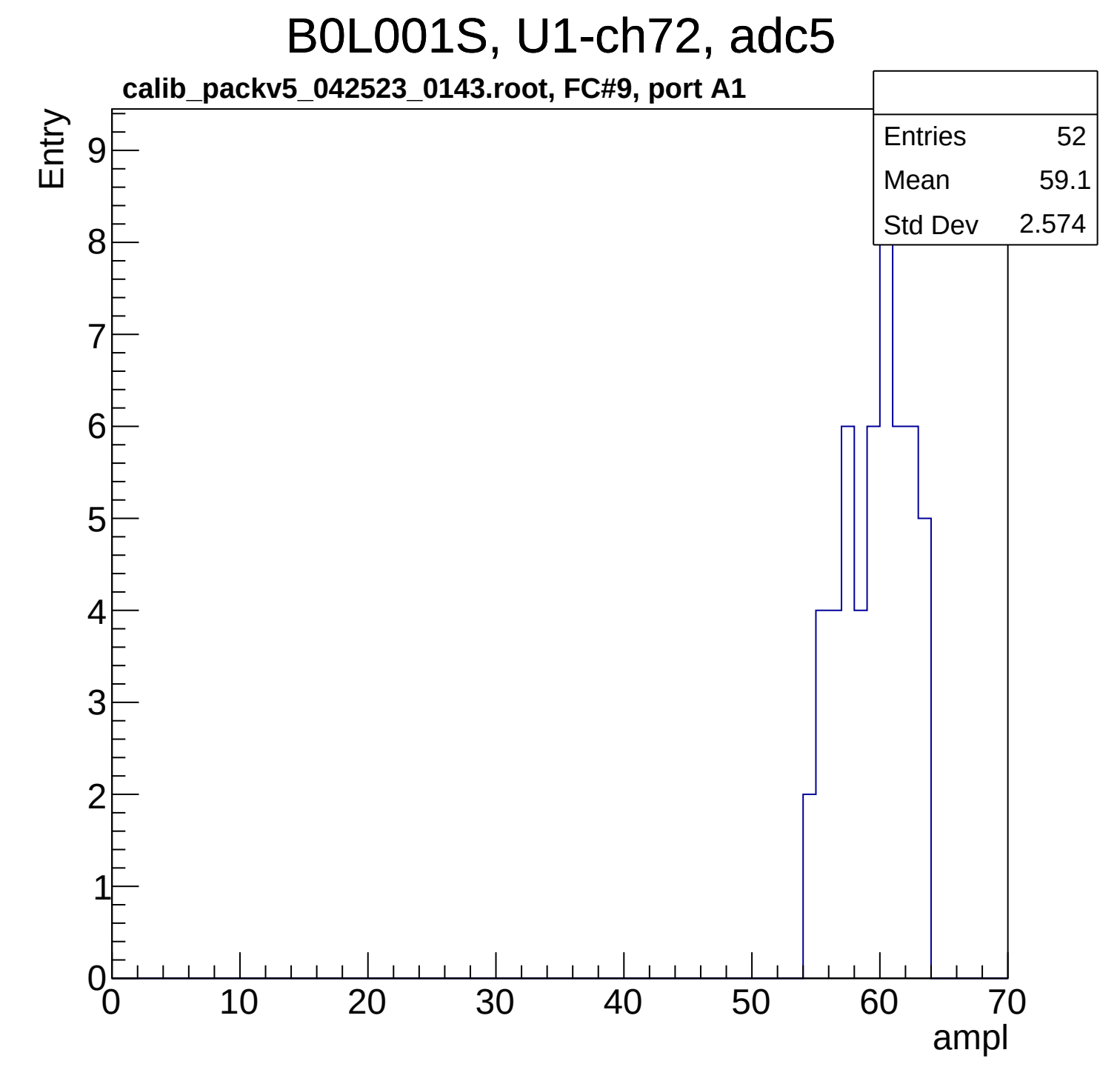

B0L001S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 29.9
Std Dev 5.231
Gaus mean : 30.1200
Gaus Width: 2.7865
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

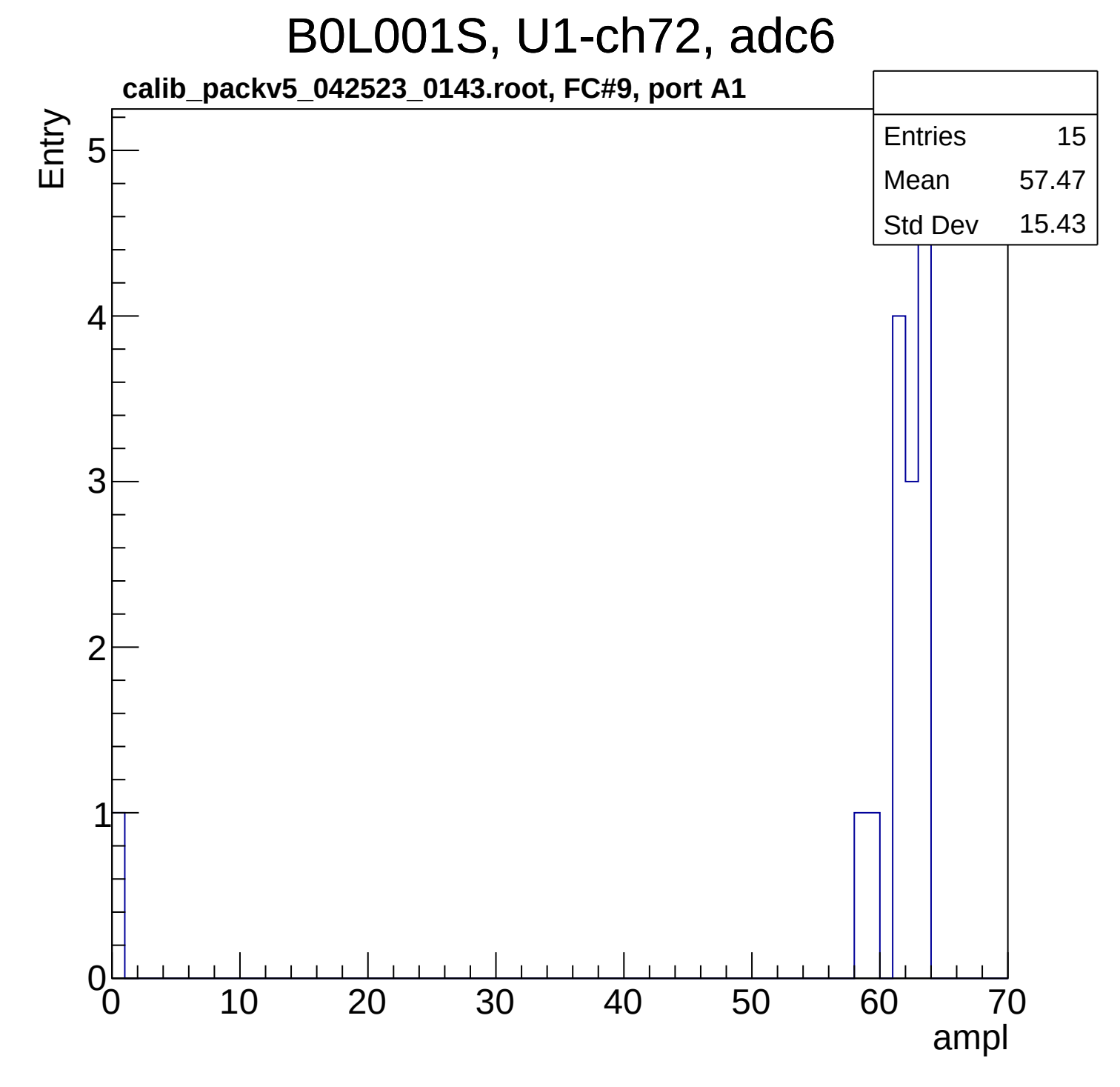

B0L001S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.58
Std Dev 3.418
Gaus mean : 36.9193
Gaus Width: 3.6237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

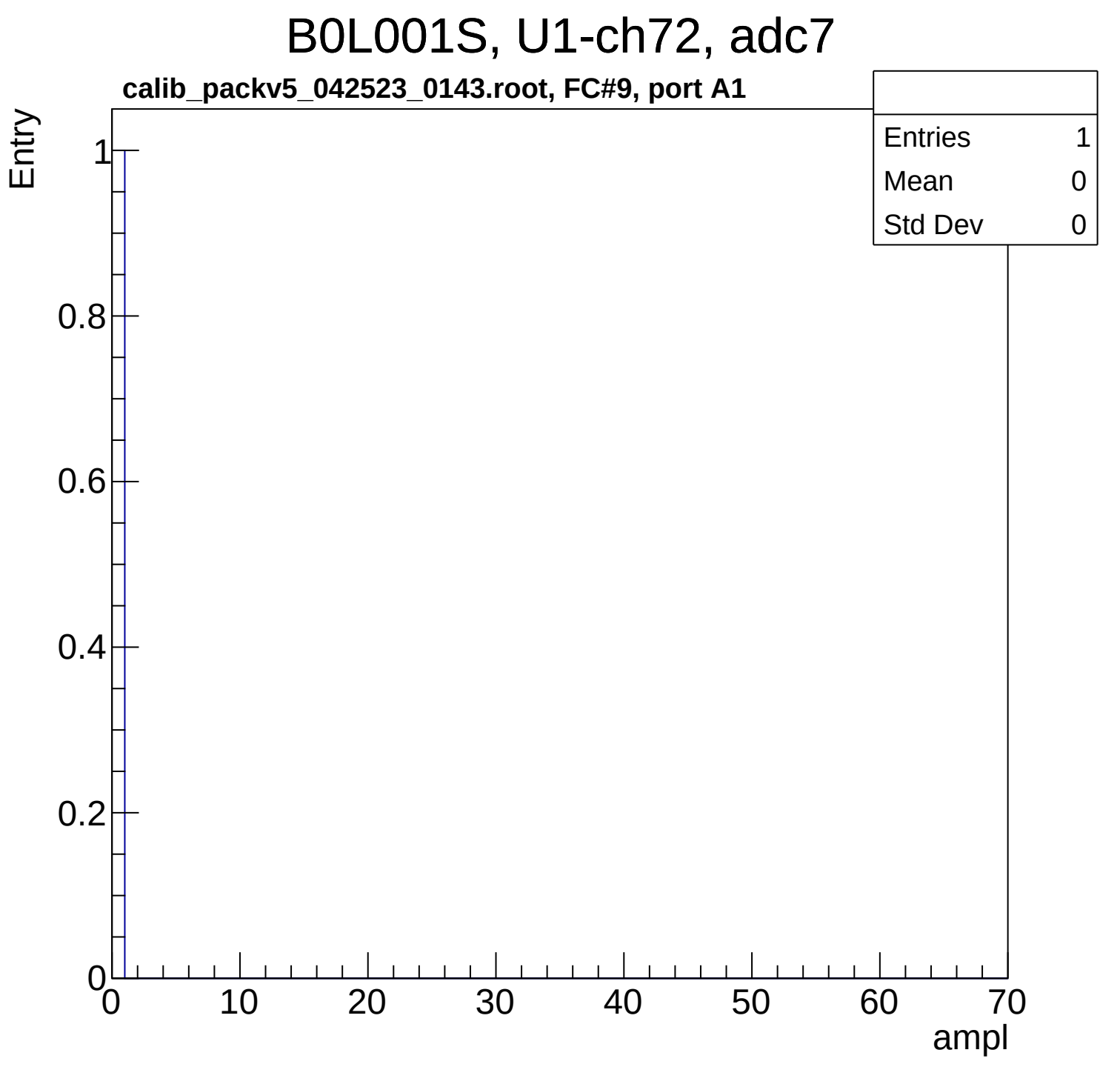

B0L001S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 43.25
Std Dev 3.644
Gaus mean : 44.4467
Gaus Width: 4.3294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.37
Std Dev 3.635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 54.41
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 59.1
Std Dev 2.574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 57.47
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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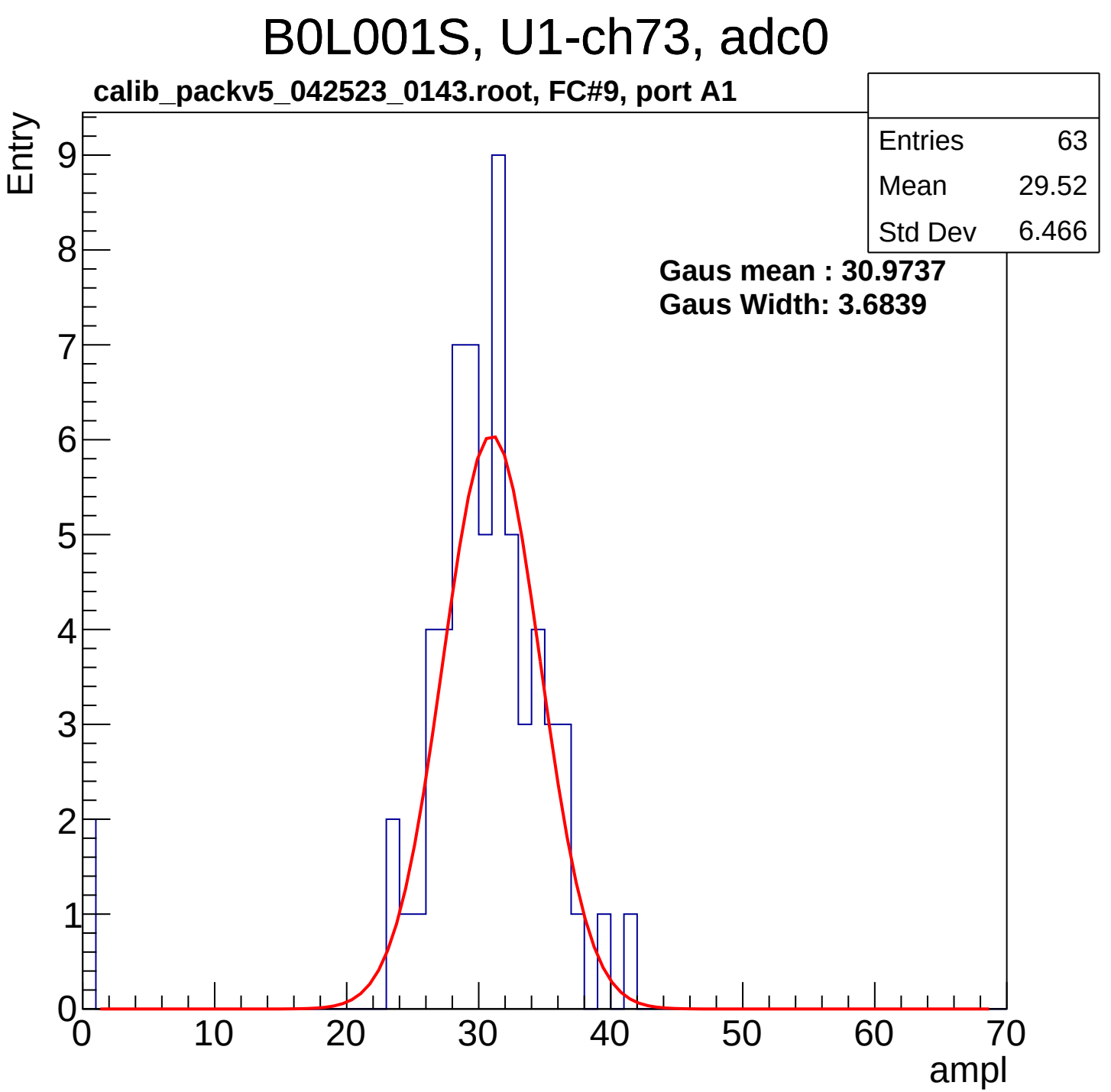
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.52
Std Dev 6.466
Gaus mean : 30.9737
Gaus Width: 3.6839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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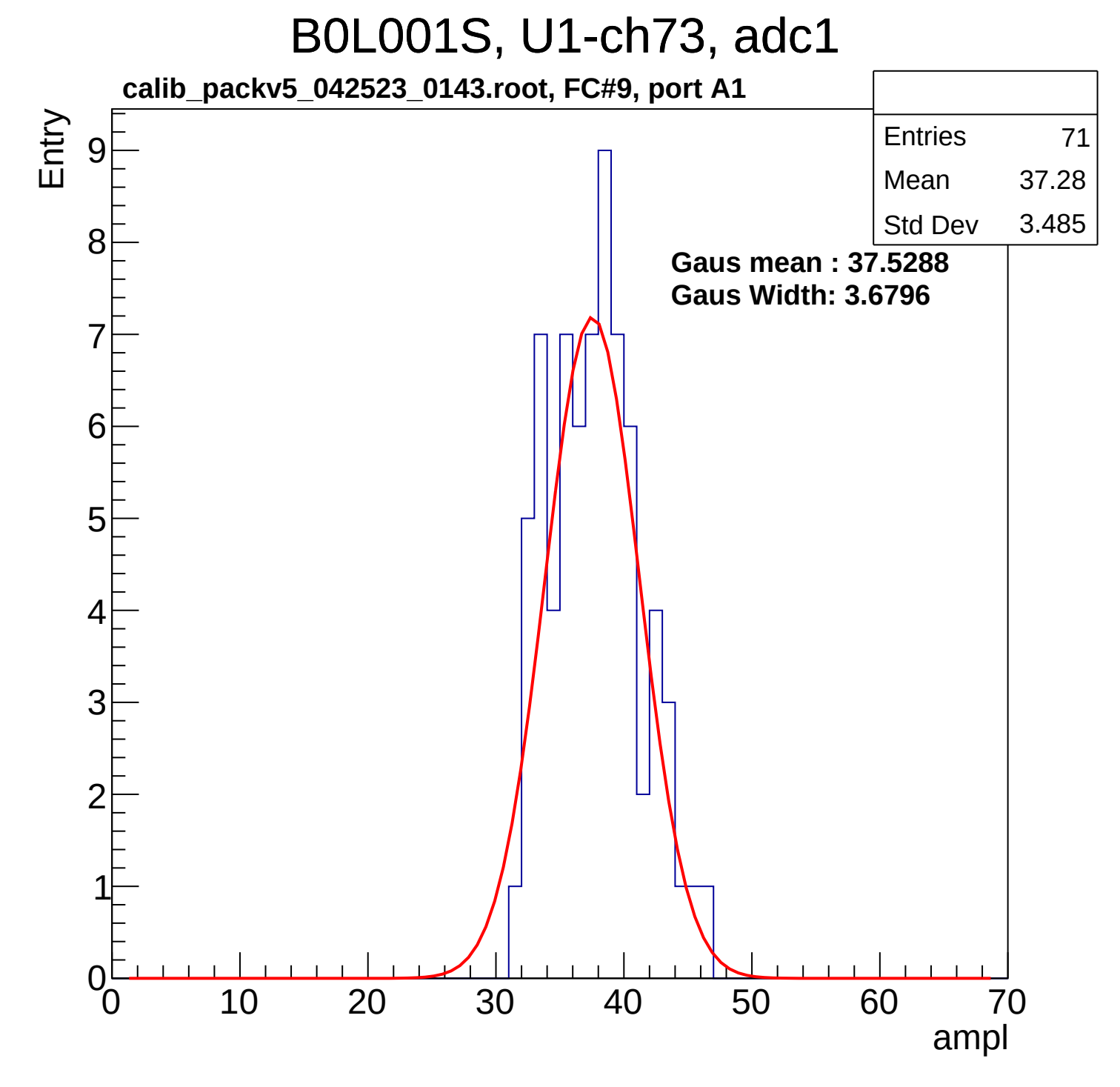
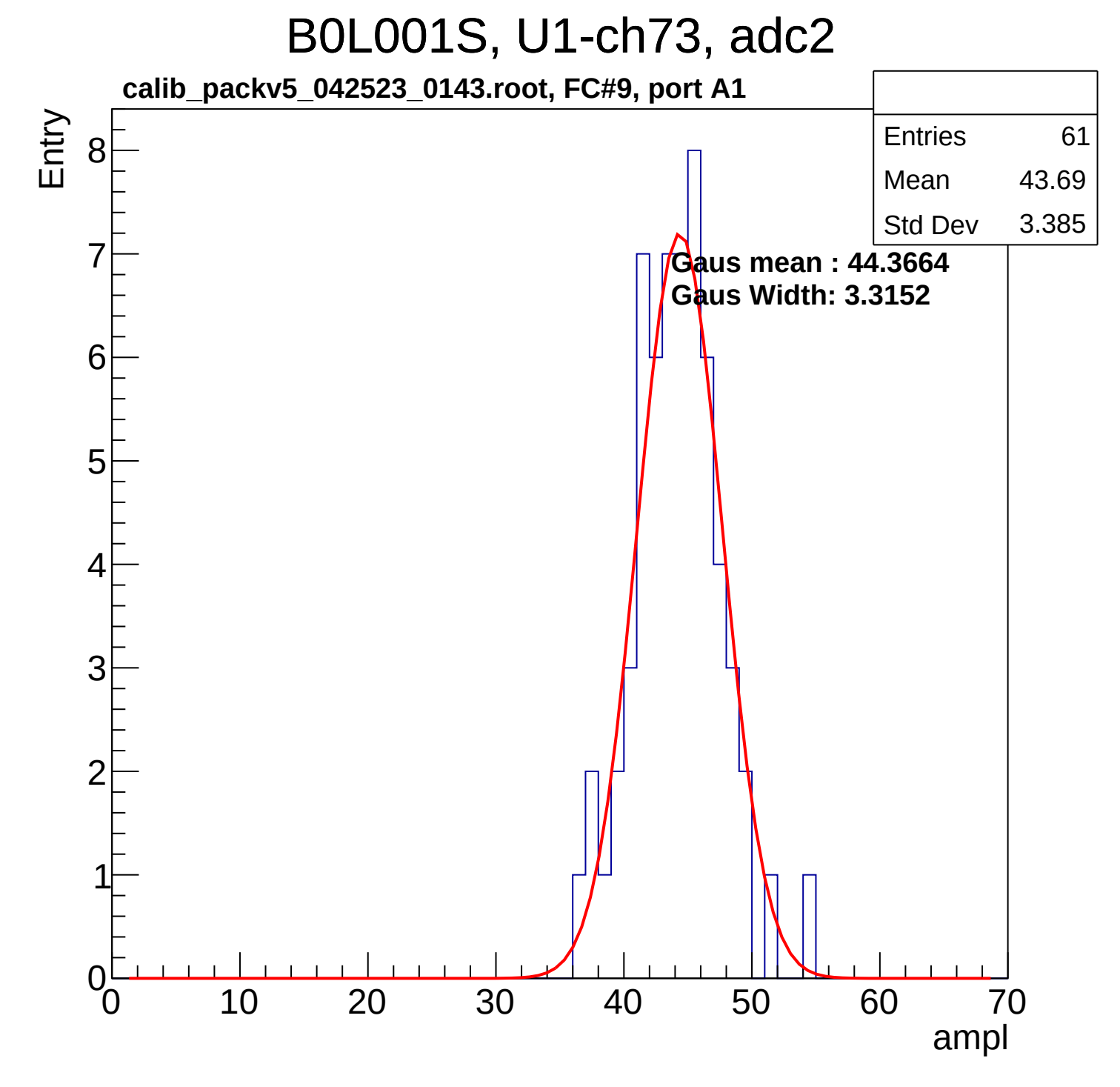
60
70


B0L001S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 37.28
Std Dev 3.485
Gaus mean : 37.5288
Gaus Width: 3.6796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 43.69
Std Dev 3.385
Gaus mean : 44.3664
Gaus Width: 3.3152
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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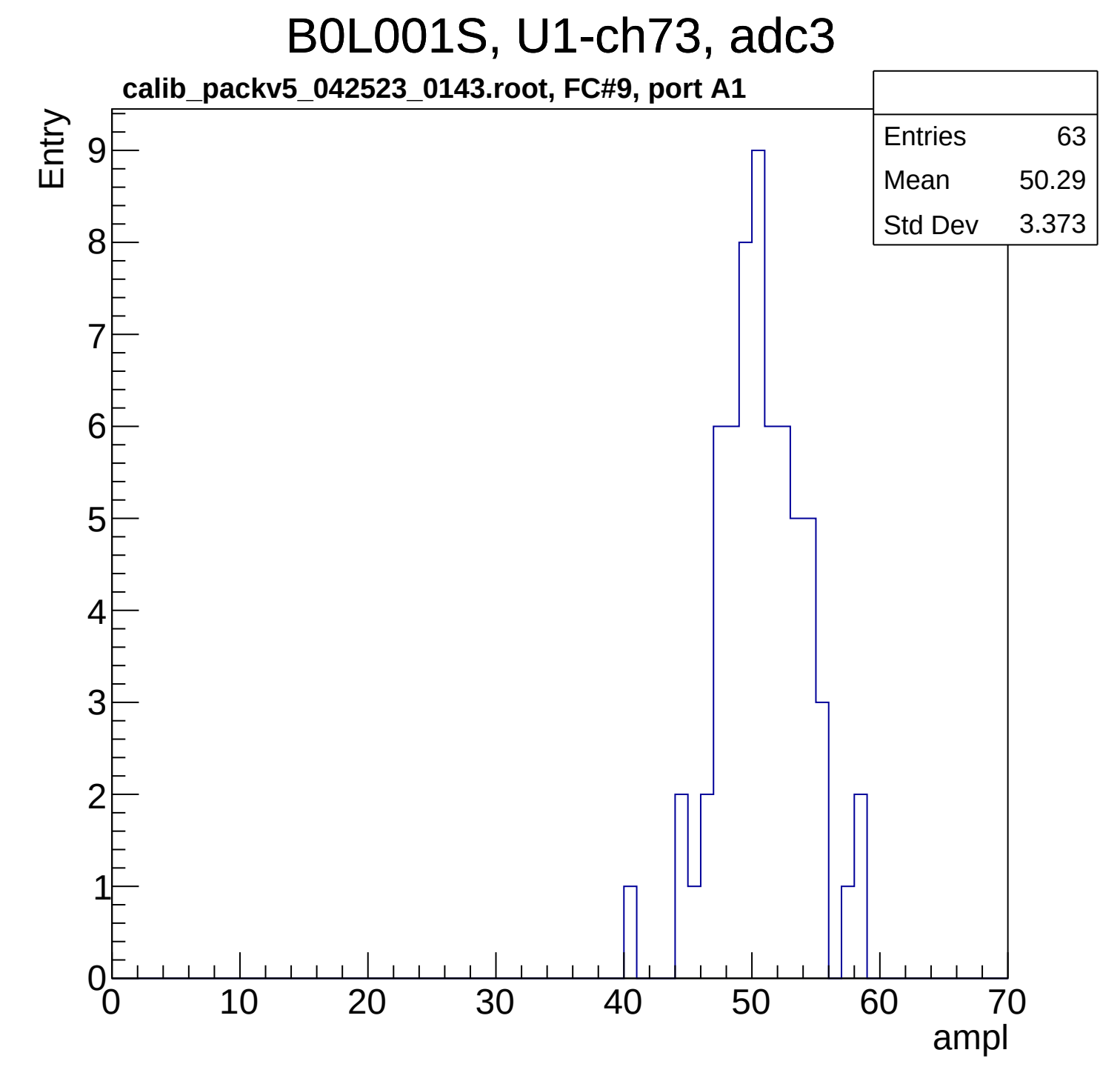
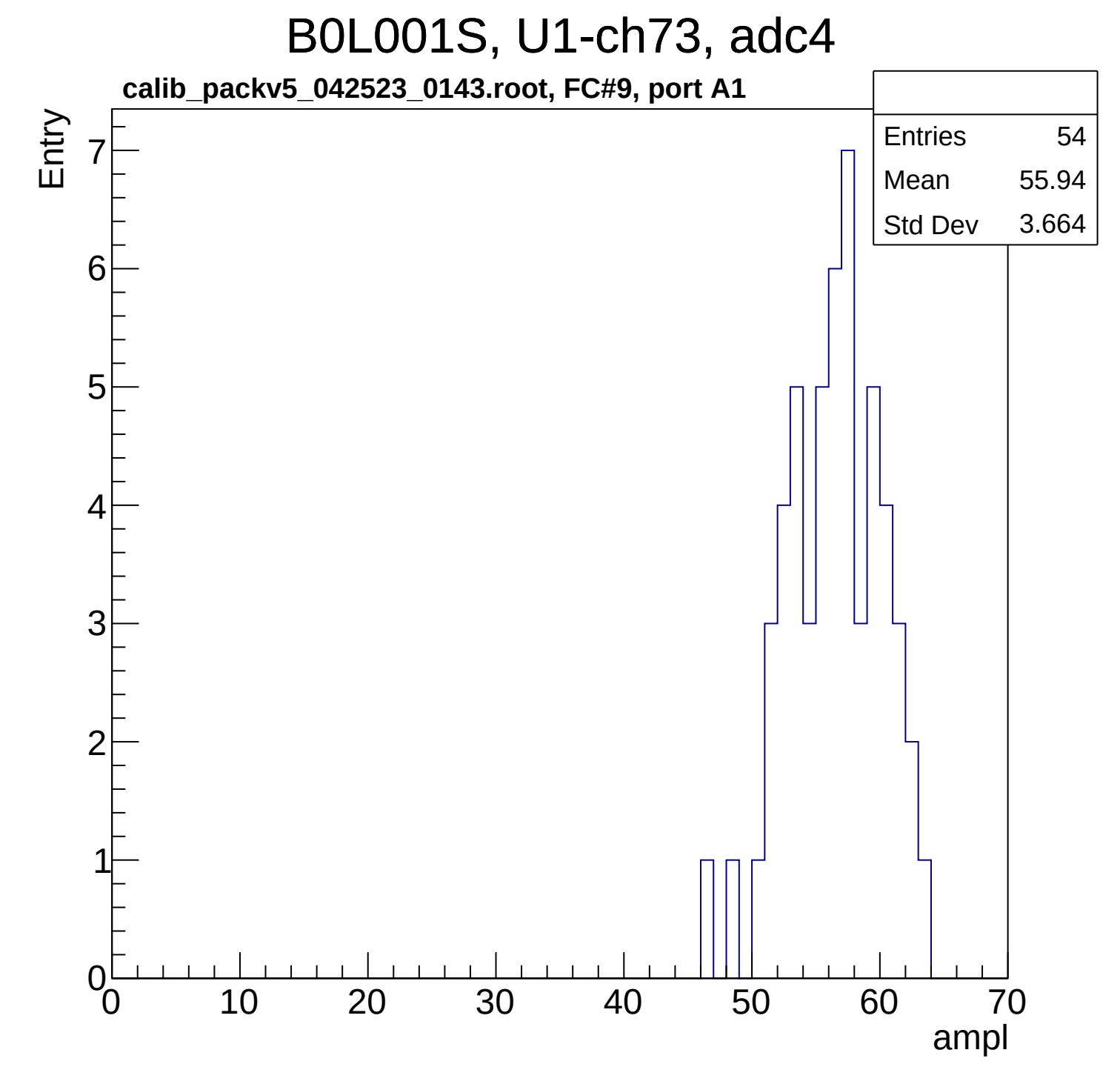
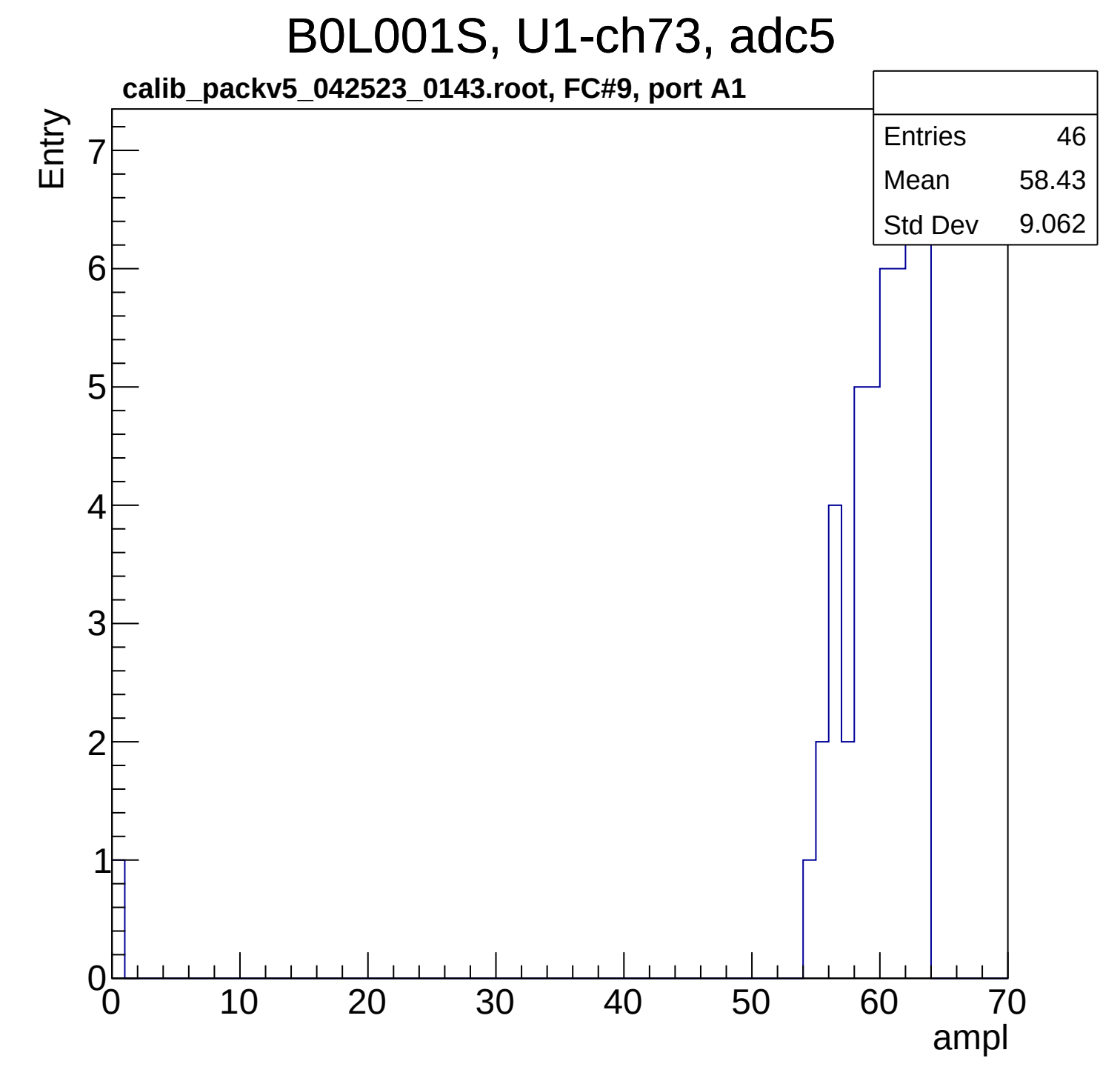
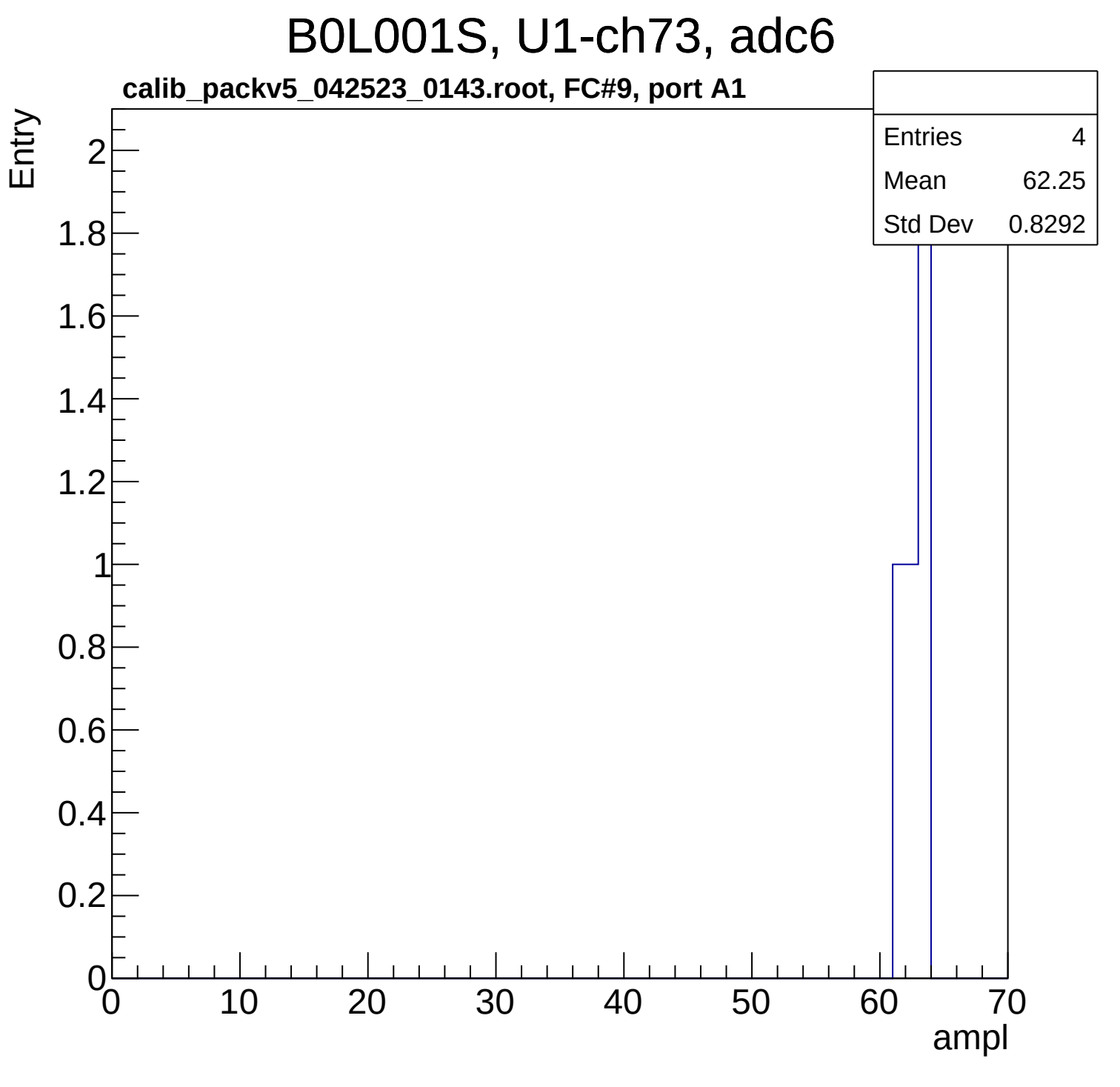
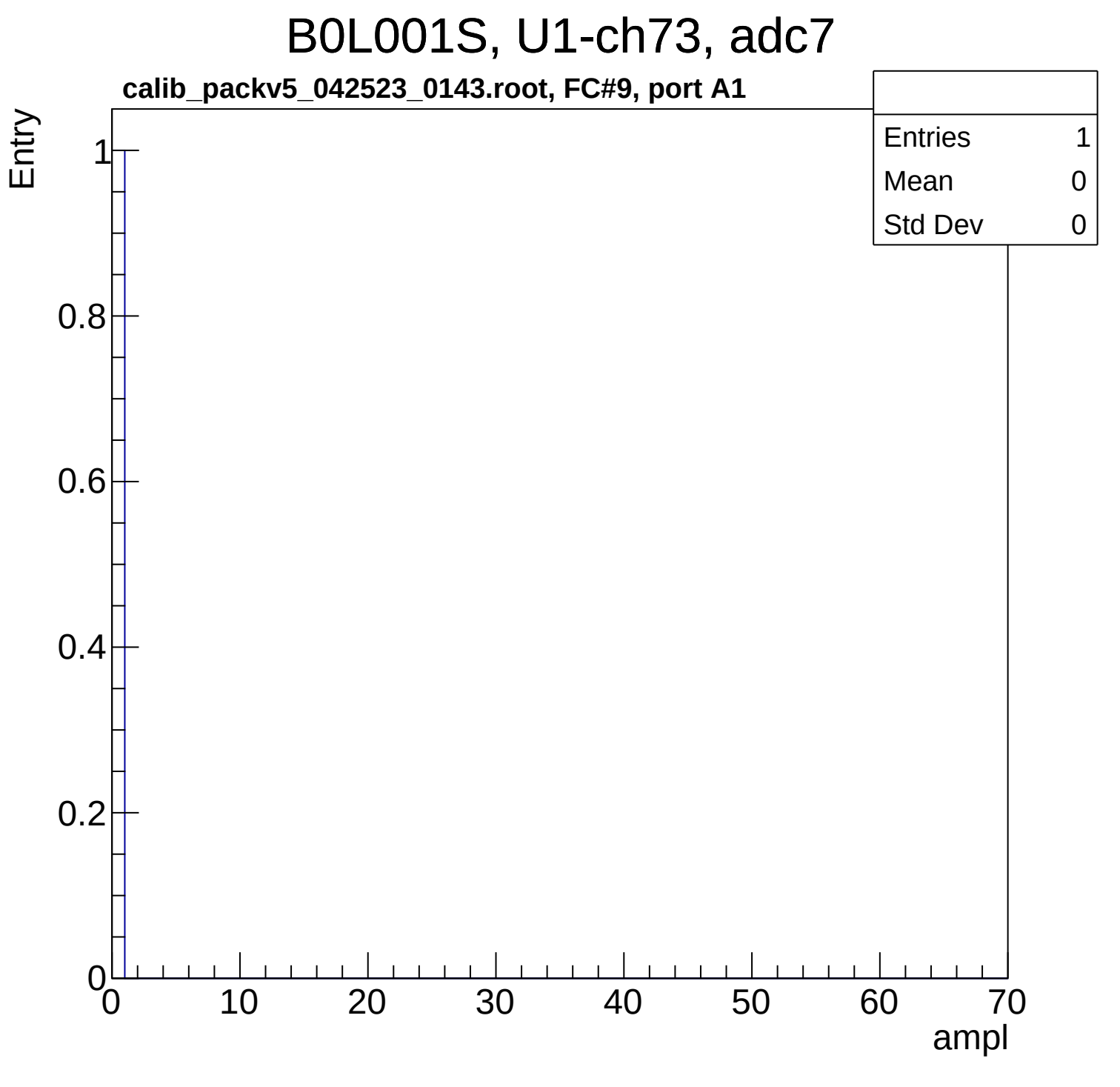
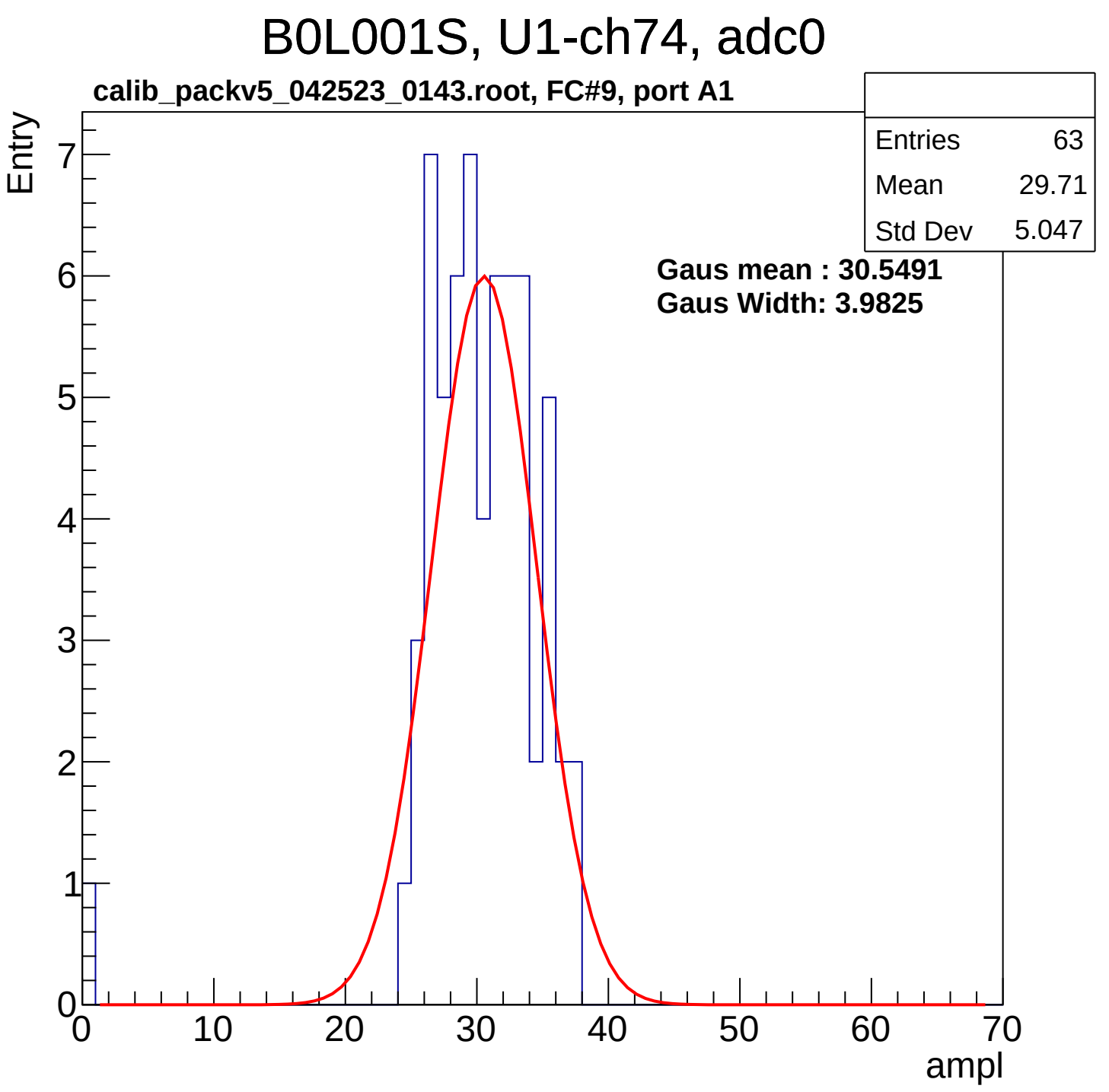

B0L001S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.29
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

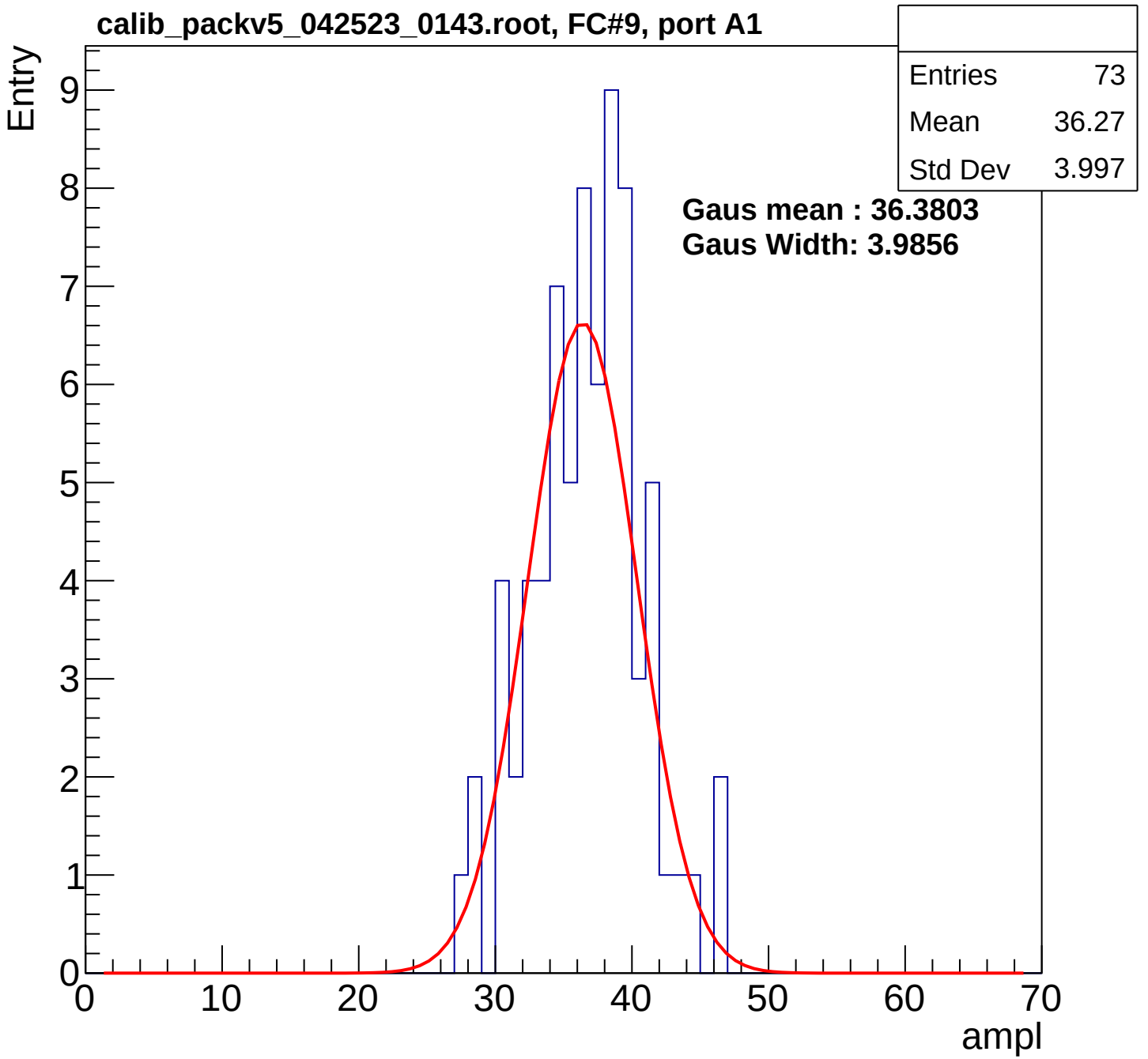

B0L001S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 55.94
Std Dev 3.664
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

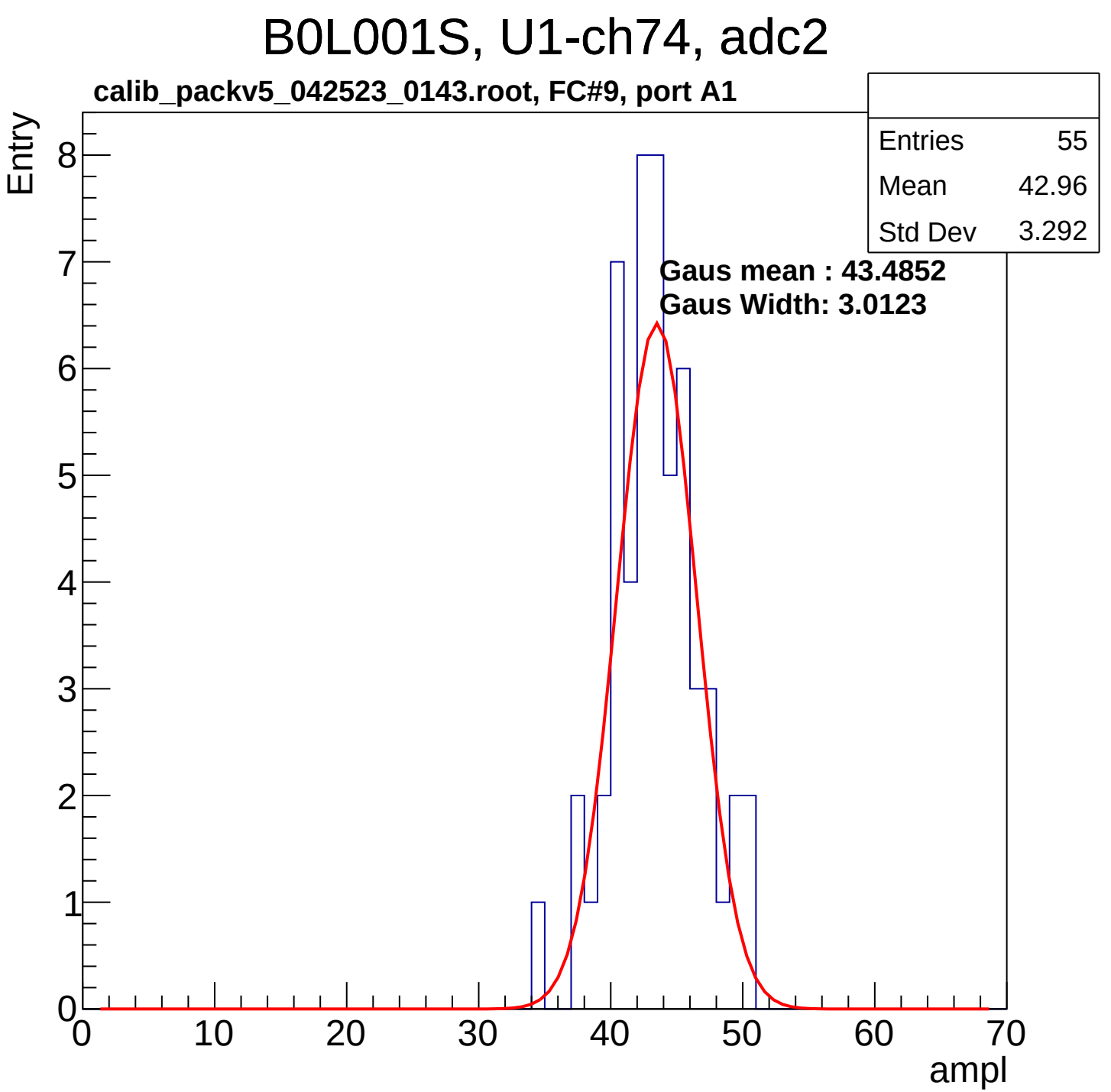

B0L001S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.43
Std Dev 9.062
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

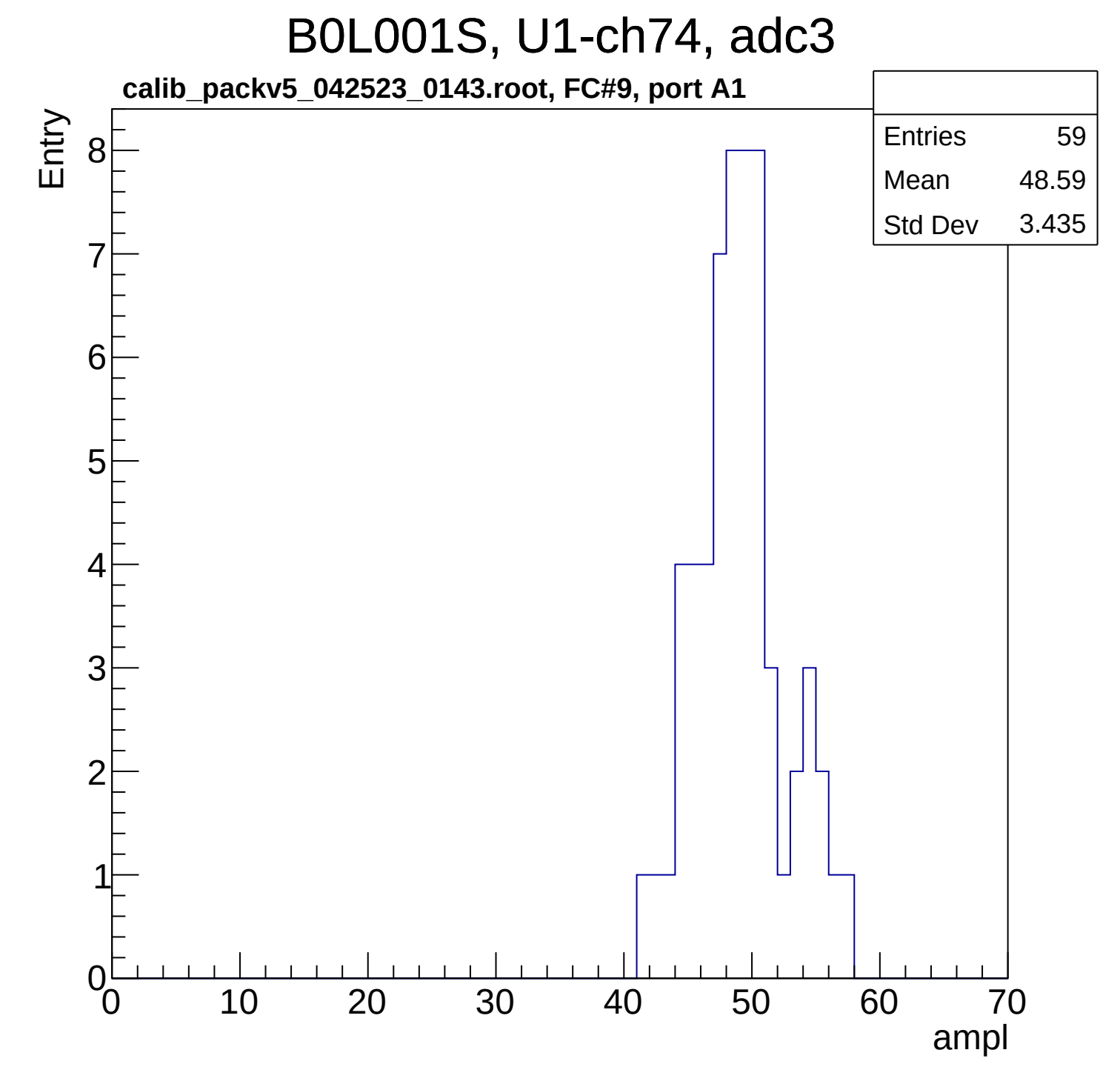

B0L001S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

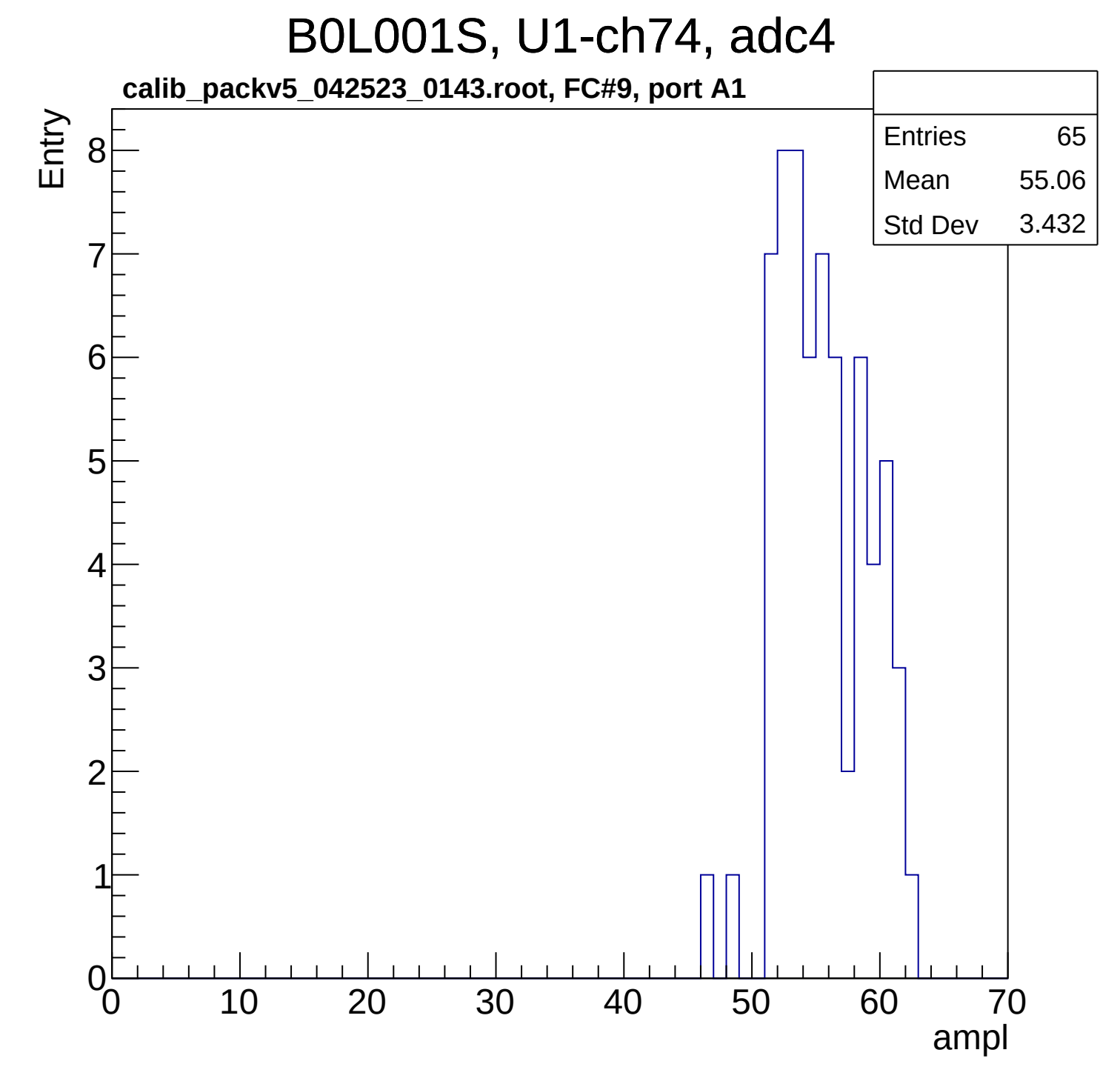

B0L001S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

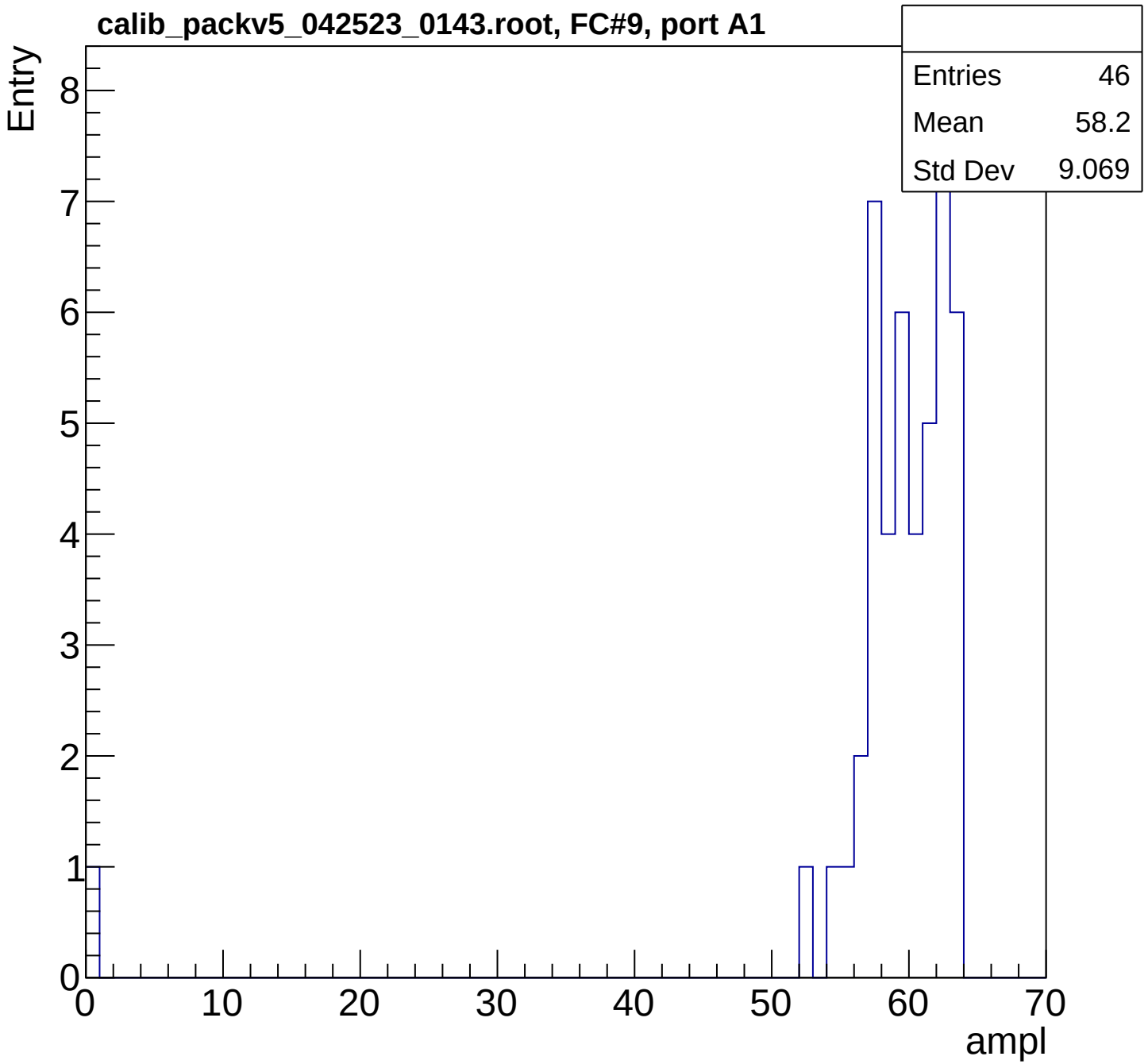

B0L001S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.71
Std Dev 5.047
Gaus mean : 30.5491
Gaus Width: 3.9825
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

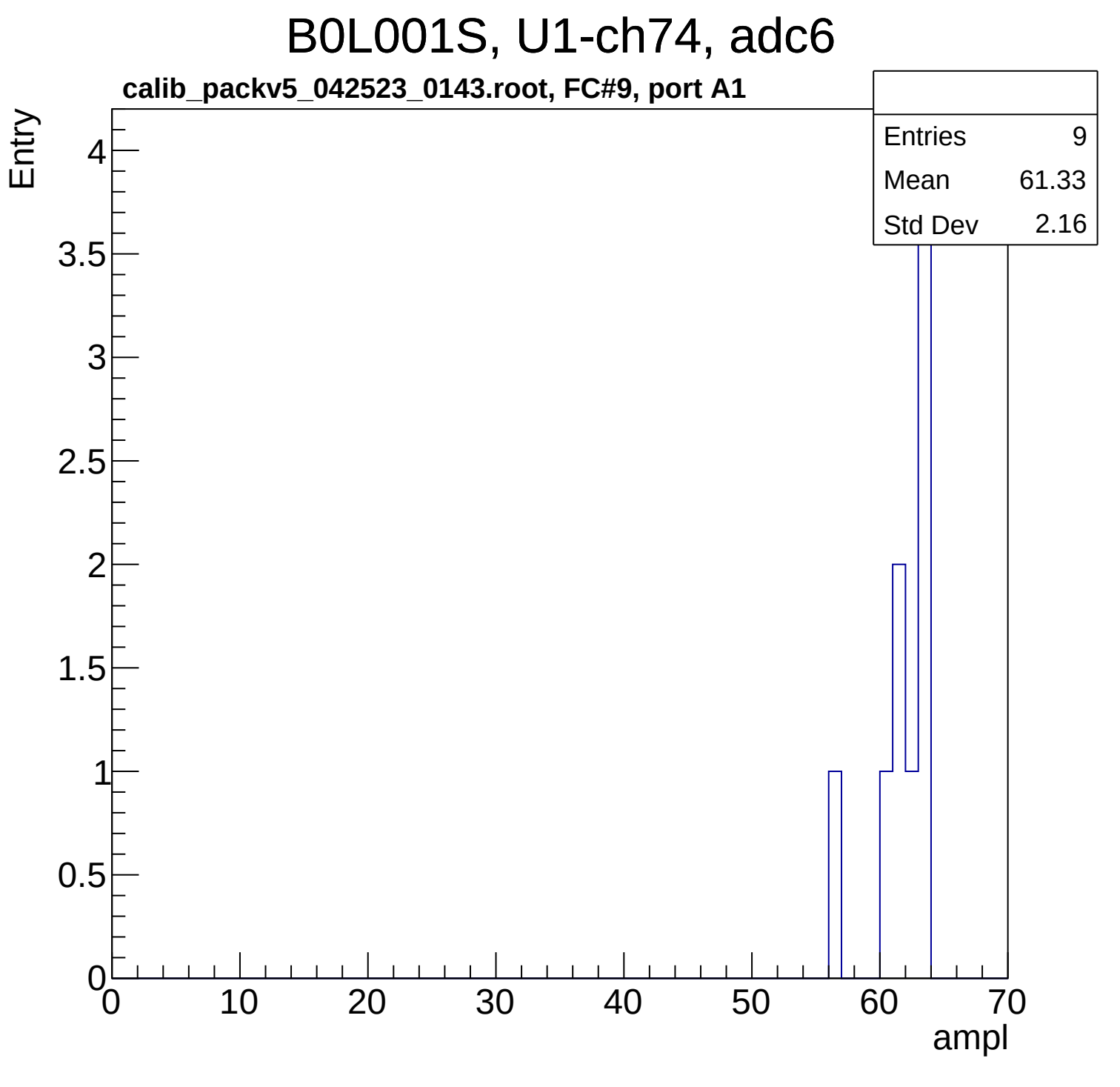

B0L001S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 36.27
Std Dev 3.997
Gaus mean : 36.3803
Gaus Width: 3.9856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

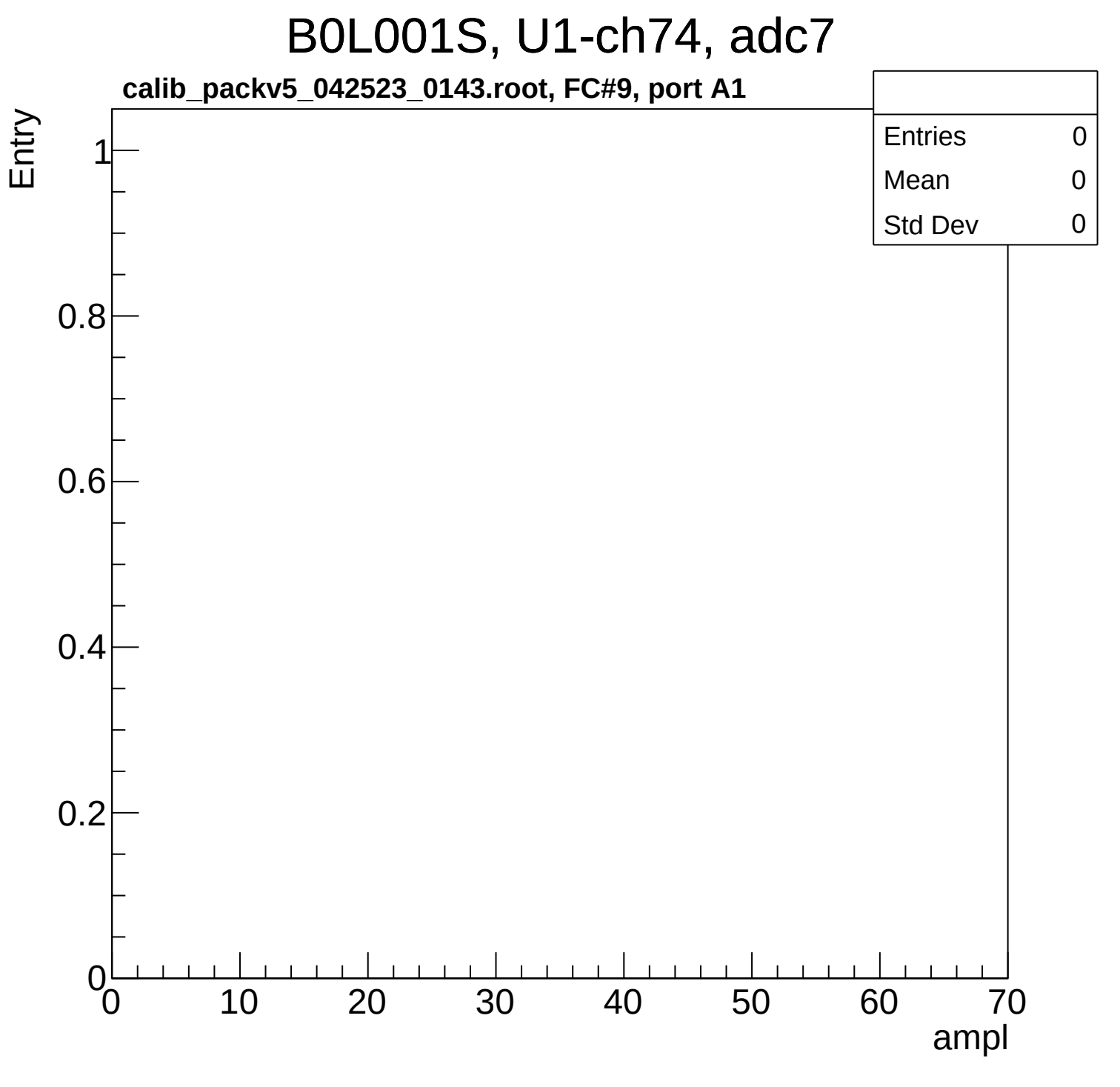

B0L001S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.96
Std Dev 3.292
Gaus mean : 43.4852
Gaus Width: 3.0123
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

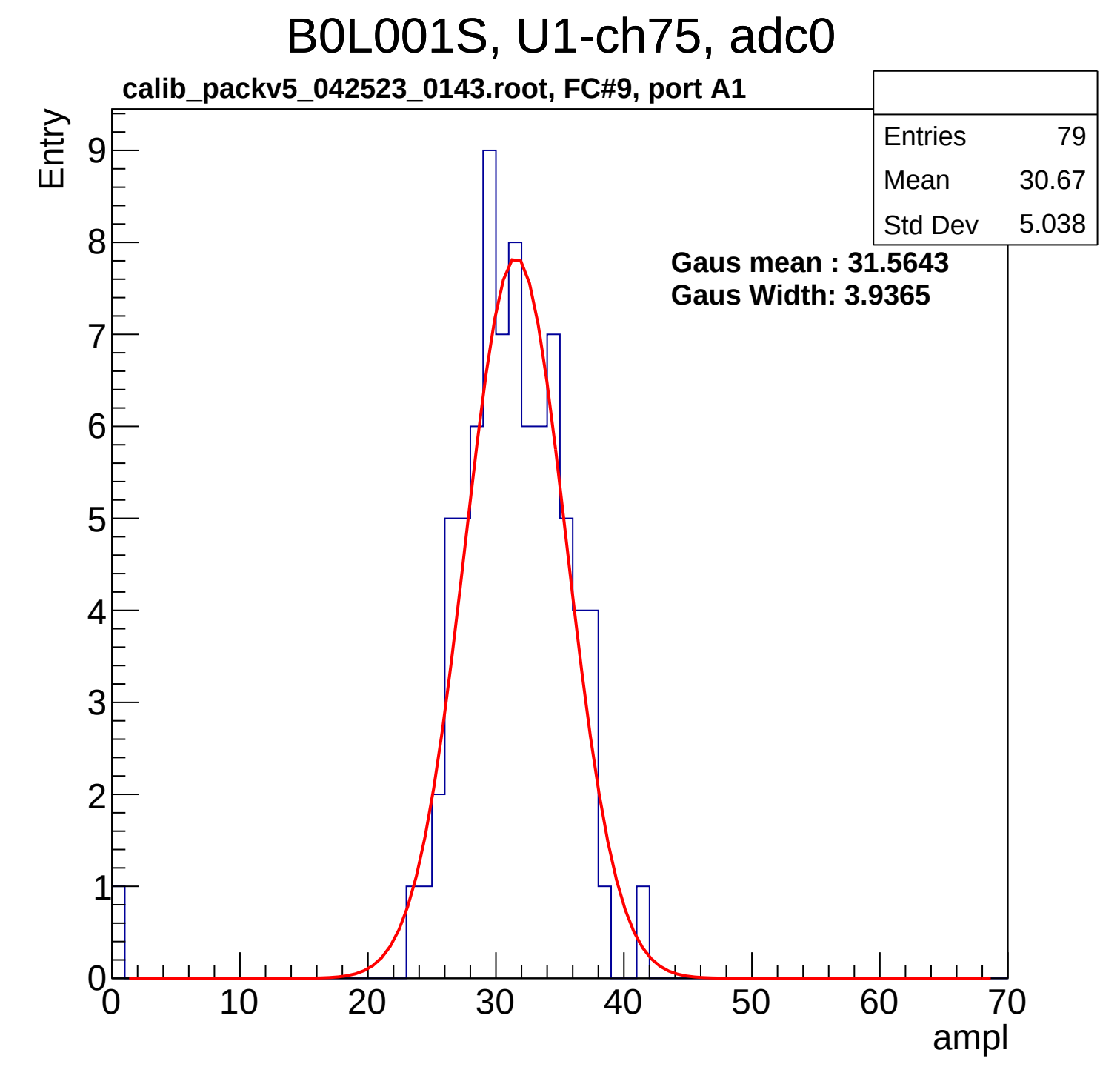

B0L001S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 48.59
Std Dev 3.435
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

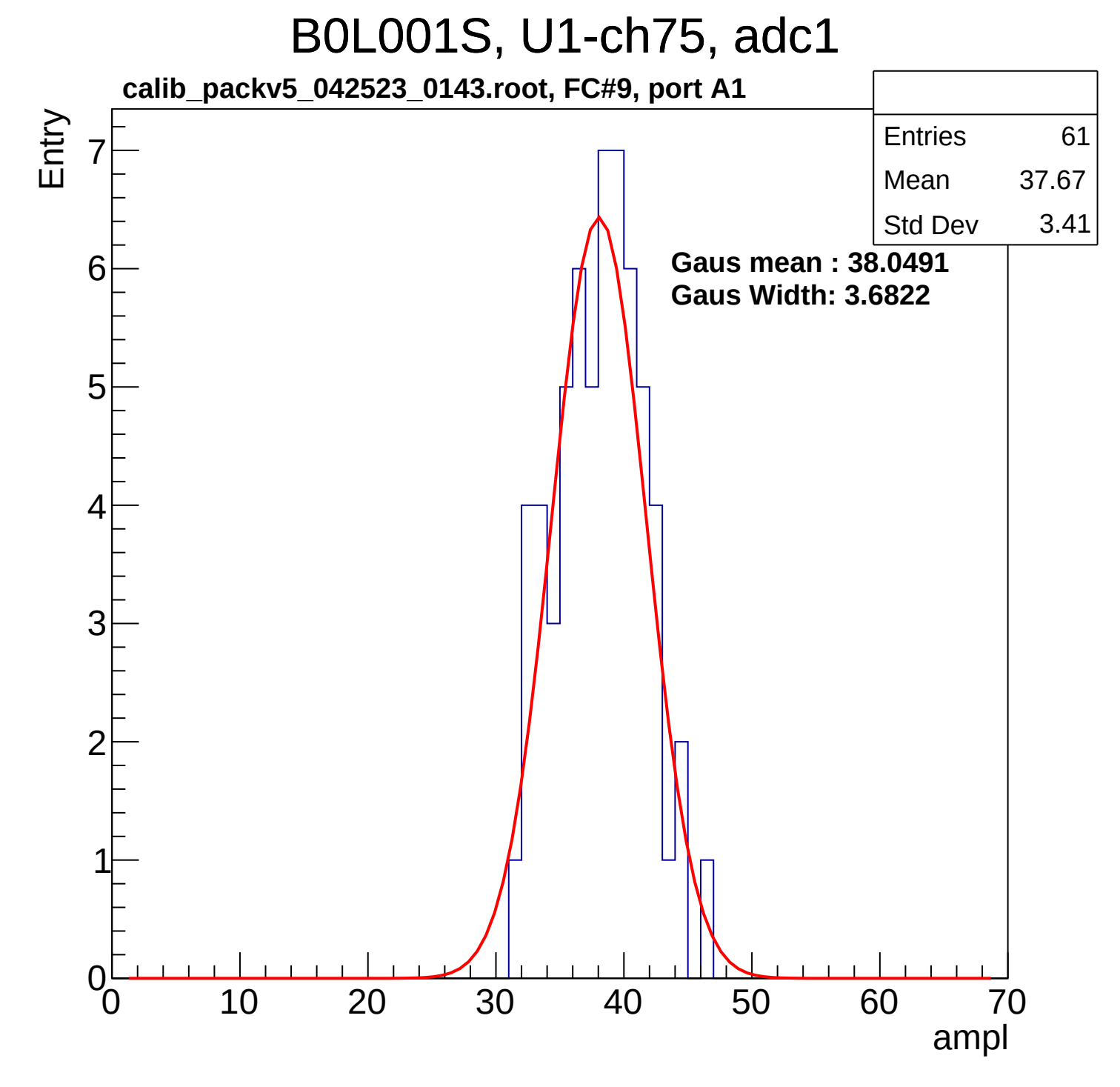

B0L001S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 55.06
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

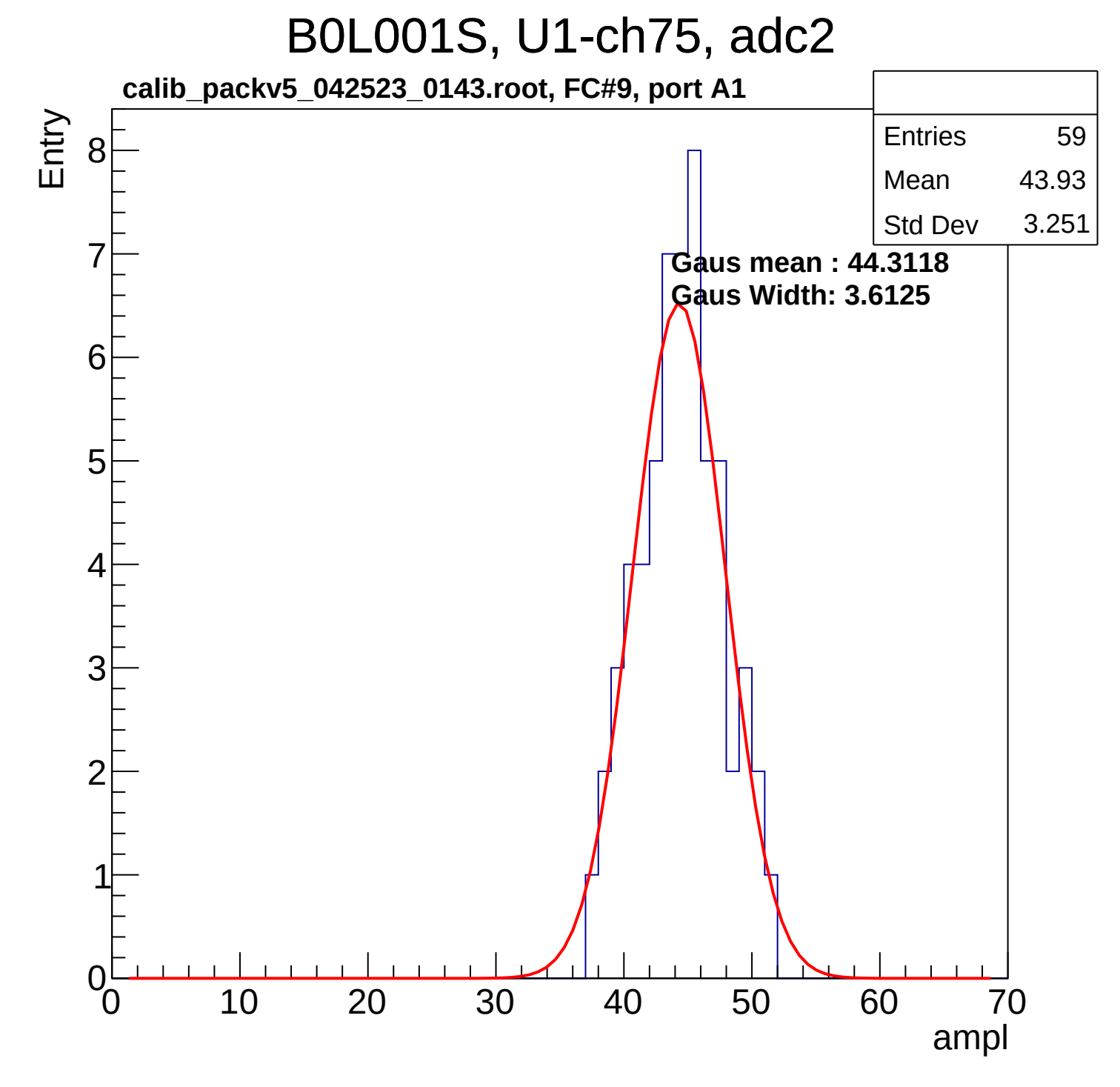

B0L001S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 58.2
Std Dev 9.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

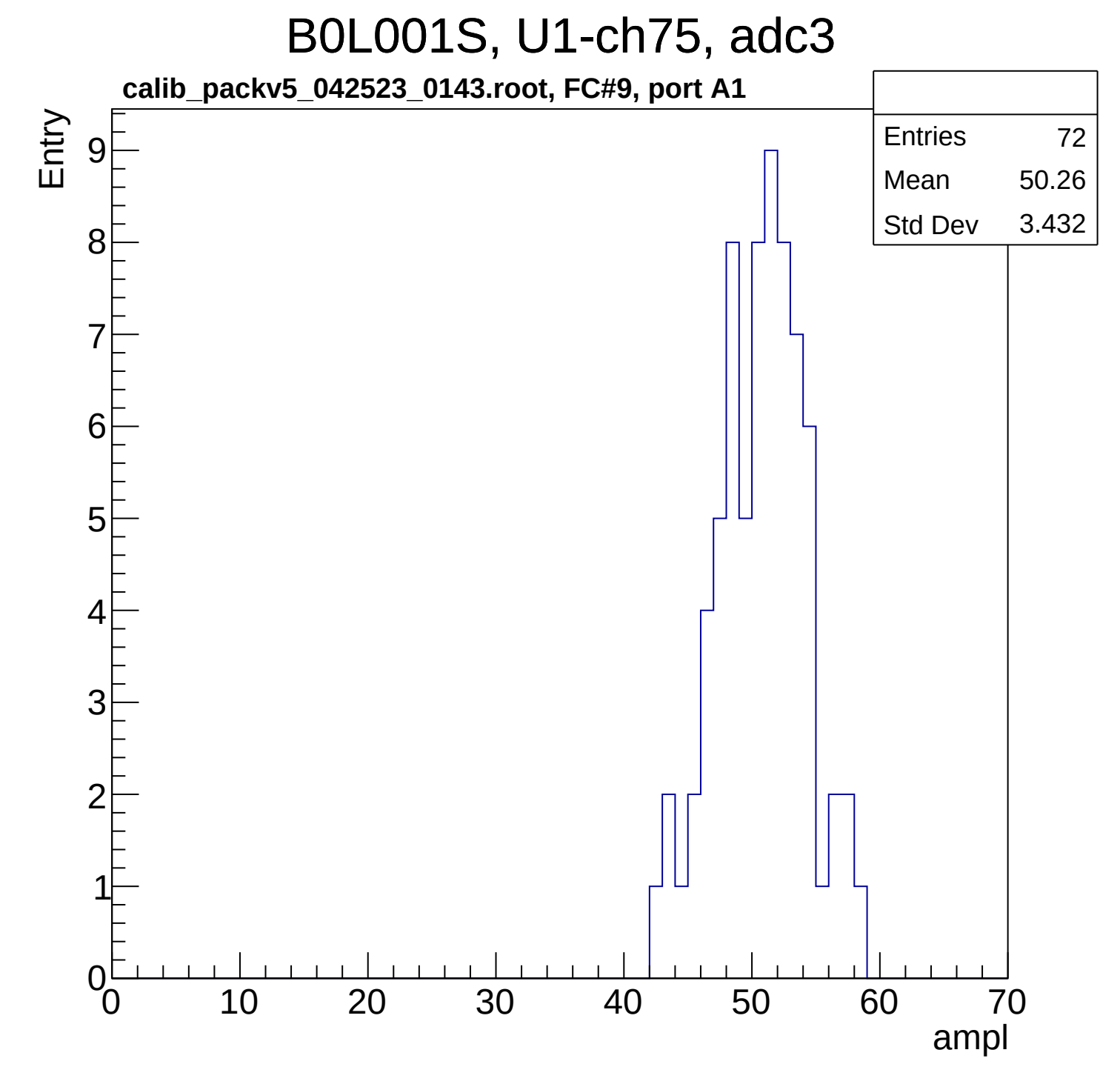

B0L001S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.33
Std Dev 2.16
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

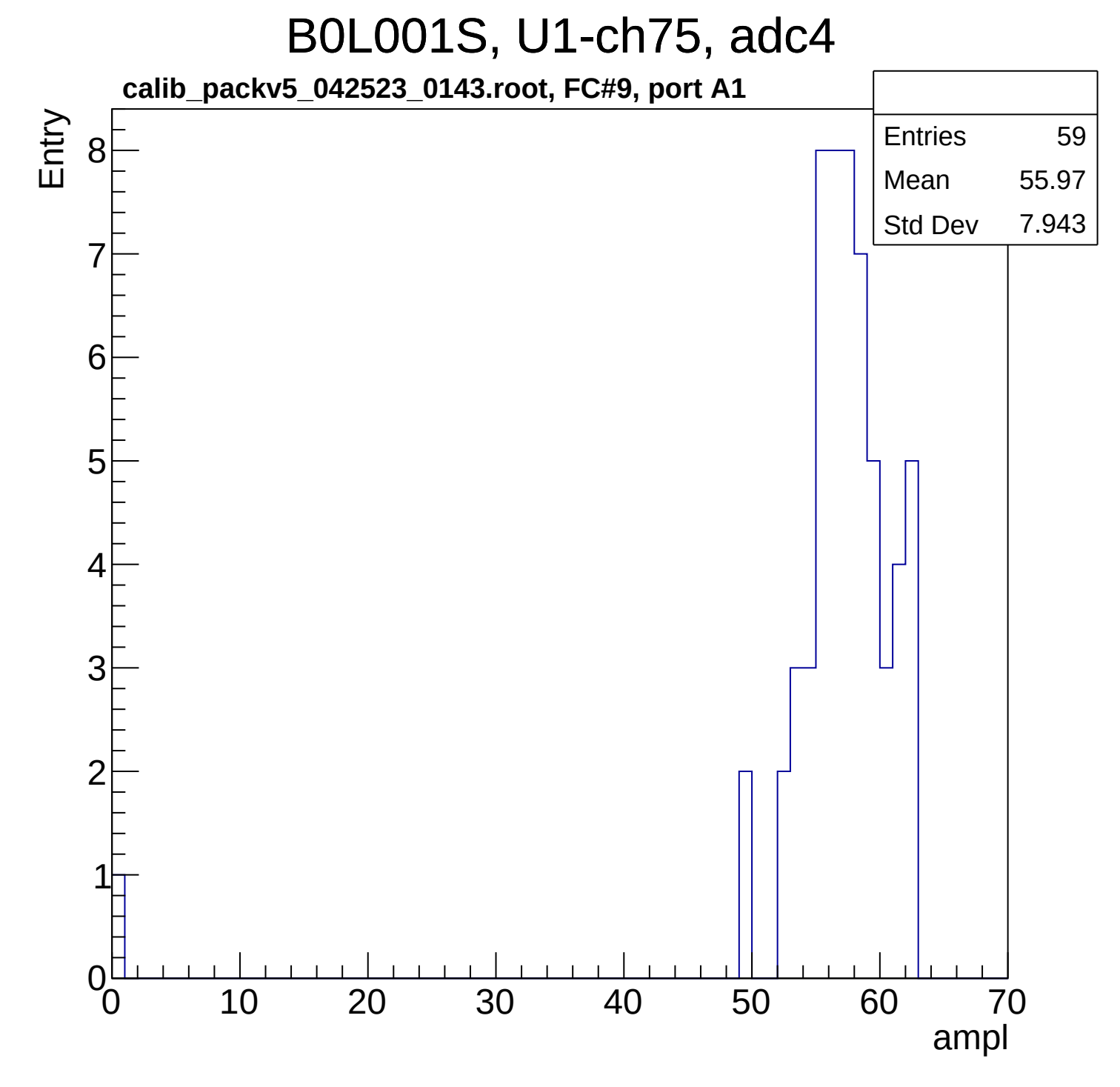

B0L001S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

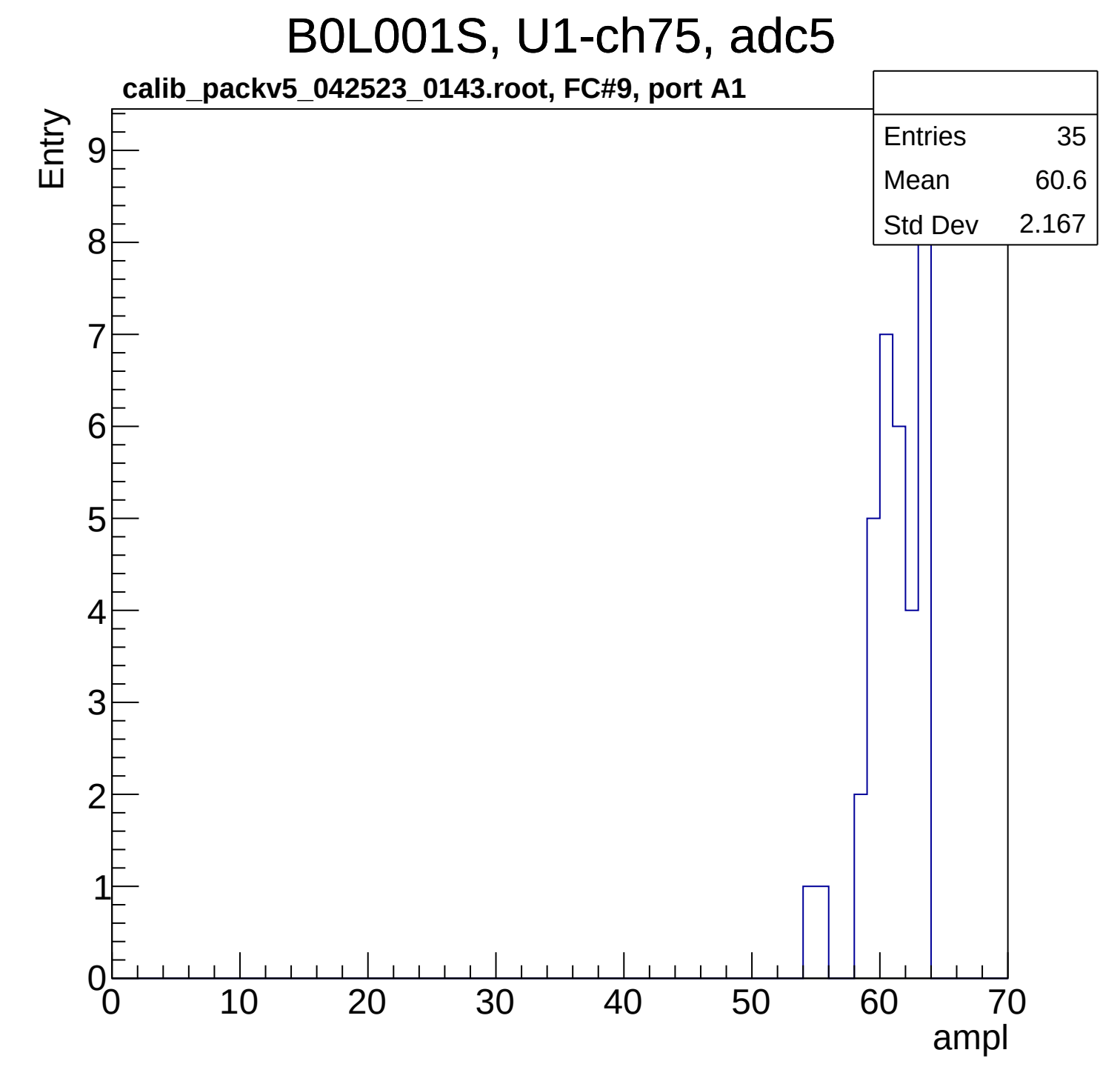

B0L001S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.67
Std Dev 5.038
Gaus mean : 31.5643
Gaus Width: 3.9365
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

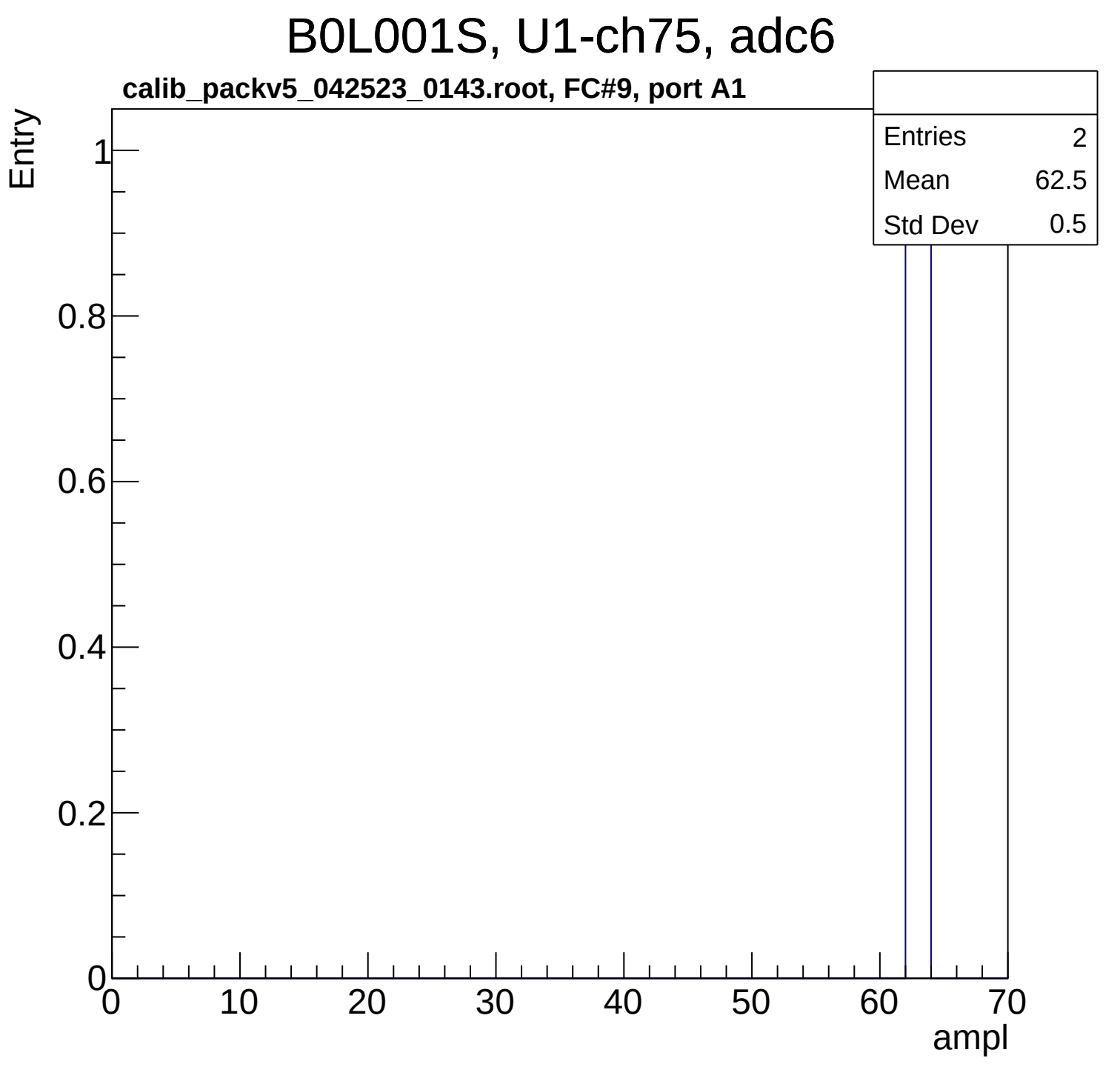

B0L001S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 37.67
Std Dev 3.41
Gaus mean : 38.0491
Gaus Width: 3.6822
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

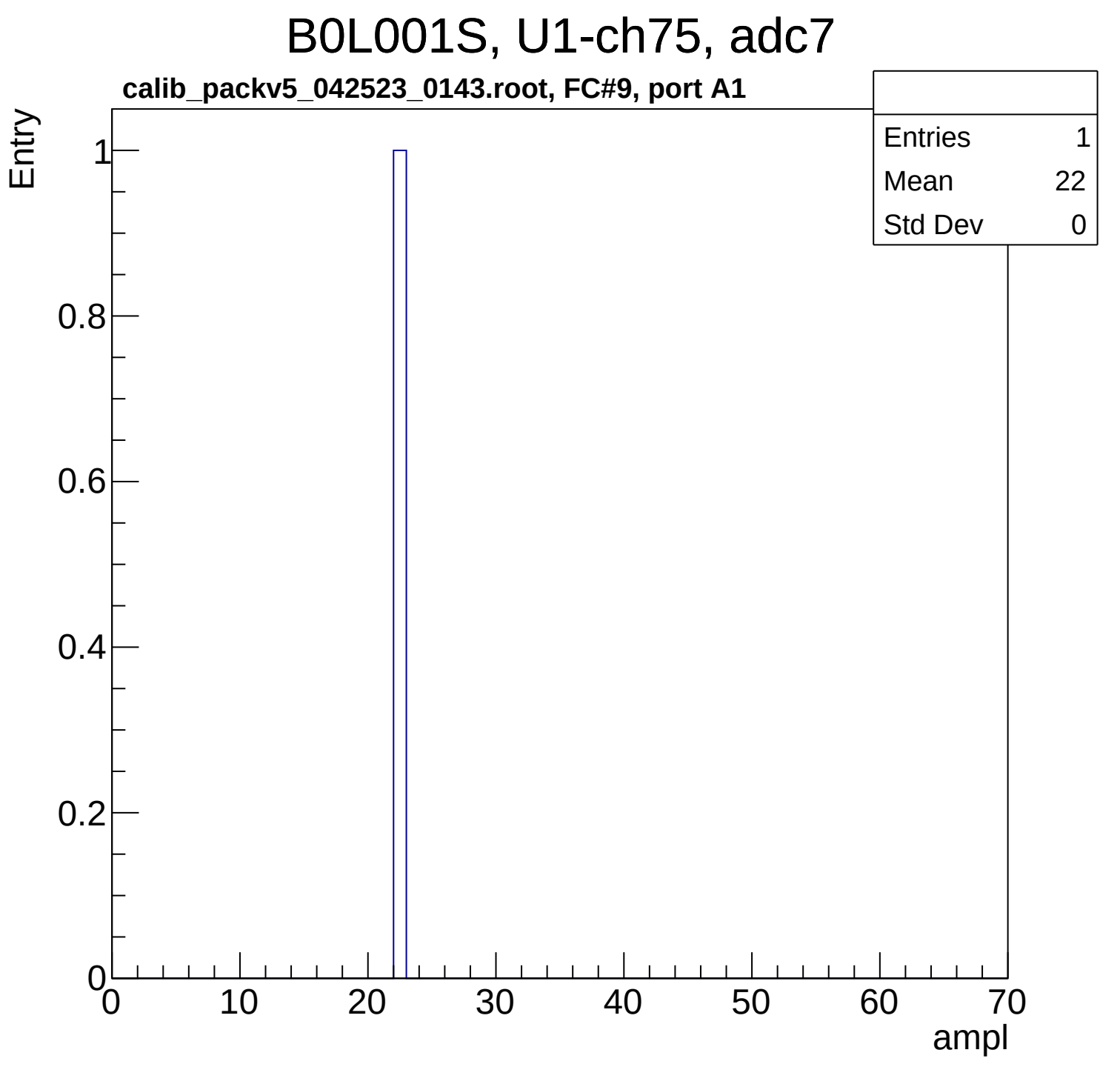

B0L001S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 43.93
Std Dev 3.251
Gaus mean : 44.3118
Gaus Width: 3.6125
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

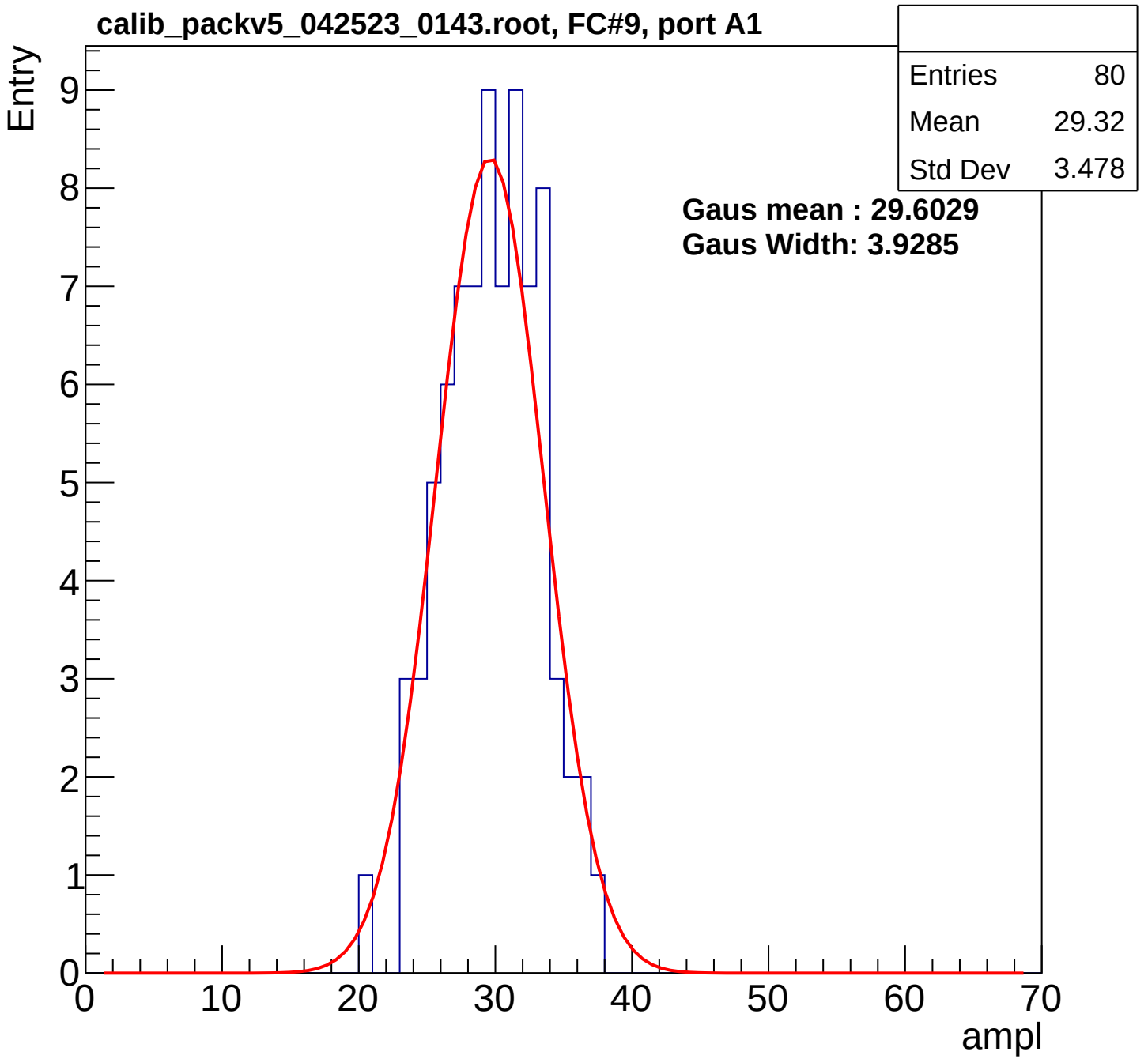

B0L001S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 50.26
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

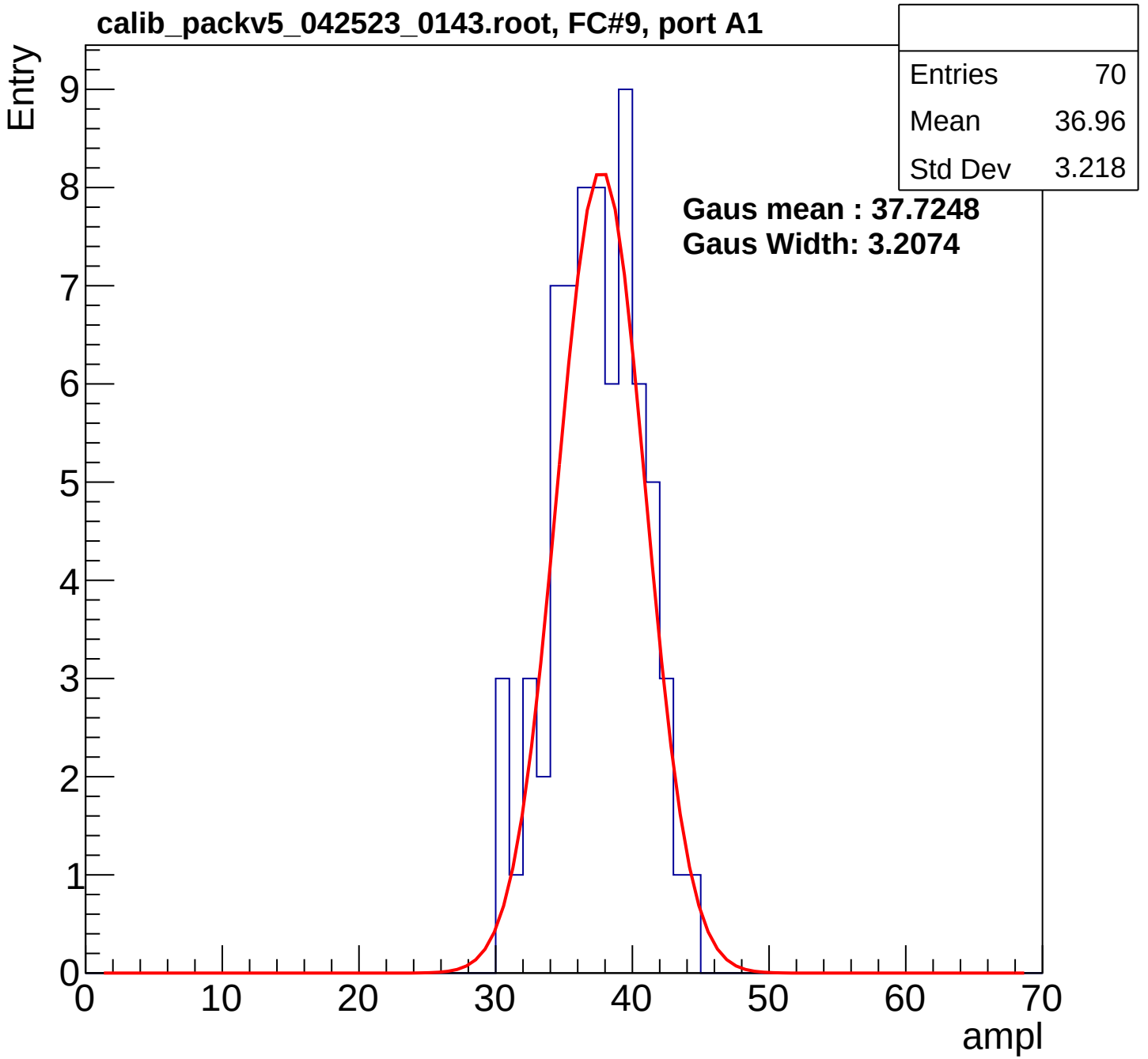

B0L001S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.97
Std Dev 7.943
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

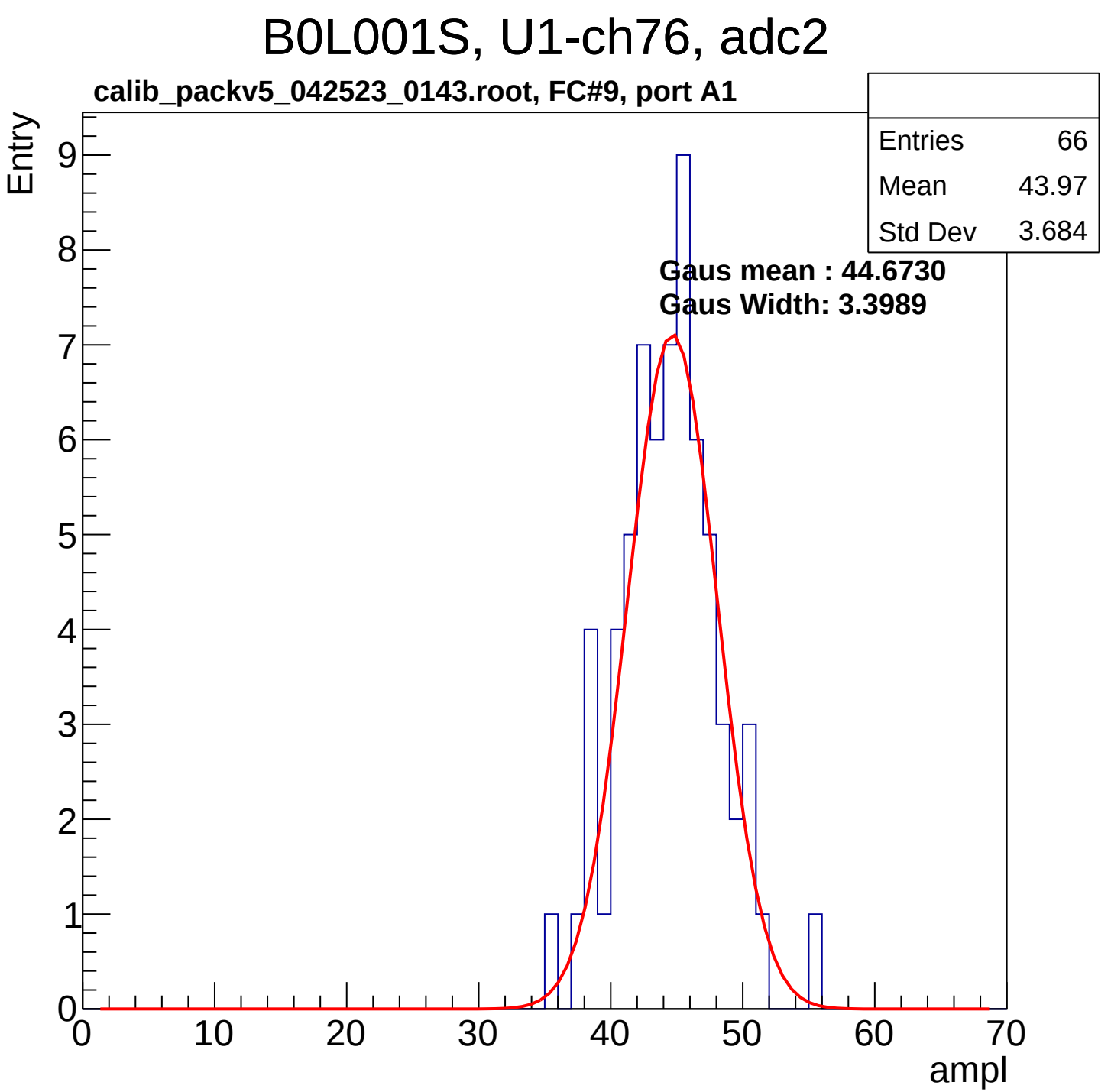

B0L001S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.6
Std Dev 2.167
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

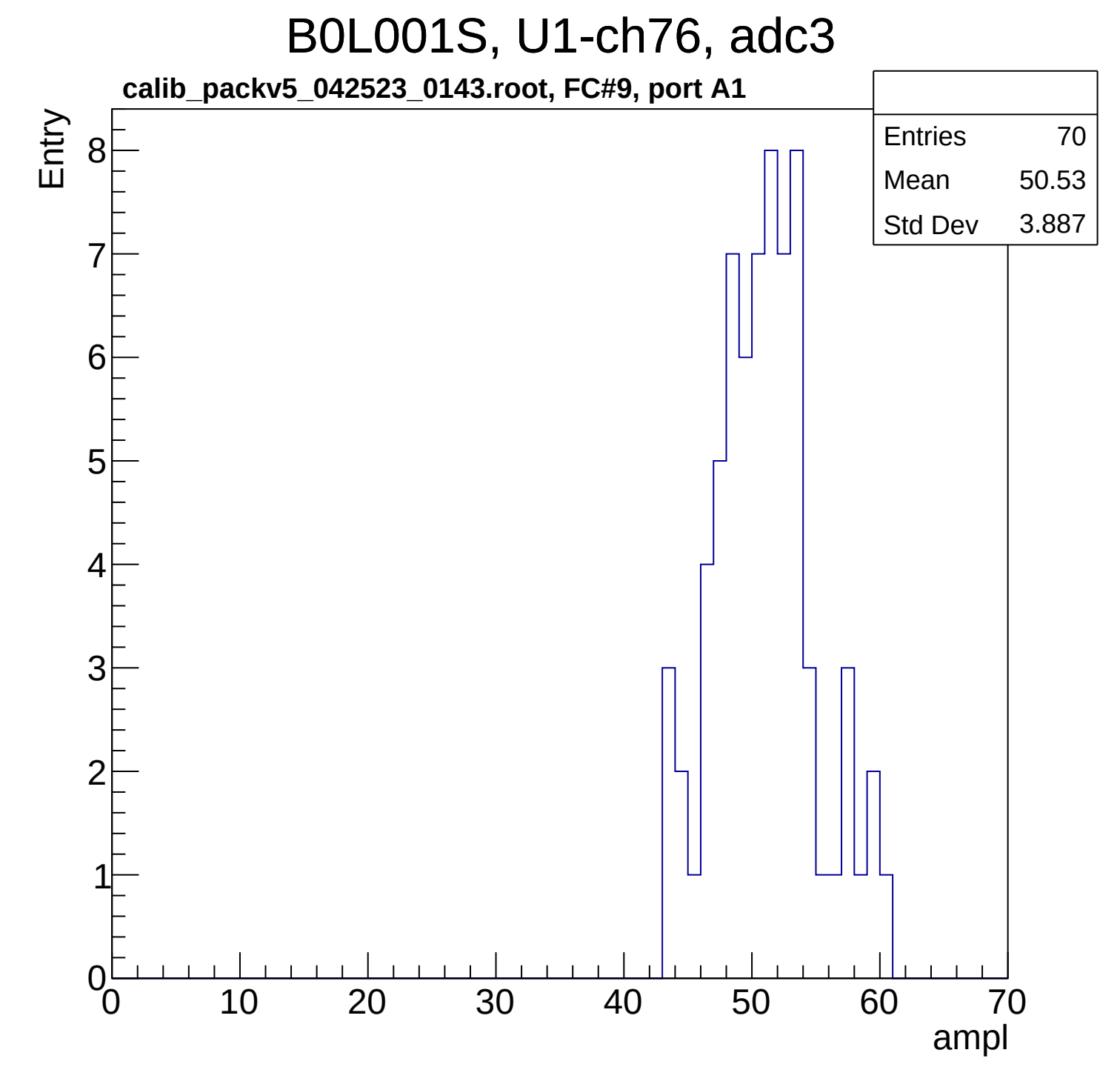

B0L001S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

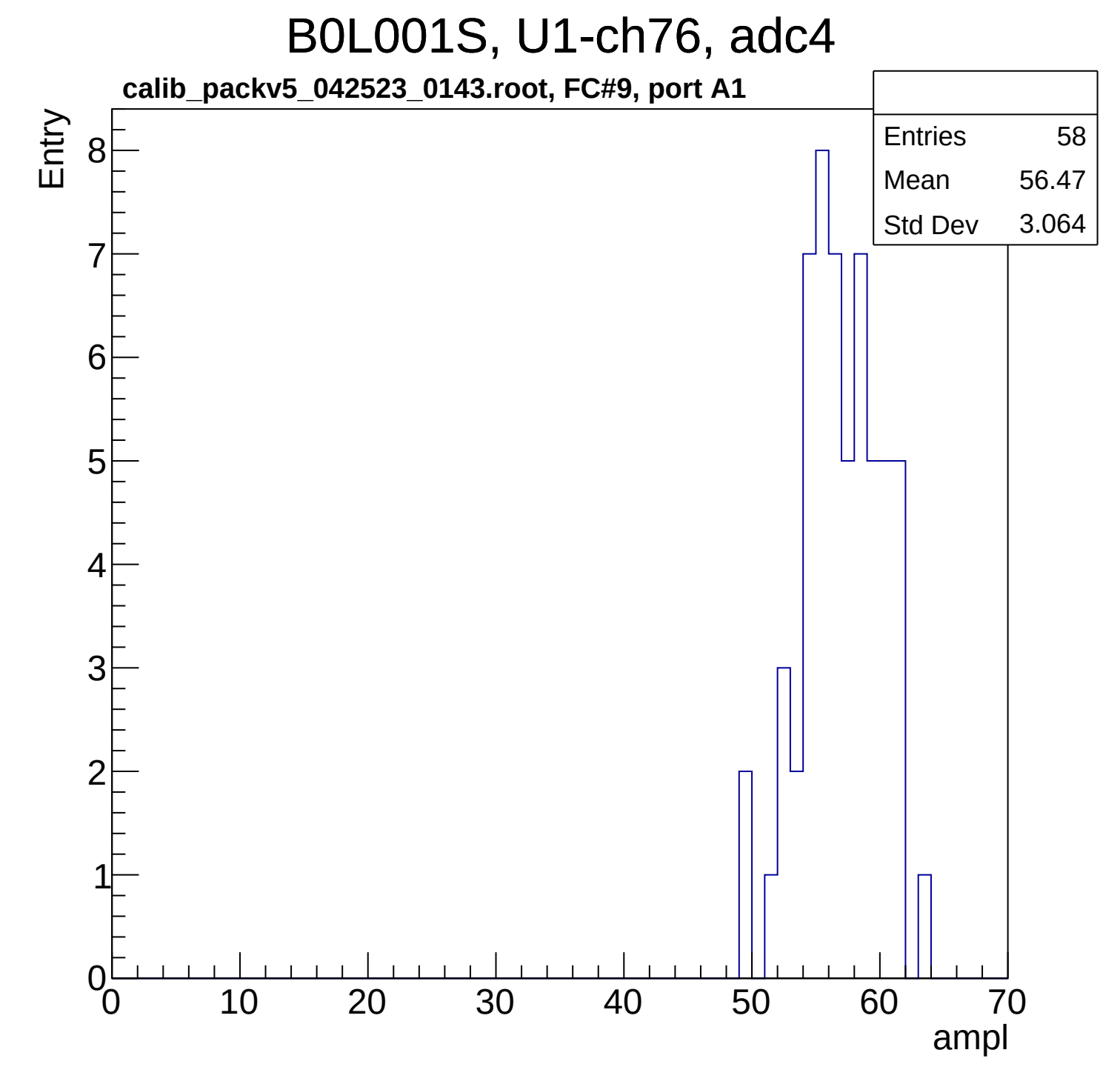

B0L001S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

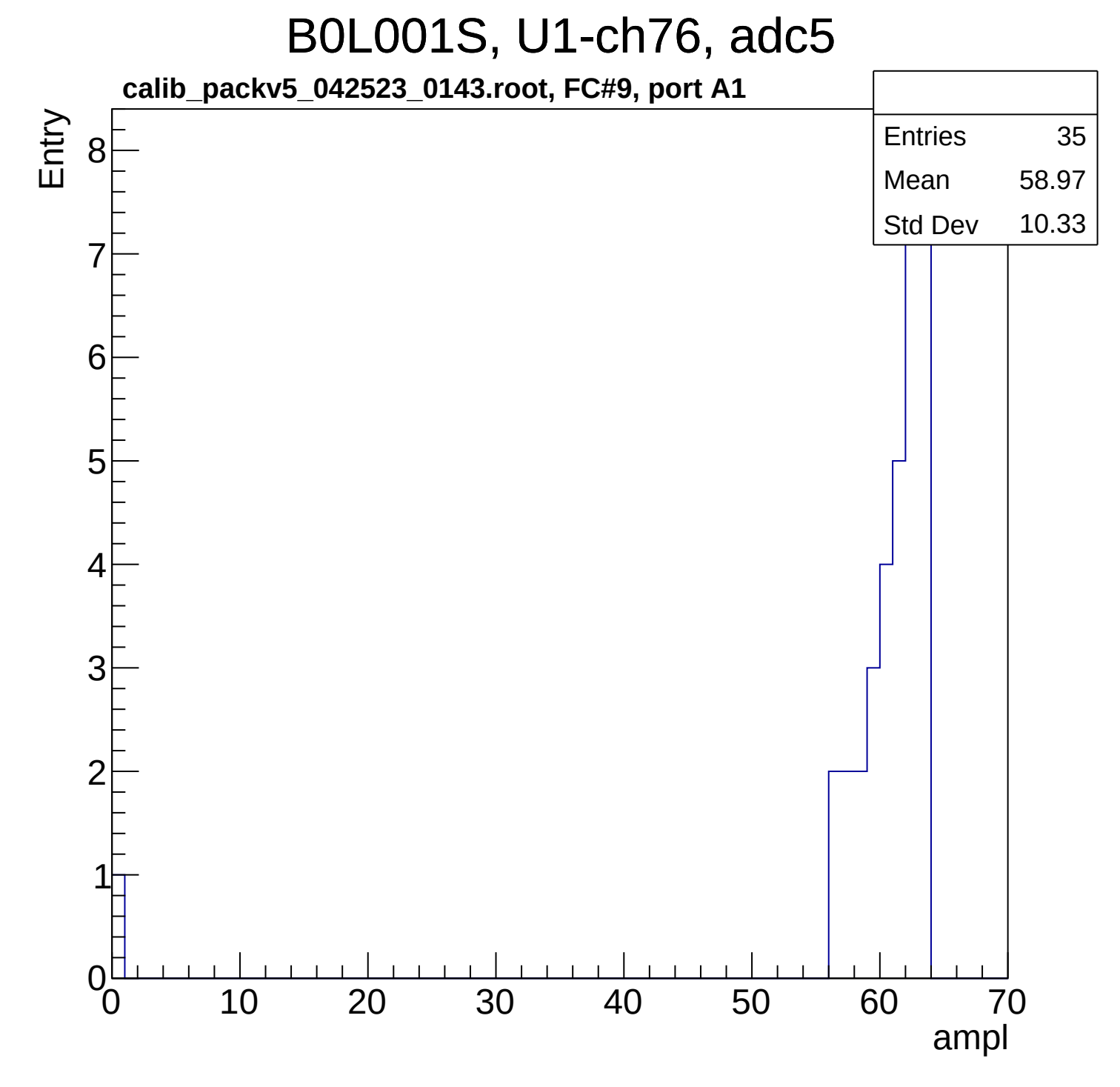

B0L001S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 29.32
Std Dev 3.478
Gaus mean : 29.6029
Gaus Width: 3.9285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

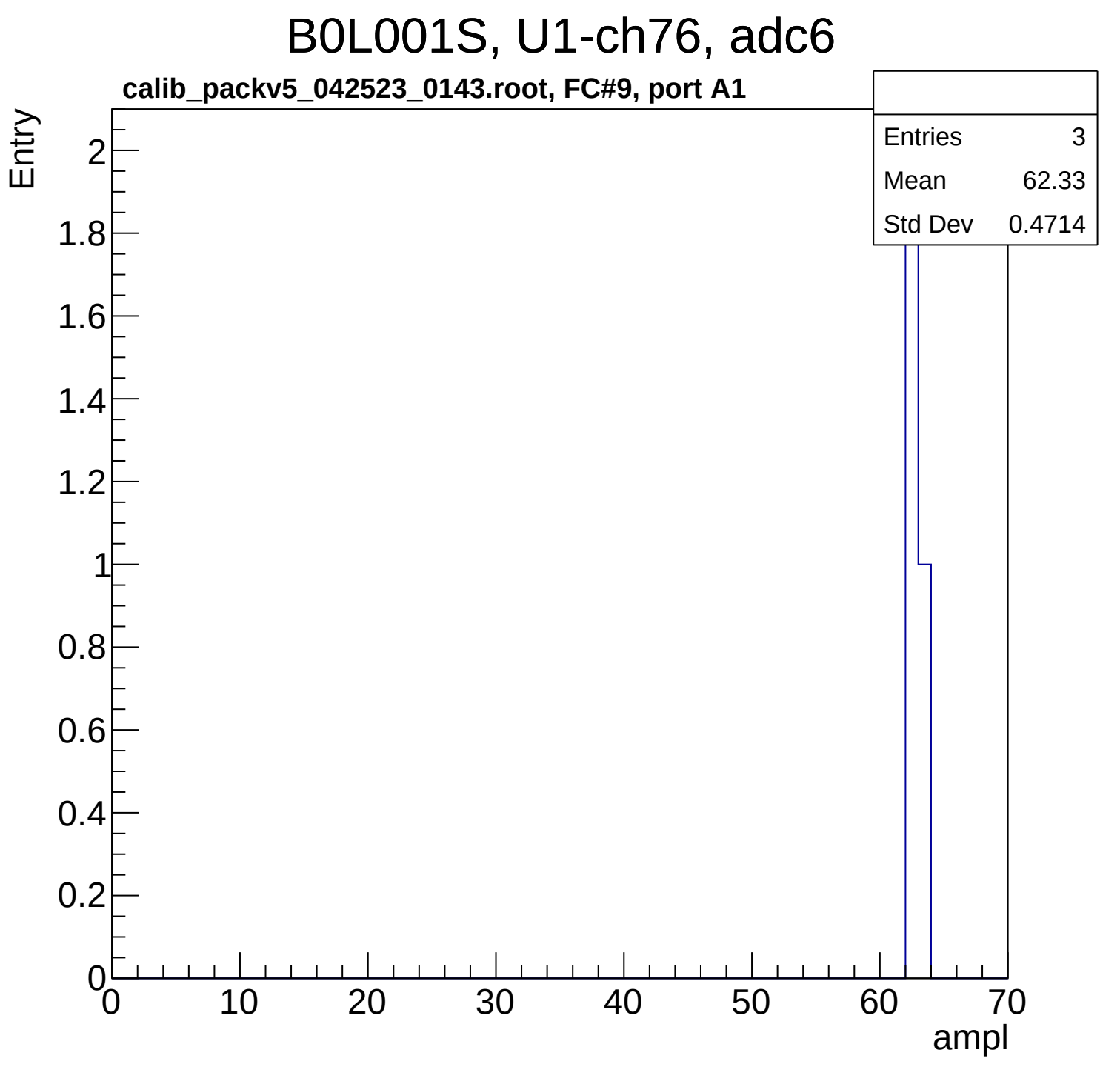

B0L001S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.96
Std Dev 3.218
Gaus mean : 37.7248
Gaus Width: 3.2074
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

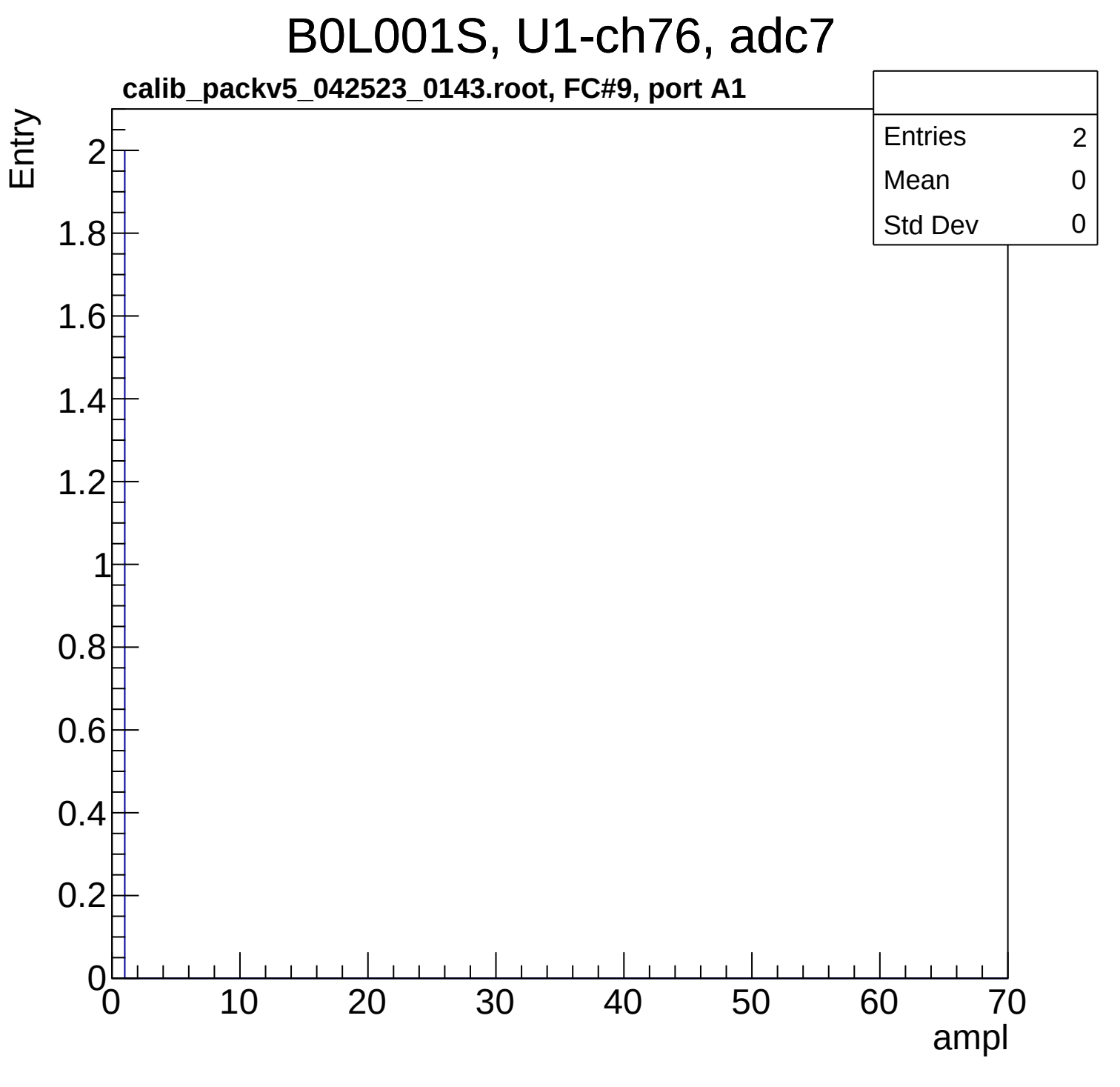

B0L001S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 43.97
Std Dev 3.684
Gaus mean : 44.6730
Gaus Width: 3.3989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

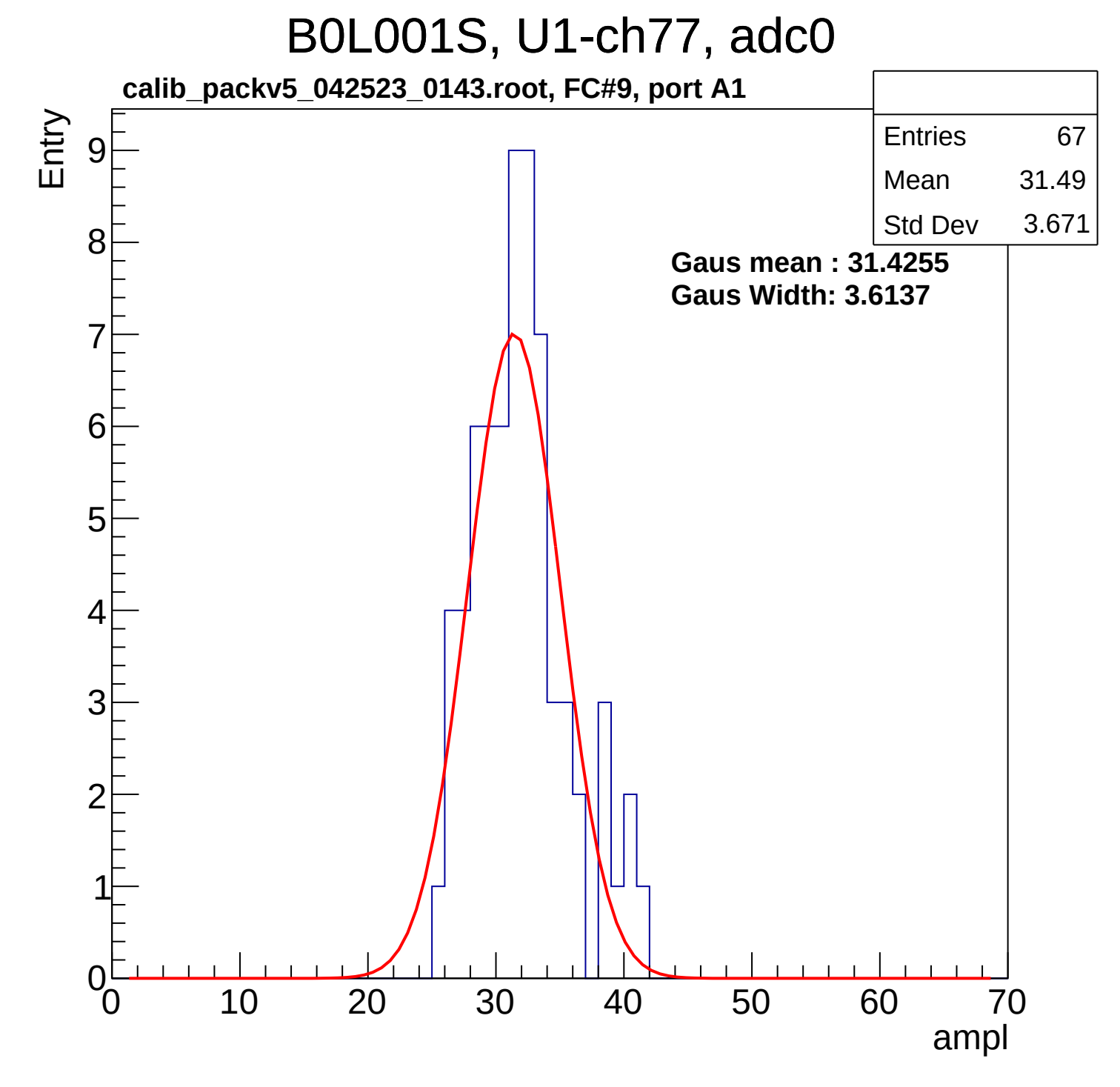

B0L001S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.53
Std Dev 3.887
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 56.47
Std Dev 3.064
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.97
Std Dev 10.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 31.49
Std Dev 3.671
Gaus mean : 31.4255
Gaus Width: 3.6137
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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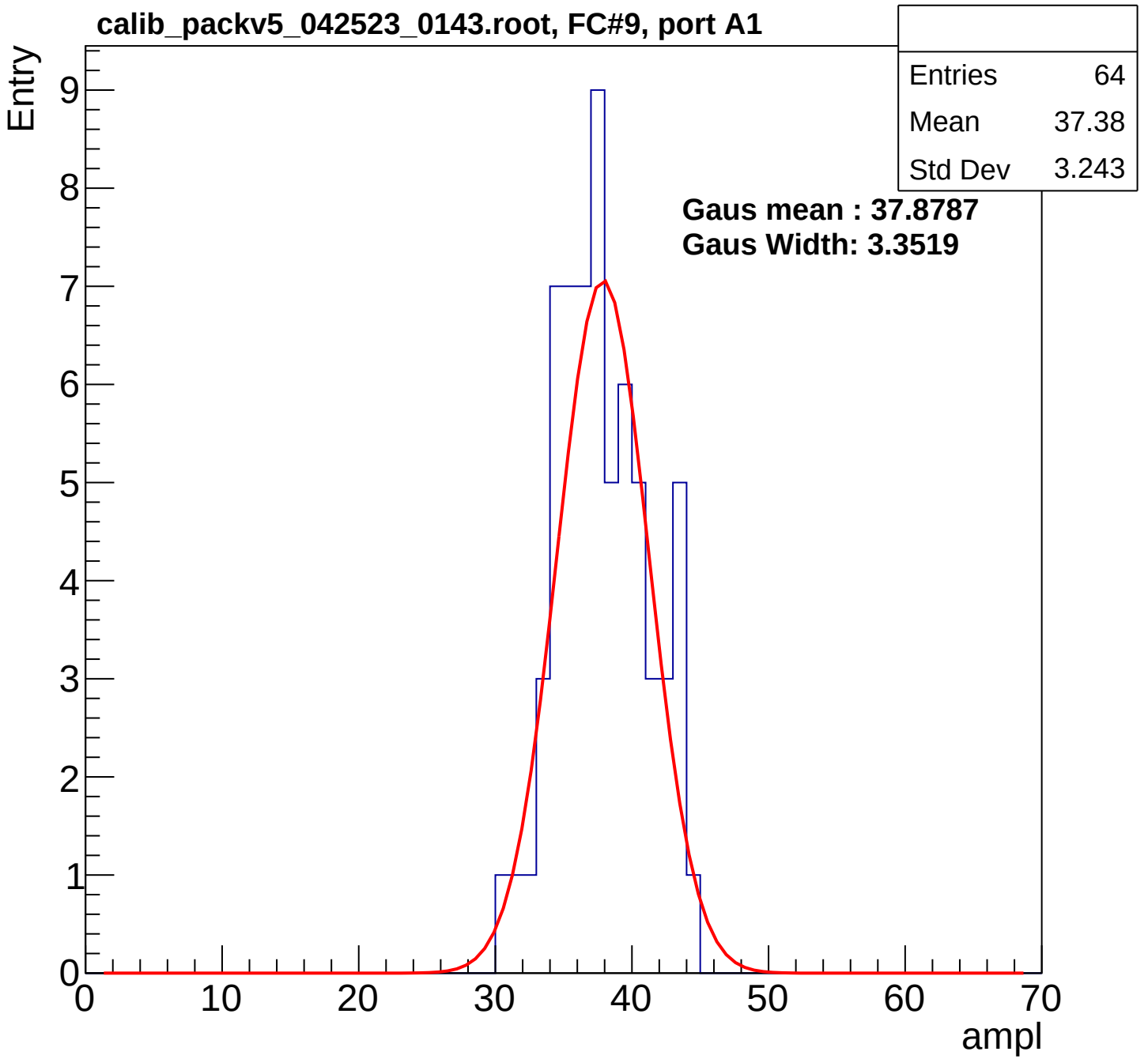
B0L001S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 37.38
Std Dev 3.243
Gaus mean : 37.8787
Gaus Width: 3.3519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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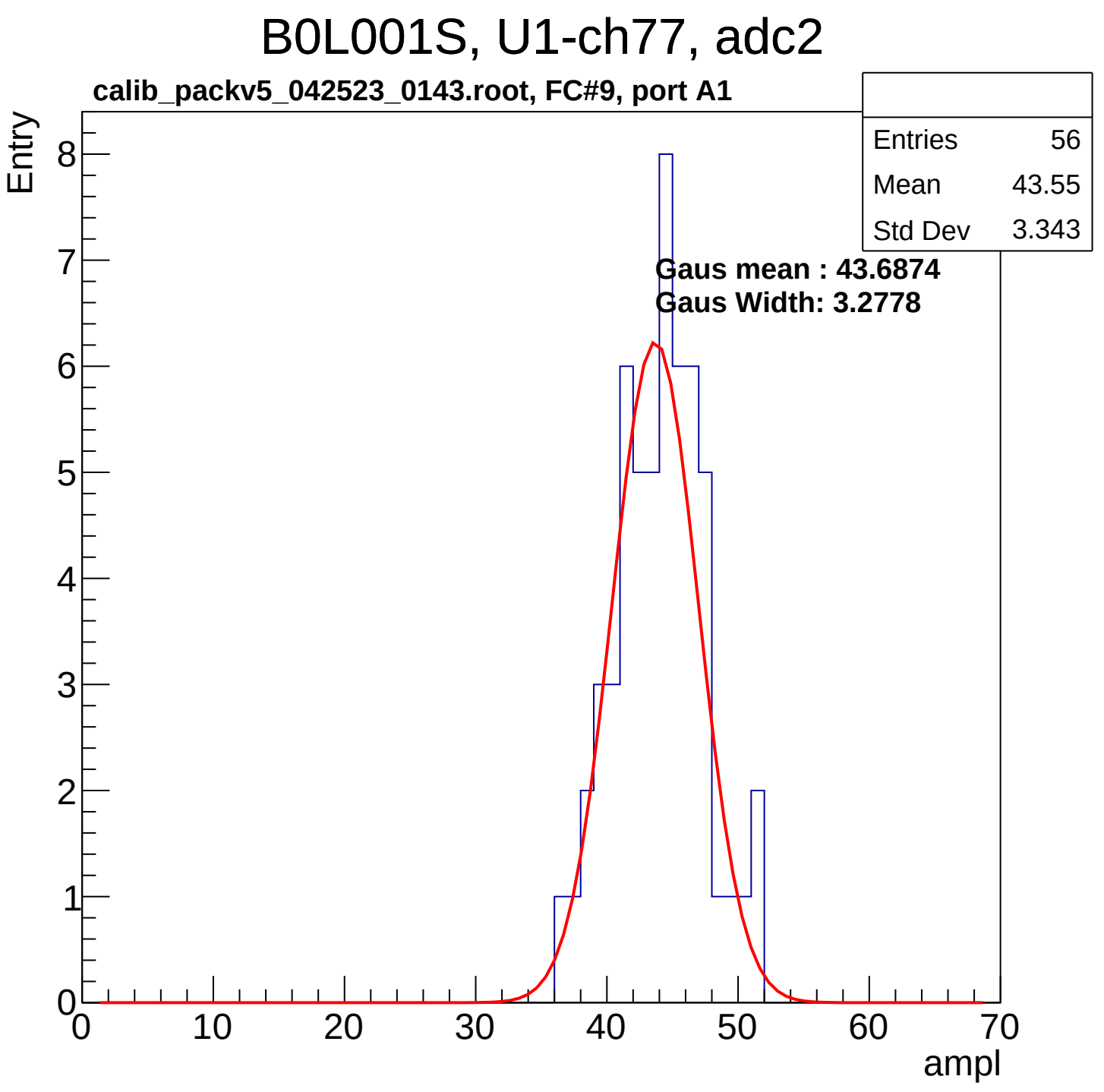
B0L001S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 43.55
Std Dev 3.343
Gaus mean : 43.6874
Gaus Width: 3.2778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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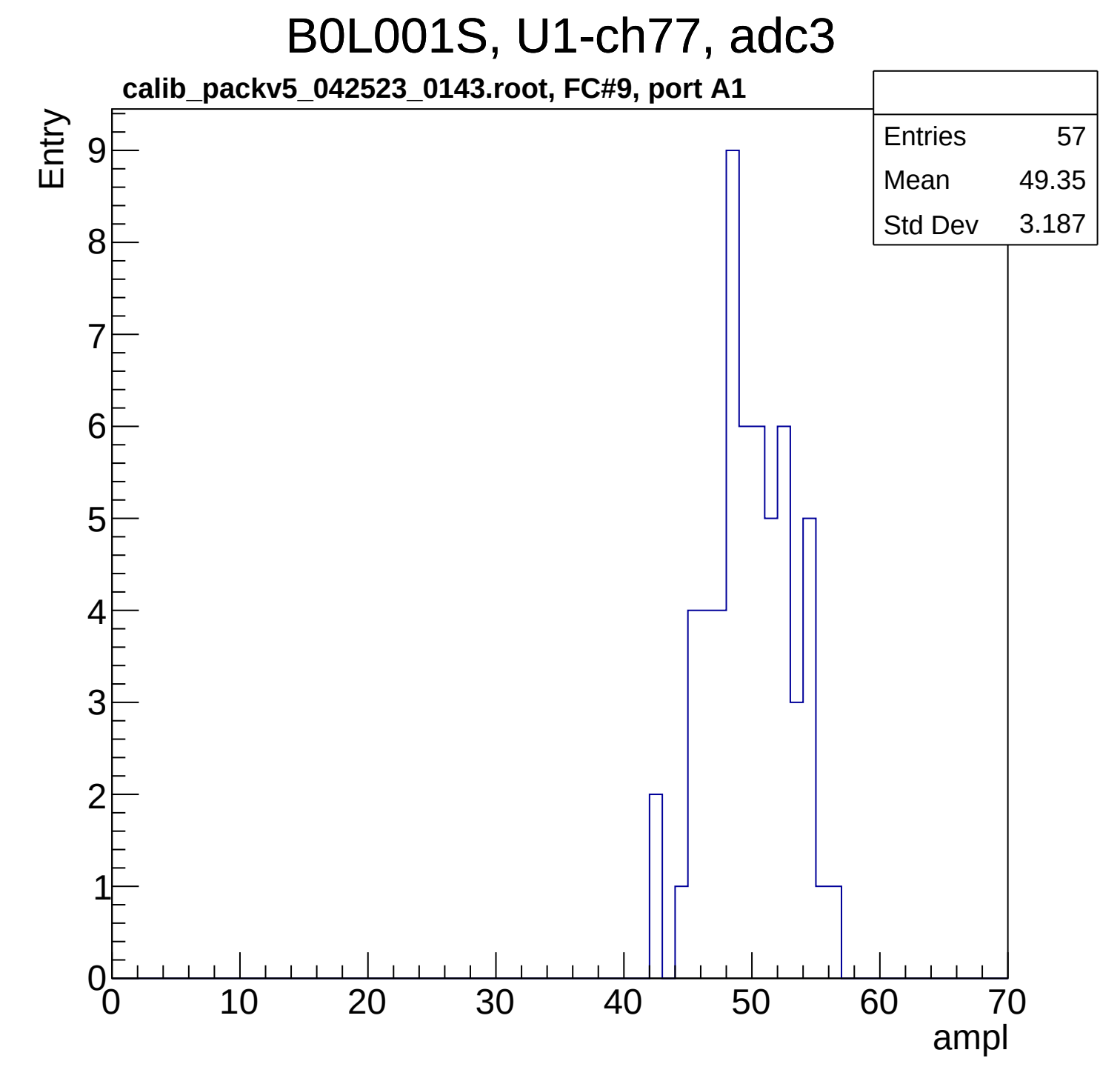
B0L001S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.35
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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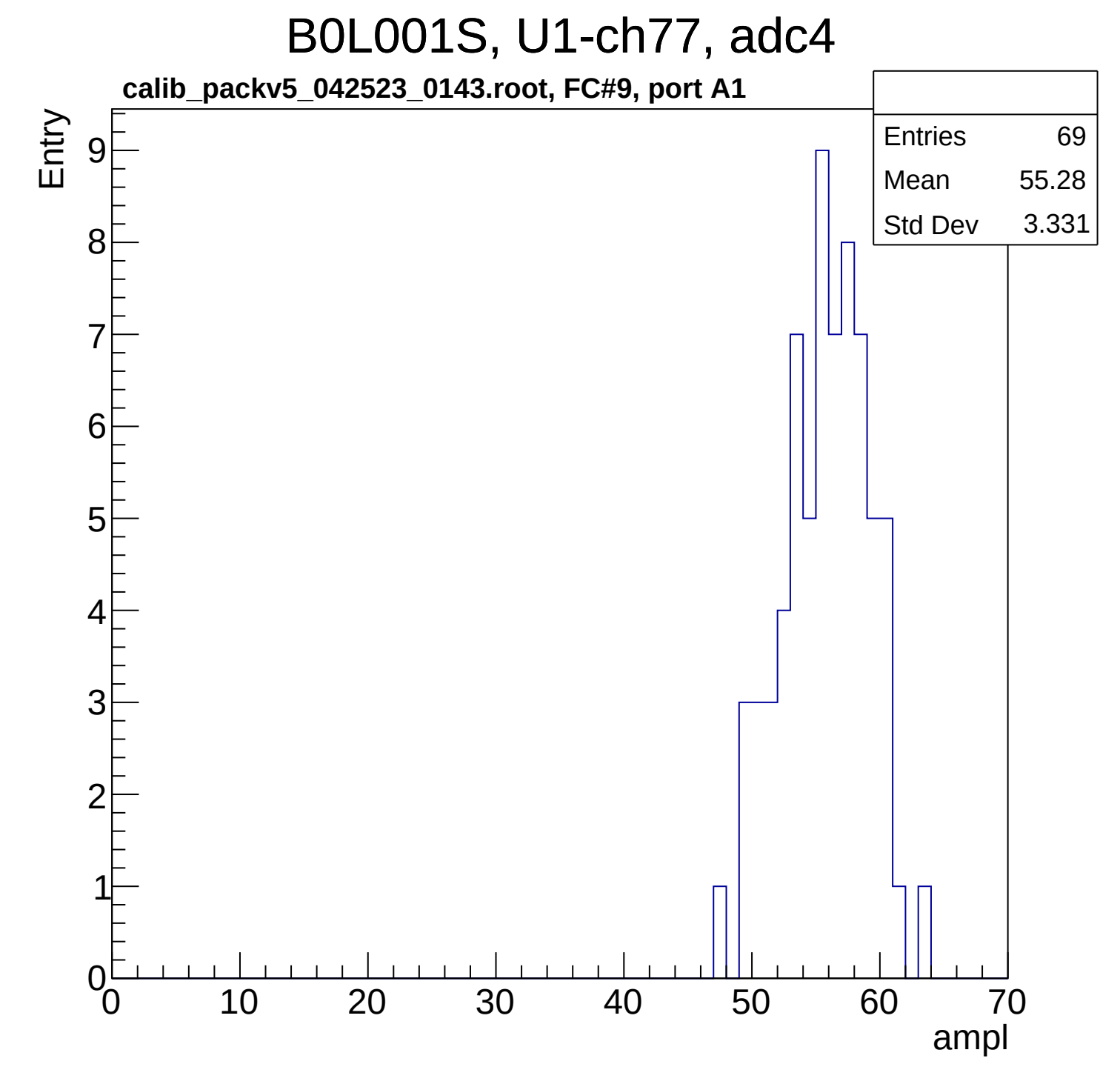
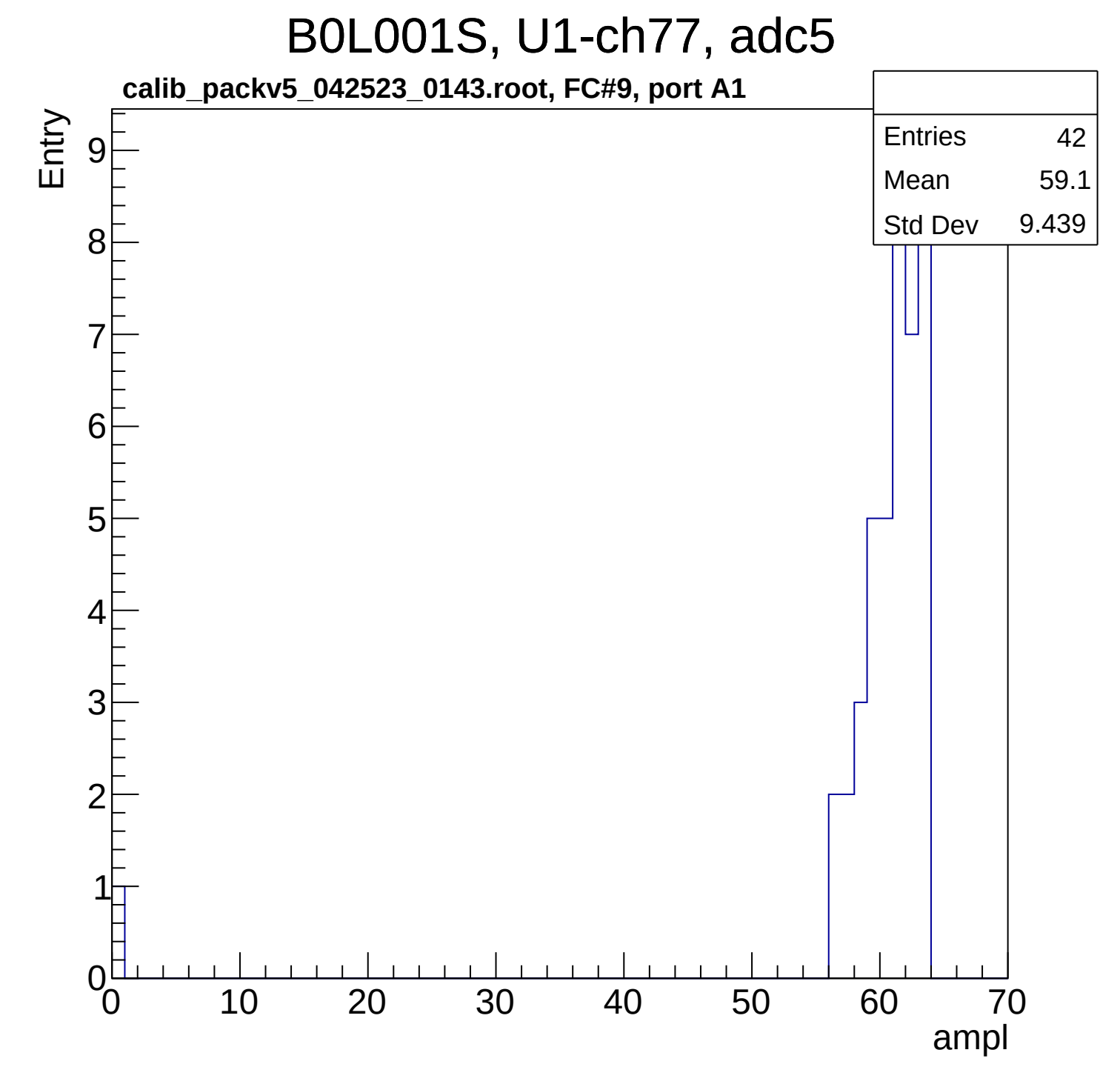

B0L001S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 55.28
Std Dev 3.331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 59.1
Std Dev 9.439
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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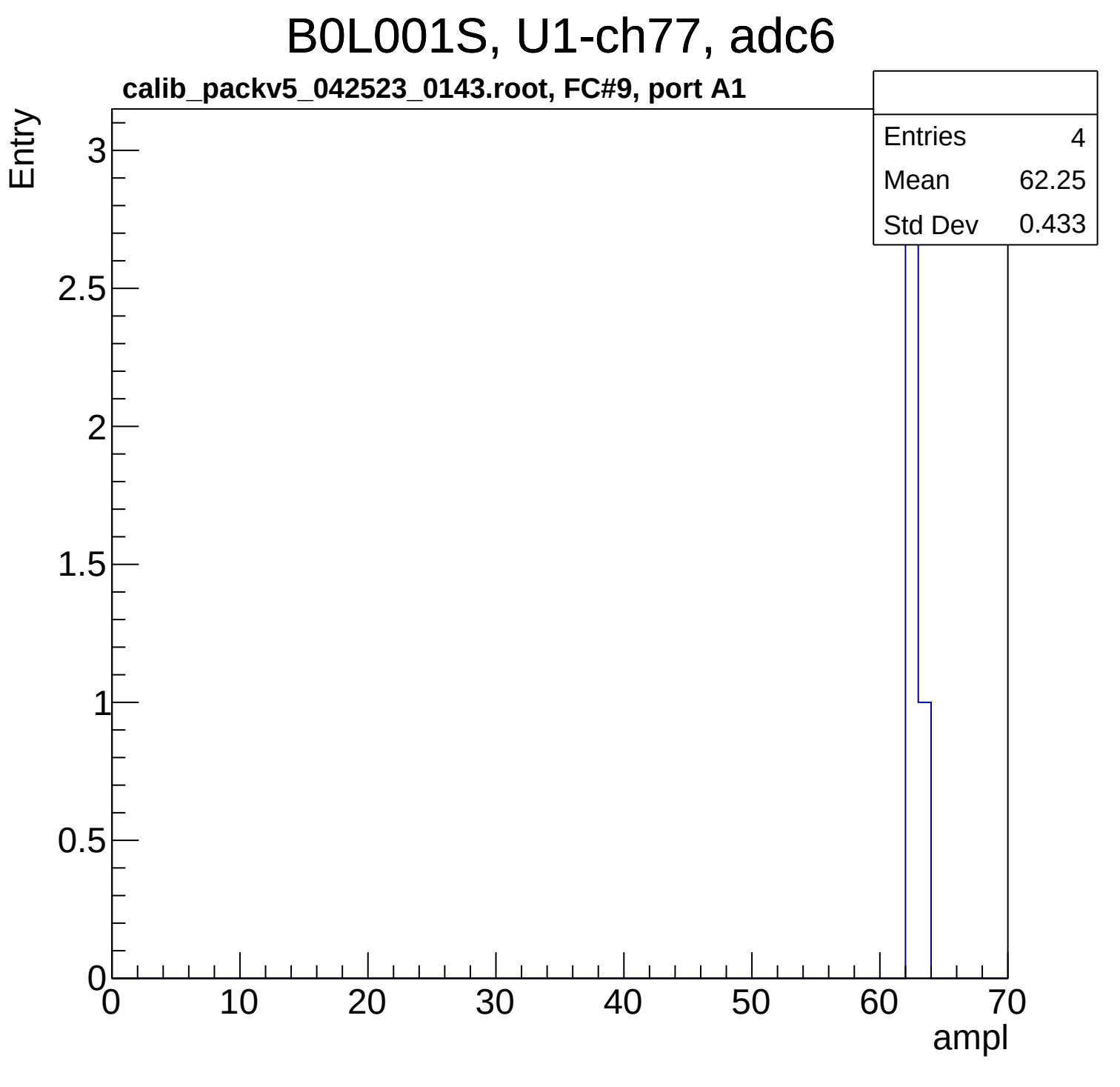
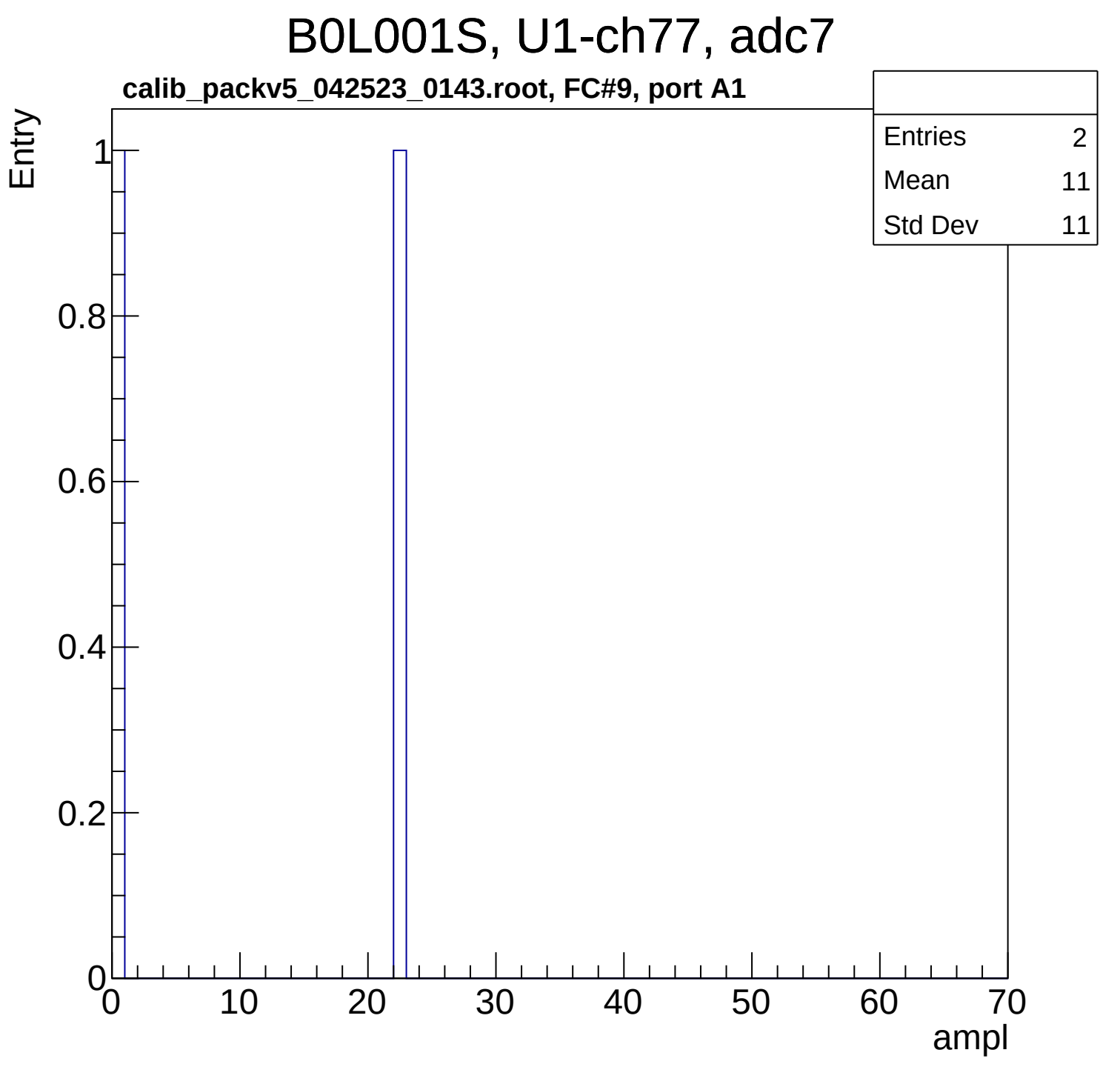
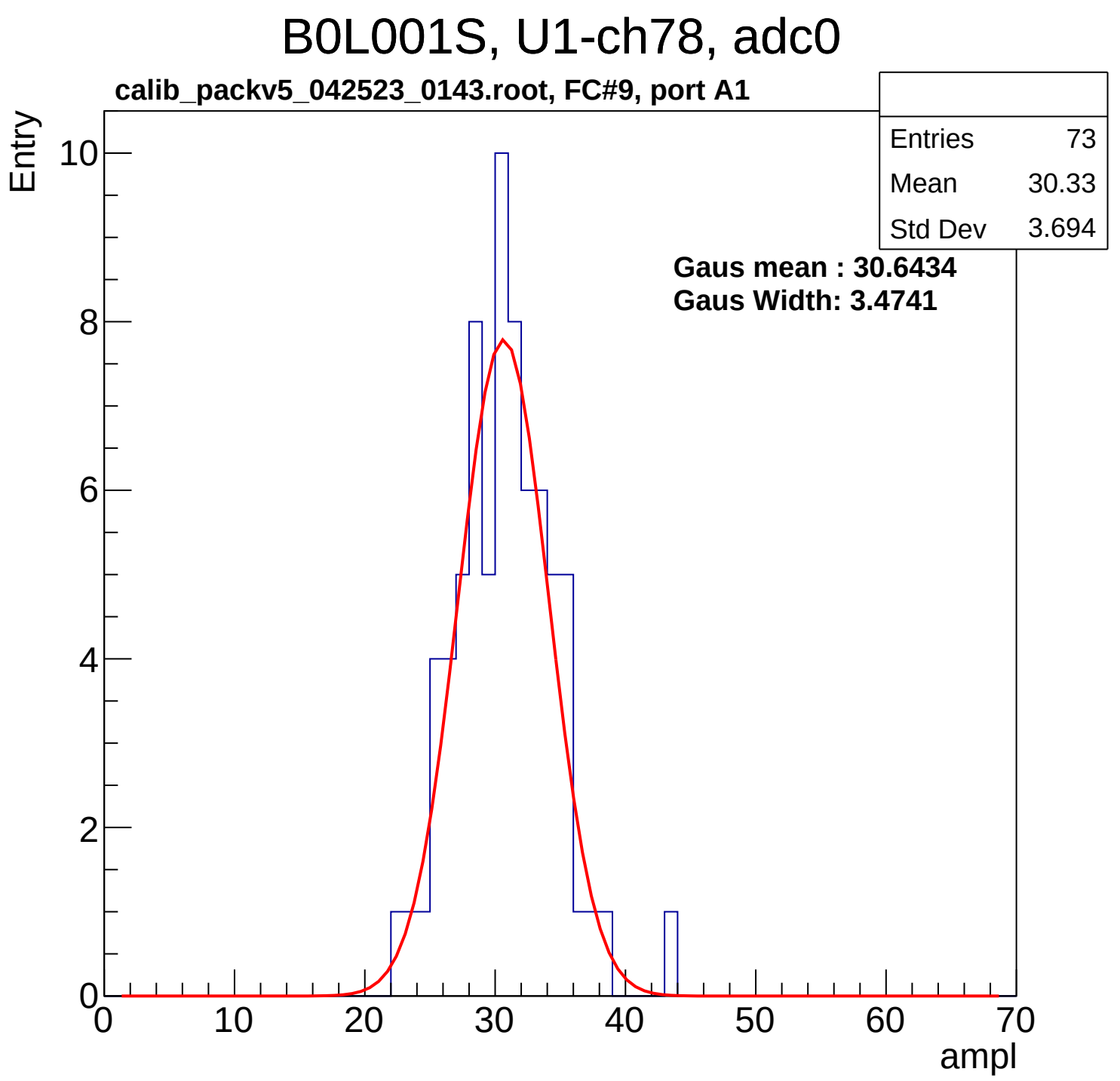
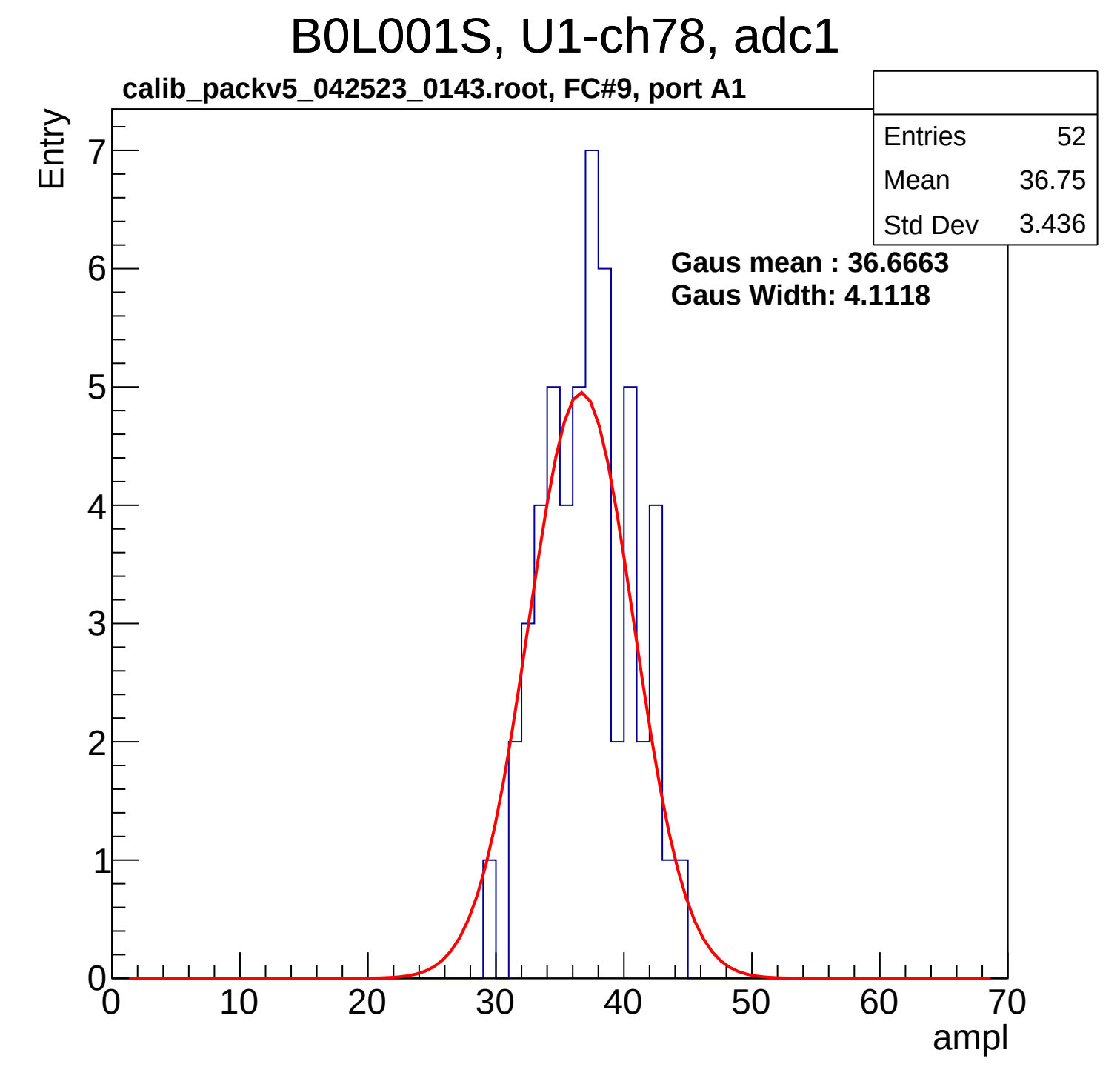
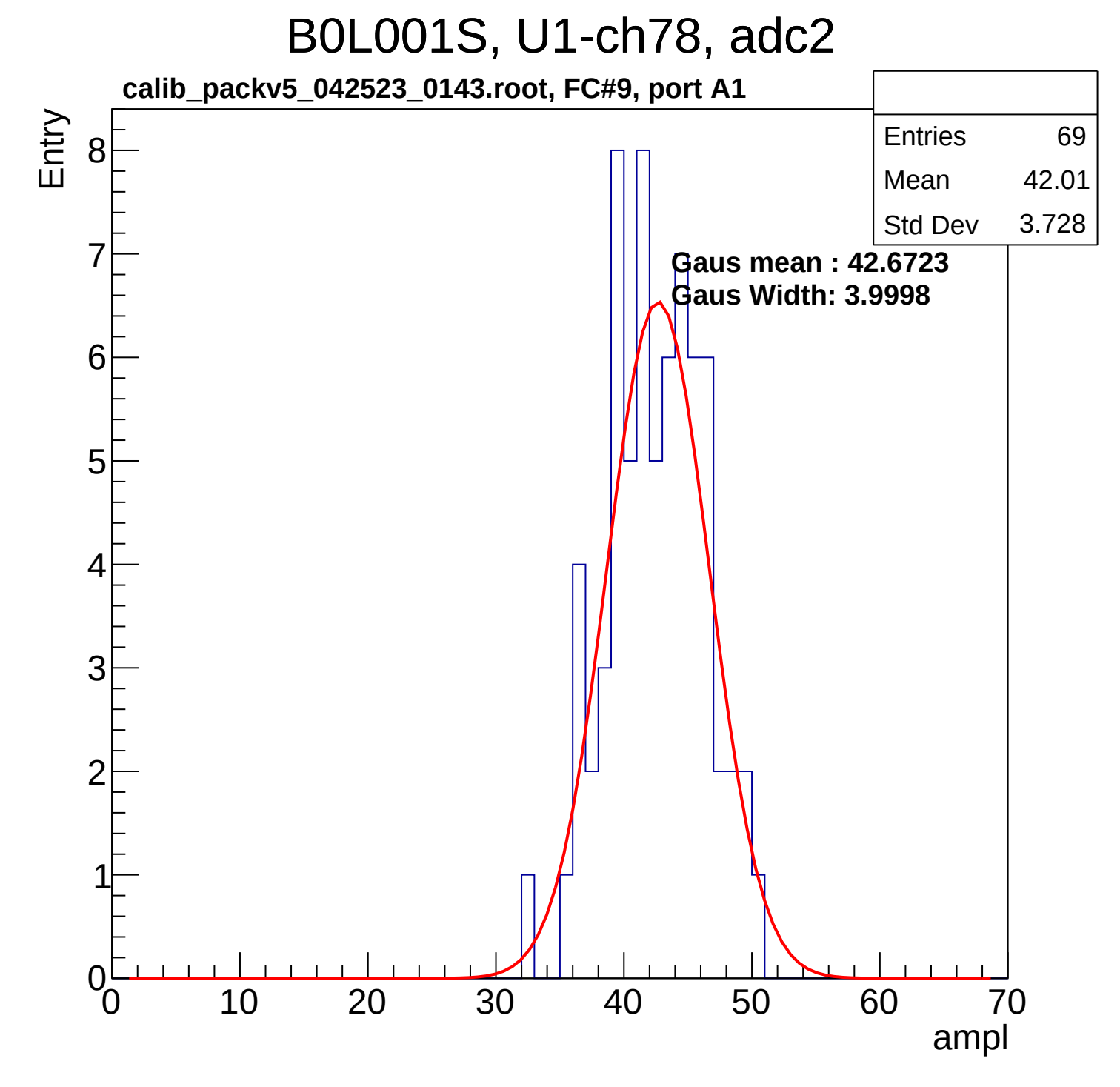

B0L001S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 30.33
Std Dev 3.694
Gaus mean : 30.6434
Gaus Width: 3.4741
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 36.75
Std Dev 3.436
Gaus mean : 36.6663
Gaus Width: 4.1118
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 42.01
Std Dev 3.728
Gaus mean : 42.6723
Gaus Width: 3.9998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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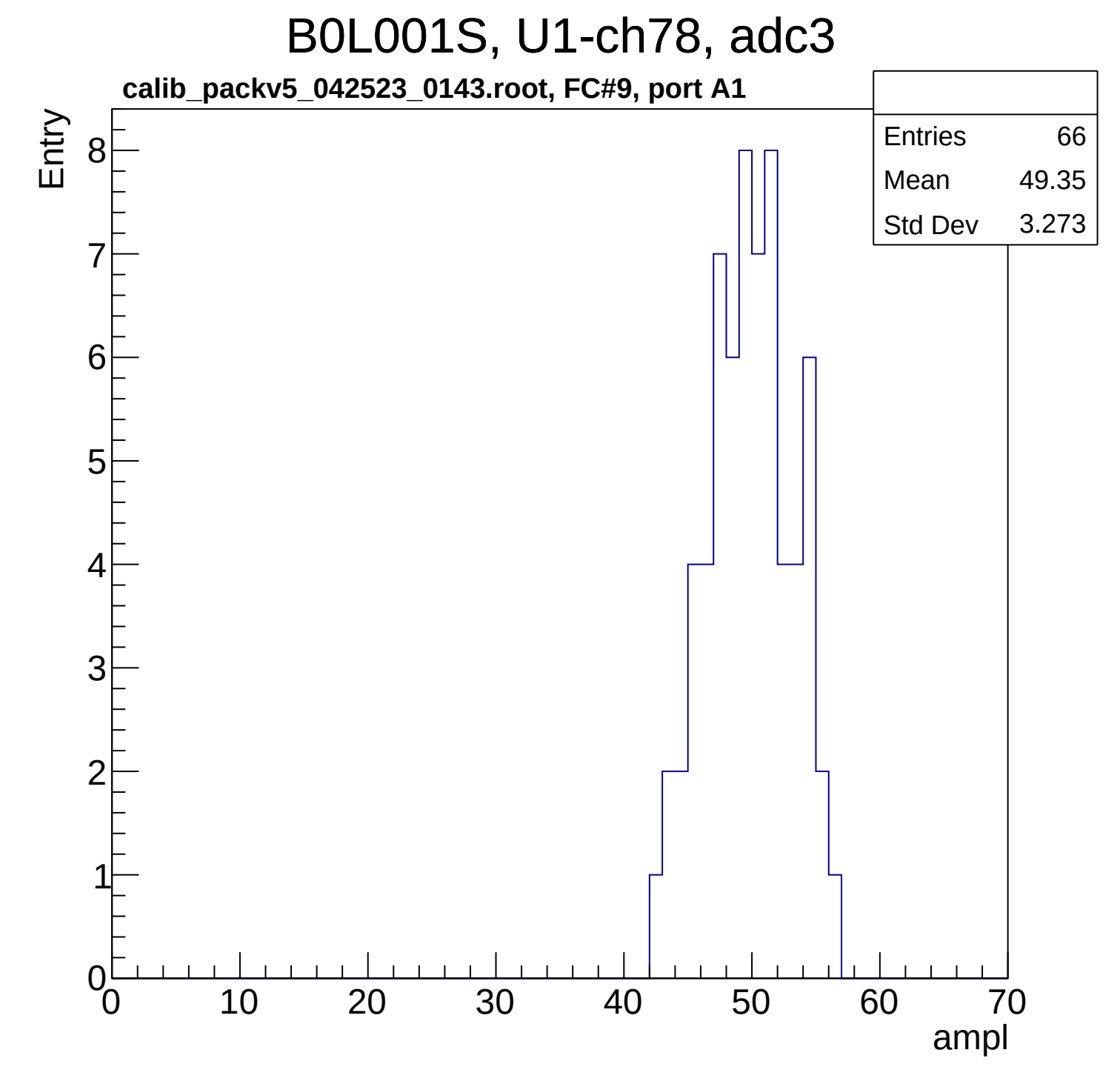
B0L001S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 49.35
Std Dev 3.273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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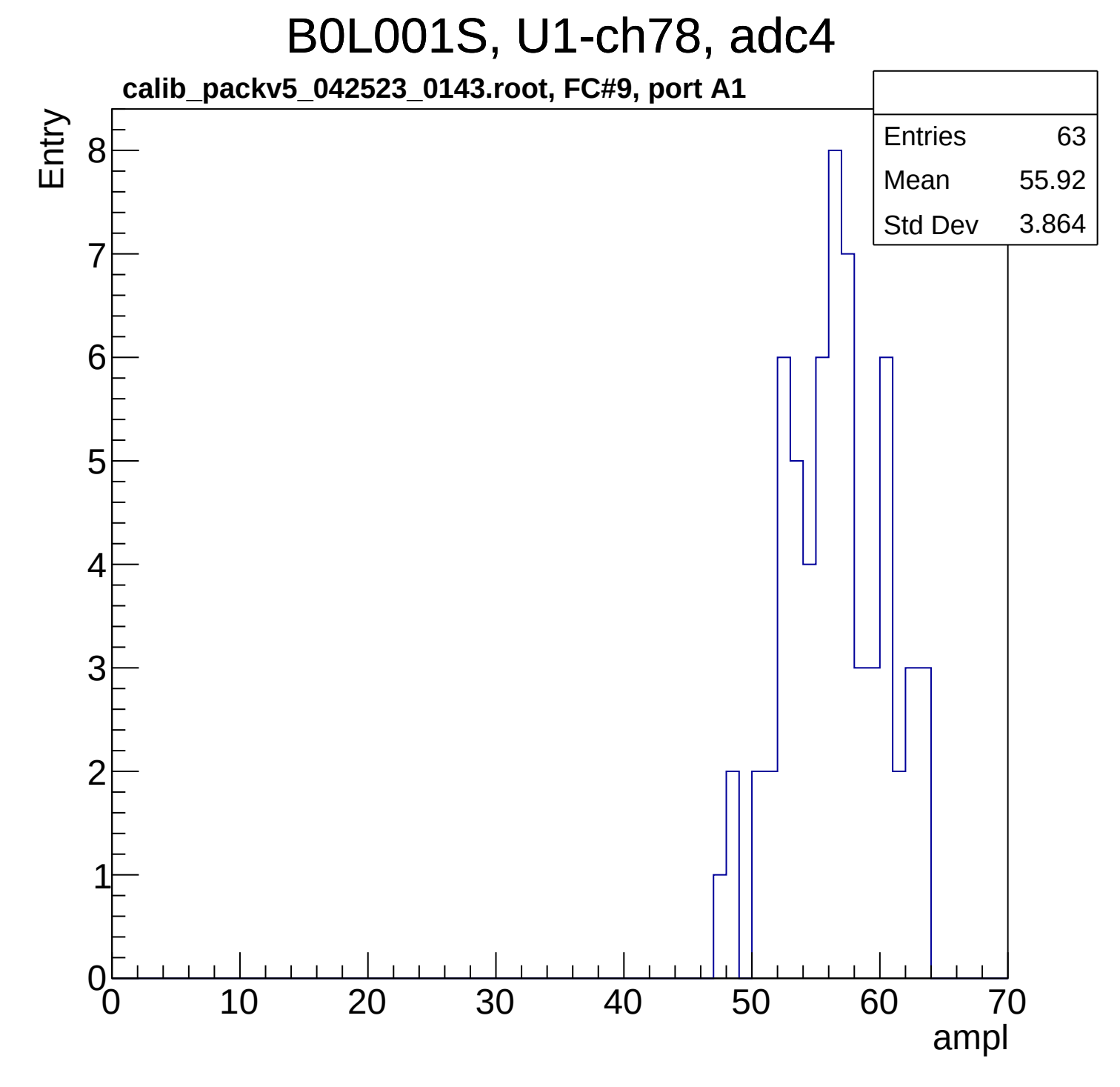
B0L001S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 55.92
Std Dev 3.864
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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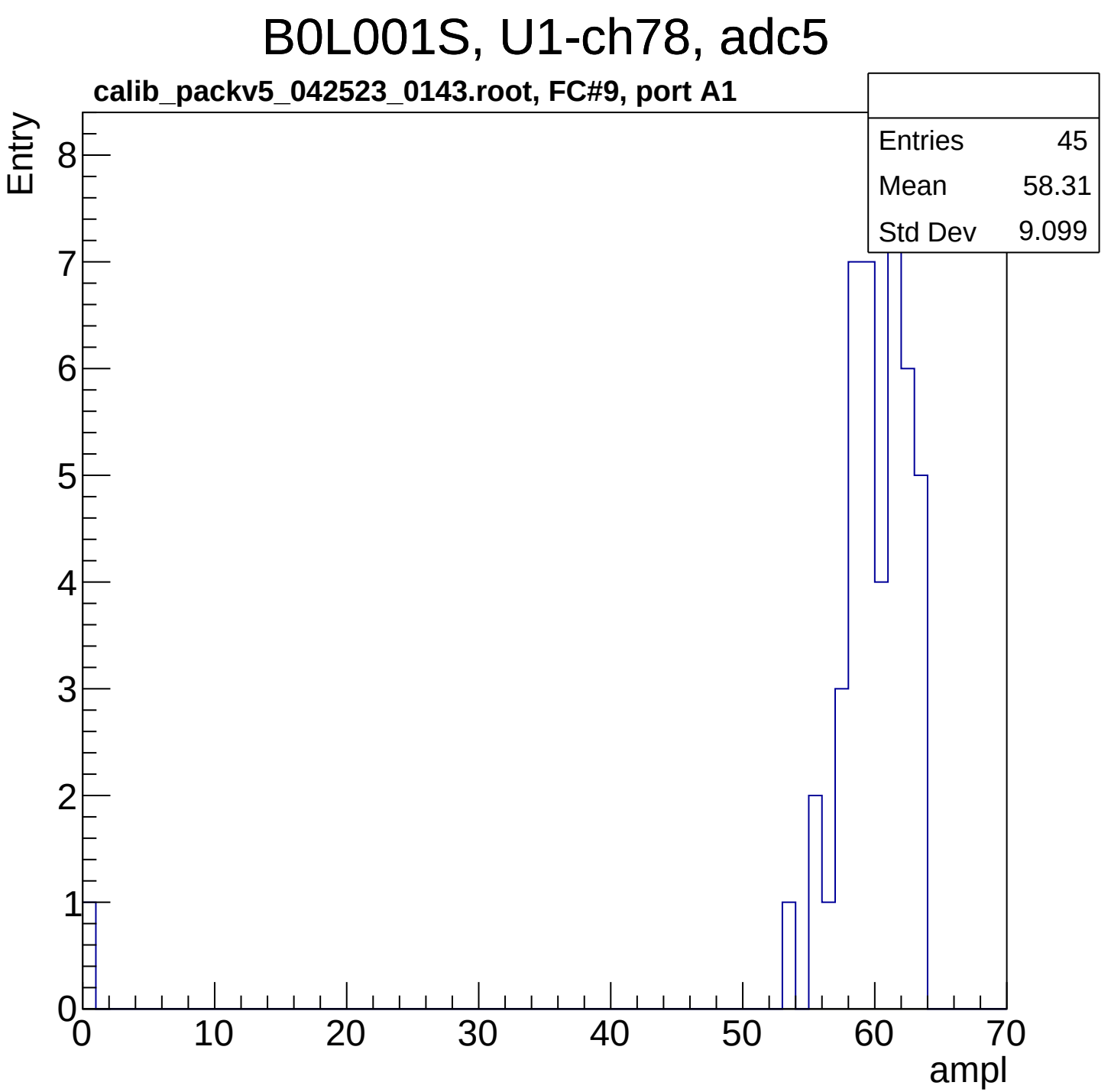
B0L001S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.31
Std Dev 9.099
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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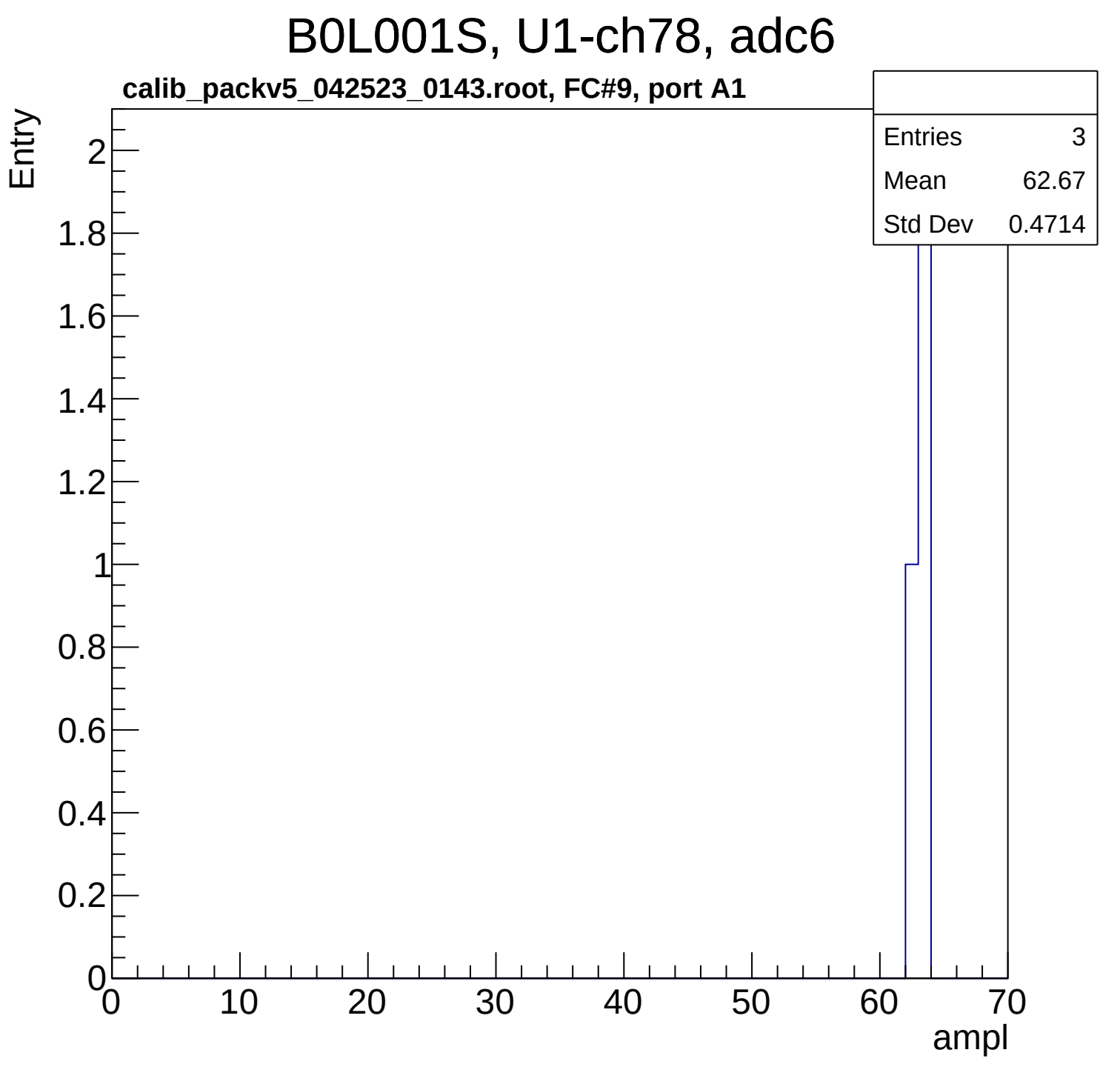
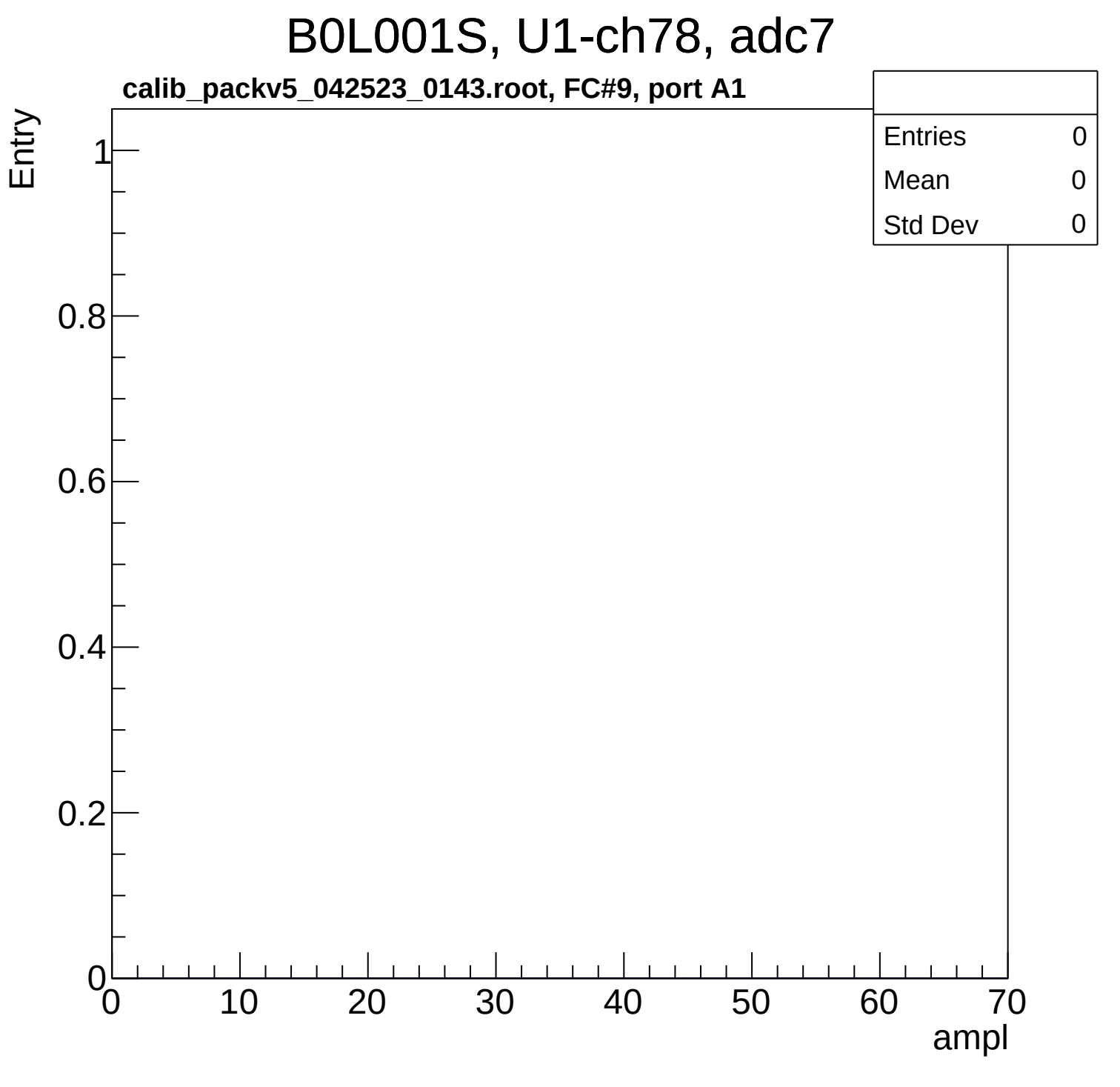
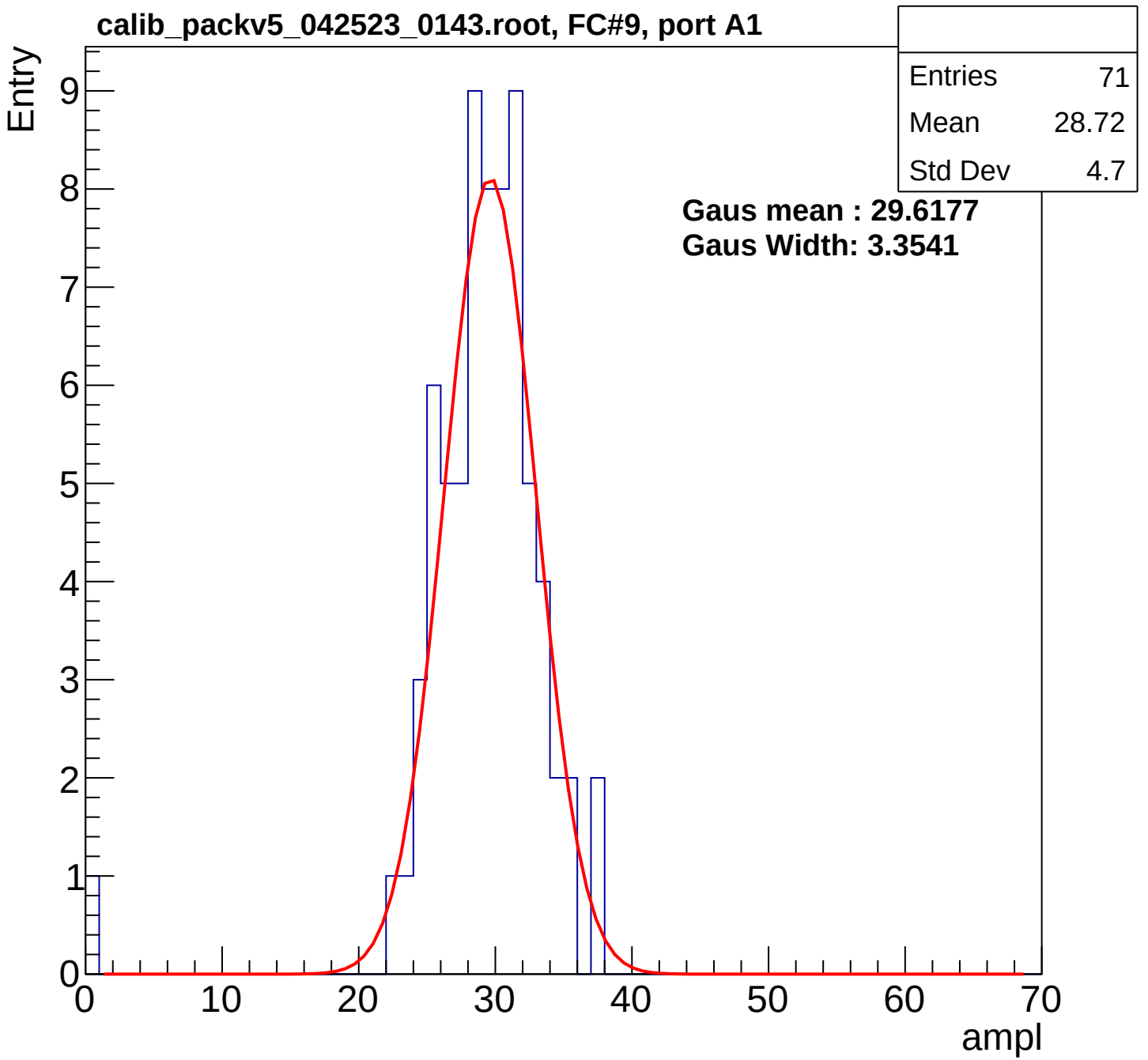

B0L001S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 28.72
Std Dev 4.7
Gaus mean : 29.6177
Gaus Width: 3.3541
Entry
ampl
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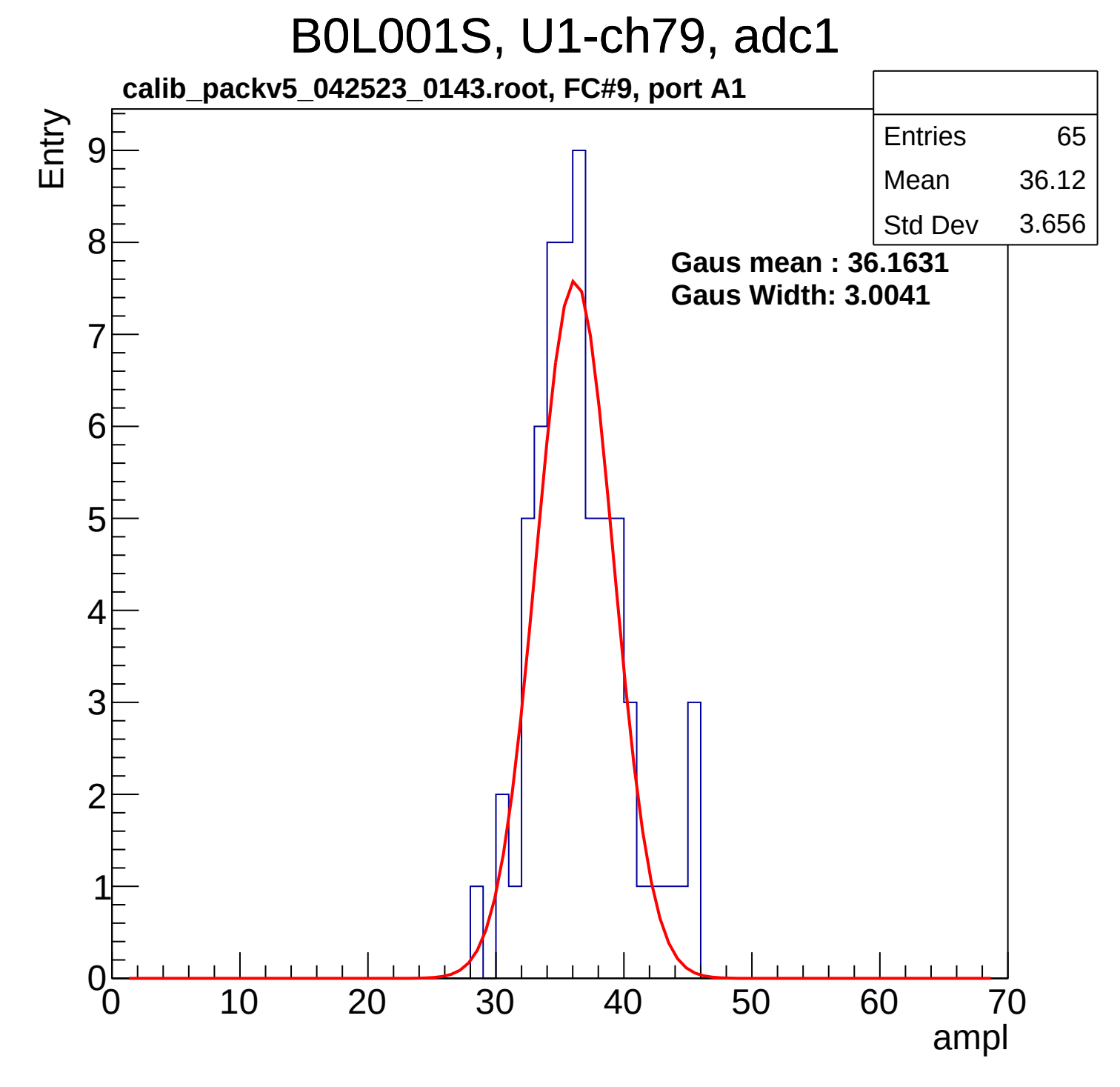
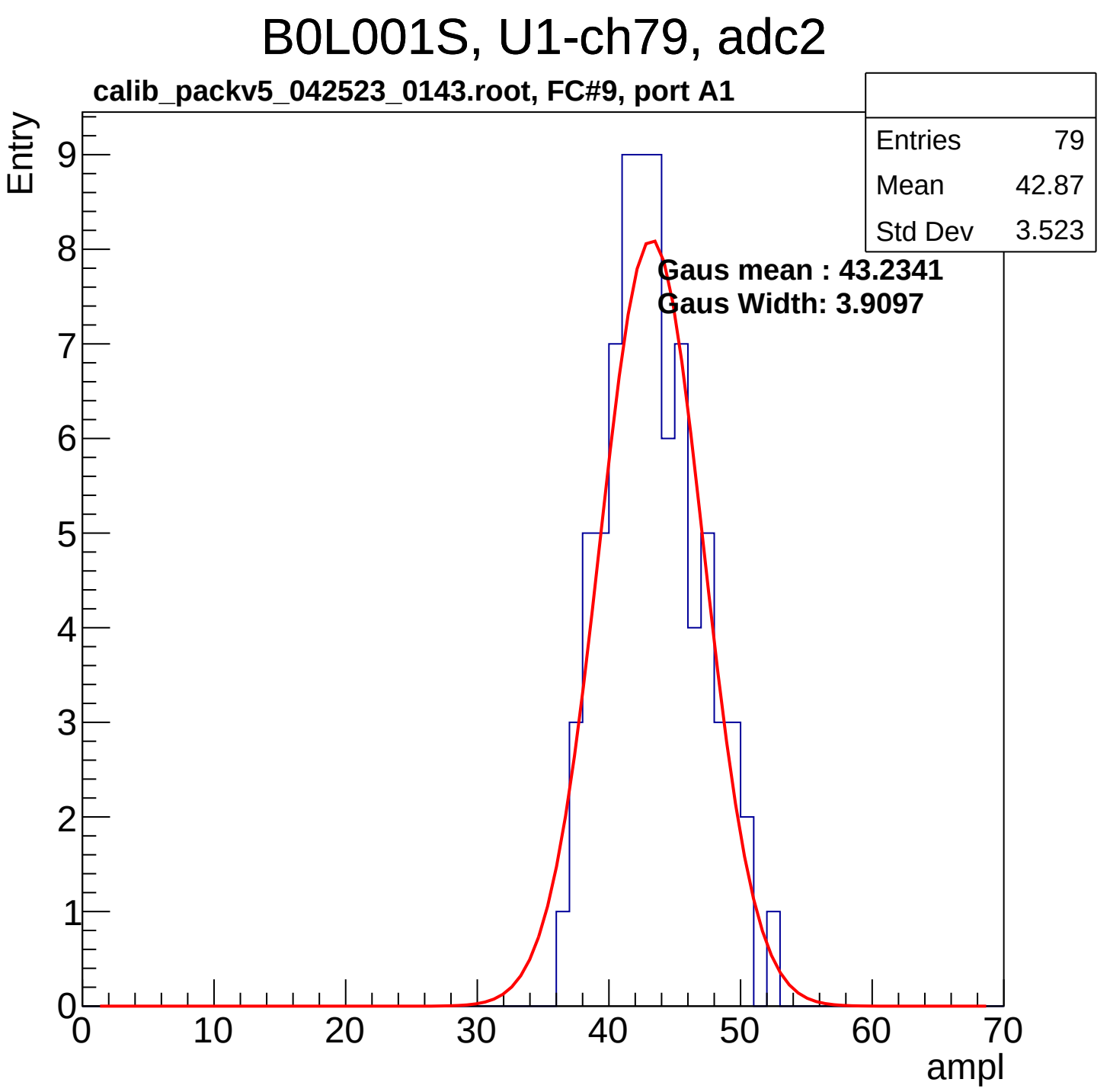
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.12
Std Dev 3.656
Gaus mean : 36.1631
Gaus Width: 3.0041
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 42.87
Std Dev 3.523
Gaus mean : 43.2341
Gaus Width: 3.9097
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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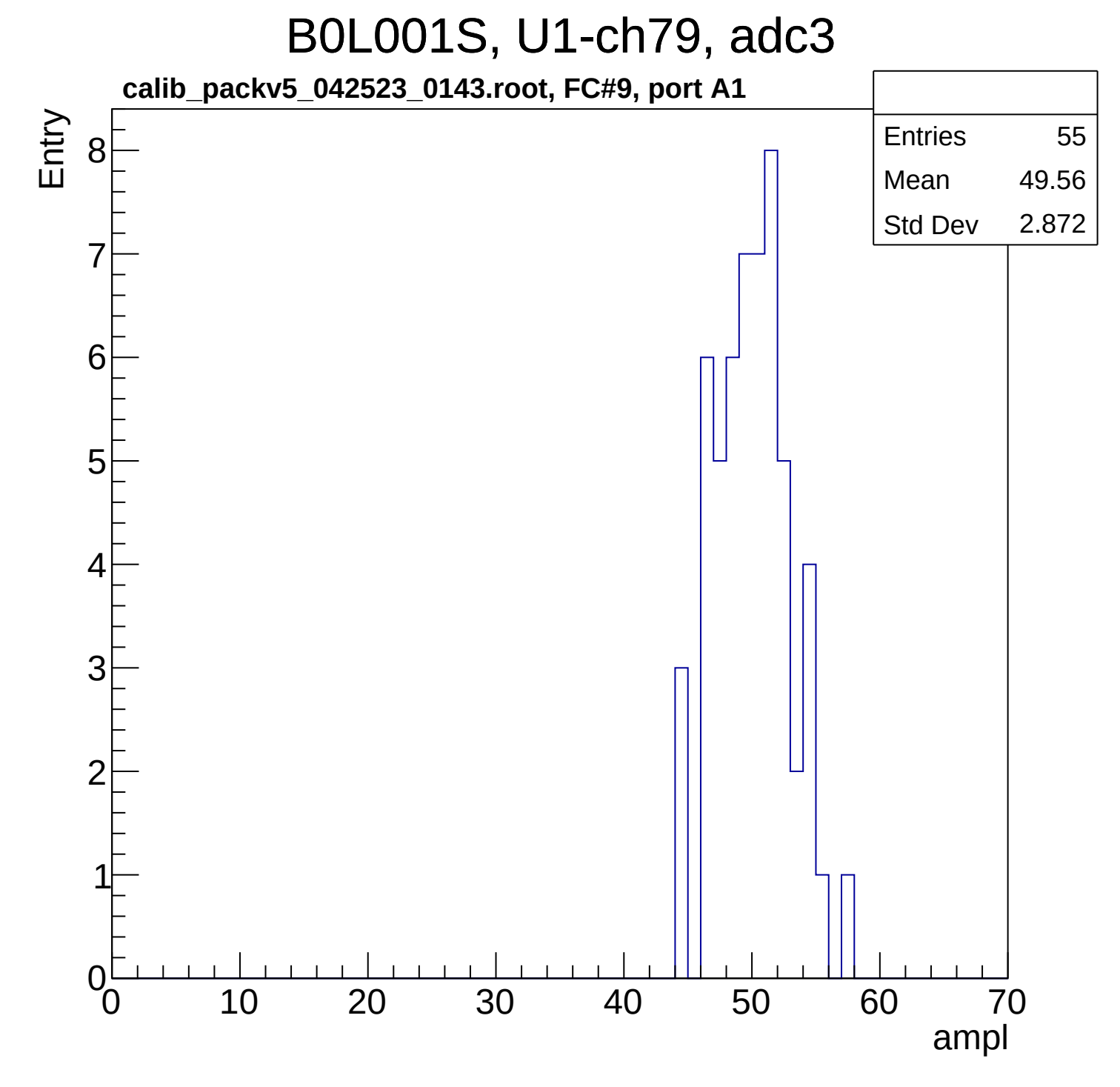
B0L001S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 49.56
Std Dev 2.872
Entry
ampl
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1
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5
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0
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20
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40
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60
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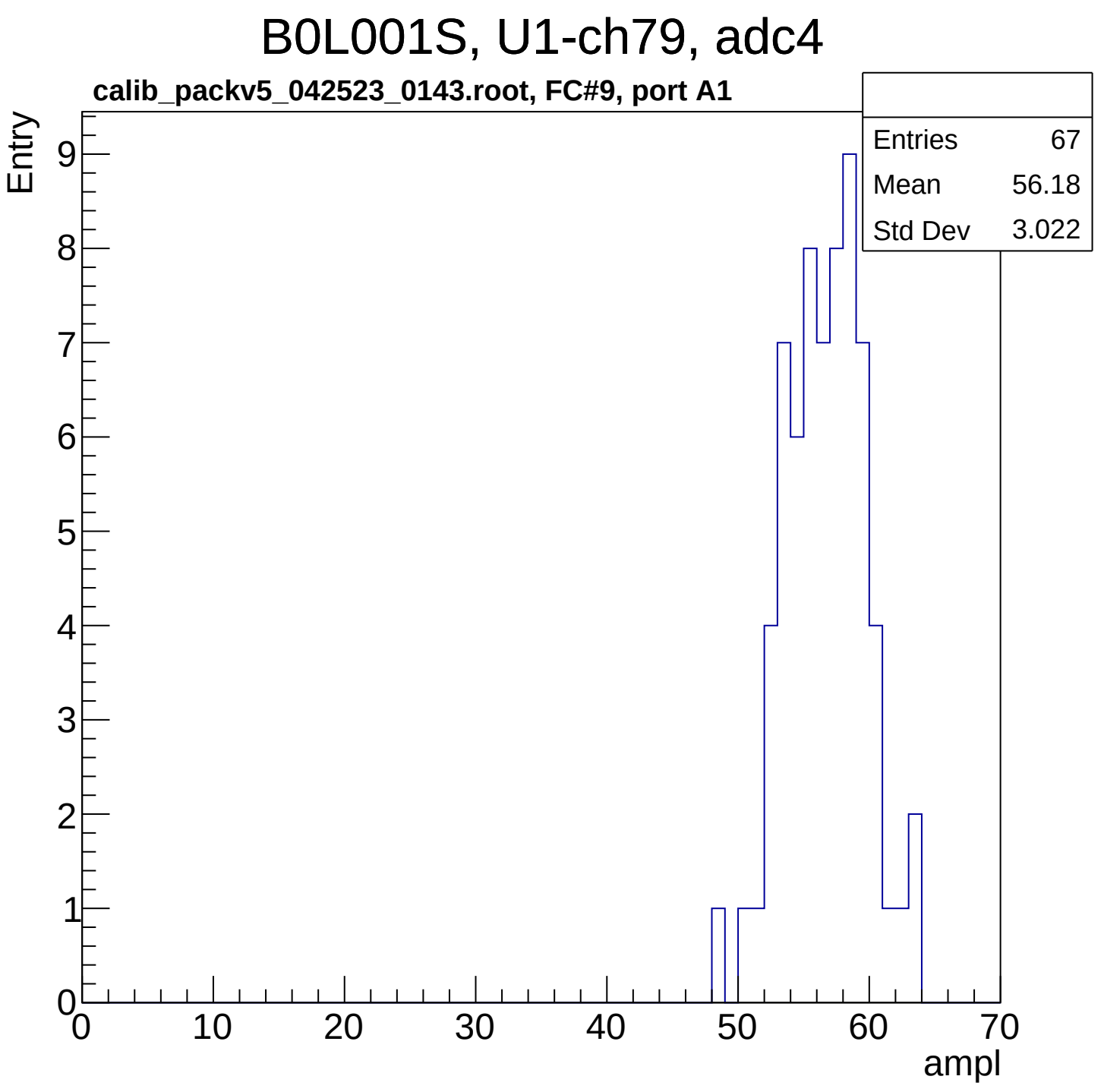
B0L001S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 56.18
Std Dev 3.022
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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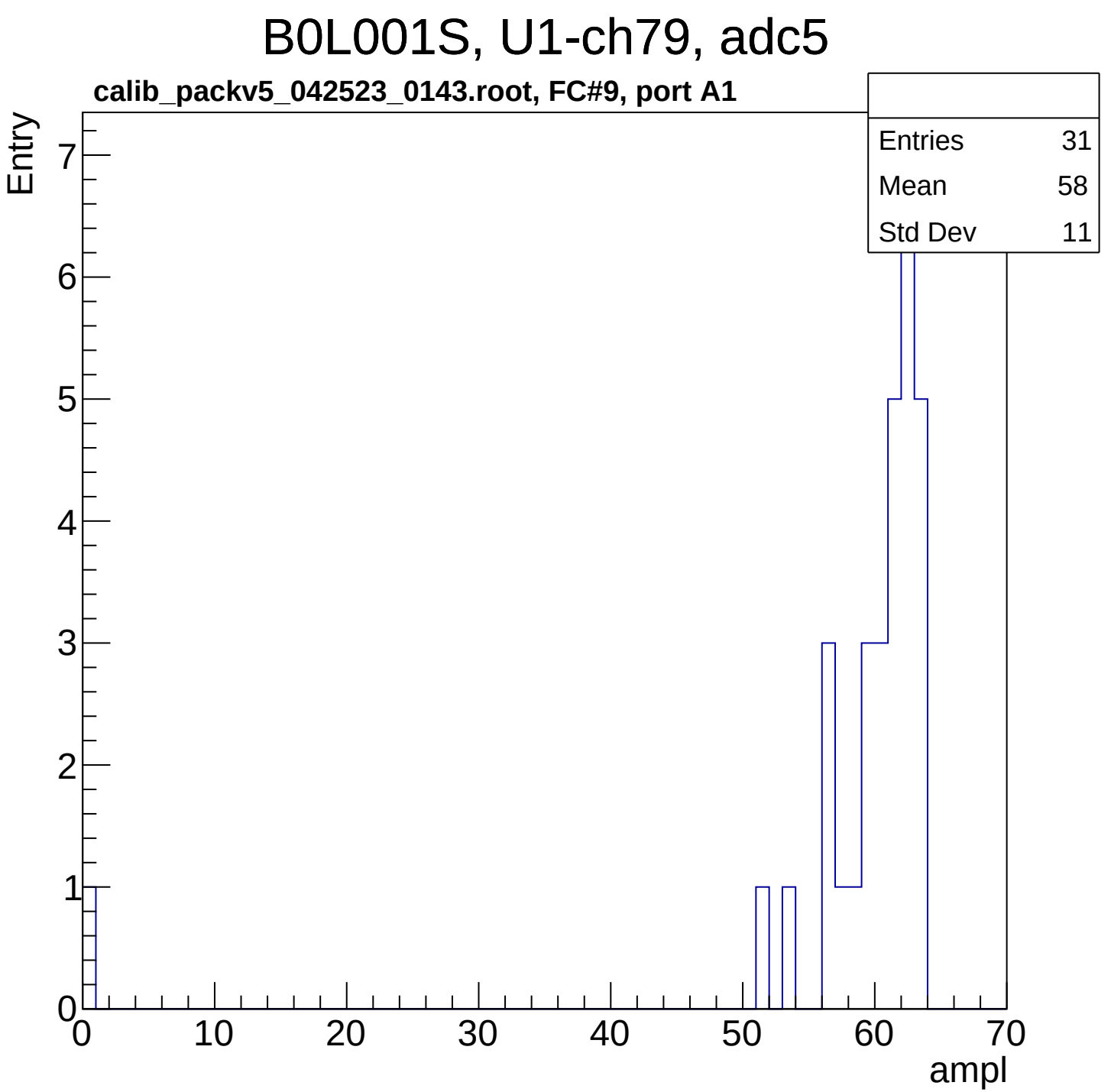
B0L001S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 58
Std Dev 11
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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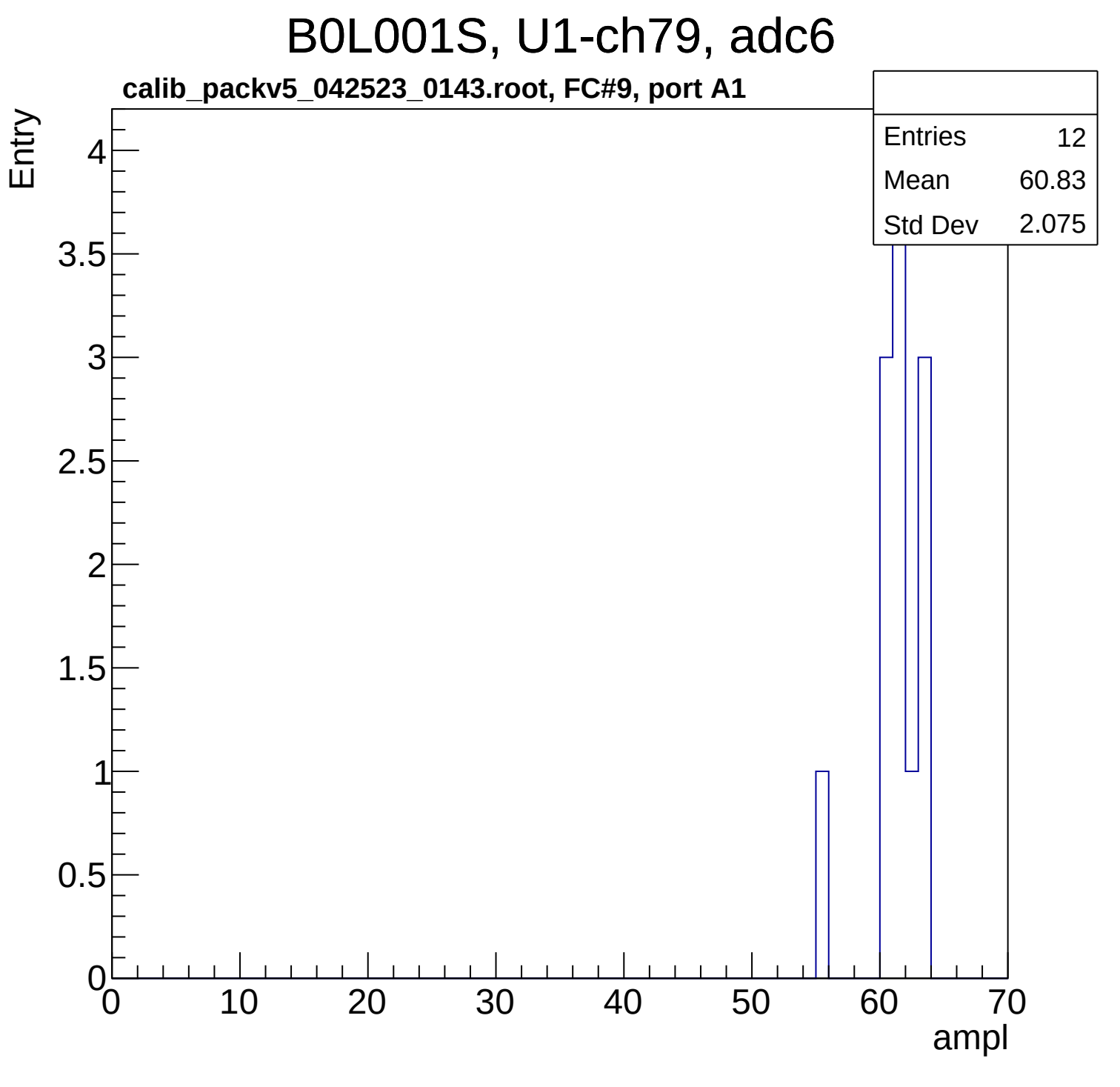
B0L001S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 60.83
Std Dev 2.075
Entry
ampl
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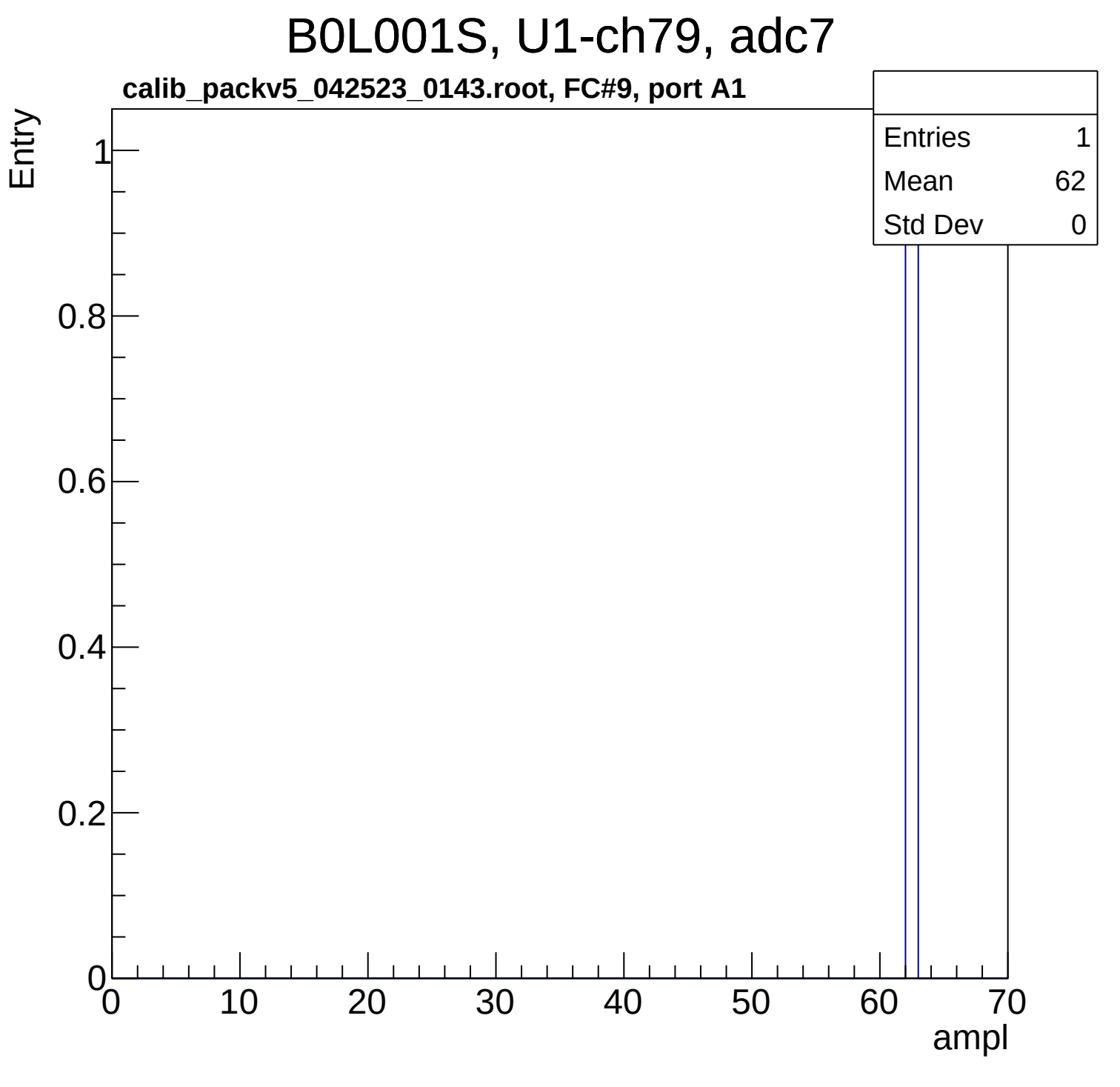
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B0L001S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 62
Std Dev 0
Entry
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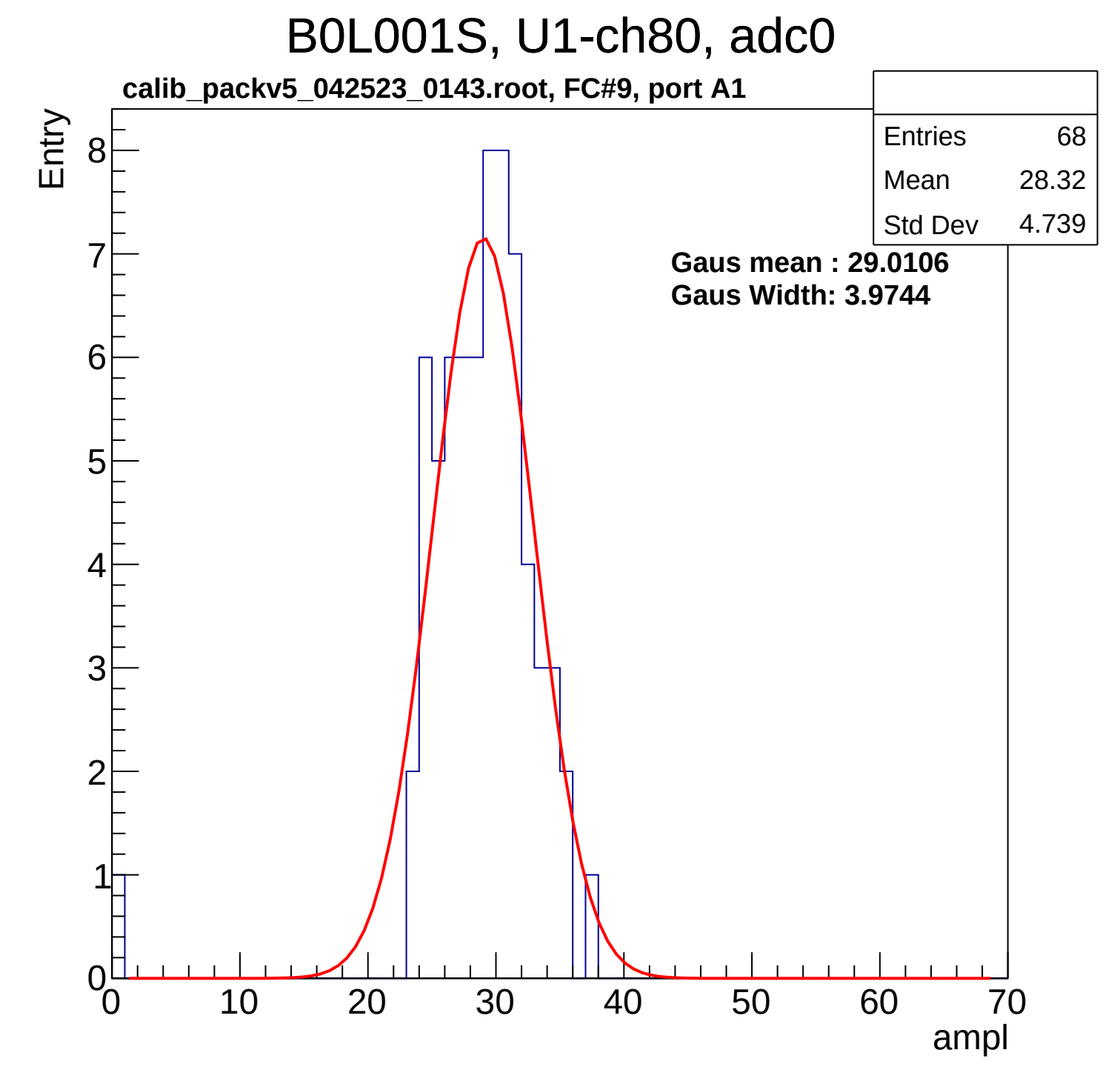
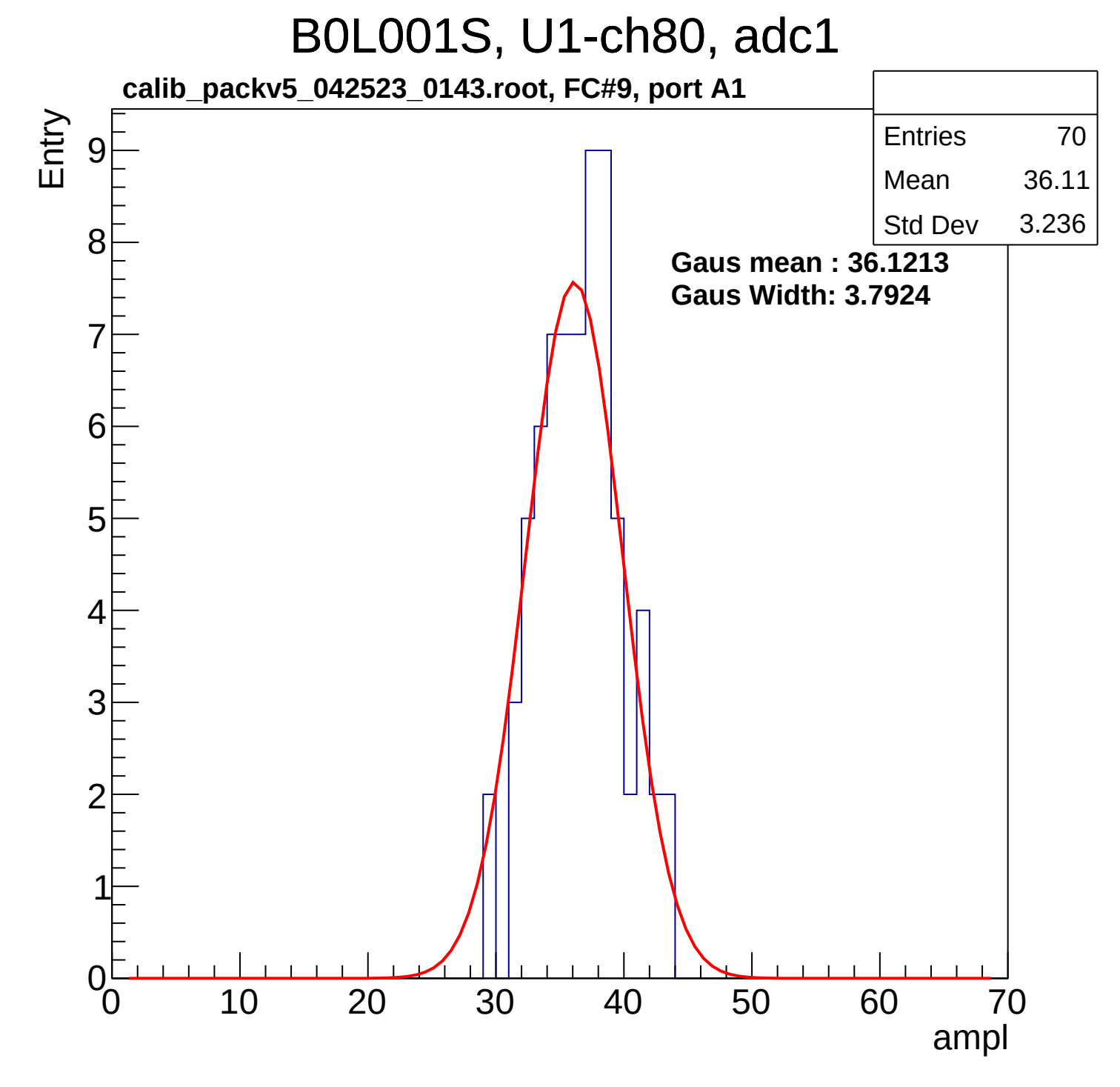
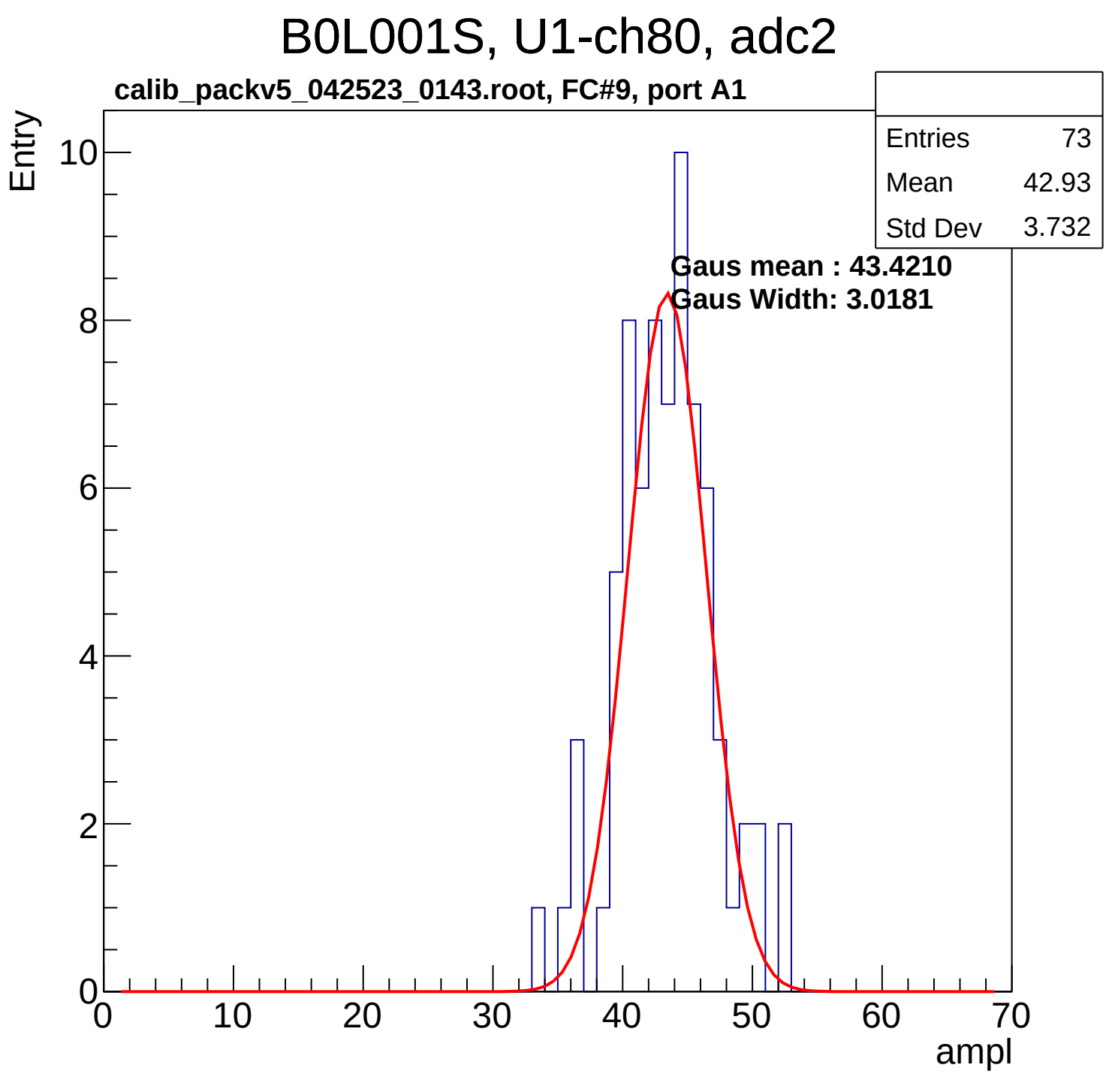
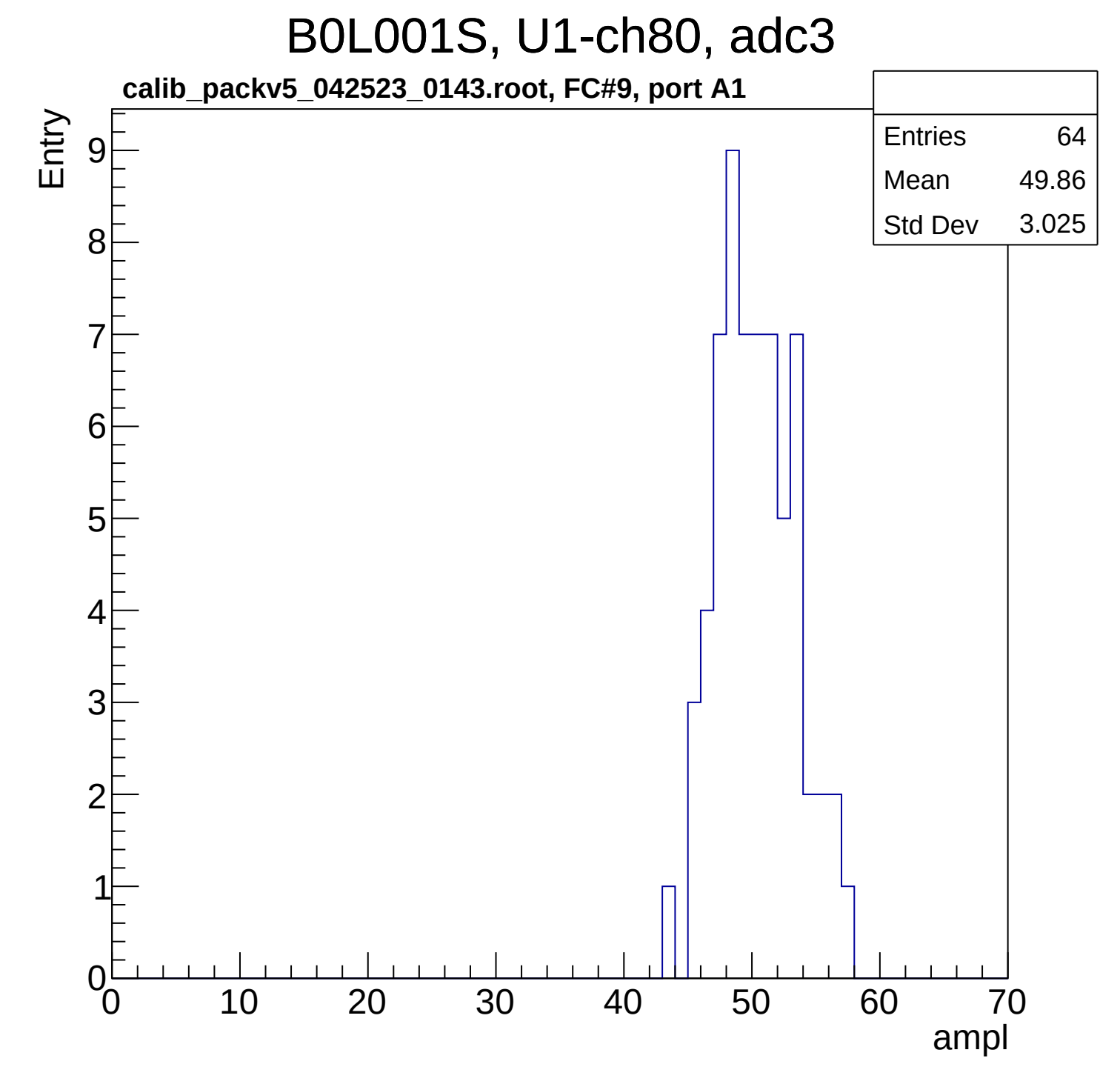
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 28.32
Std Dev 4.739
Gaus mean : 29.0106
Gaus Width: 3.9744
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 36.11
Std Dev 3.236
Gaus mean : 36.1213
Gaus Width: 3.7924
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.93
Std Dev 3.732
Gaus mean : 43.4210
Gaus Width: 3.0181
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 49.86
Std Dev 3.025
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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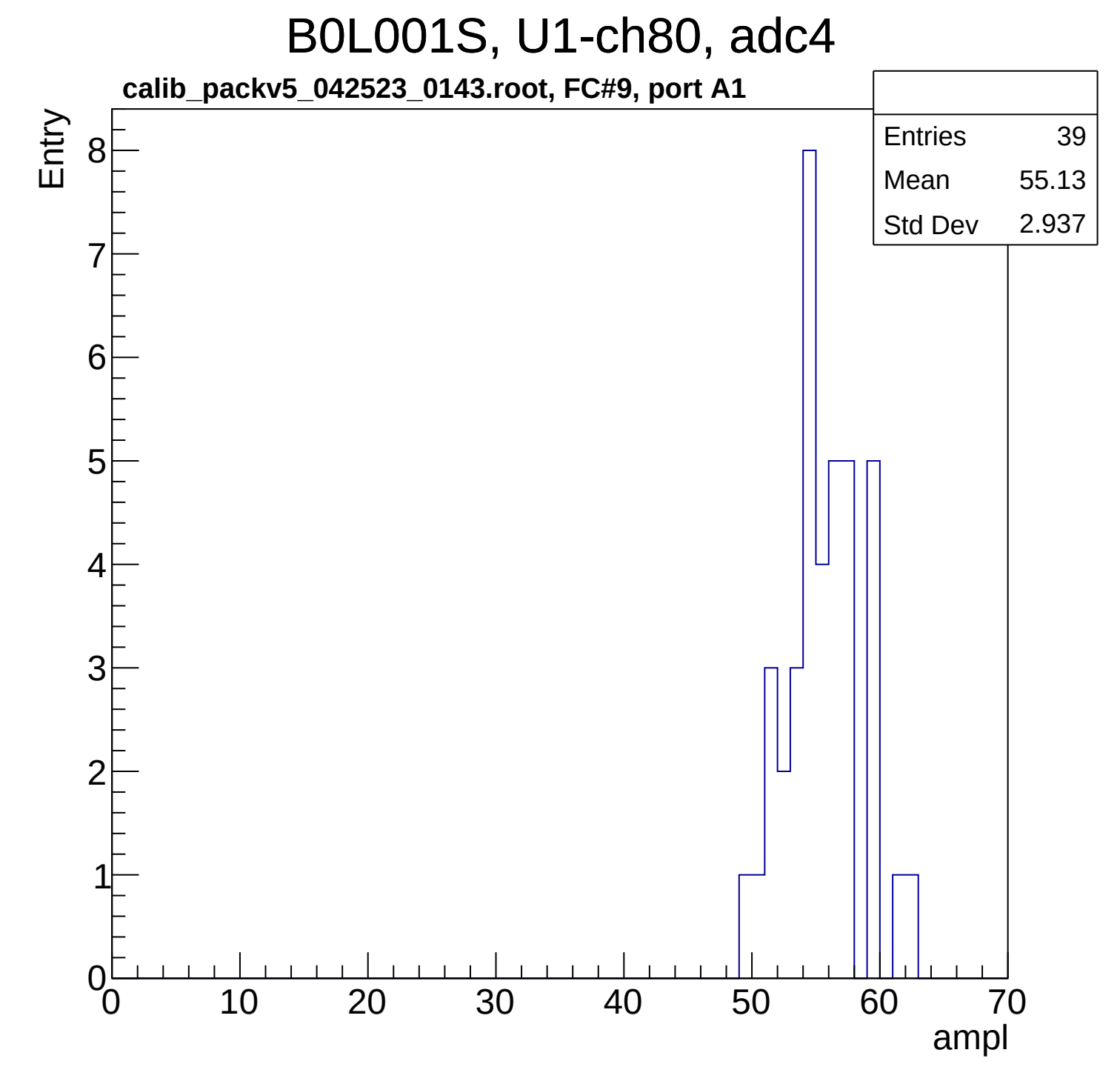
B0L001S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 55.13
Std Dev 2.937
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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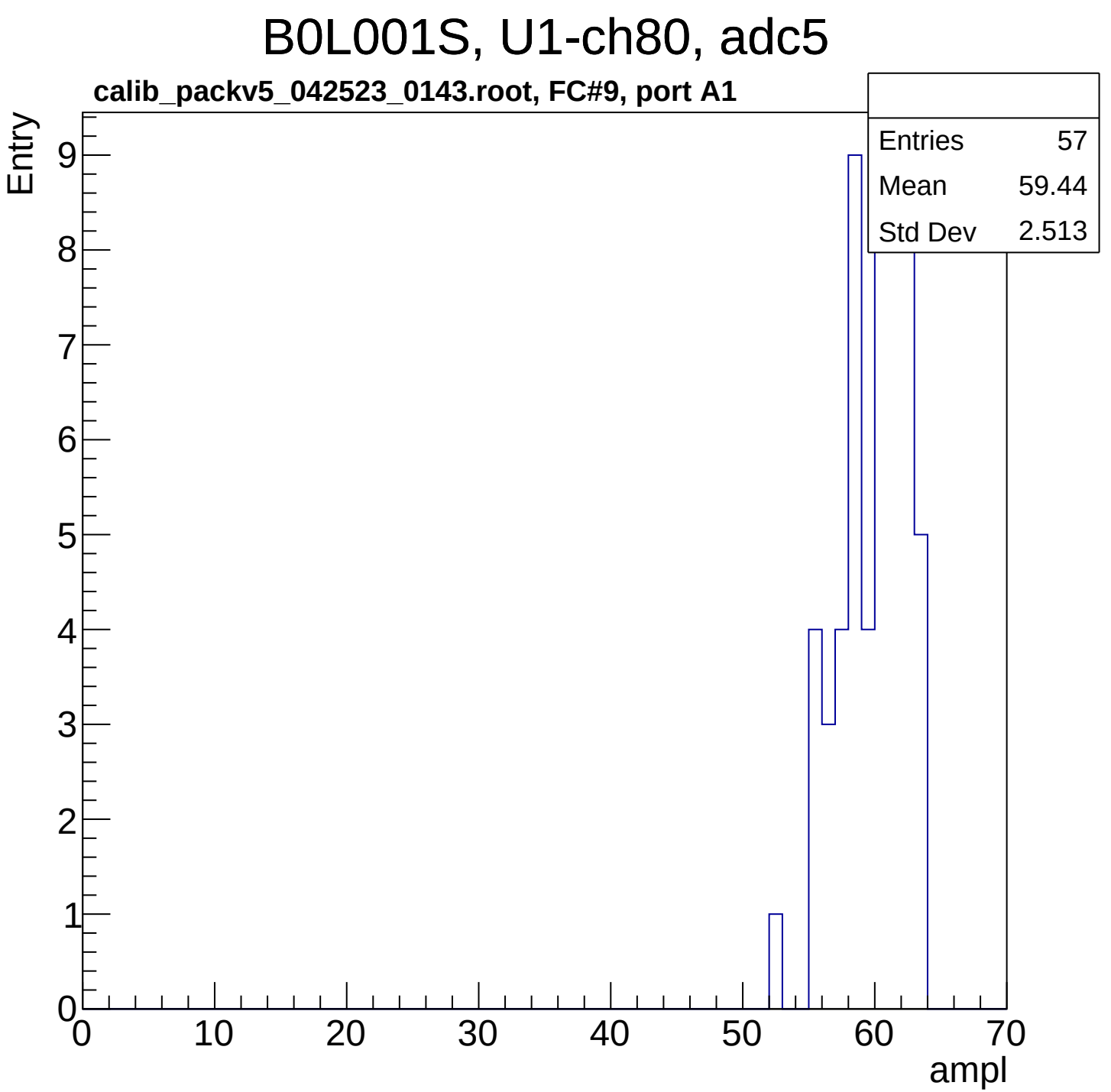
B0L001S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 59.44
Std Dev 2.513
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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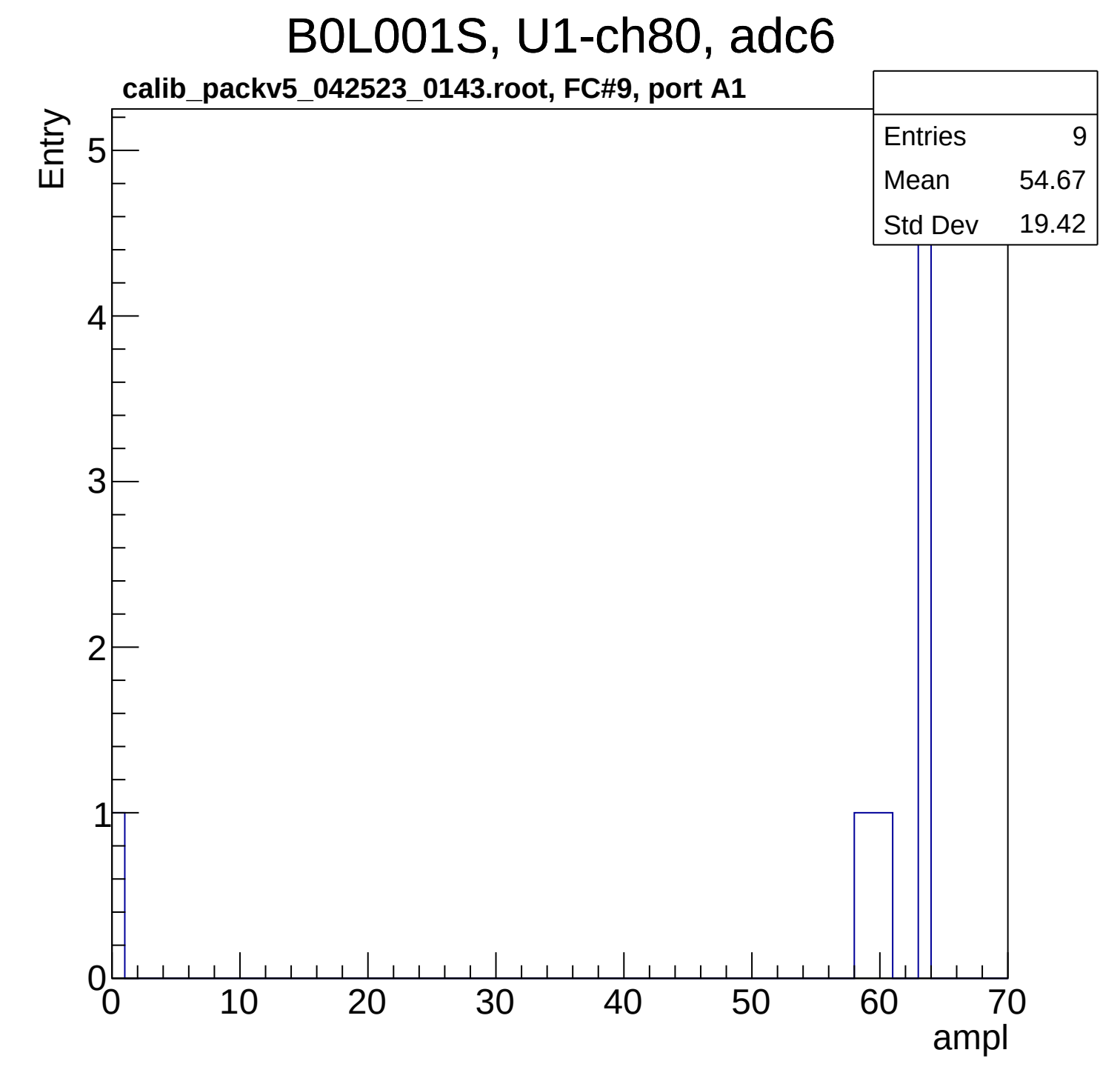
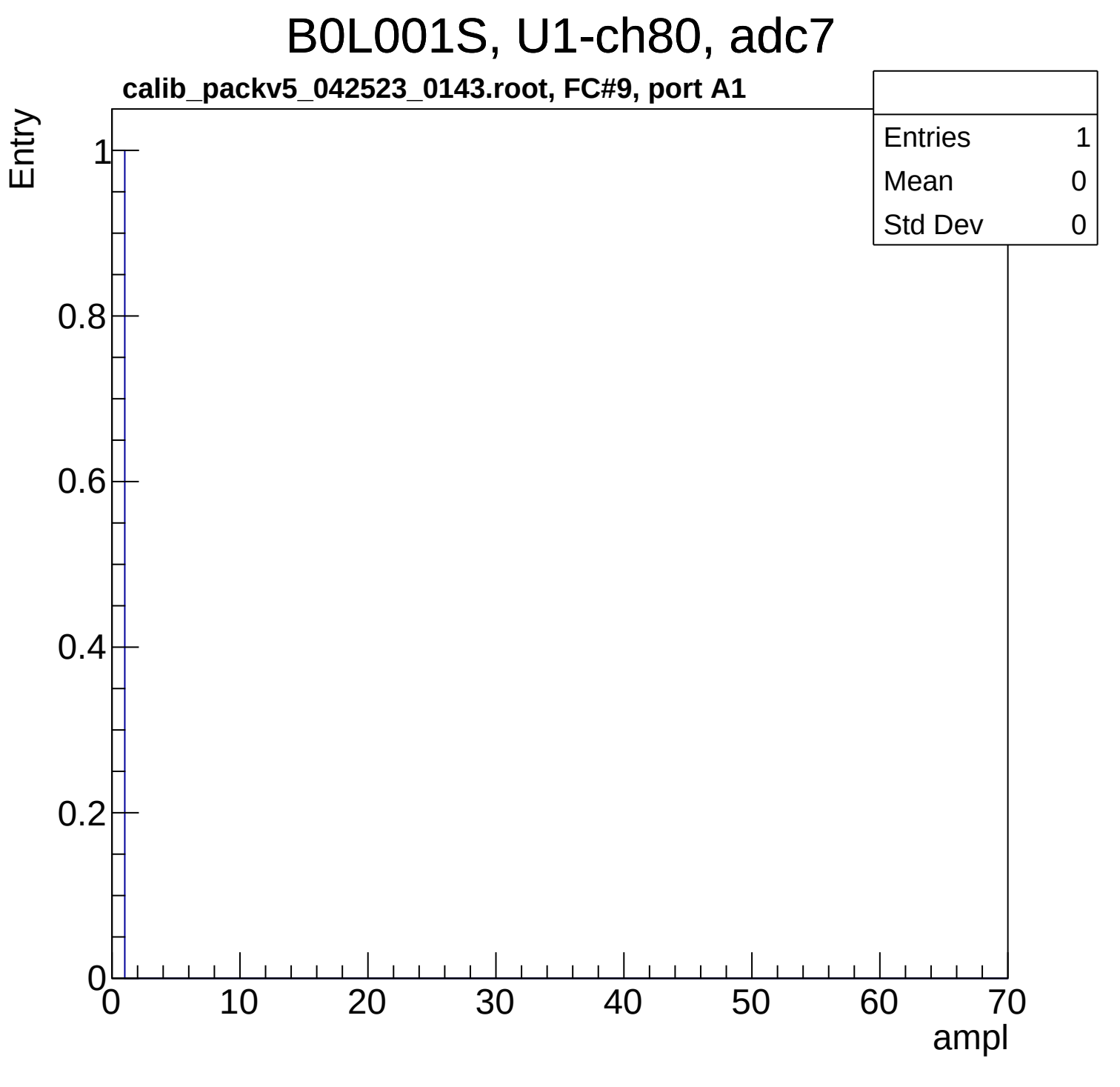

B0L001S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 54.67
Std Dev 19.42
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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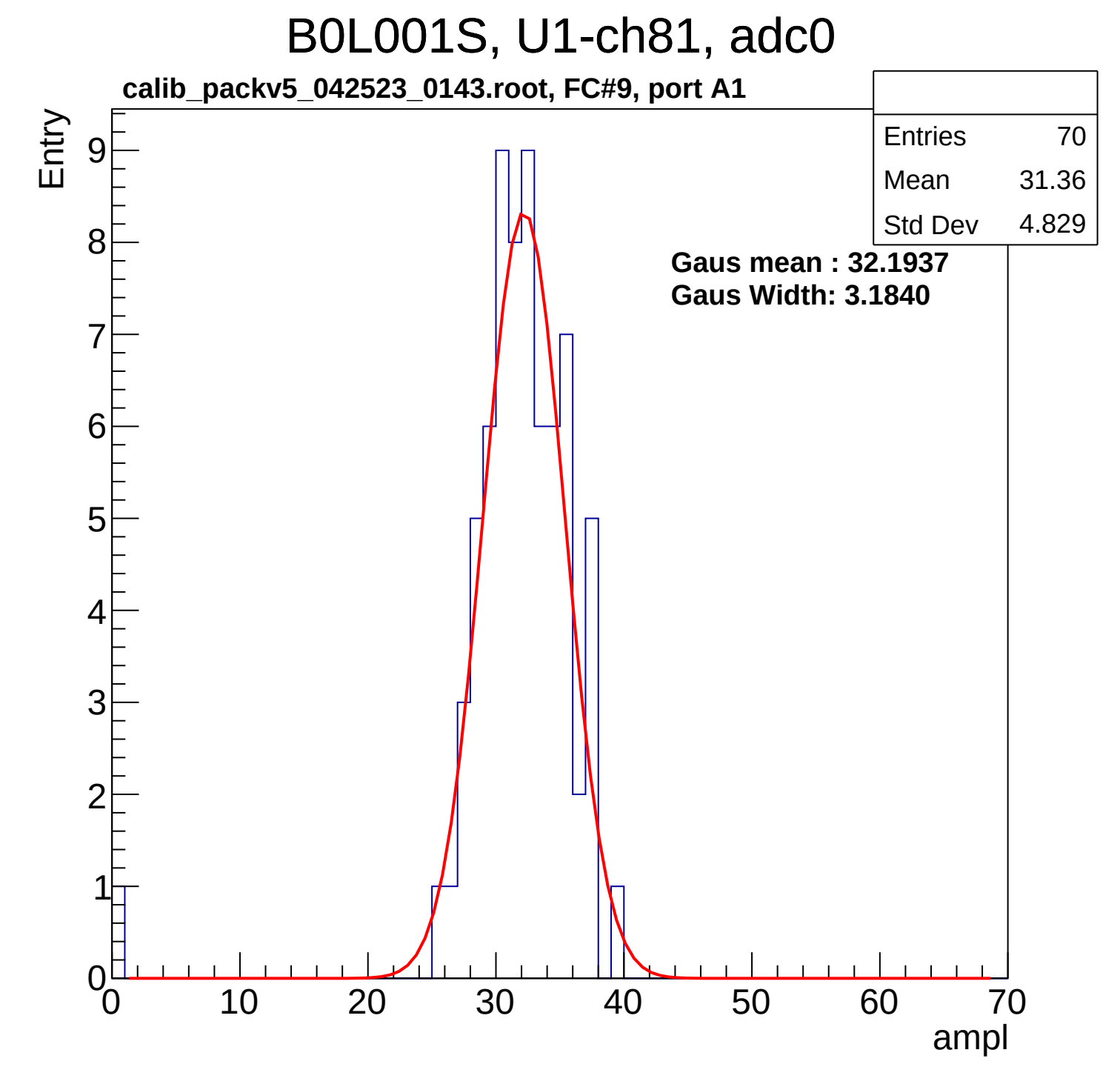
ampl


B0L001S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 31.36
Std Dev 4.829
Gaus mean : 32.1937
Gaus Width: 3.1840
Entry
ampl
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2
3
4
5
6
7
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0
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60
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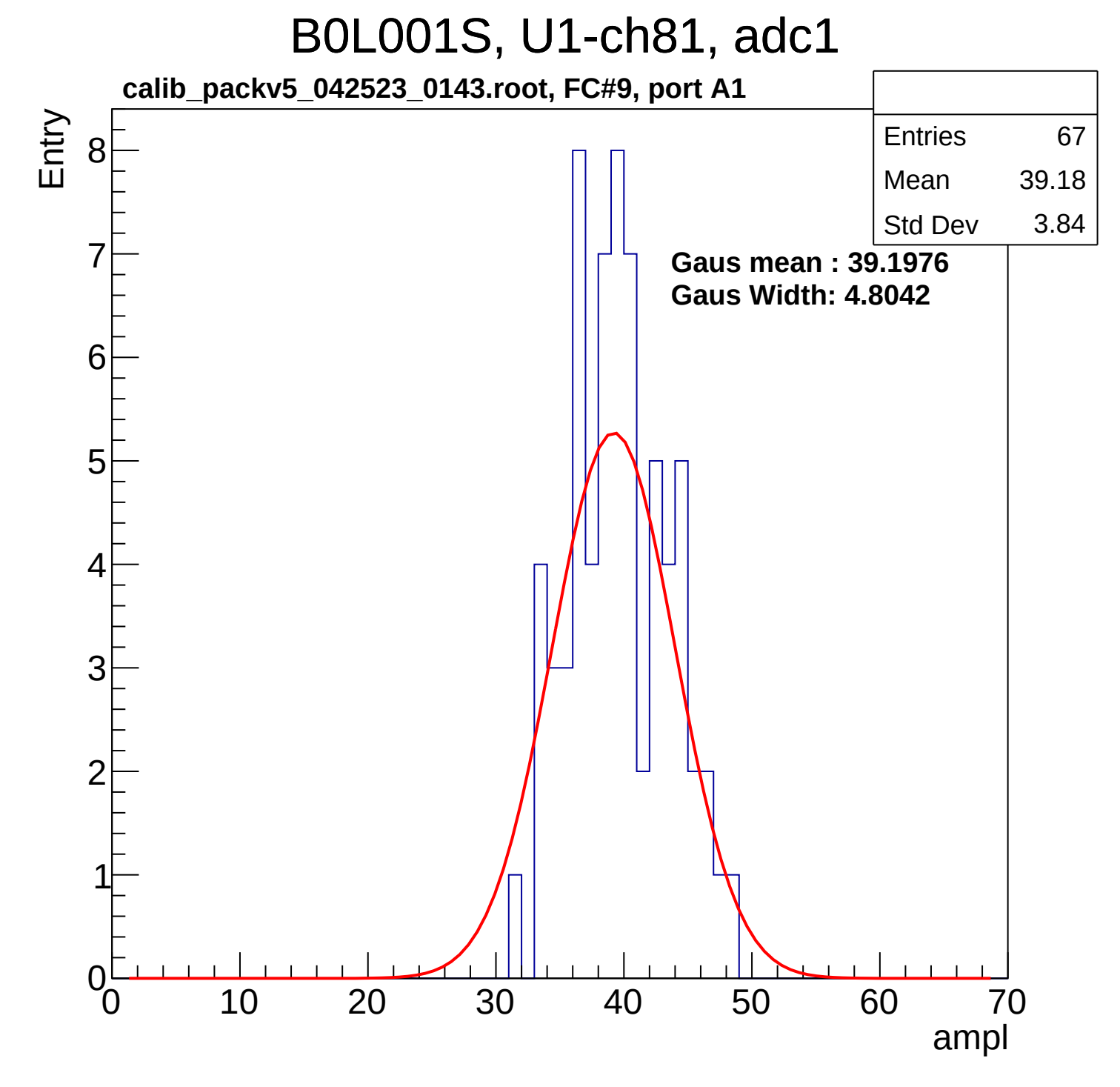
B0L001S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 39.18
Std Dev 3.84
Gaus mean : 39.1976
Gaus Width: 4.8042
Entry
ampl
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2
3
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5
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7
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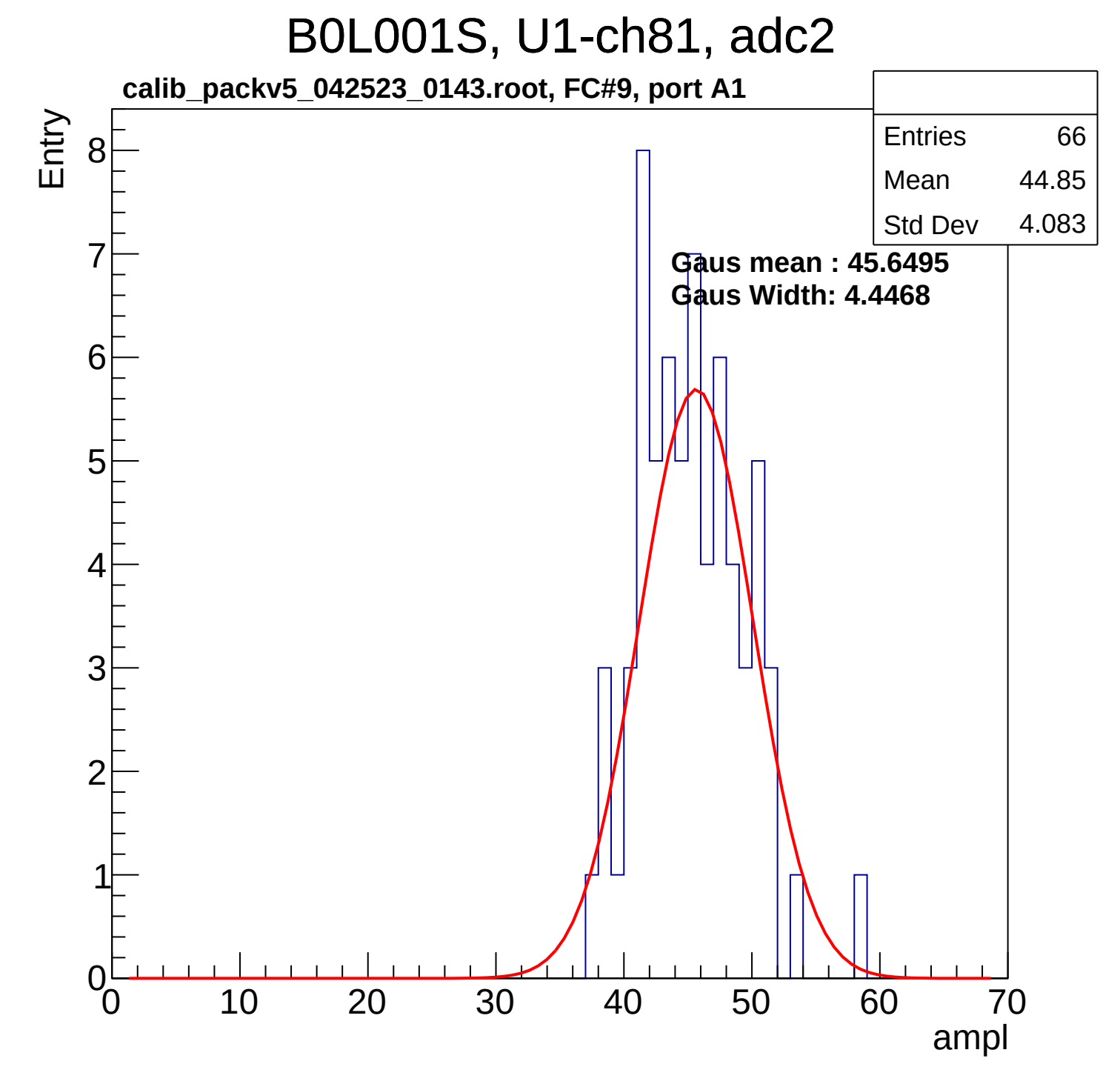
B0L001S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 44.85
Std Dev 4.083
Gaus mean : 45.6495
Gaus Width: 4.4468
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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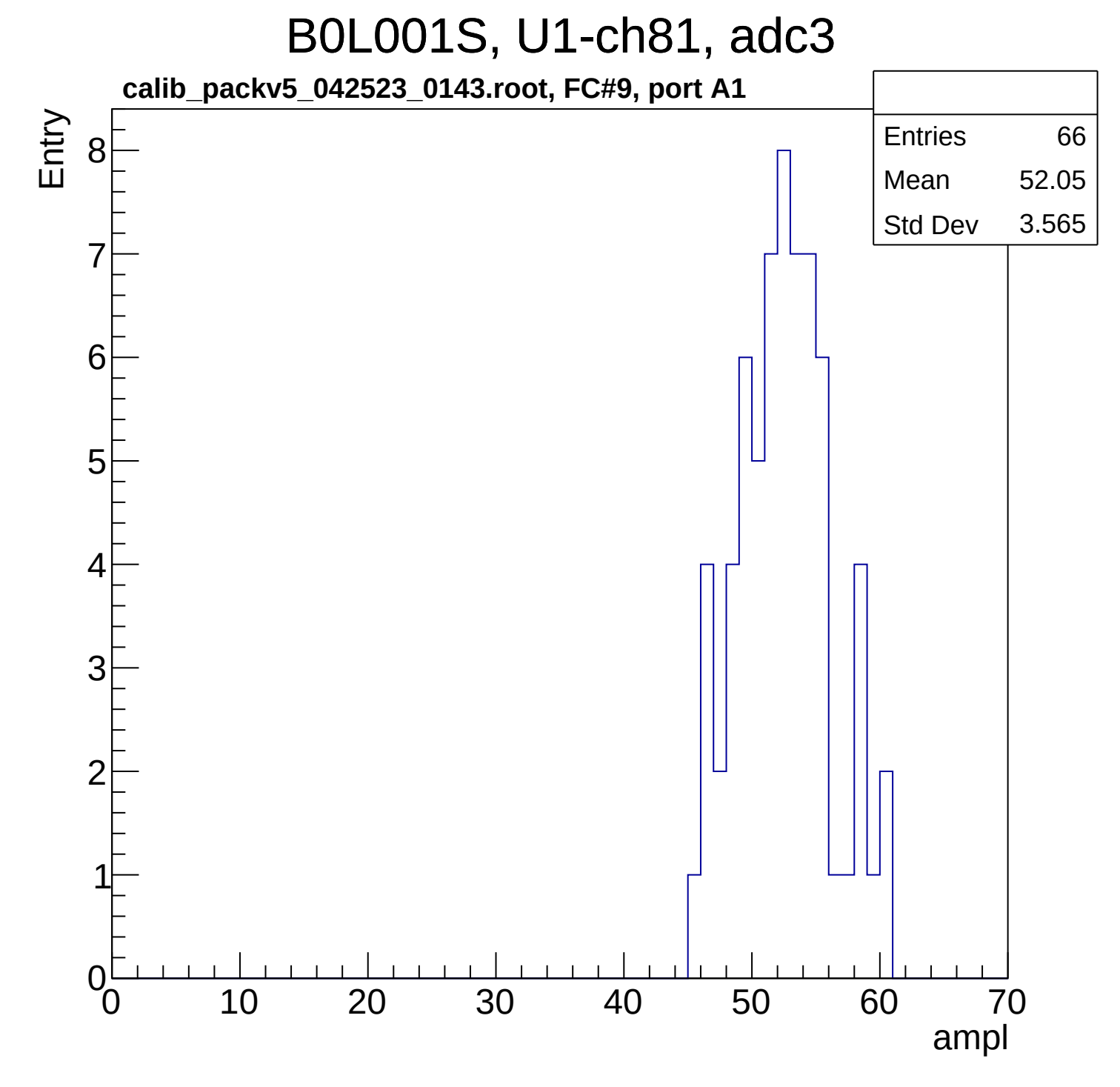
B0L001S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 52.05
Std Dev 3.565
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
50
60
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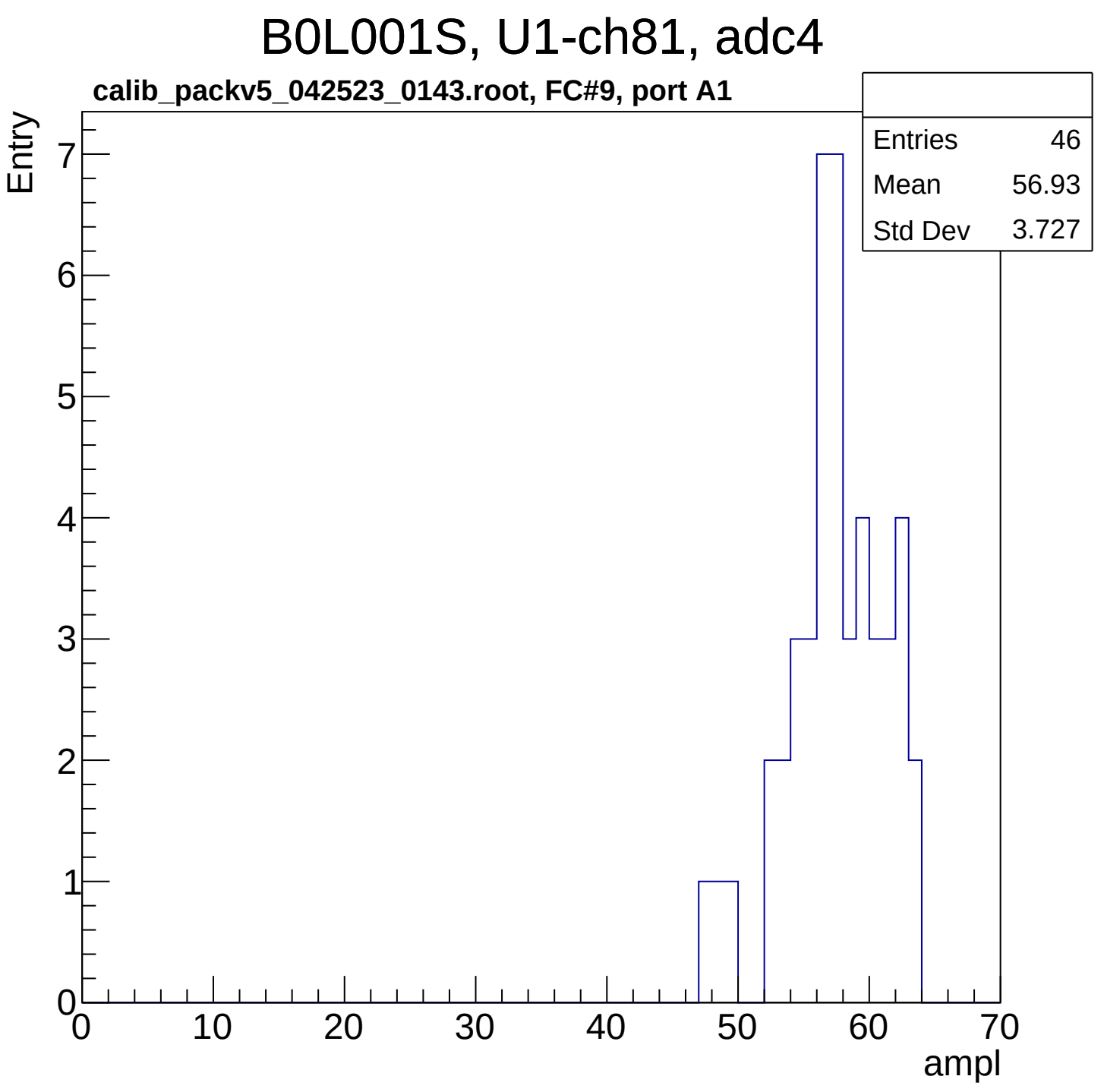
B0L001S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 56.93
Std Dev 3.727
Entry
ampl
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1
2
3
4
5
6
7
0
10
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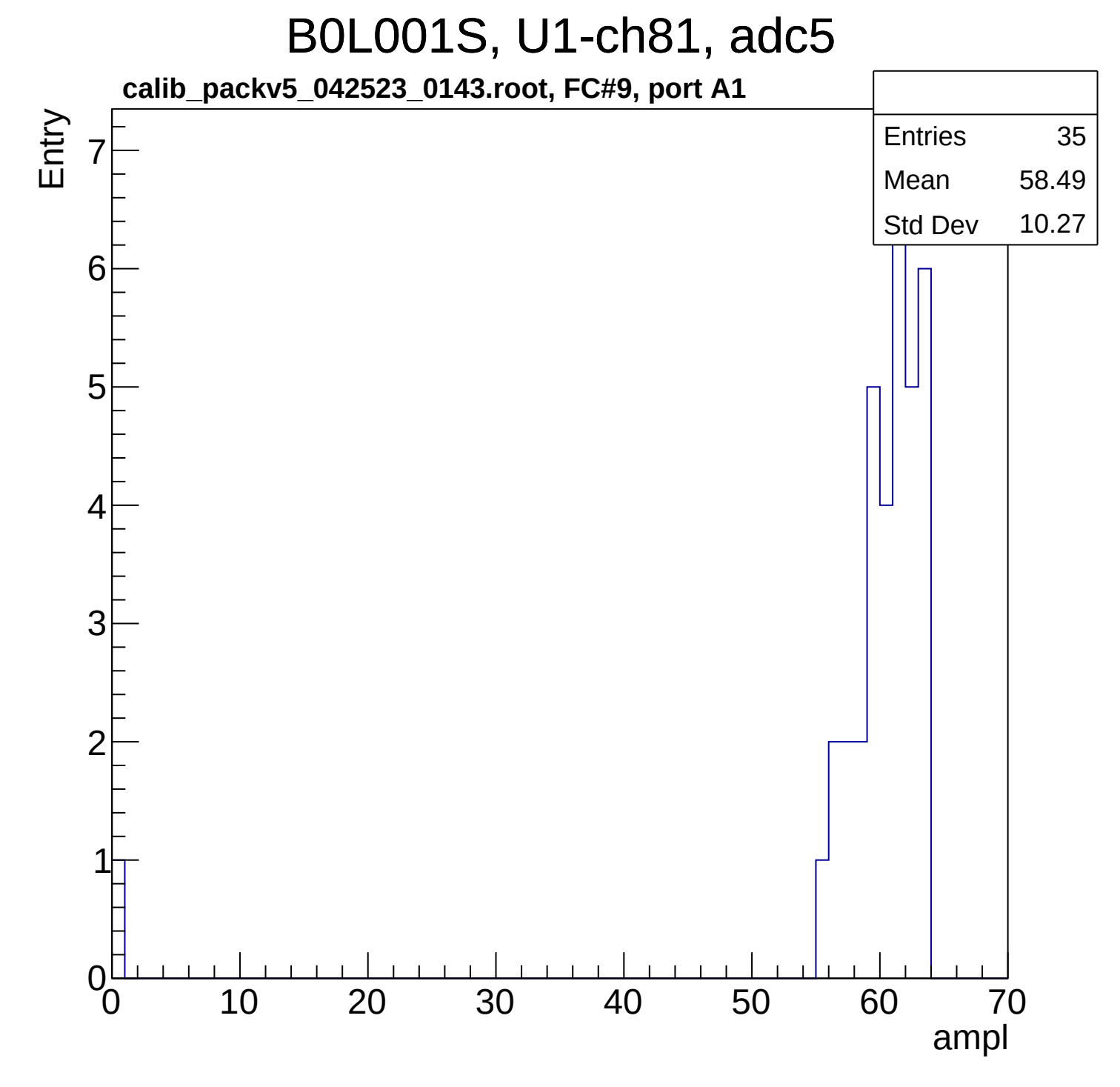
40
50
60
70


B0L001S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.49
Std Dev 10.27
Entry
ampl
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3
4
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0
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40
50
60
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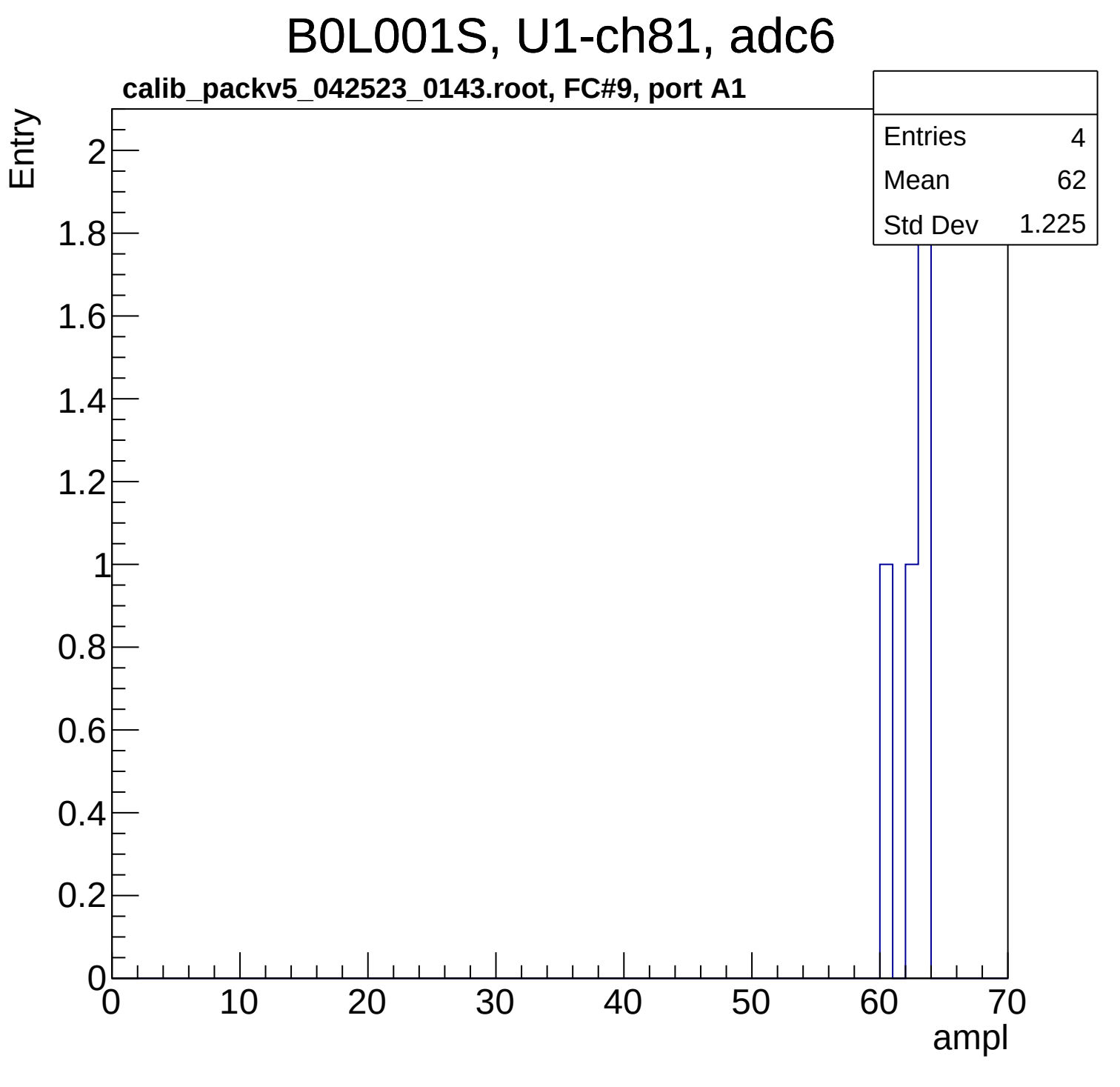
B0L001S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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40
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60
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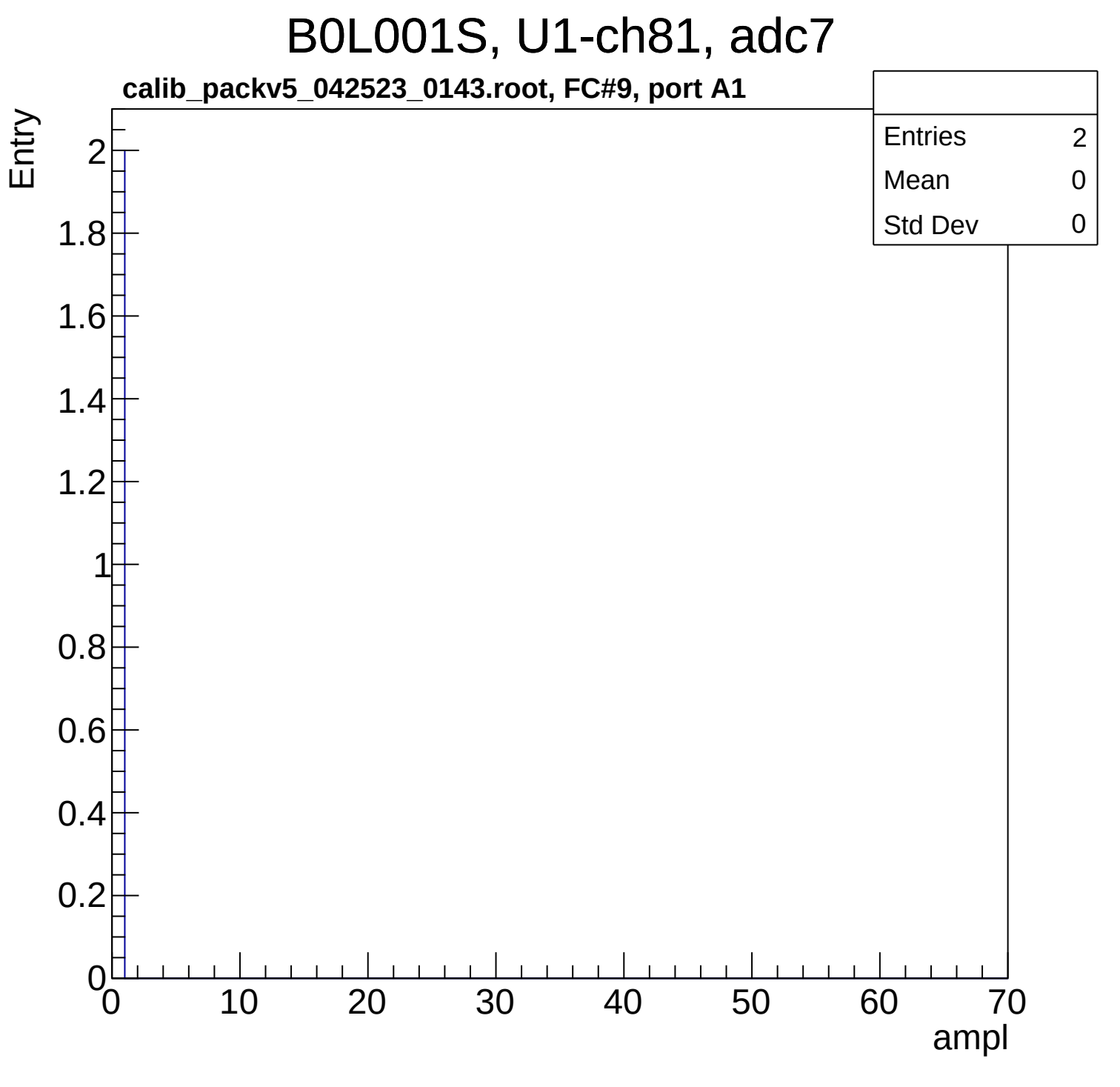
B0L001S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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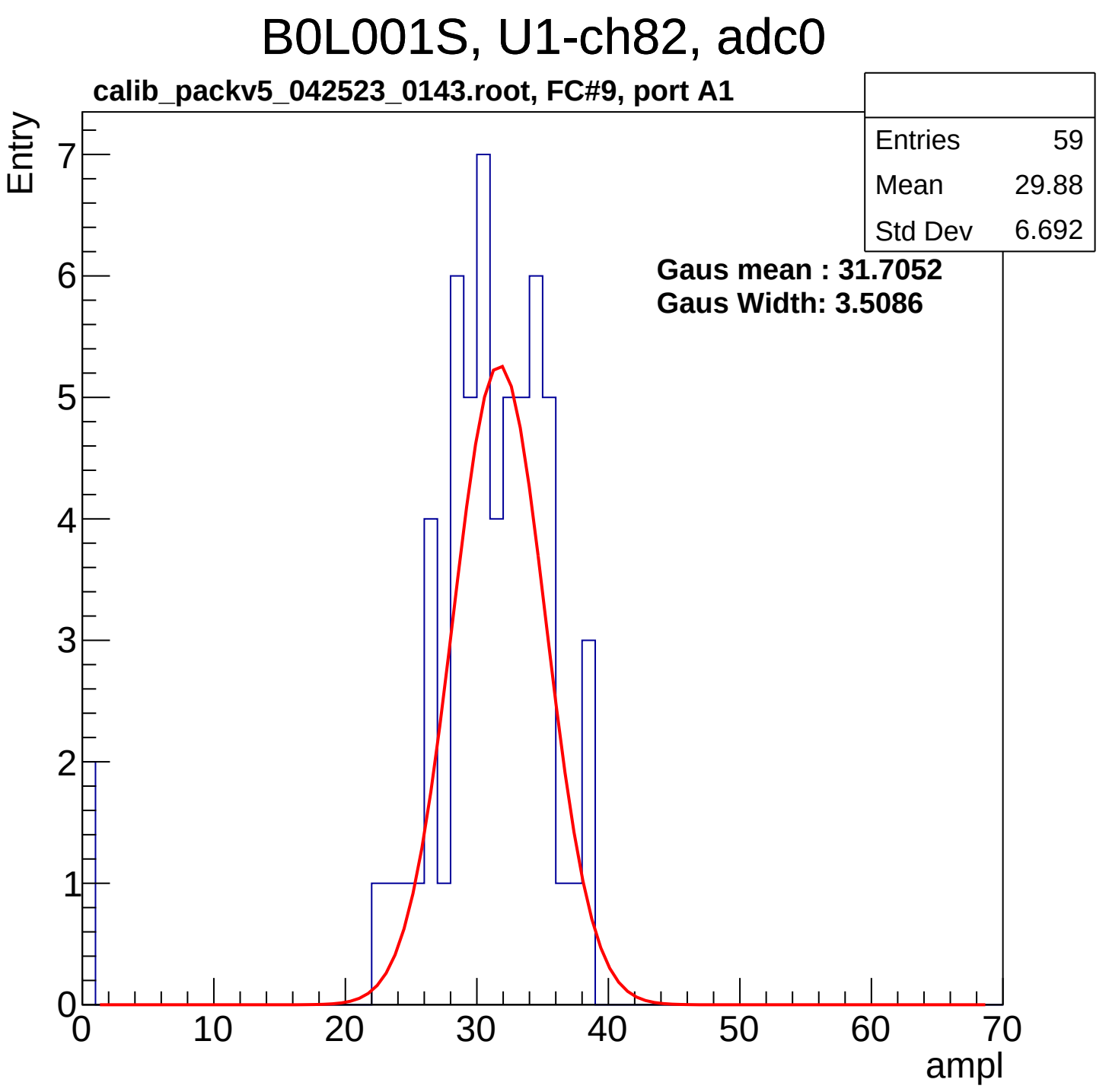
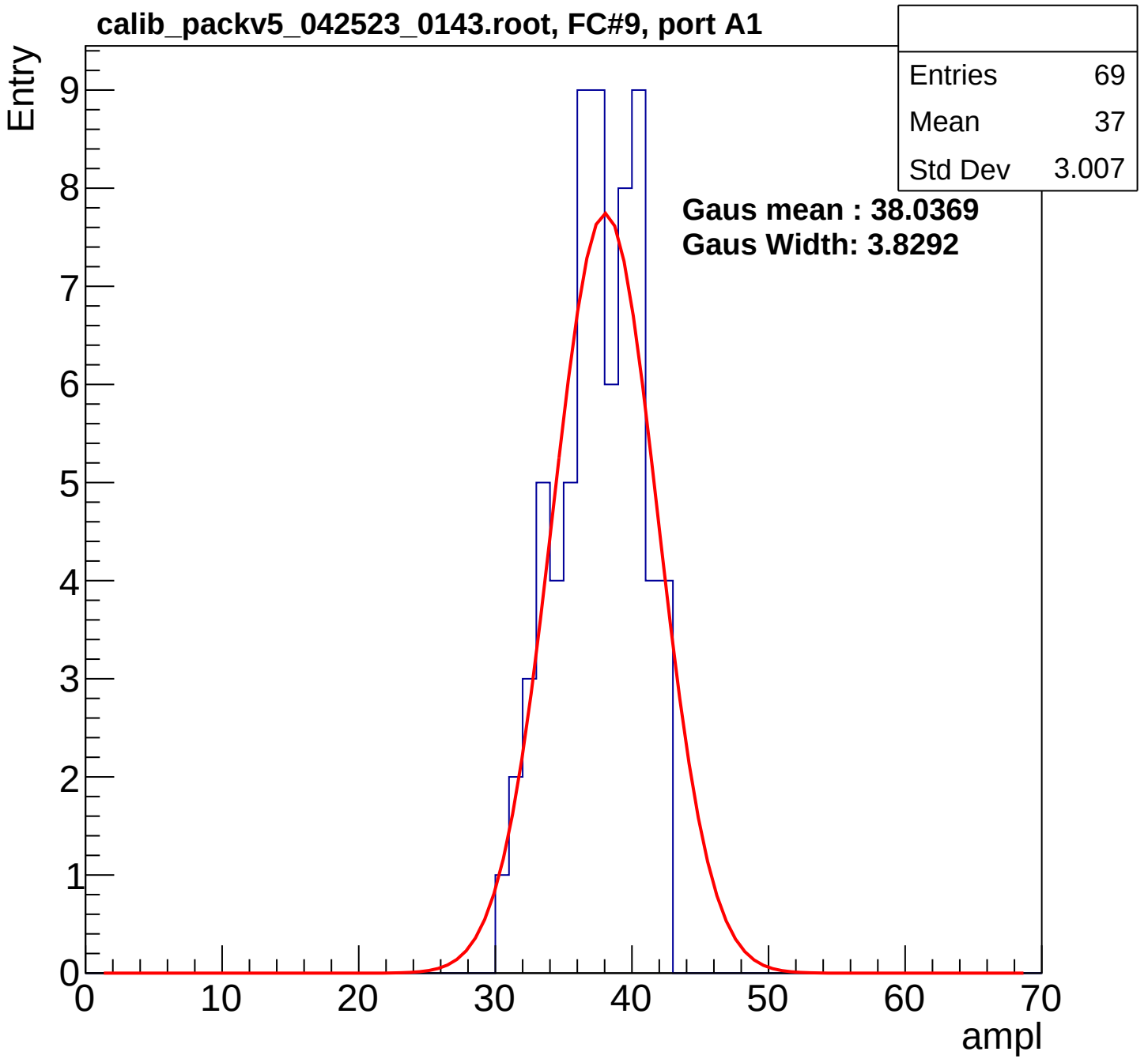
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.88
Std Dev 6.692
Gaus mean : 31.7052
Gaus Width: 3.5086
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 37
Std Dev 3.007
Gaus mean : 38.0369
Gaus Width: 3.8292
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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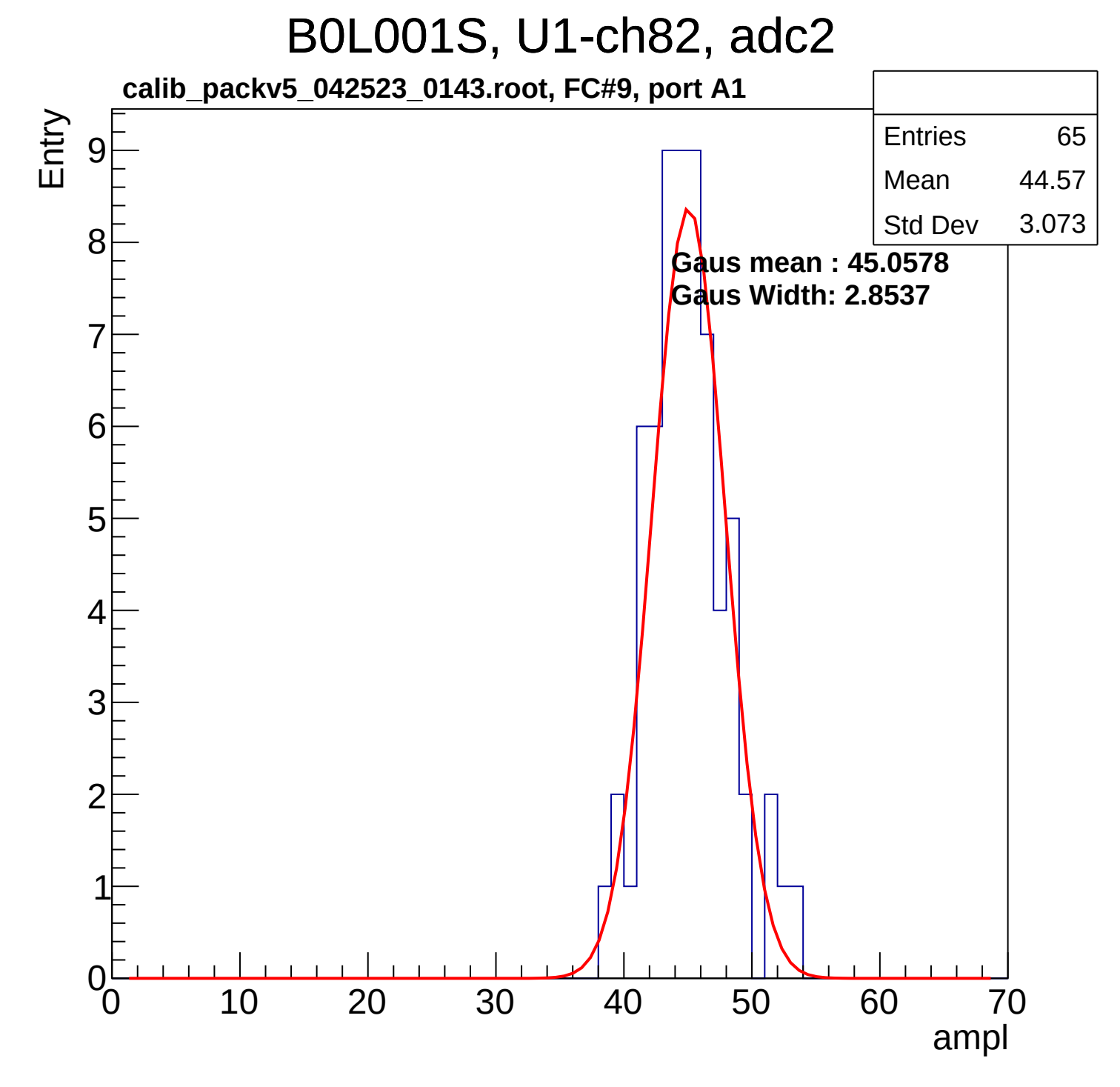
B0L001S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.57
Std Dev 3.073
Gaus mean : 45.0578
Gaus Width: 2.8537
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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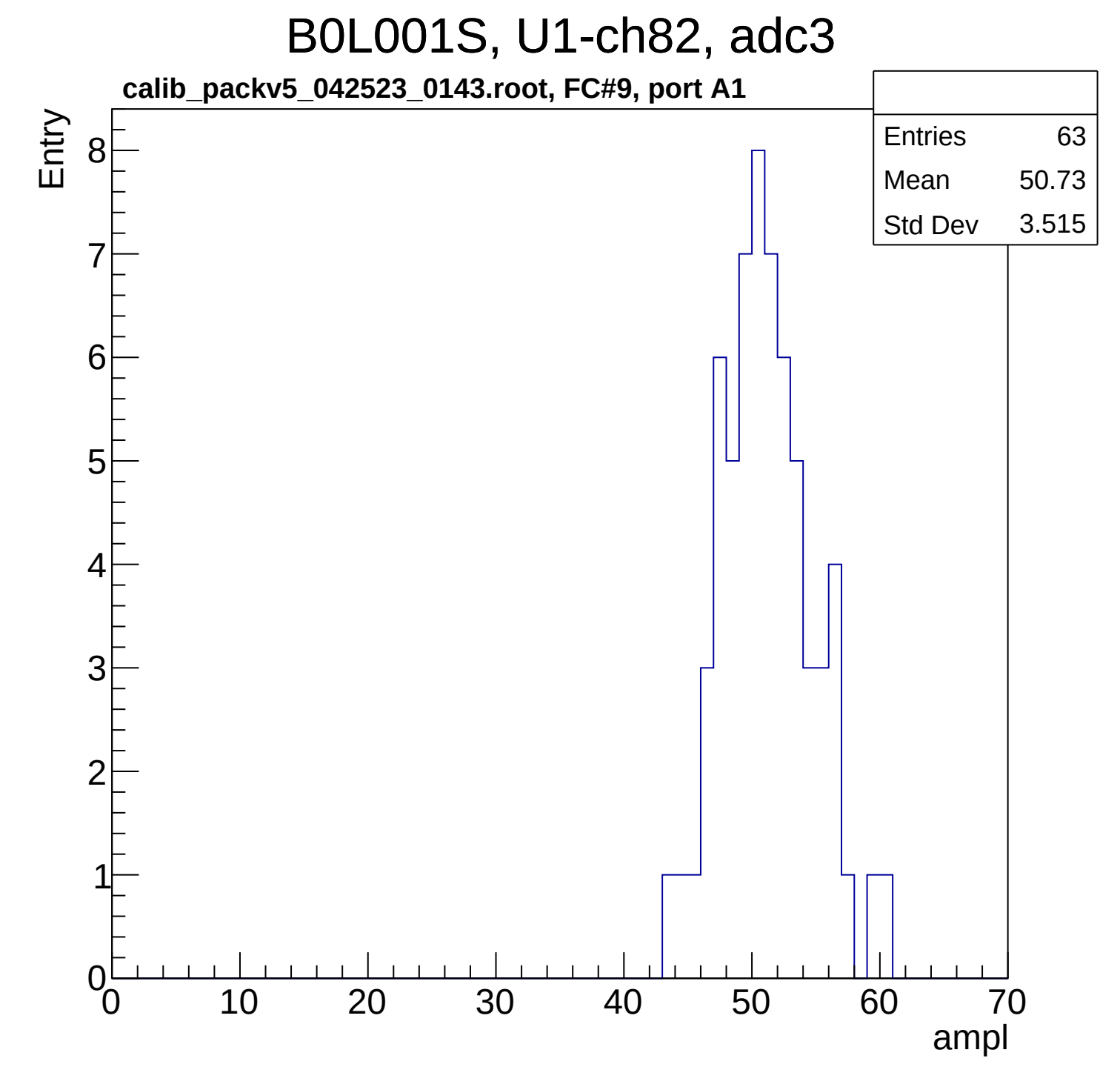
B0L001S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.73
Std Dev 3.515
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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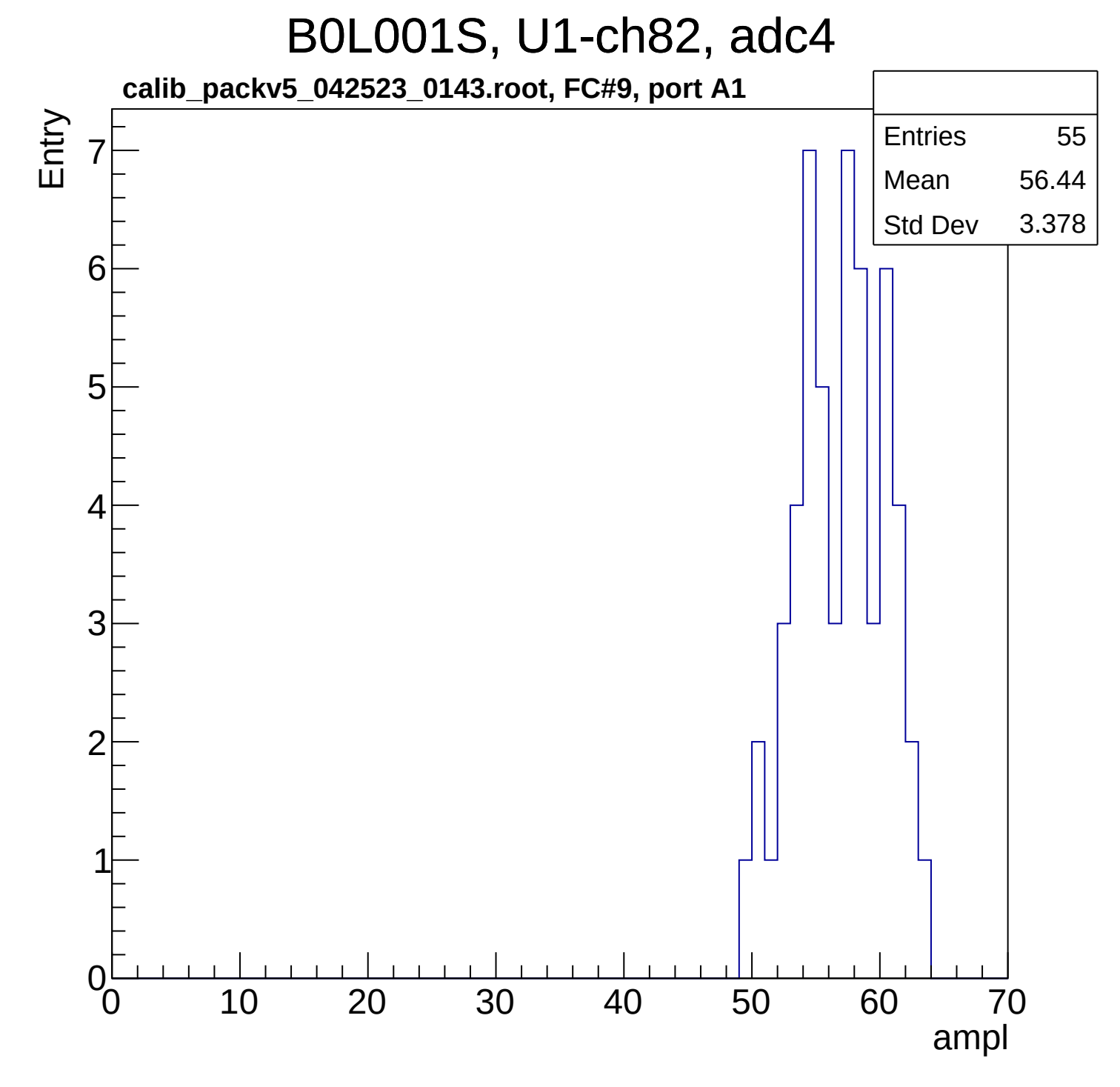
B0L001S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 56.44
Std Dev 3.378
Entry
ampl
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4
5
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20
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40
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60
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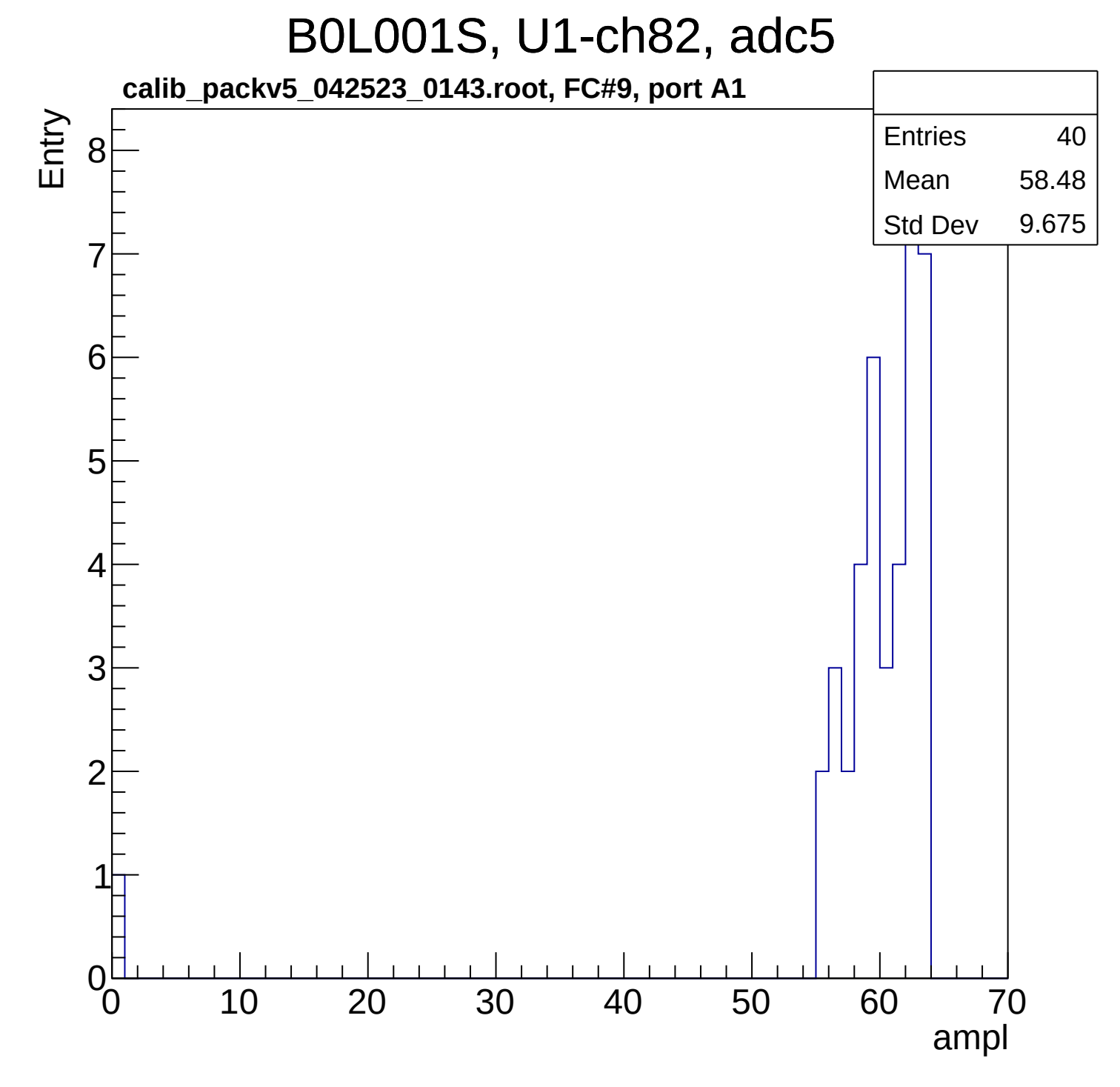
B0L001S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.48
Std Dev 9.675
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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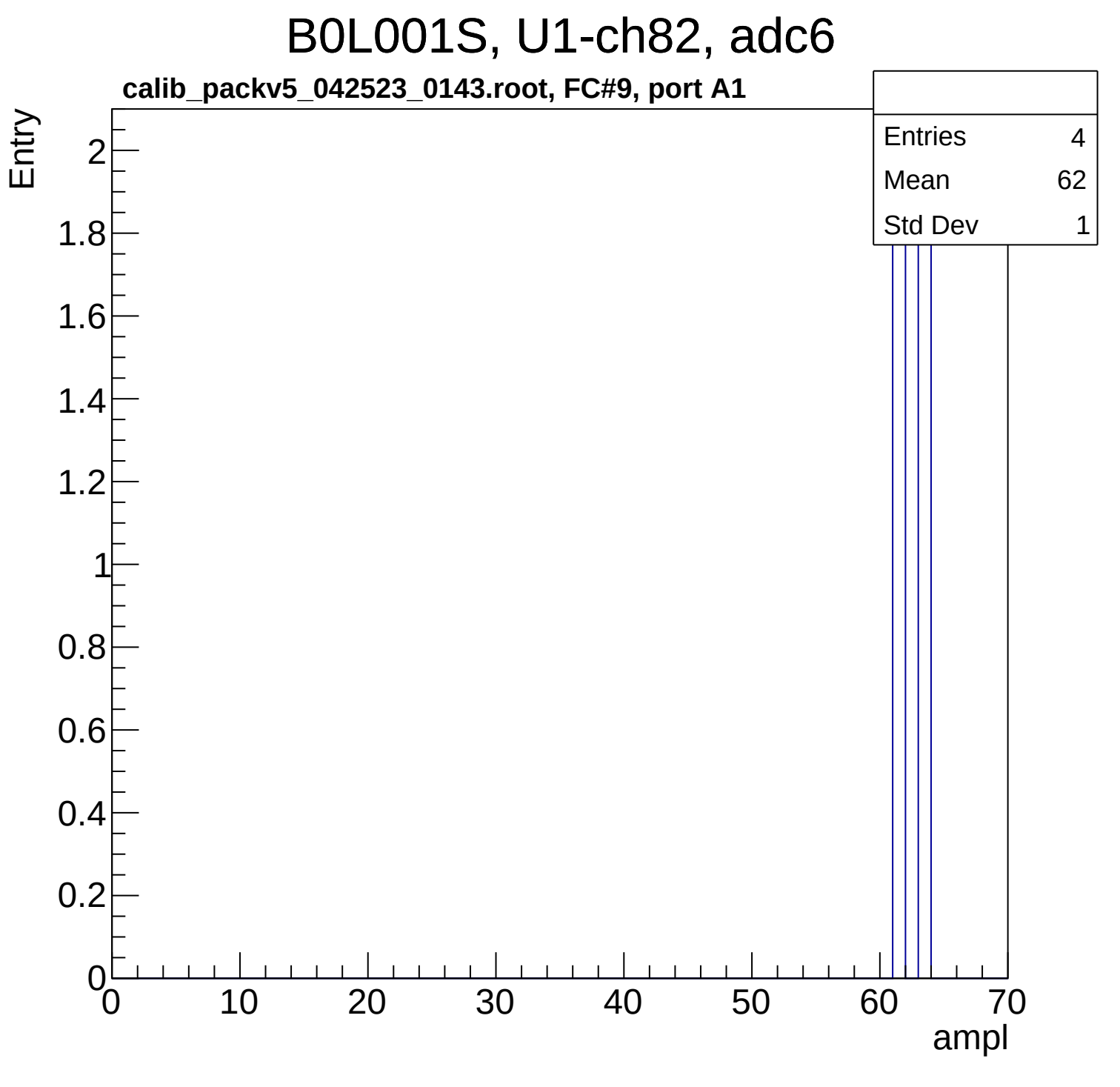
B0L001S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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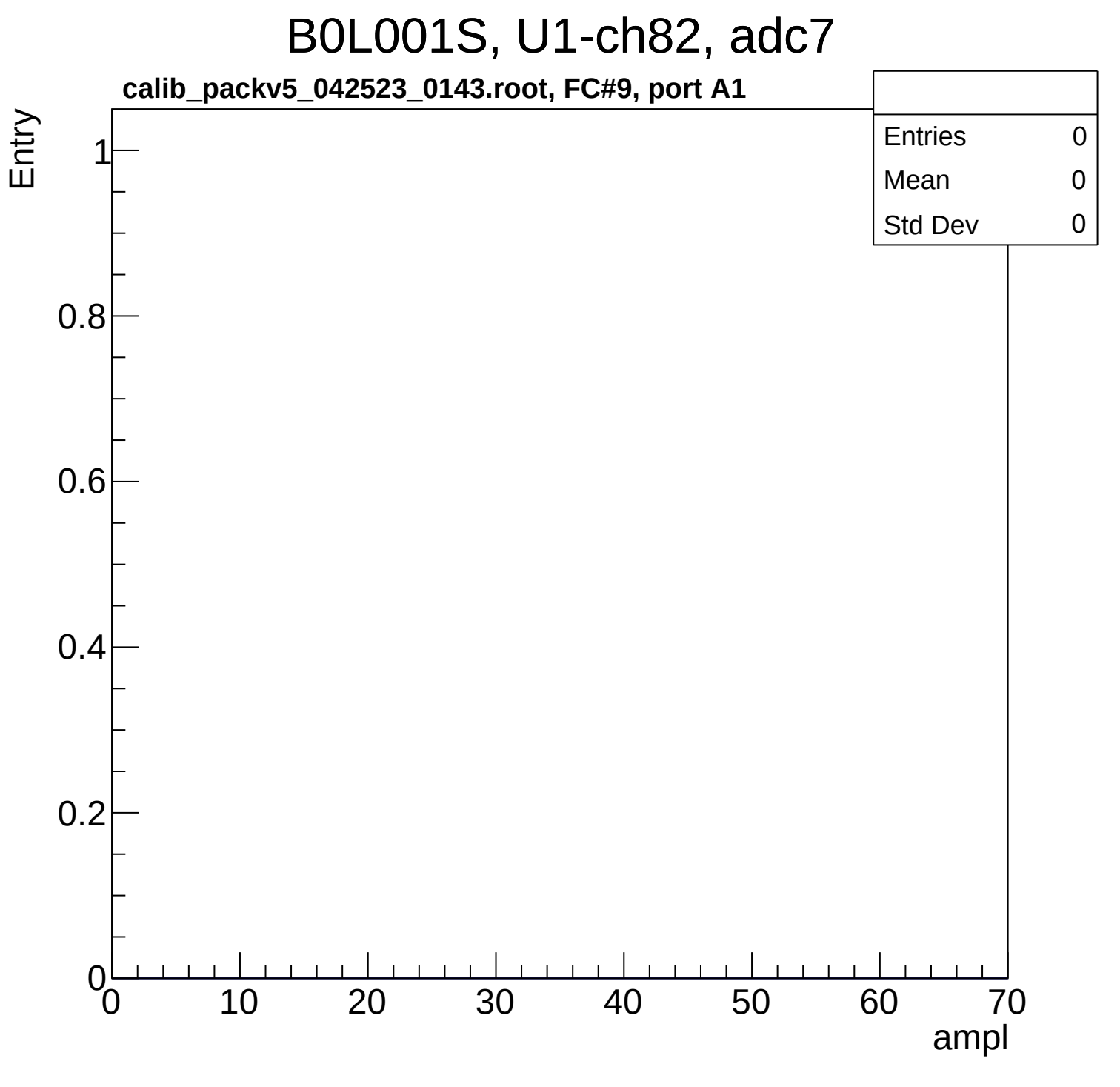
B0L001S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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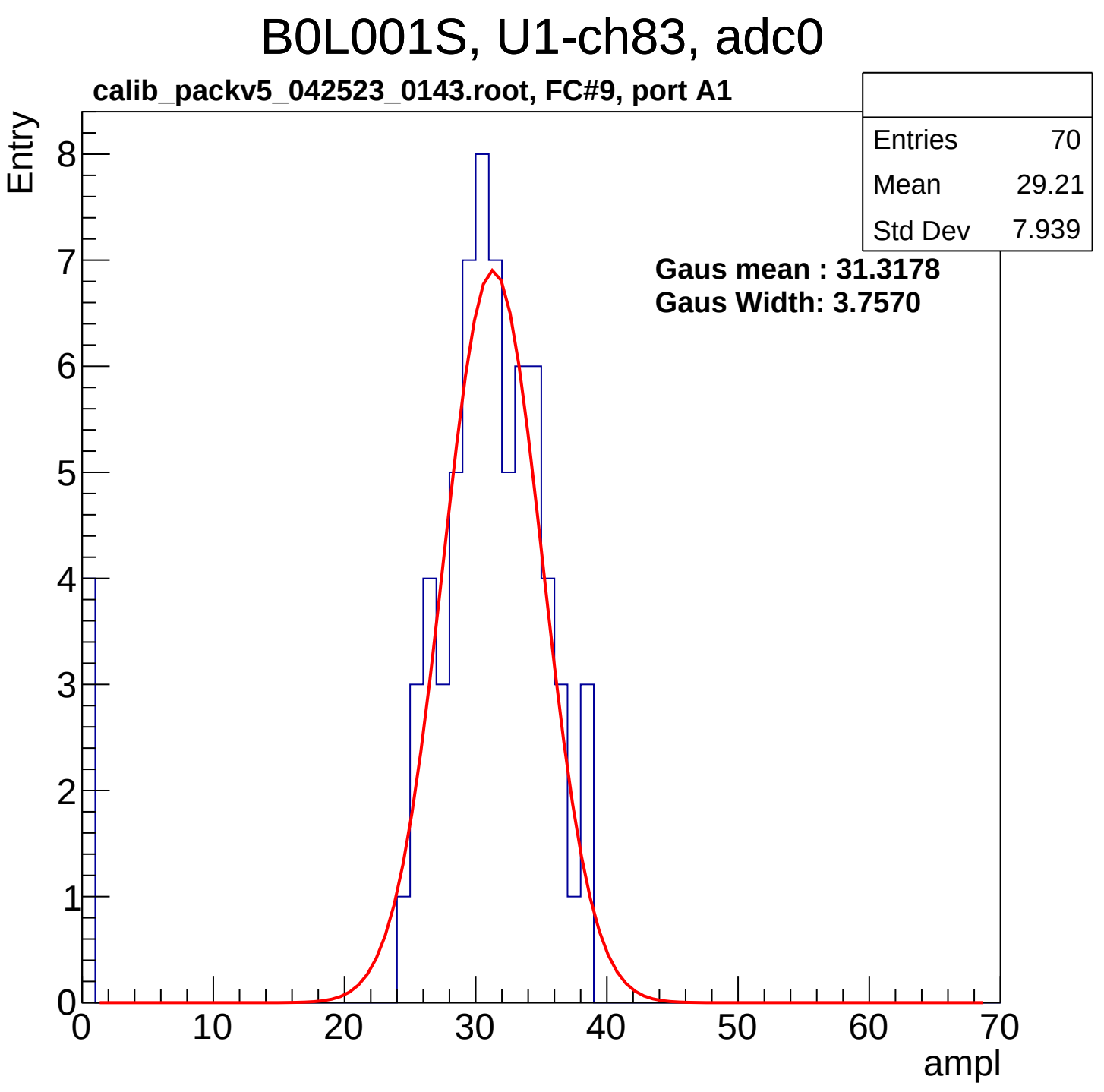
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.21
Std Dev 7.939
Gaus mean : 31.3178
Gaus Width: 3.7570
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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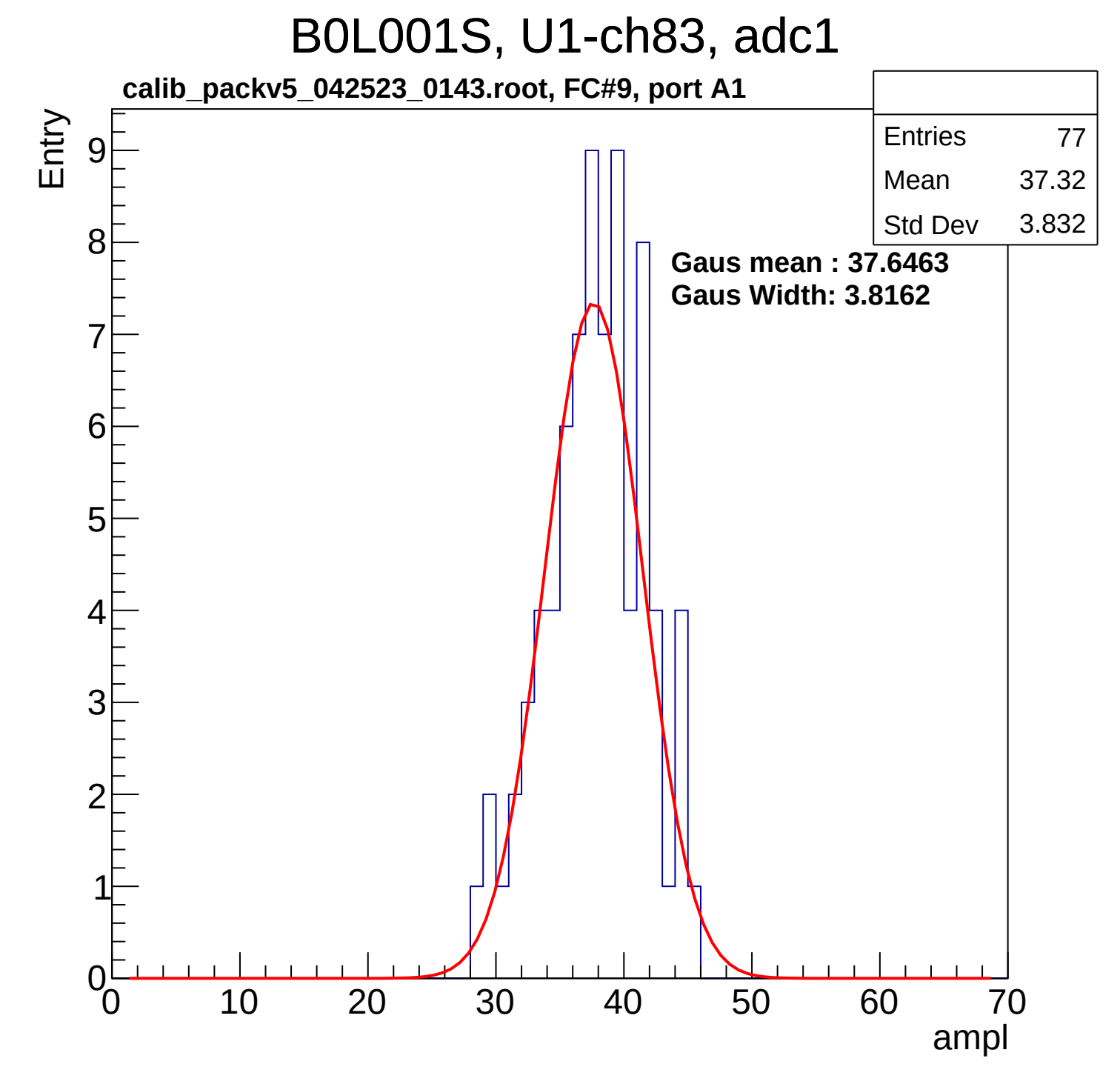
B0L001S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 37.32
Std Dev 3.832
Gaus mean : 37.6463
Gaus Width: 3.8162
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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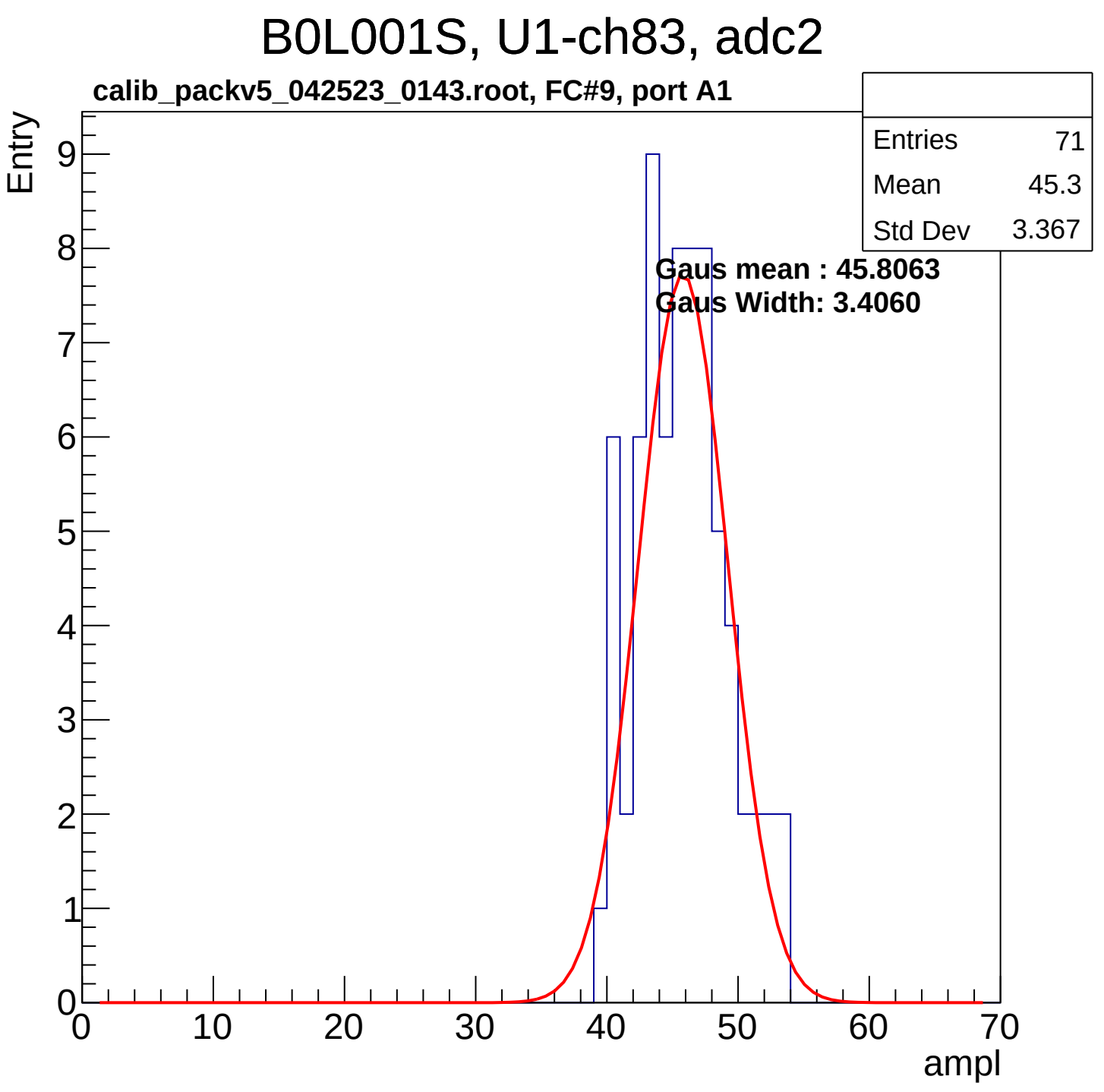
B0L001S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.3
Std Dev 3.367
Gaus mean : 45.8063
Gaus Width: 3.4060
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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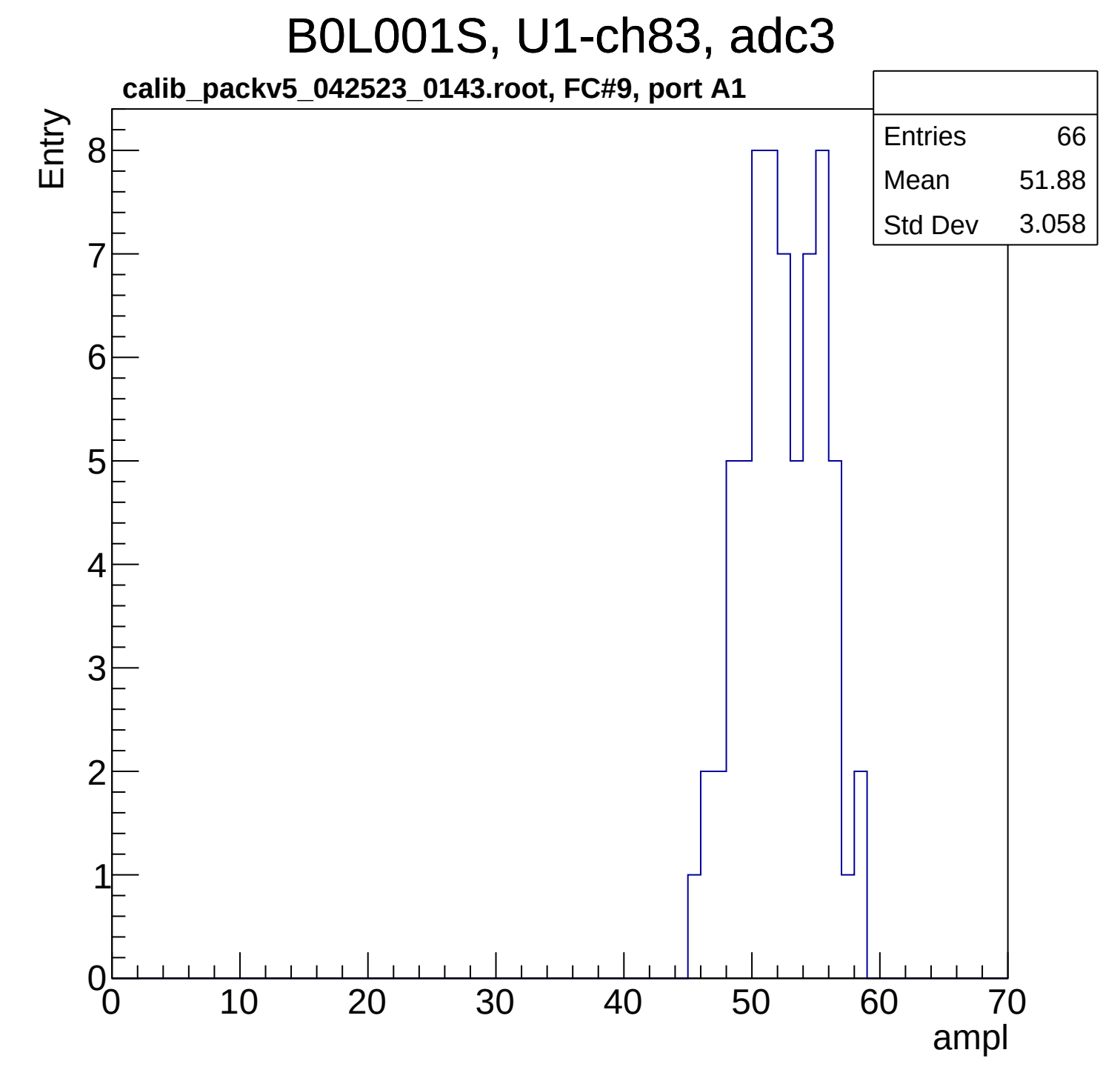
B0L001S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 51.88
Std Dev 3.058
Entry
ampl
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4
5
6
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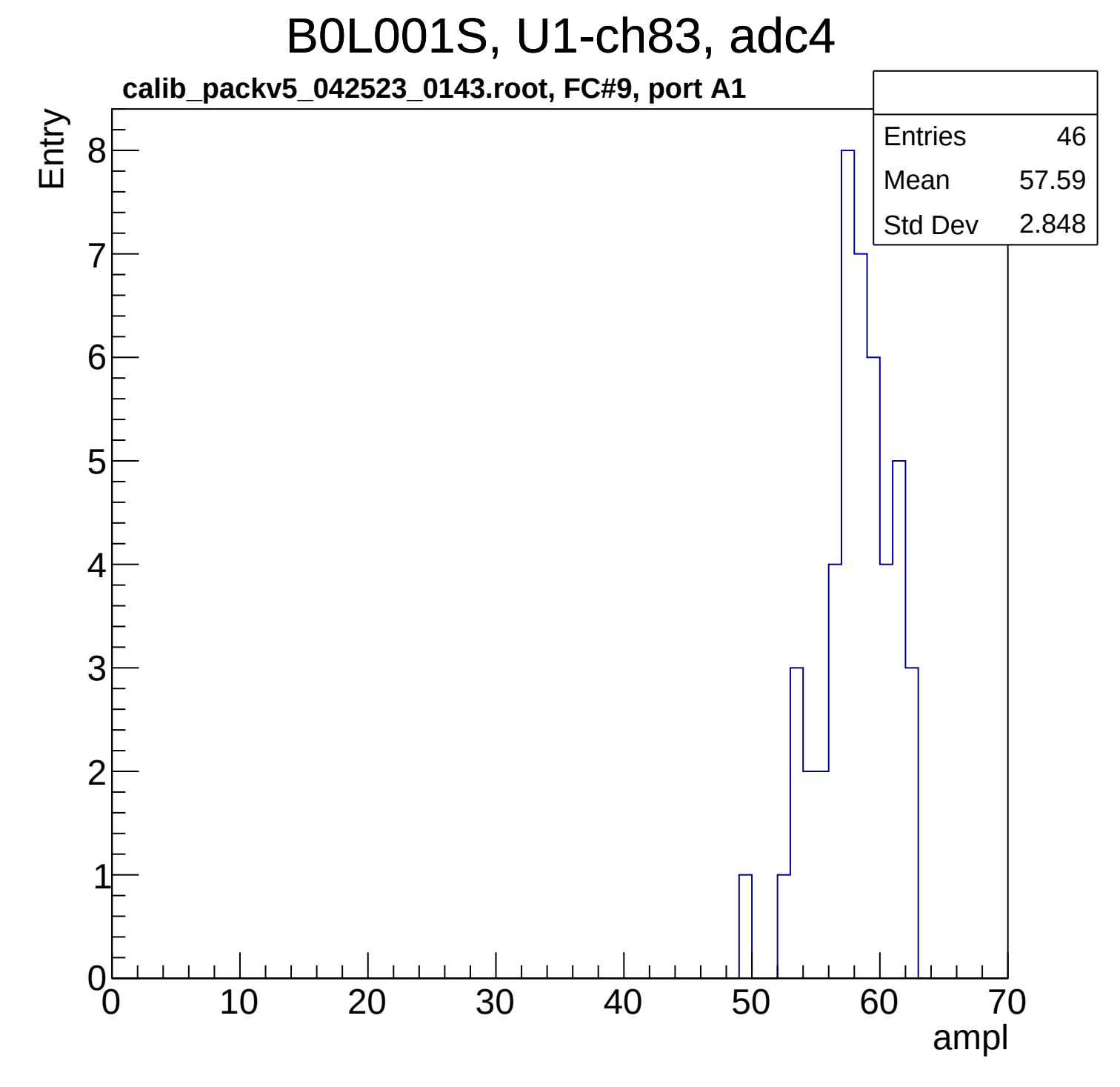
B0L001S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.59
Std Dev 2.848
Entry
ampl
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5
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0
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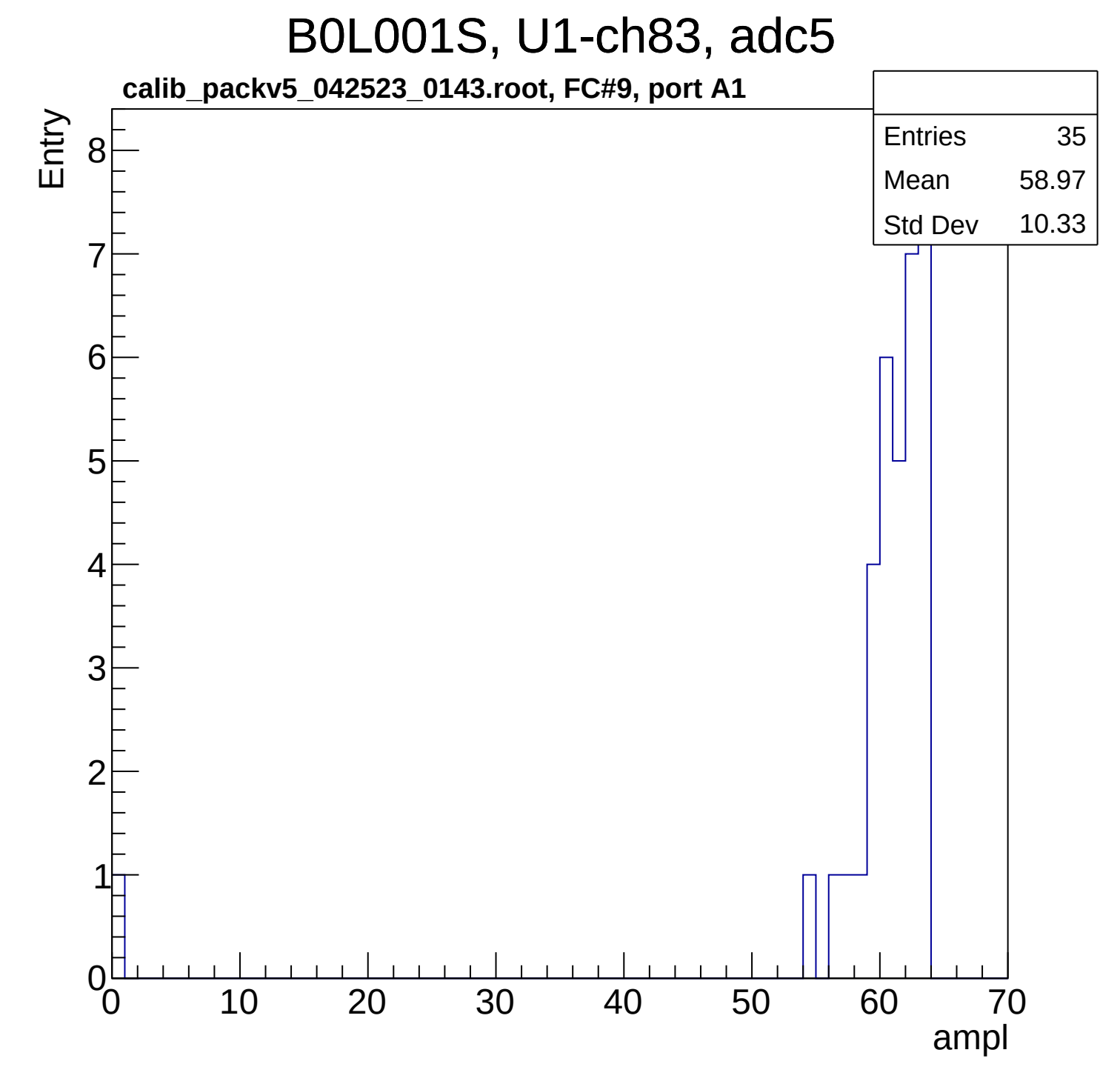
B0L001S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 58.97
Std Dev 10.33
Entry
ampl
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0
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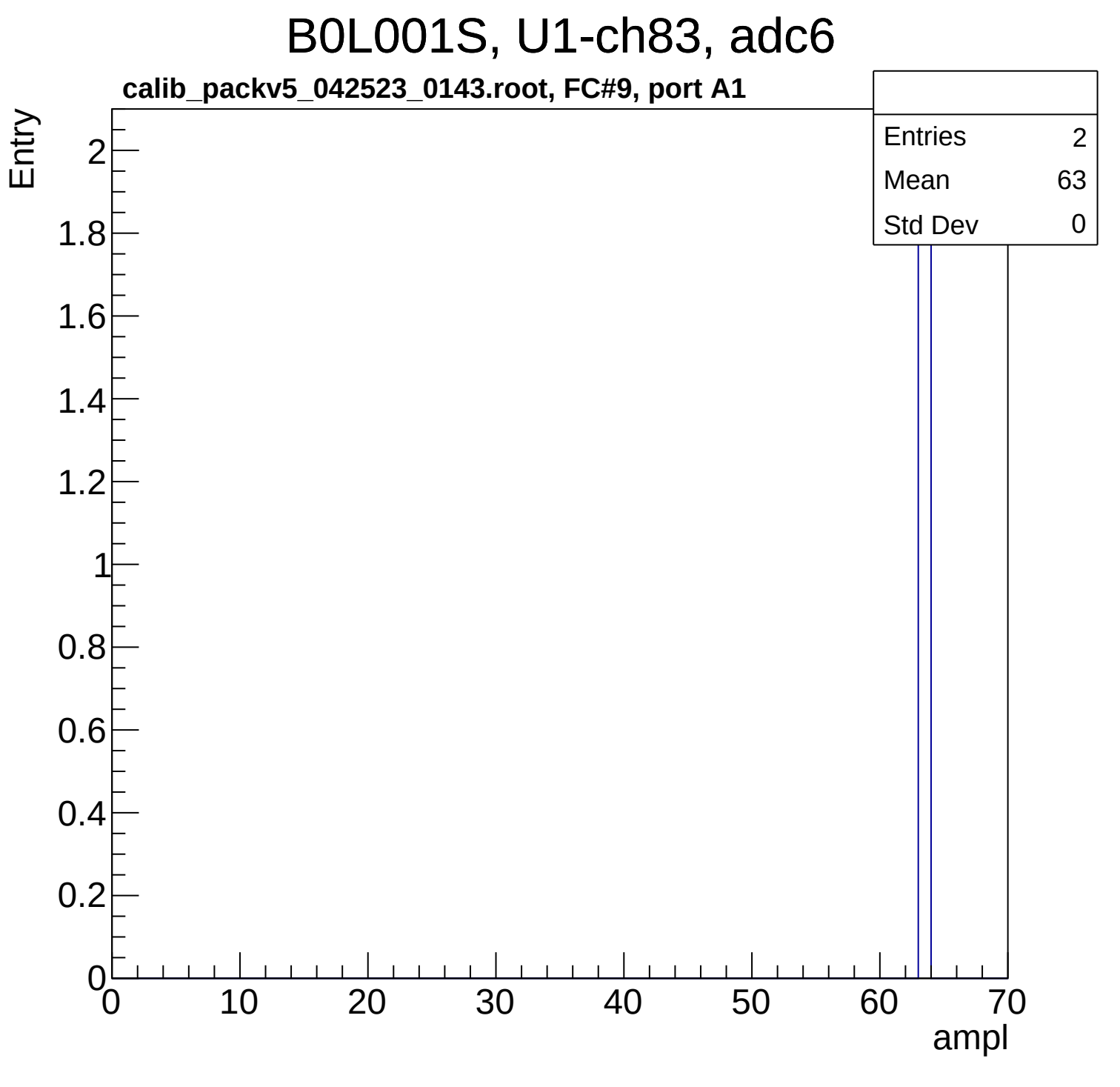
B0L001S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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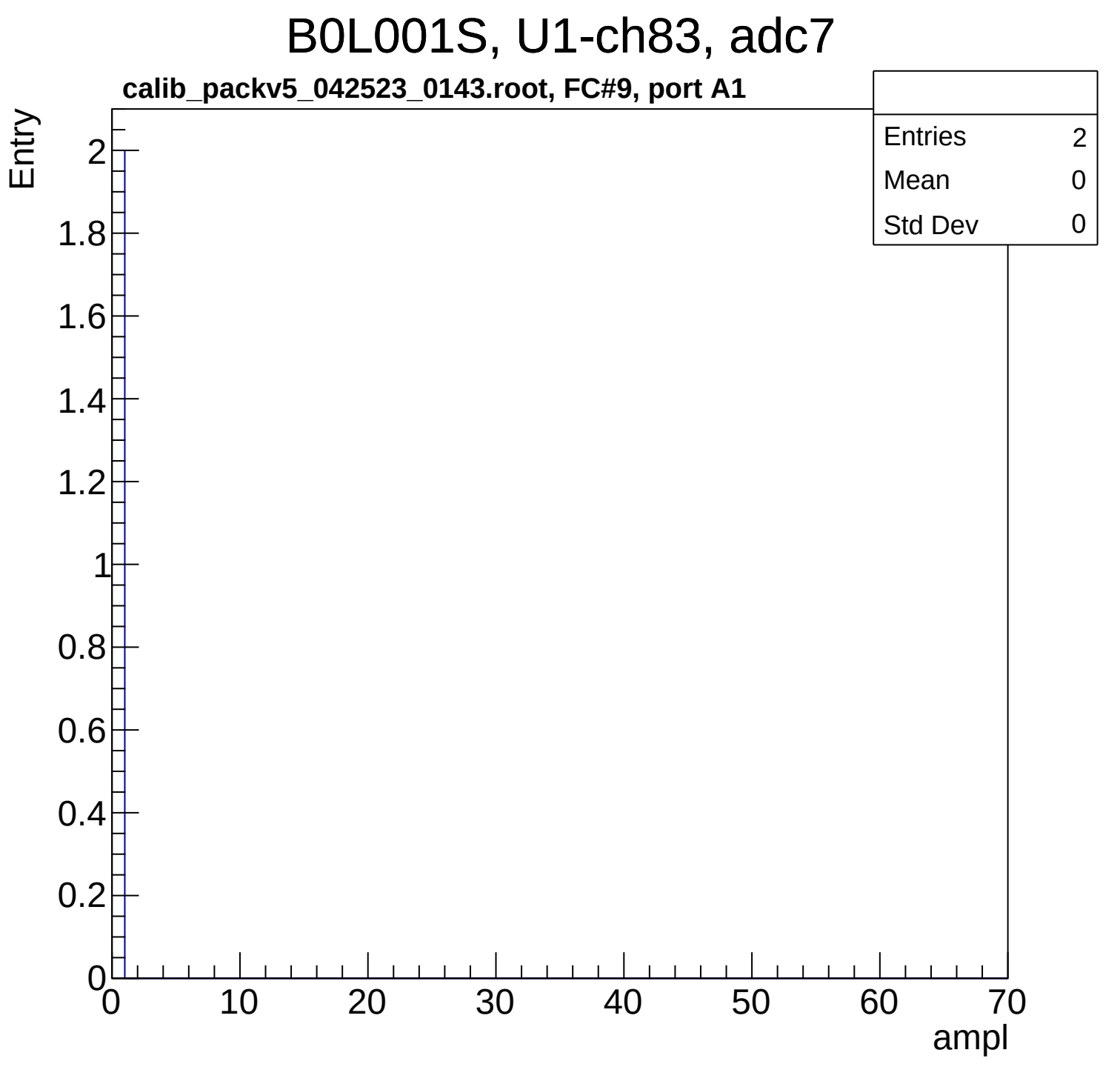
B0L001S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
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0.6
0.4
0.2
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10
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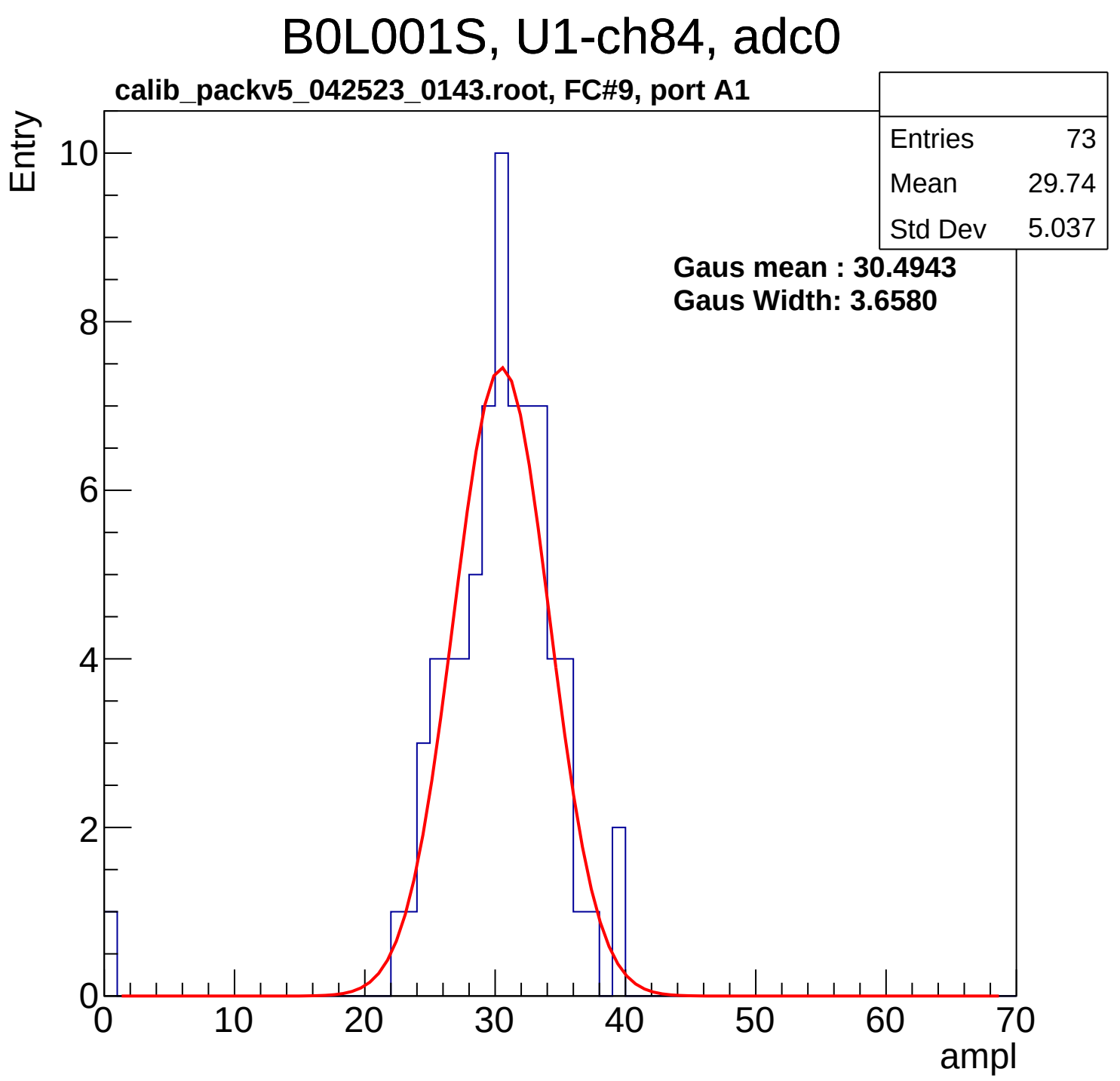
50
60
70
ampl


B0L001S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.74
Std Dev 5.037
Gaus mean : 30.4943
Gaus Width: 3.6580
Entry
ampl
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6
8
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0
10
20
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60
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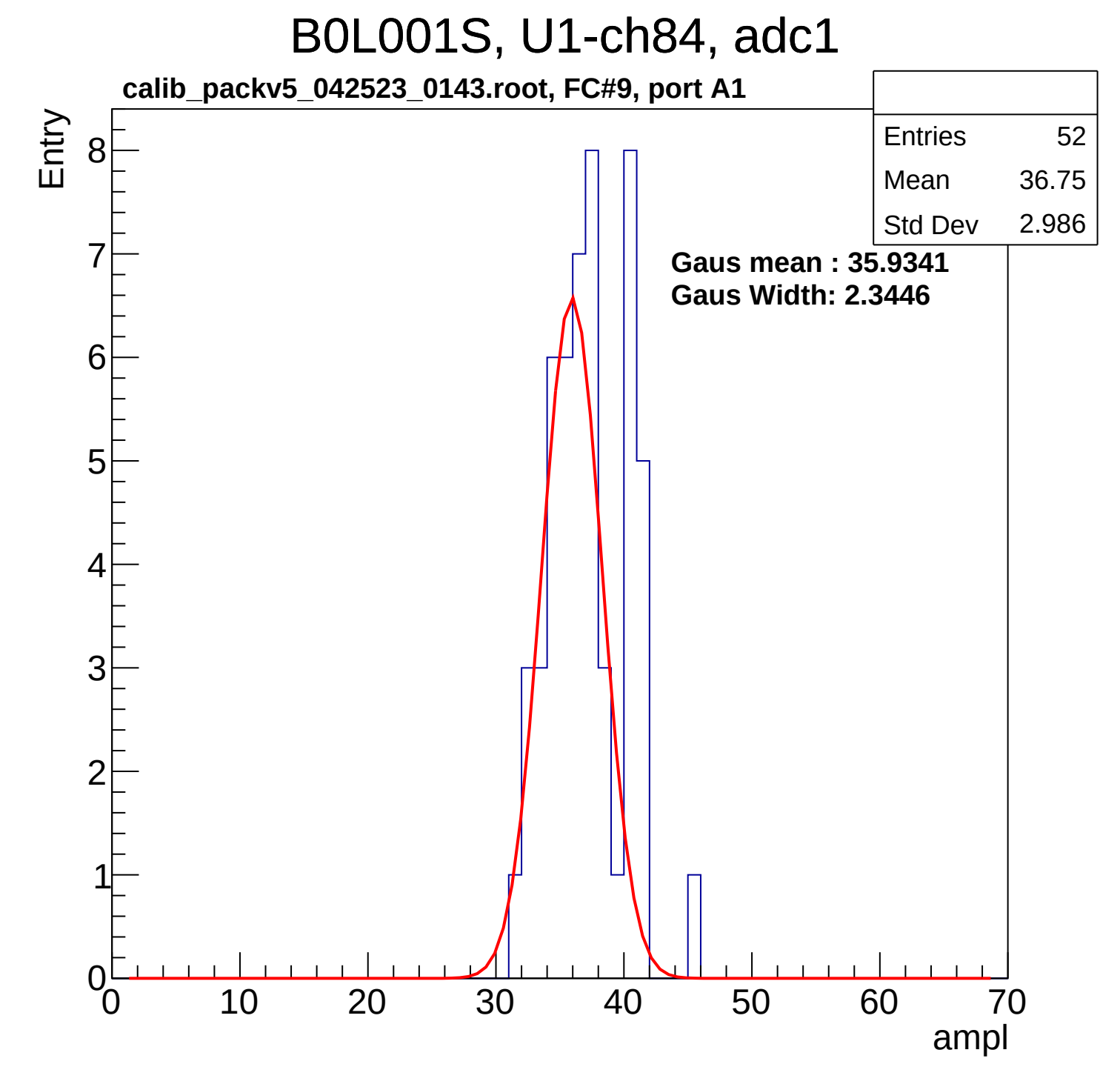
B0L001S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 36.75
Std Dev 2.986
Gaus mean : 35.9341
Gaus Width: 2.3446
Entry
ampl
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1
2
3
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5
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8
0
10
20
30
40
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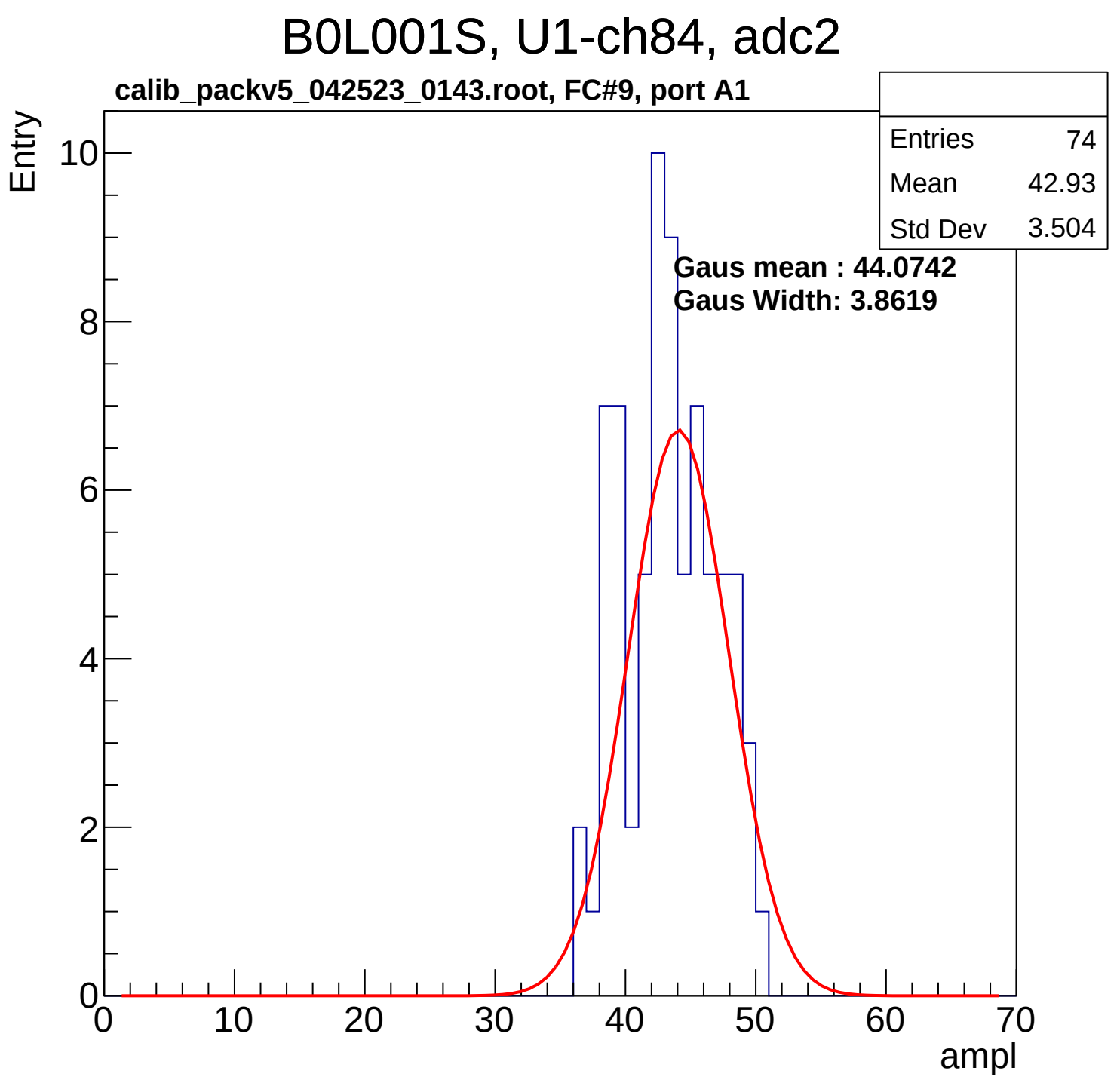
B0L001S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 42.93
Std Dev 3.504
Gaus mean : 44.0742
Gaus Width: 3.8619
Entry
ampl
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2
4
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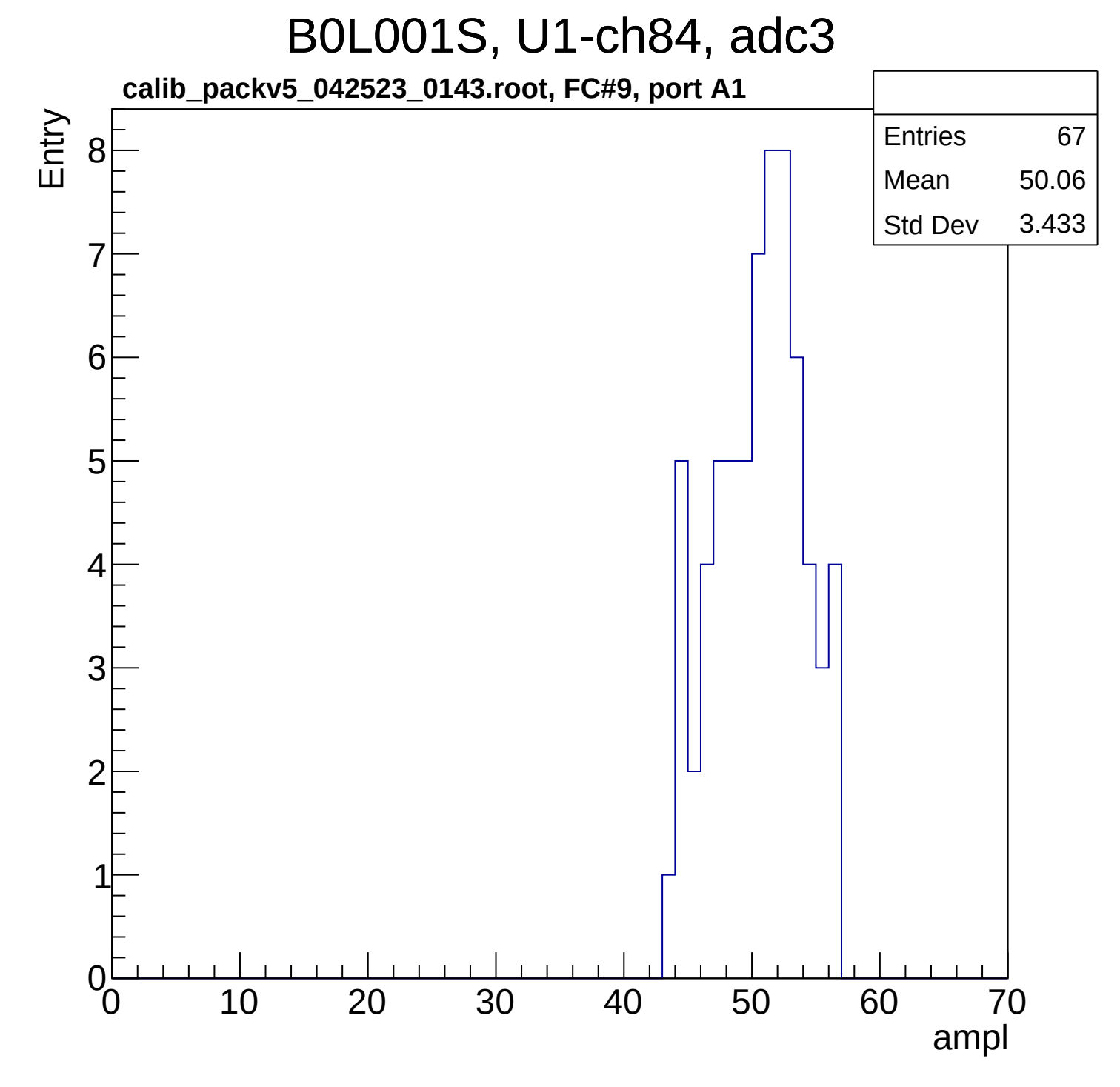
B0L001S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 50.06
Std Dev 3.433
Entry
ampl
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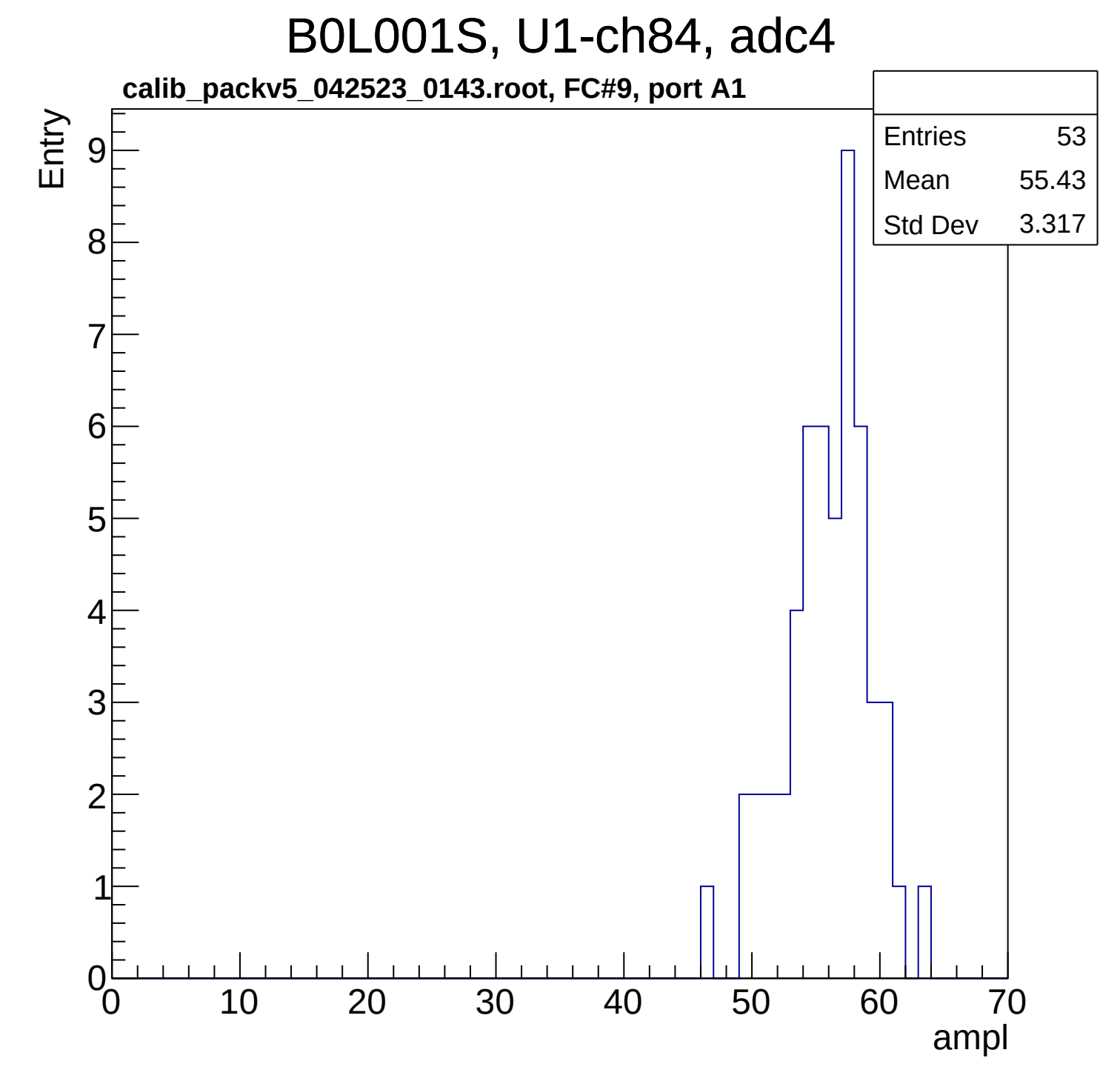
B0L001S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.43
Std Dev 3.317
Entry
ampl
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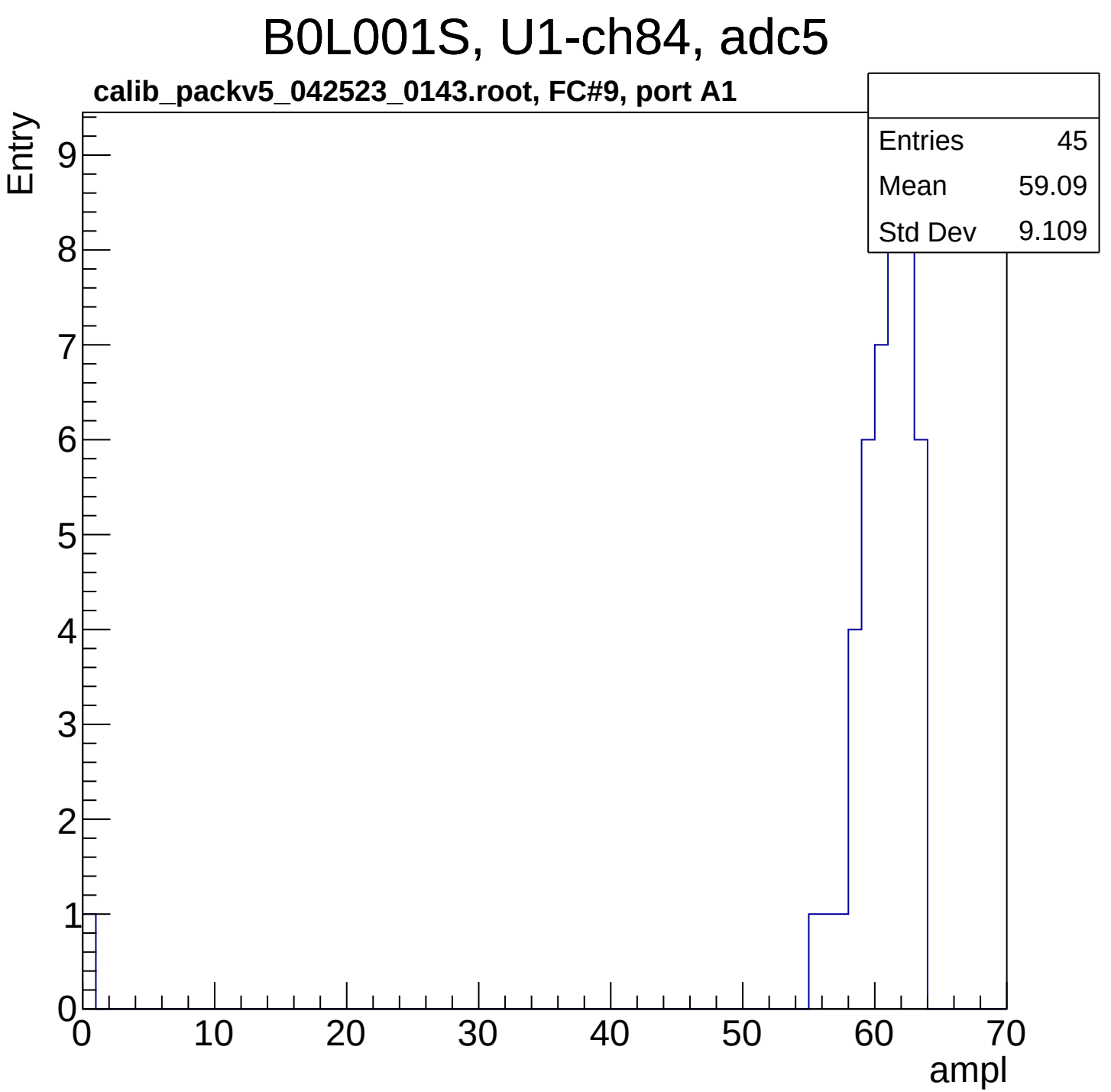
B0L001S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 59.09
Std Dev 9.109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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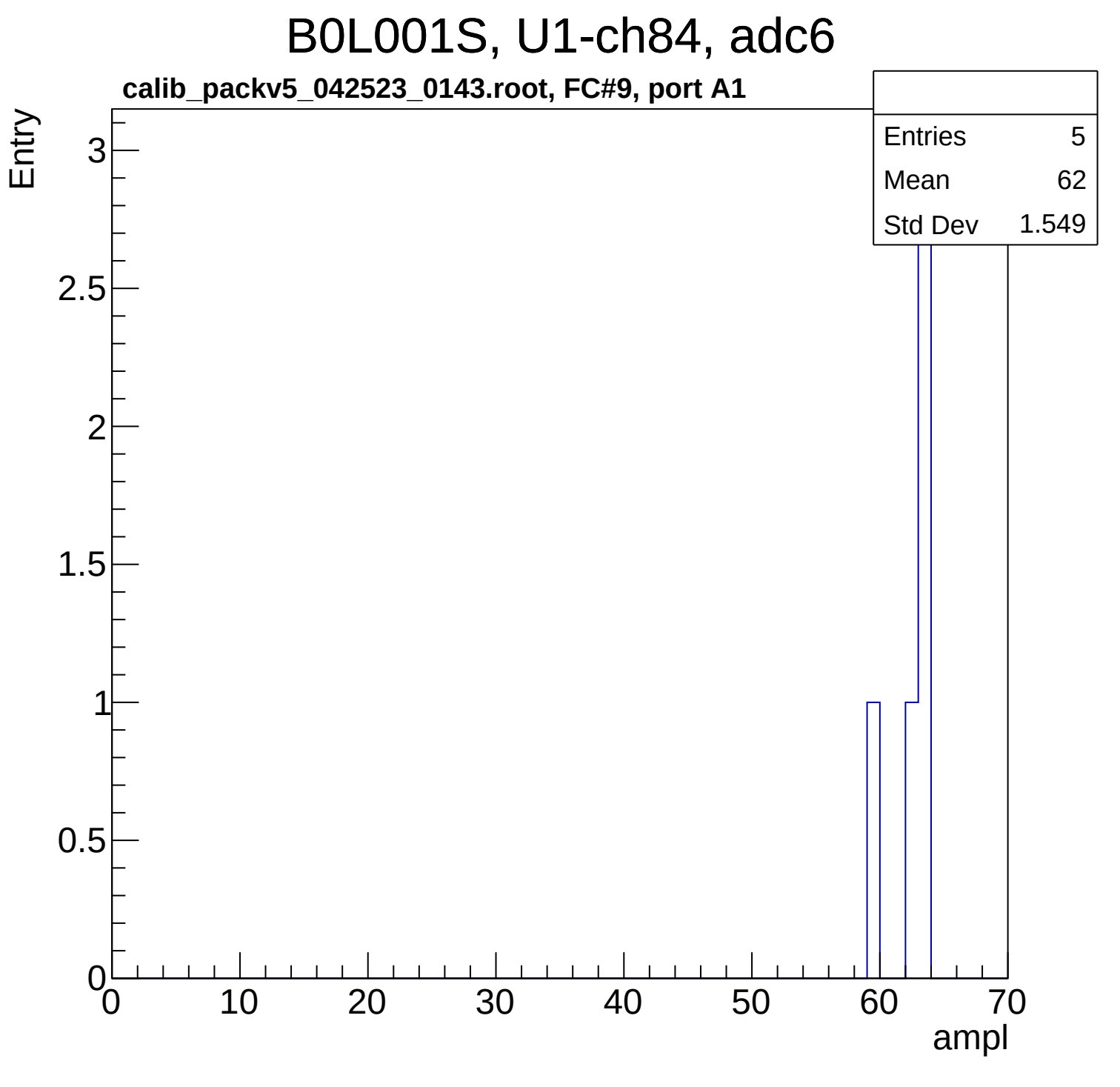
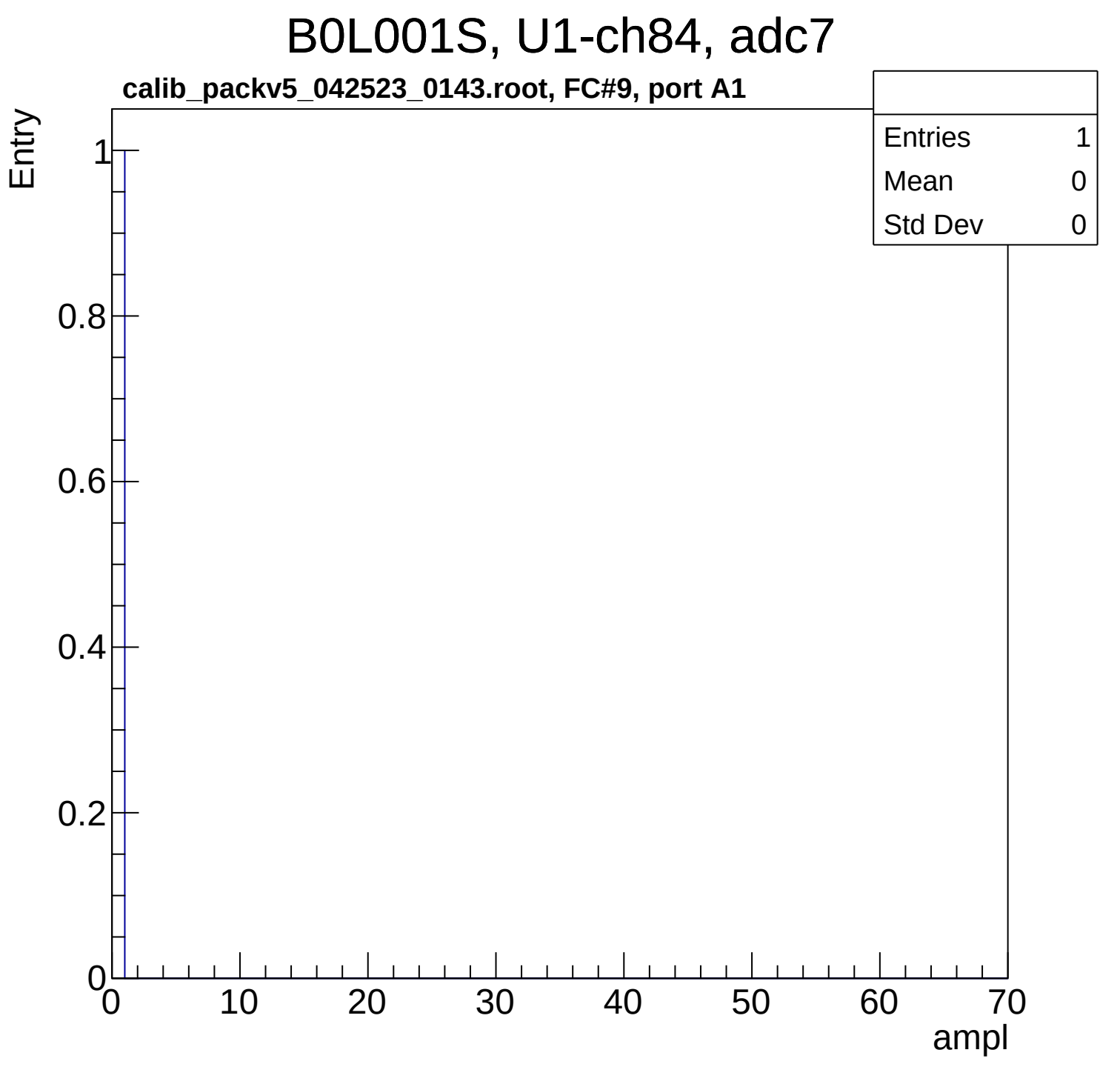
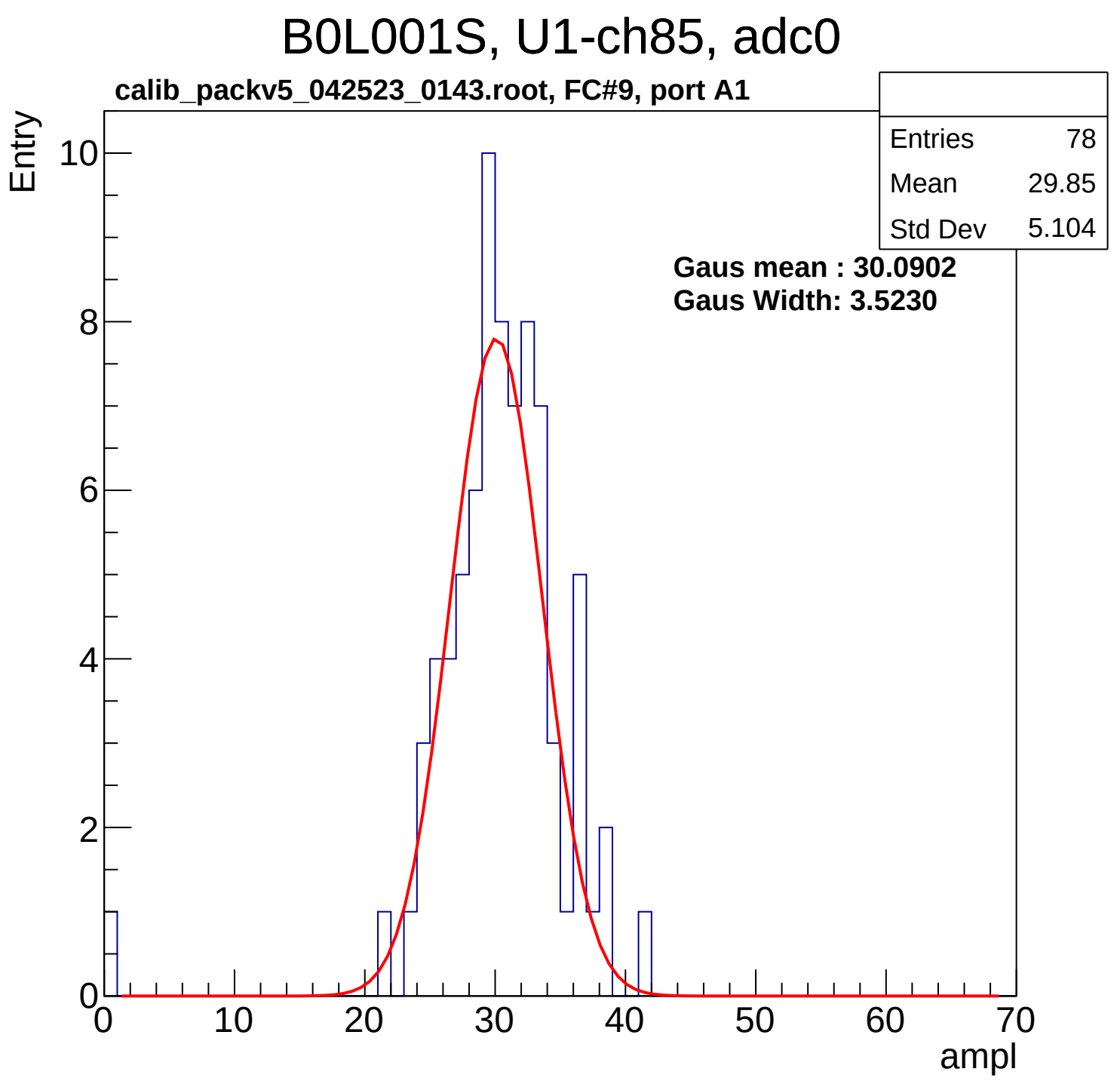
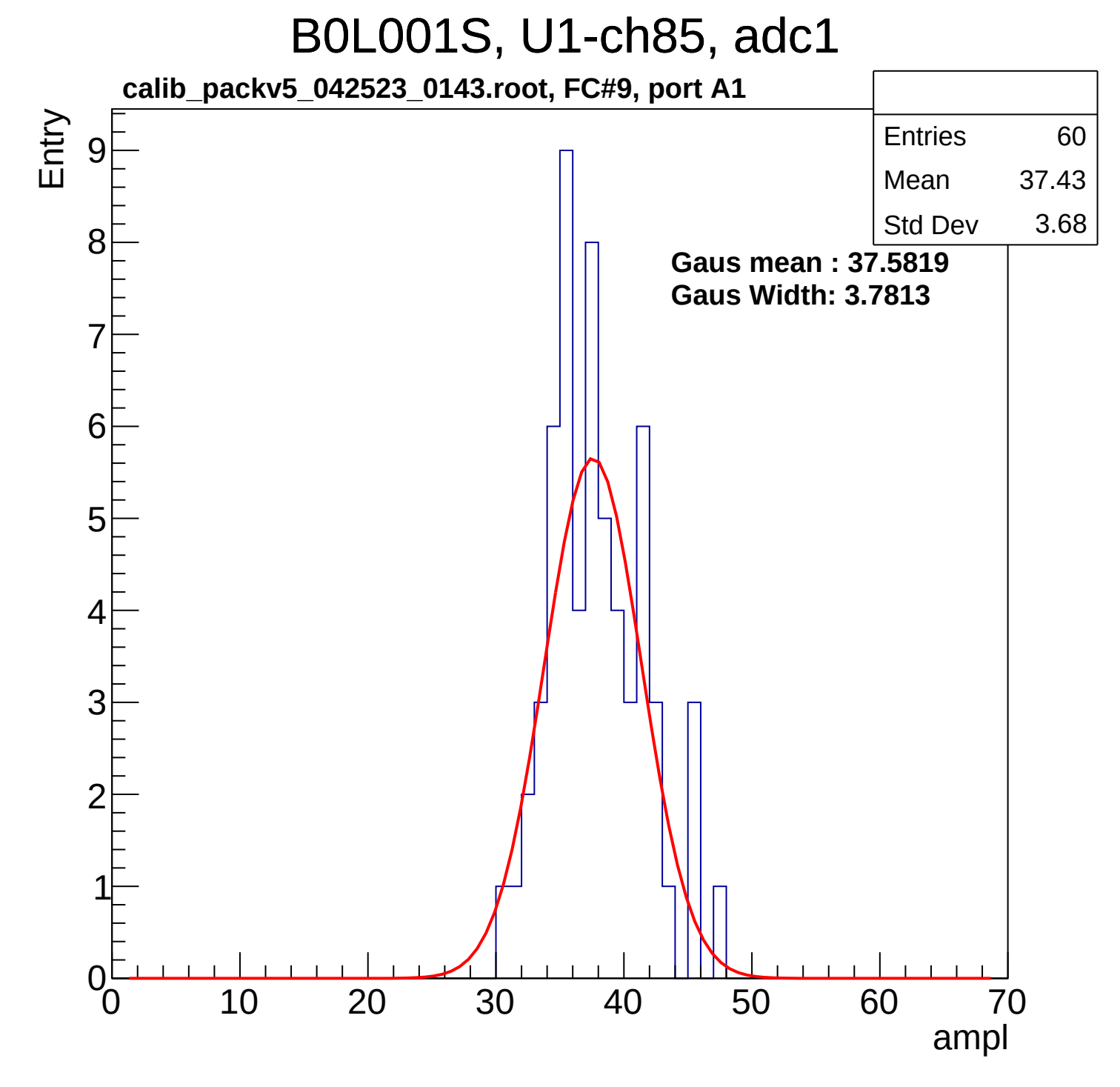
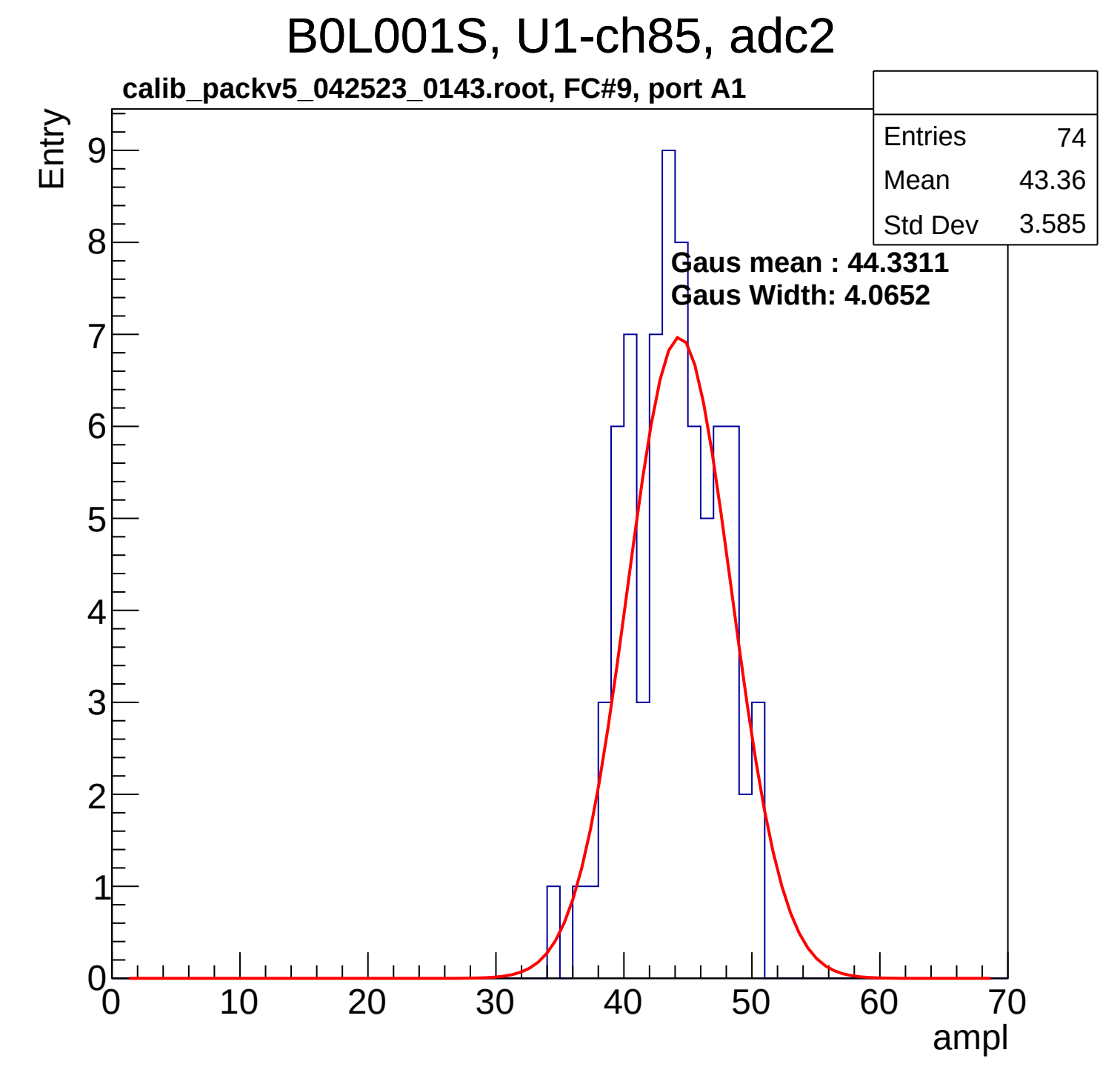
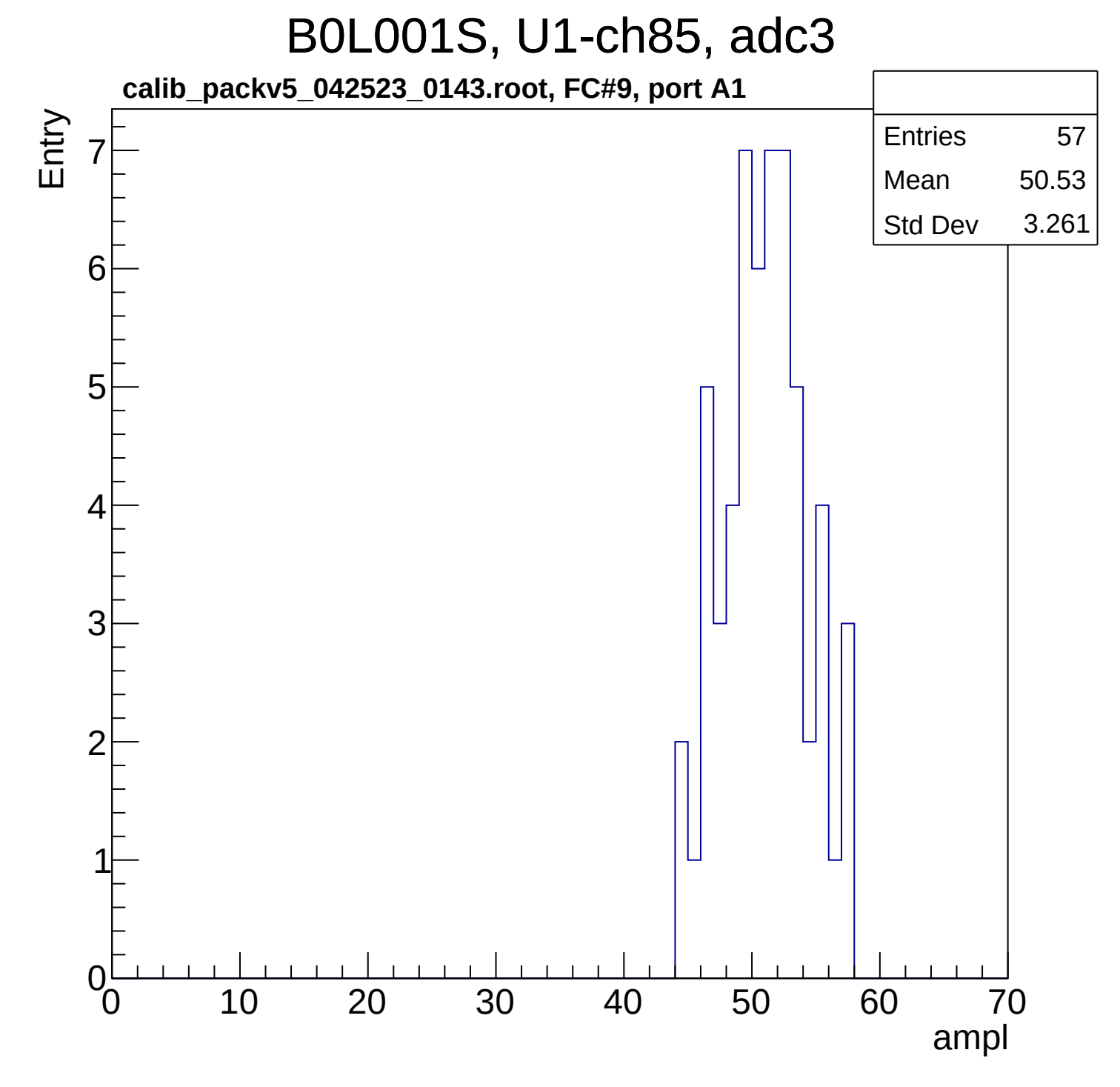

B0L001S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62
Std Dev 1.549
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

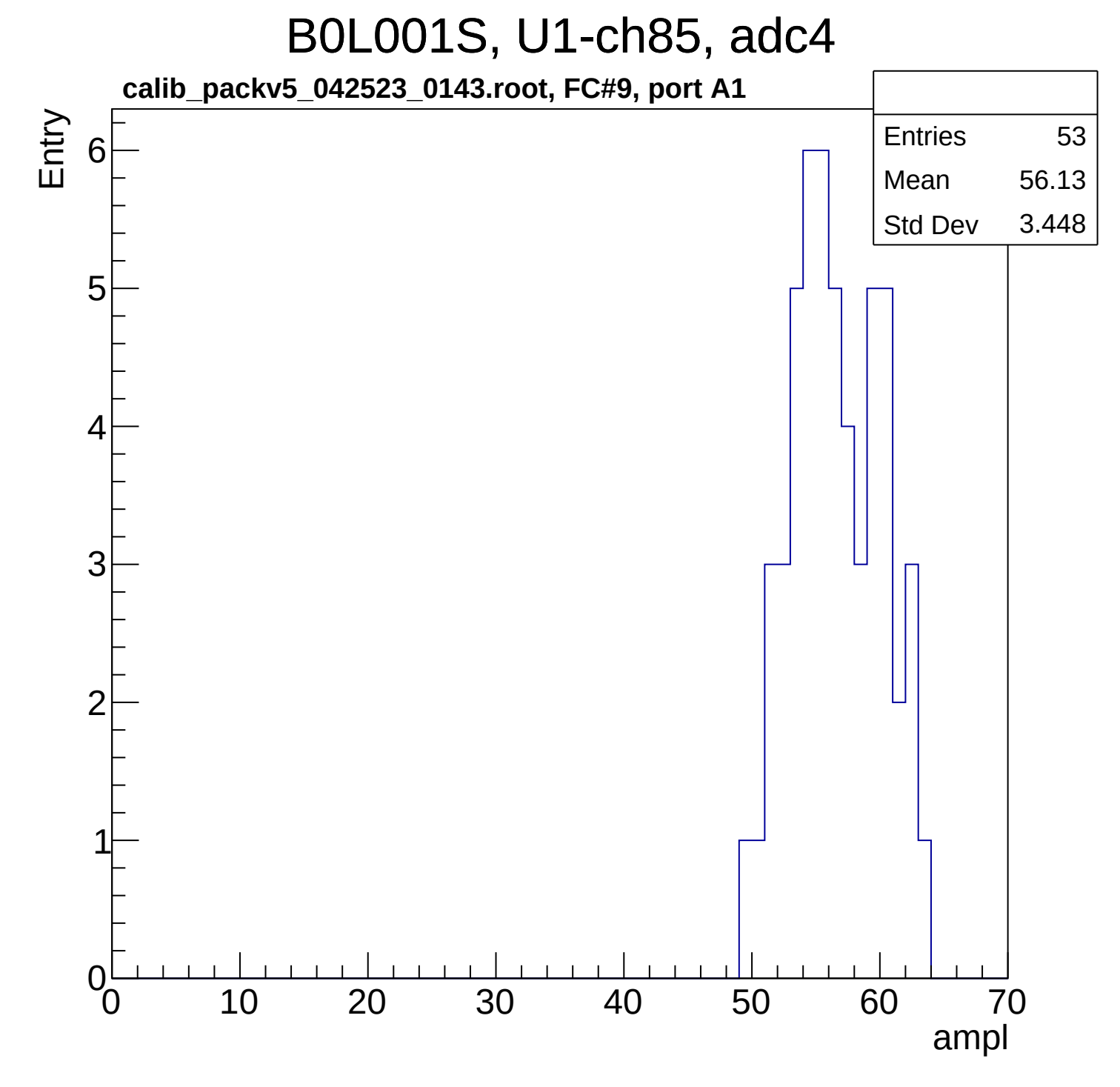

B0L001S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

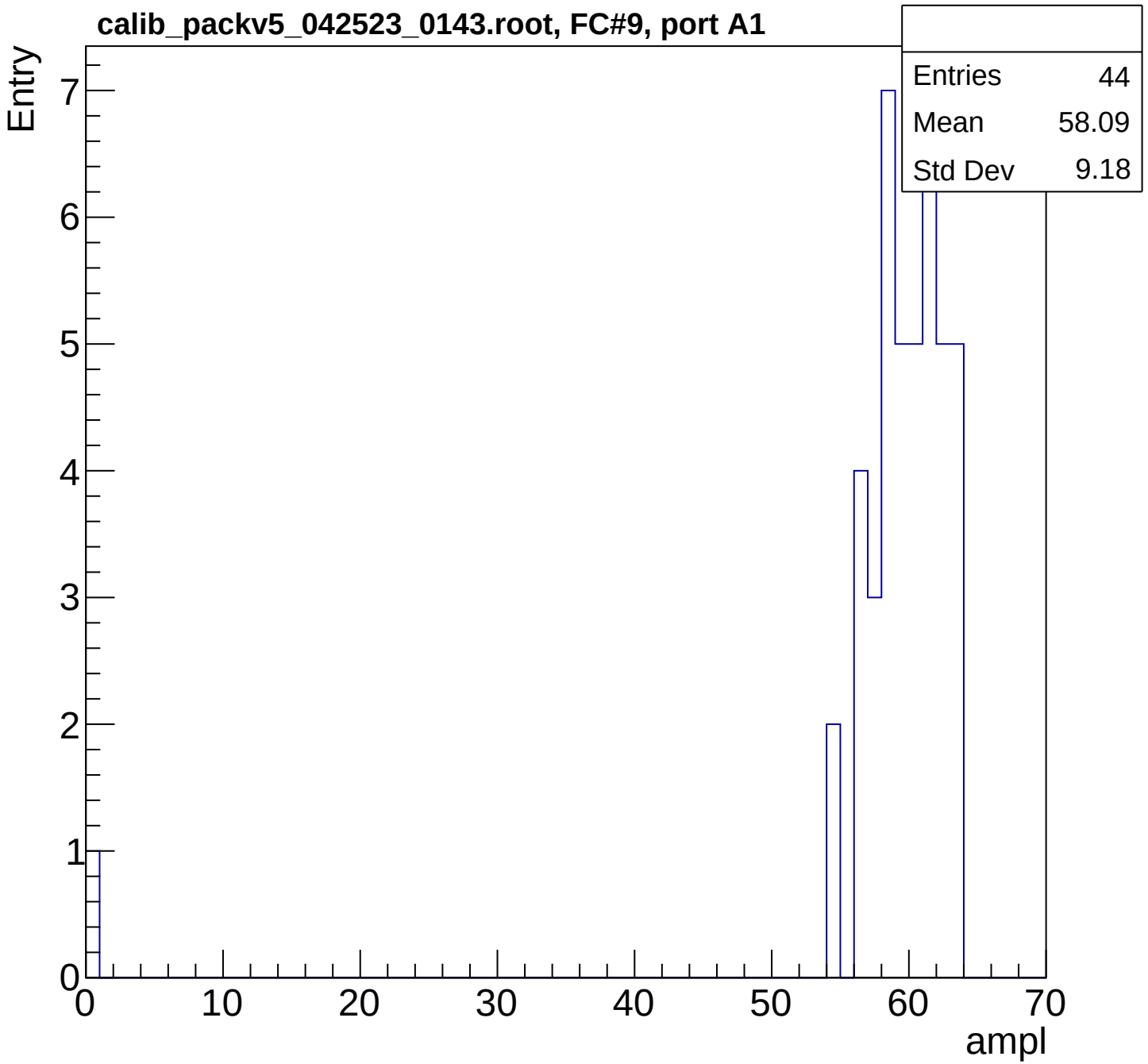

B0L001S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 29.85
Std Dev 5.104
Gaus mean : 30.0902
Gaus Width: 3.5230
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

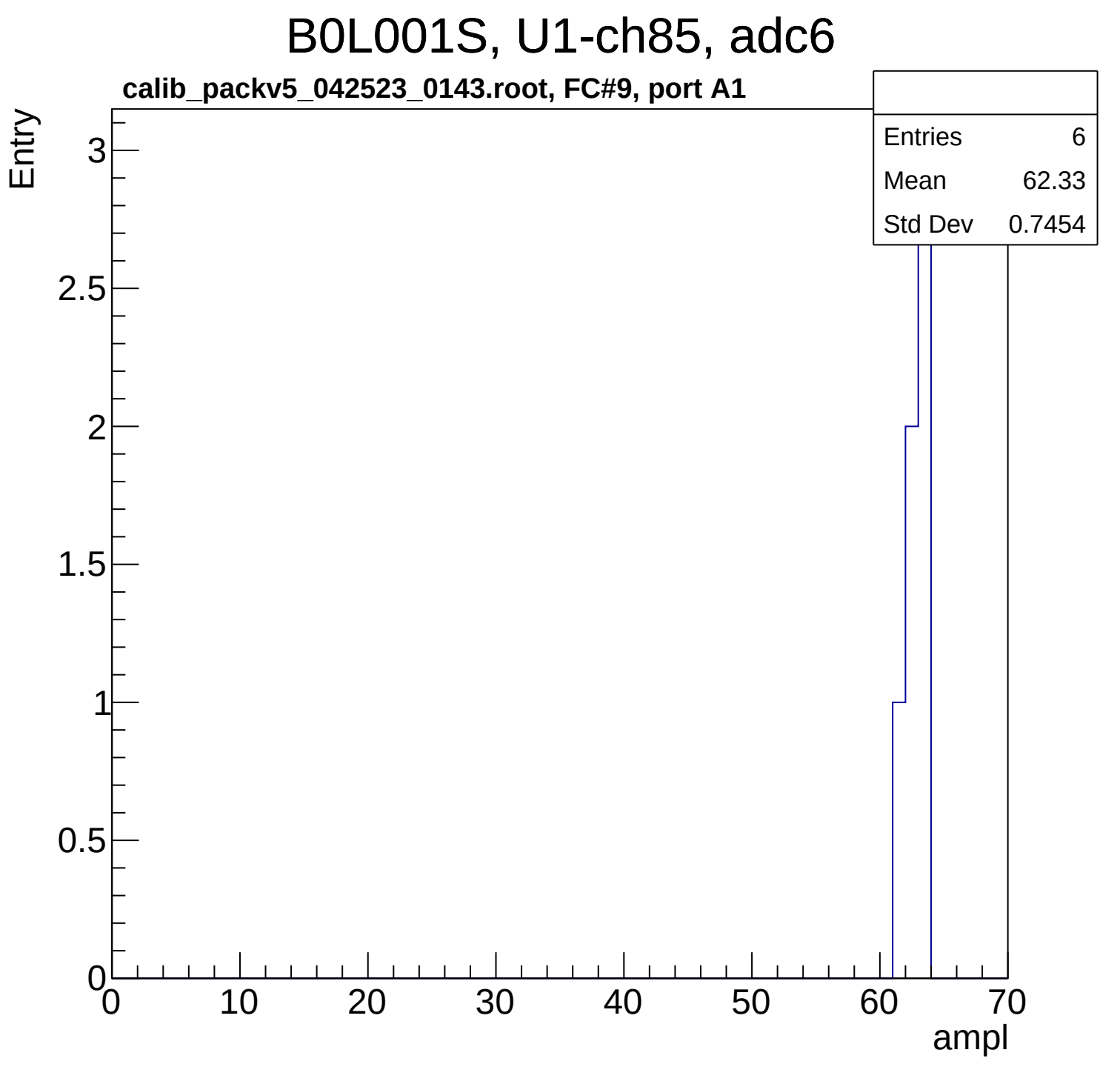

B0L001S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 37.43
Std Dev 3.68
Gaus mean : 37.5819
Gaus Width: 3.7813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

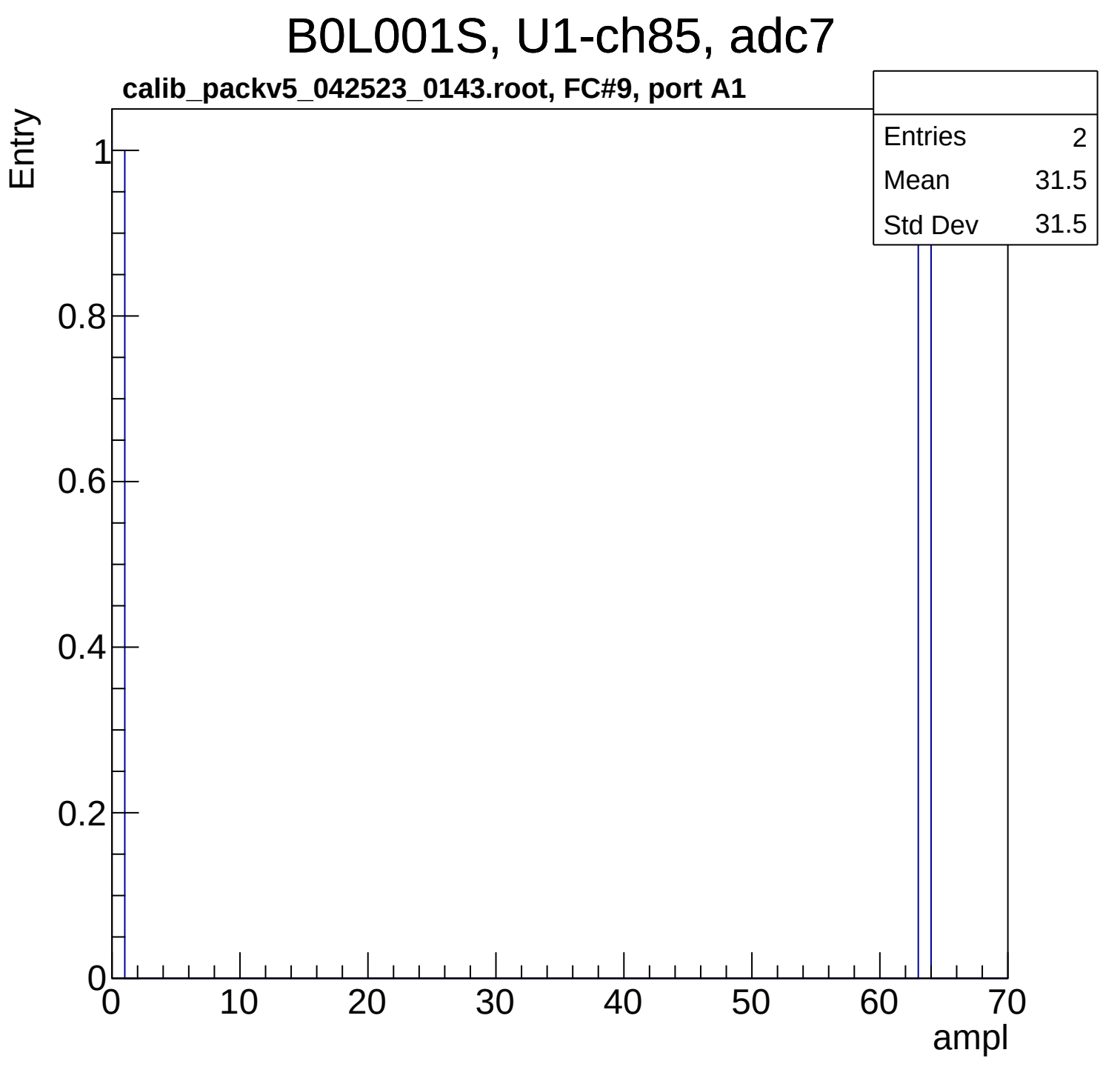

B0L001S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 43.36
Std Dev 3.585
Gaus mean : 44.3311
Gaus Width: 4.0652
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

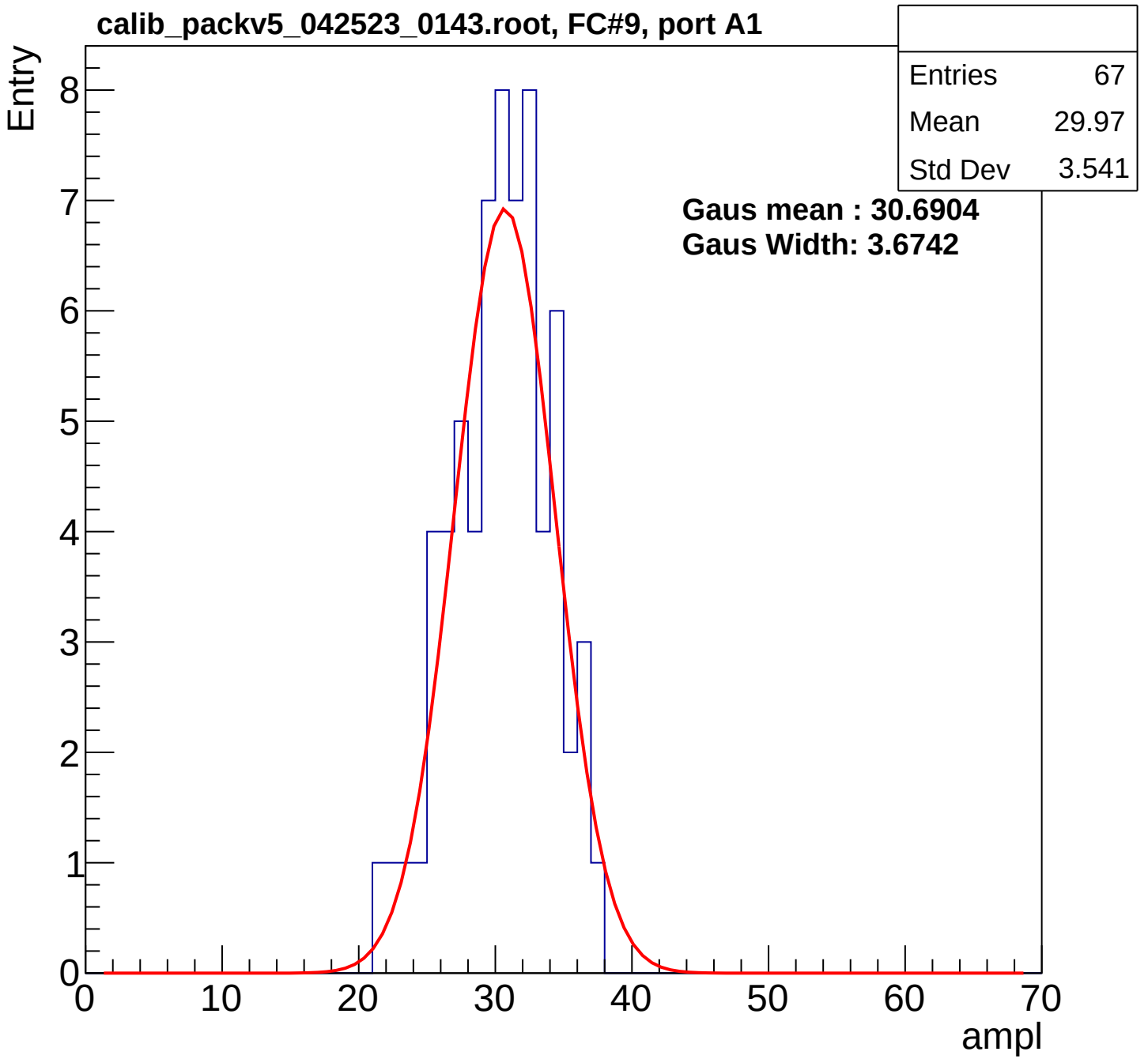

B0L001S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 50.53
Std Dev 3.261
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

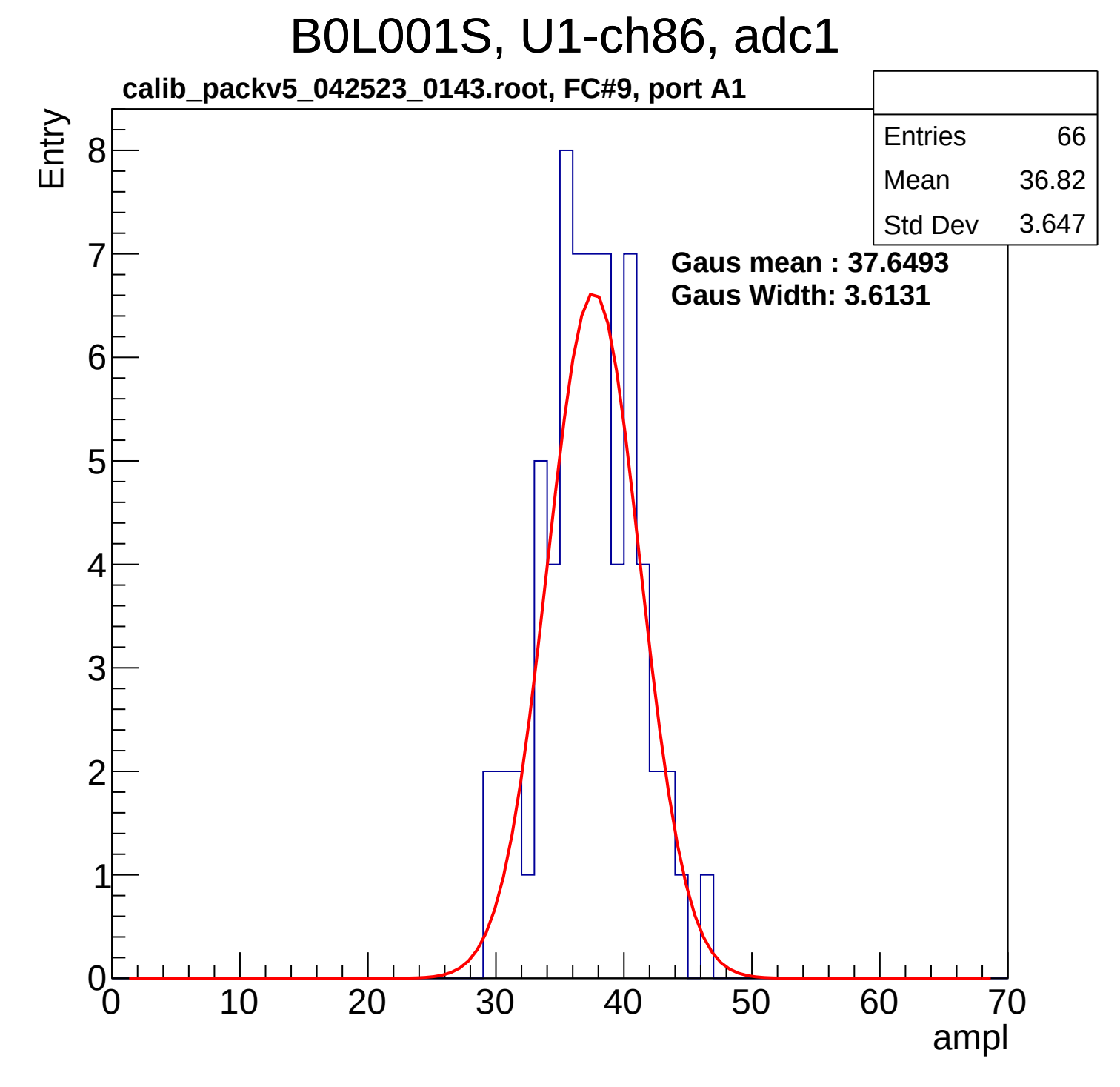

B0L001S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.13
Std Dev 3.448
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

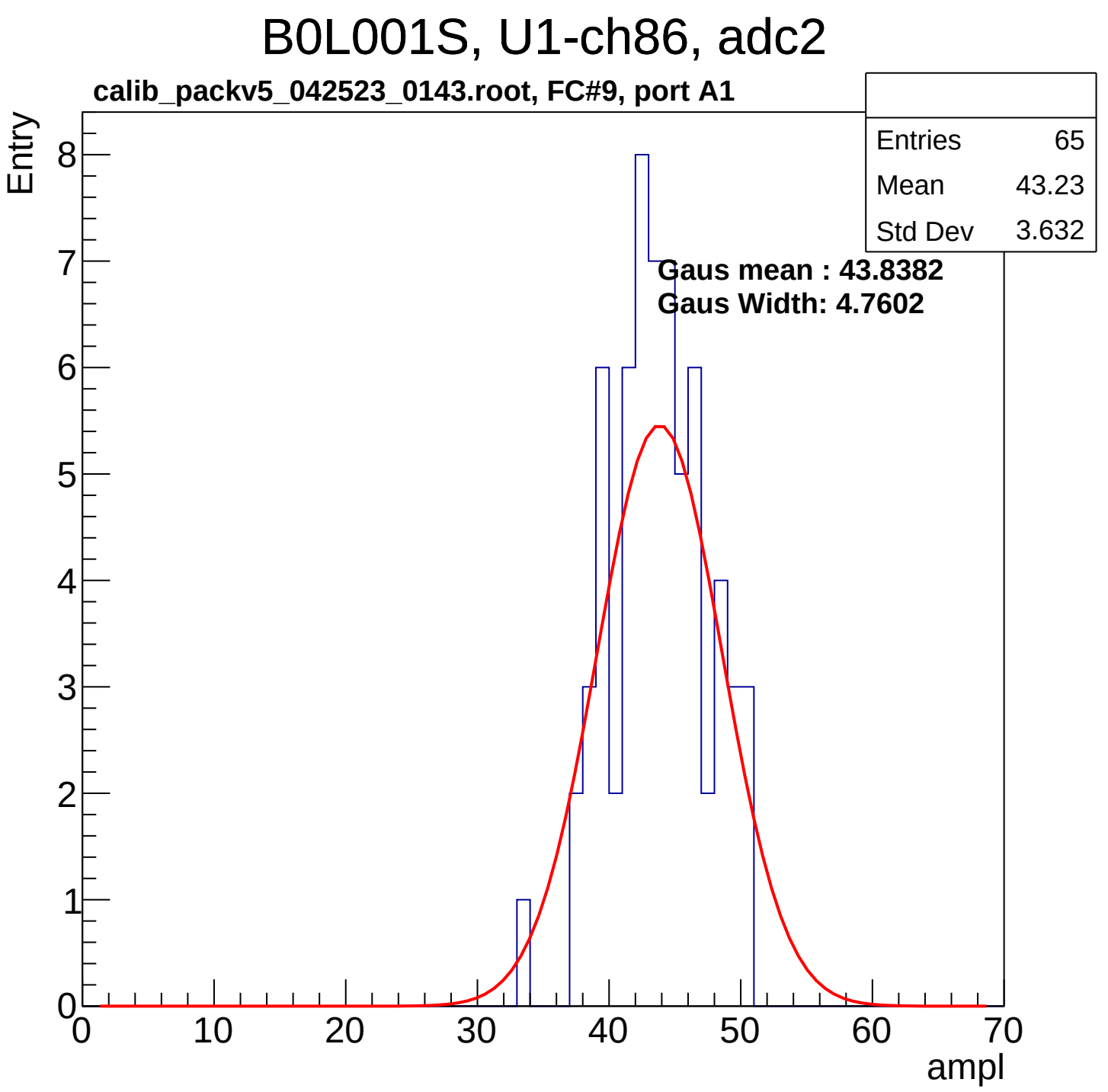

B0L001S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 58.09
Std Dev 9.18
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 29.97
Std Dev 3.541
Gaus mean : 30.6904
Gaus Width: 3.6742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.82
Std Dev 3.647
Gaus mean : 37.6493
Gaus Width: 3.6131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 43.23
Std Dev 3.632
Gaus mean : 43.8382
Gaus Width: 4.7602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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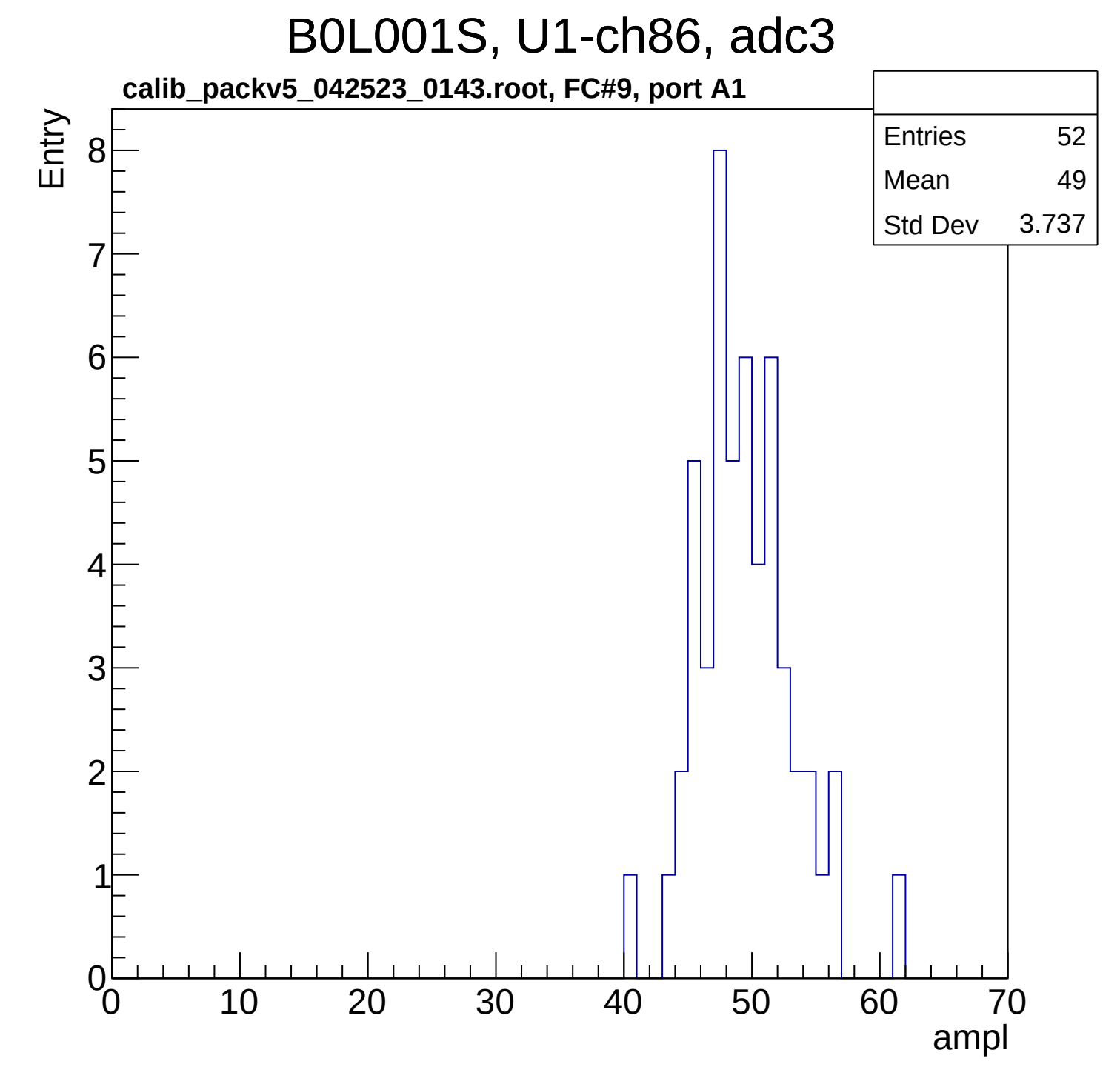
B0L001S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 49
Std Dev 3.737
Entry
ampl
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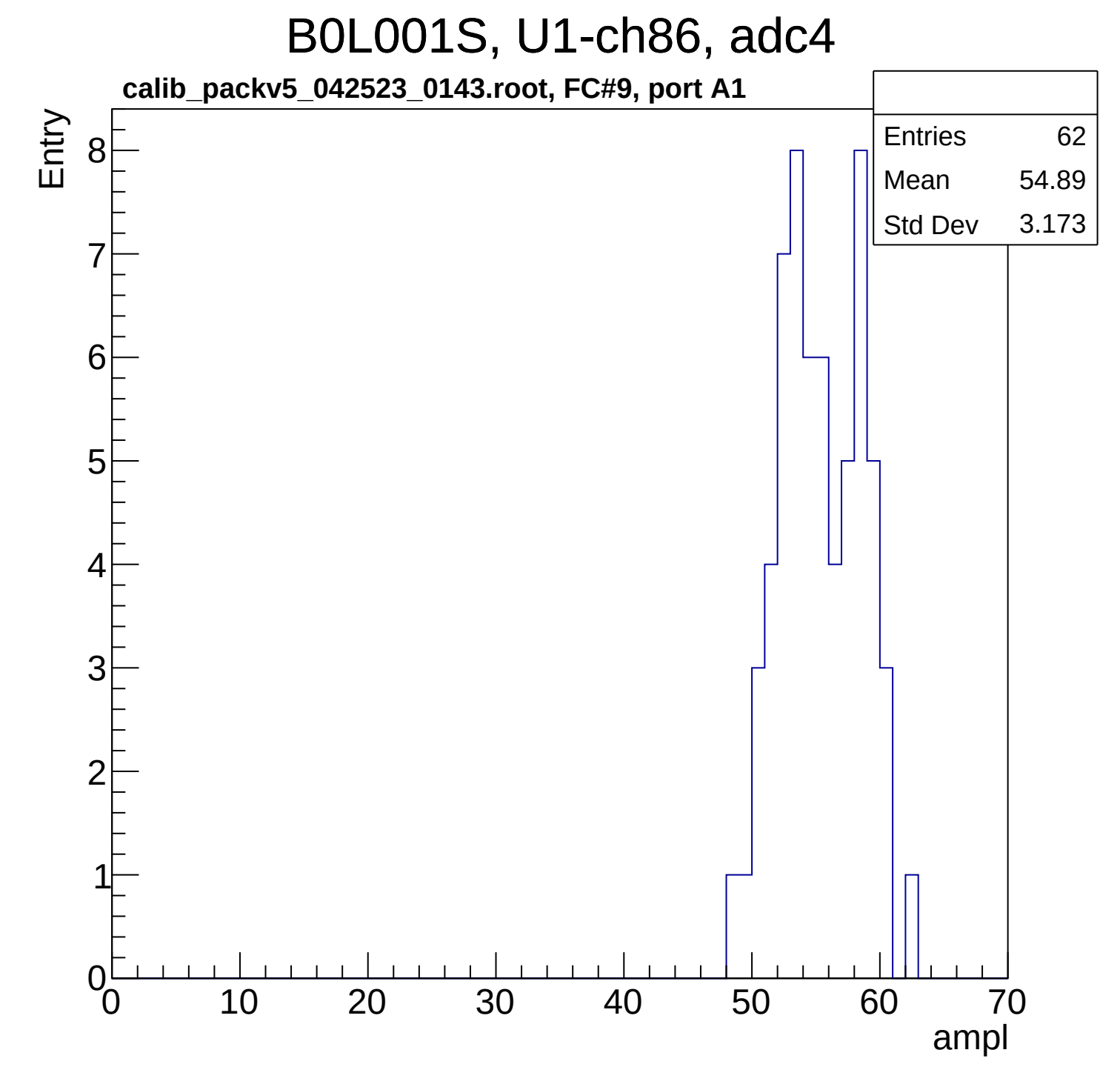
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 54.89
Std Dev 3.173
Entry
ampl
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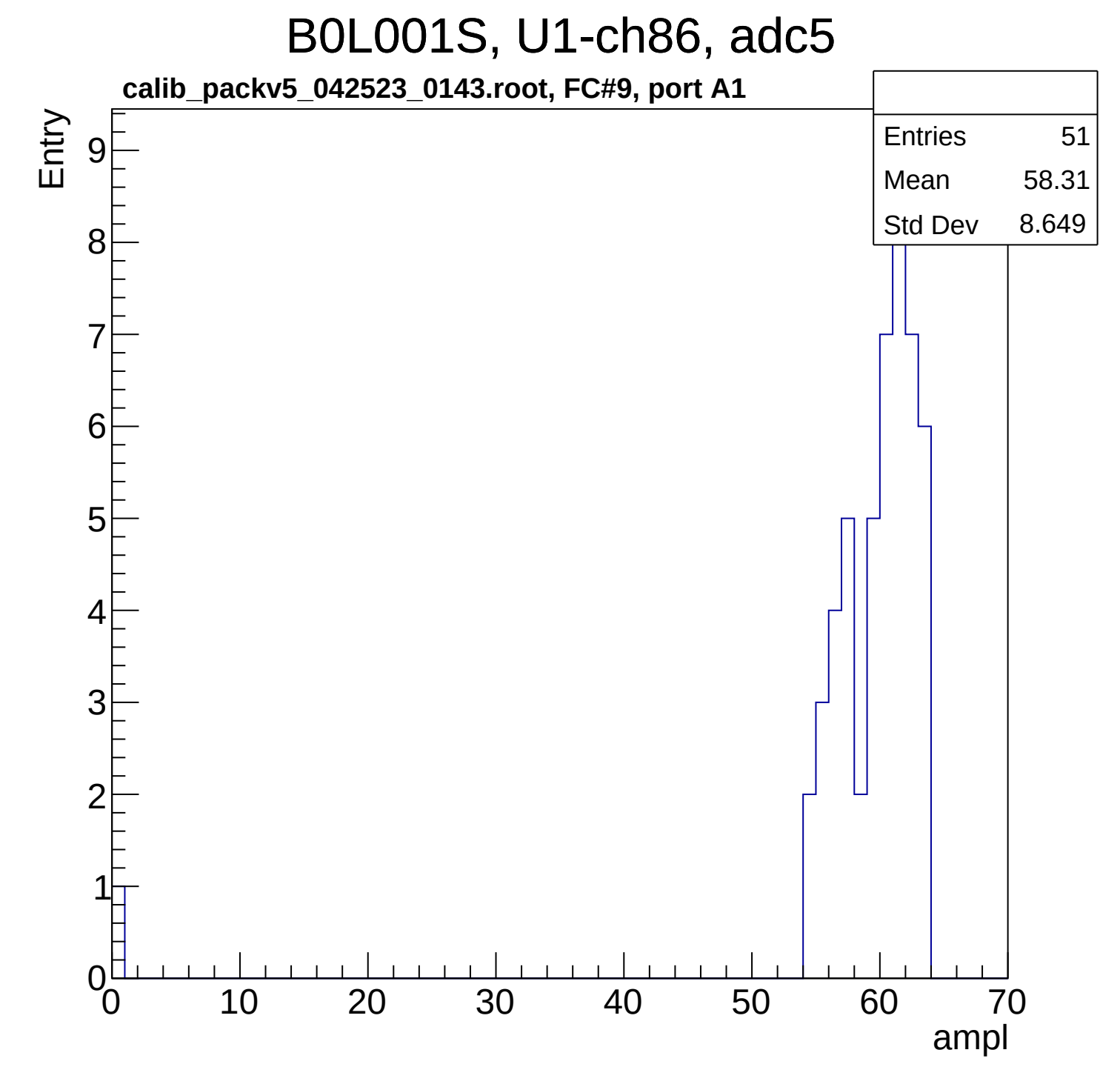
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 58.31
Std Dev 8.649
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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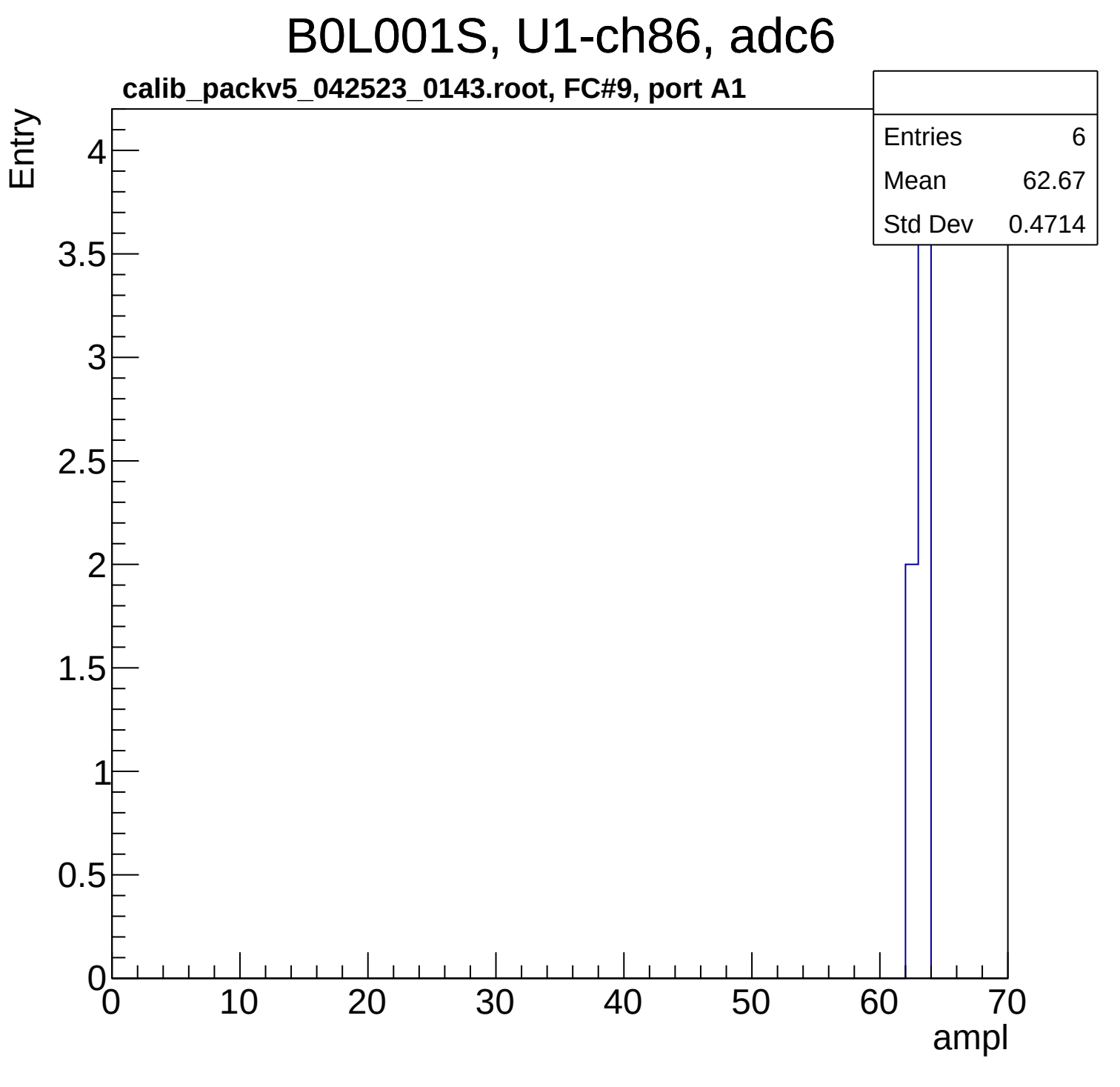
60
70


B0L001S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
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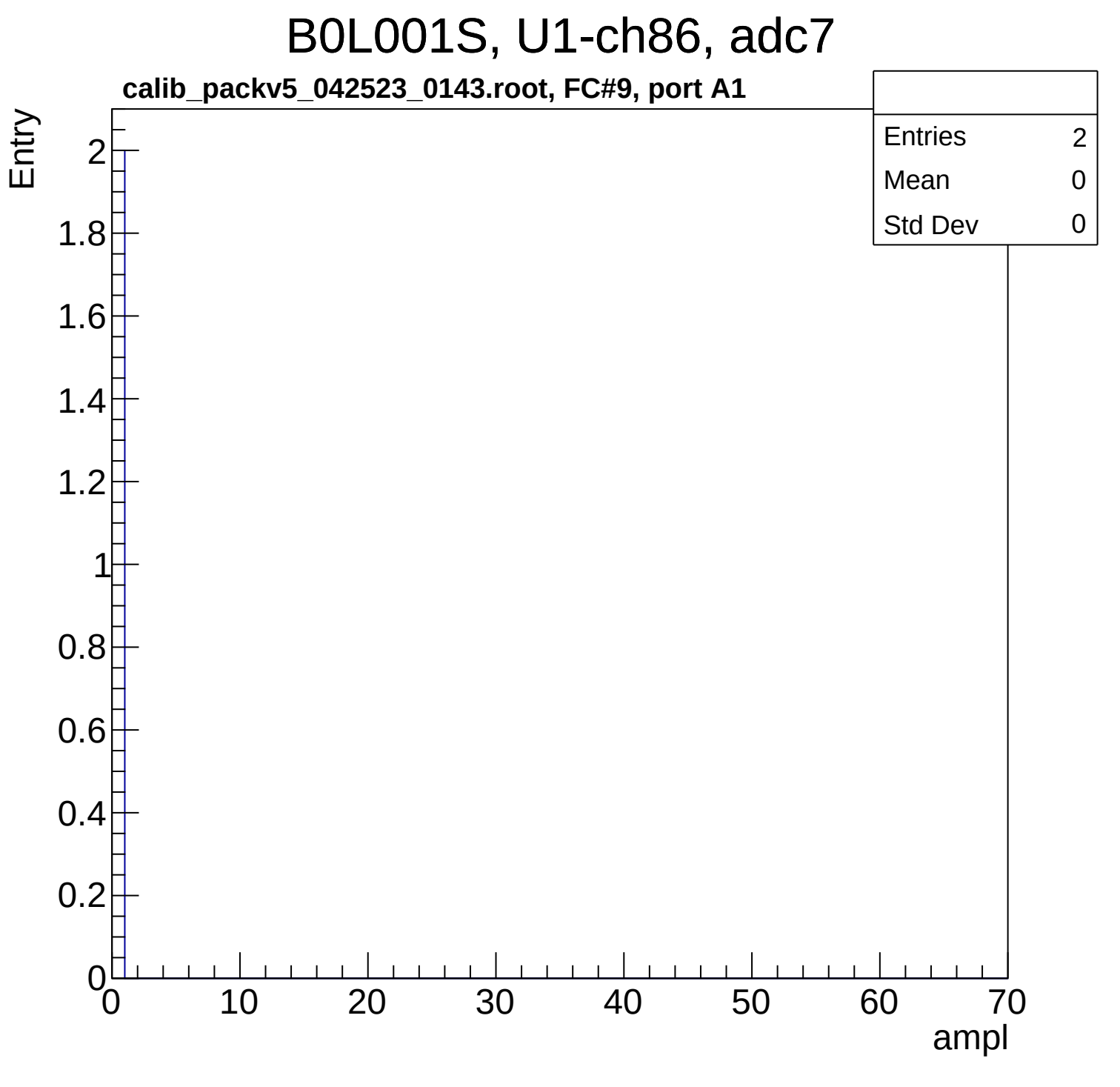
60
70


B0L001S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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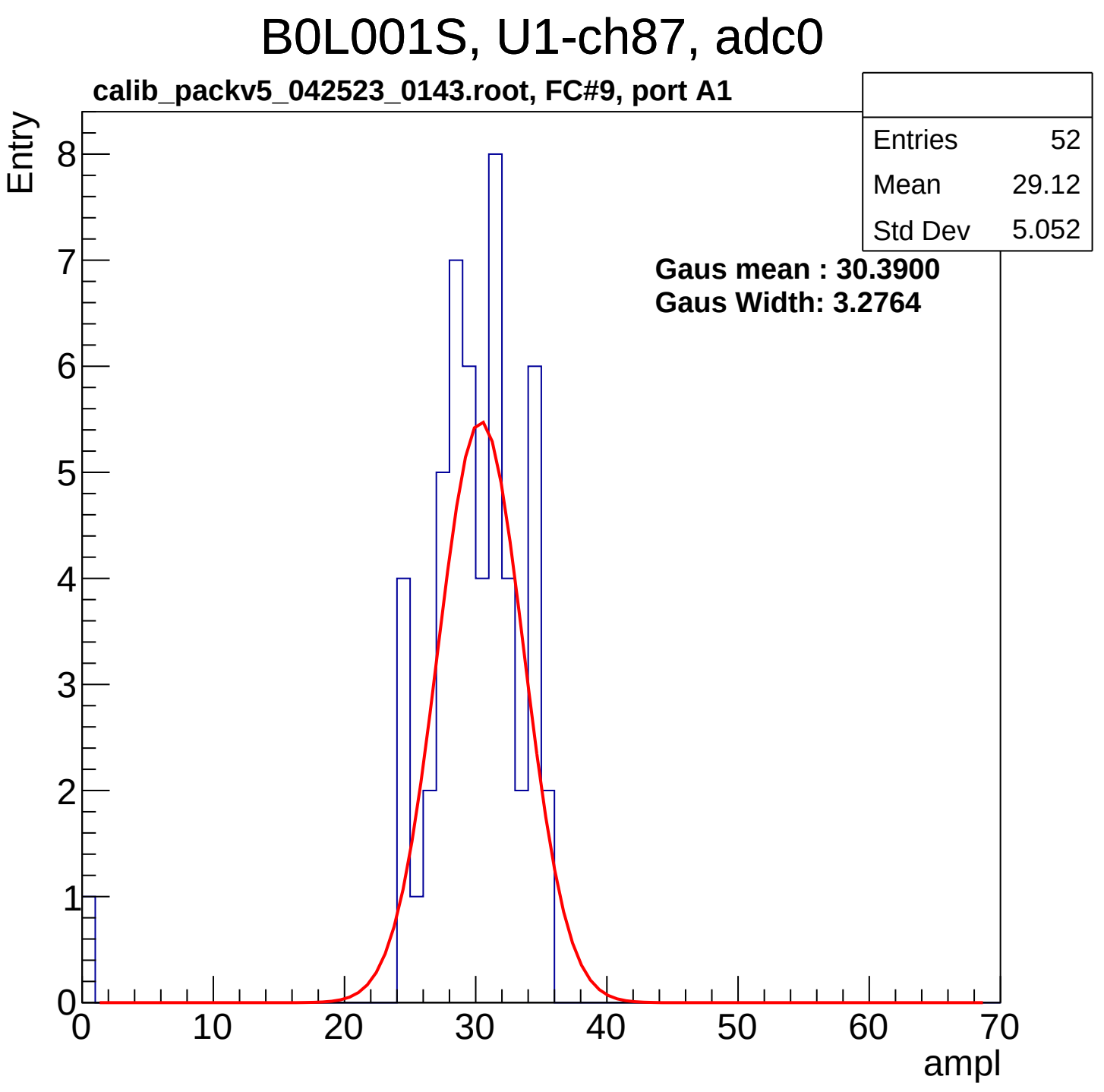
50
60
70
ampl


B0L001S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 29.12
Std Dev 5.052
Gaus mean : 30.3900
Gaus Width: 3.2764
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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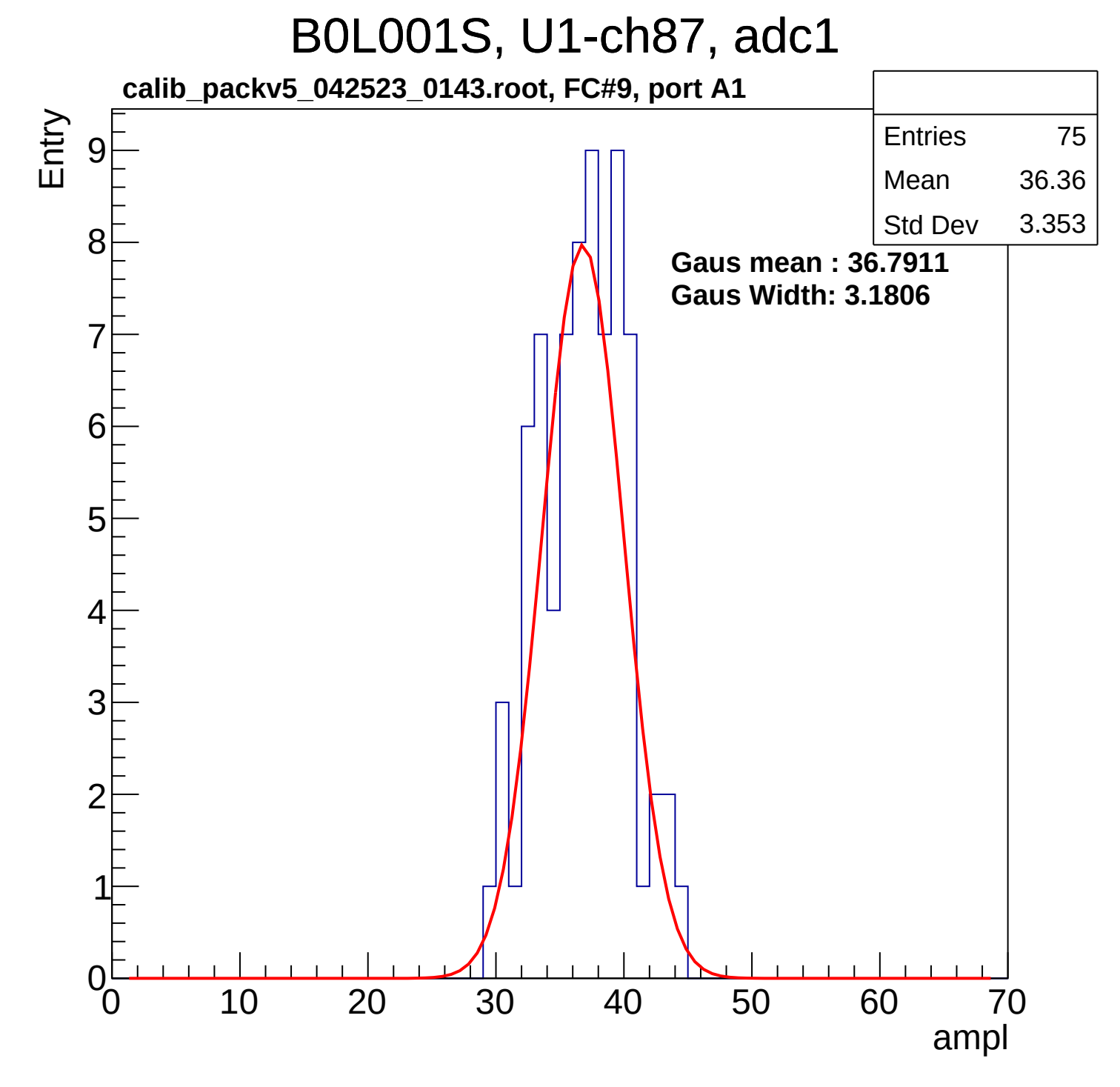
40
50
60
70


B0L001S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 36.36
Std Dev 3.353
Gaus mean : 36.7911
Gaus Width: 3.1806
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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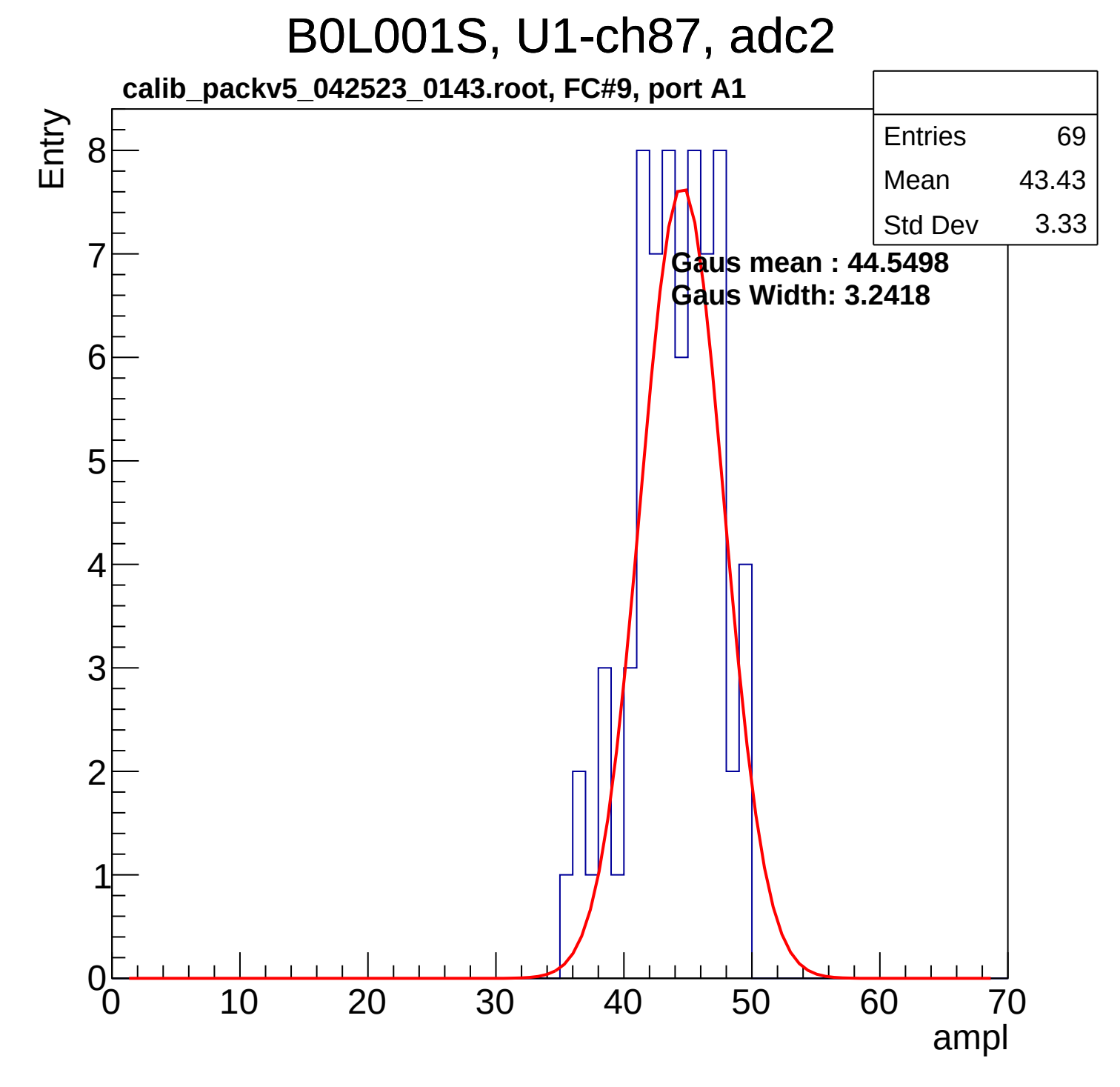
B0L001S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 43.43
Std Dev 3.33
Gaus mean : 44.5498
Gaus Width: 3.2418
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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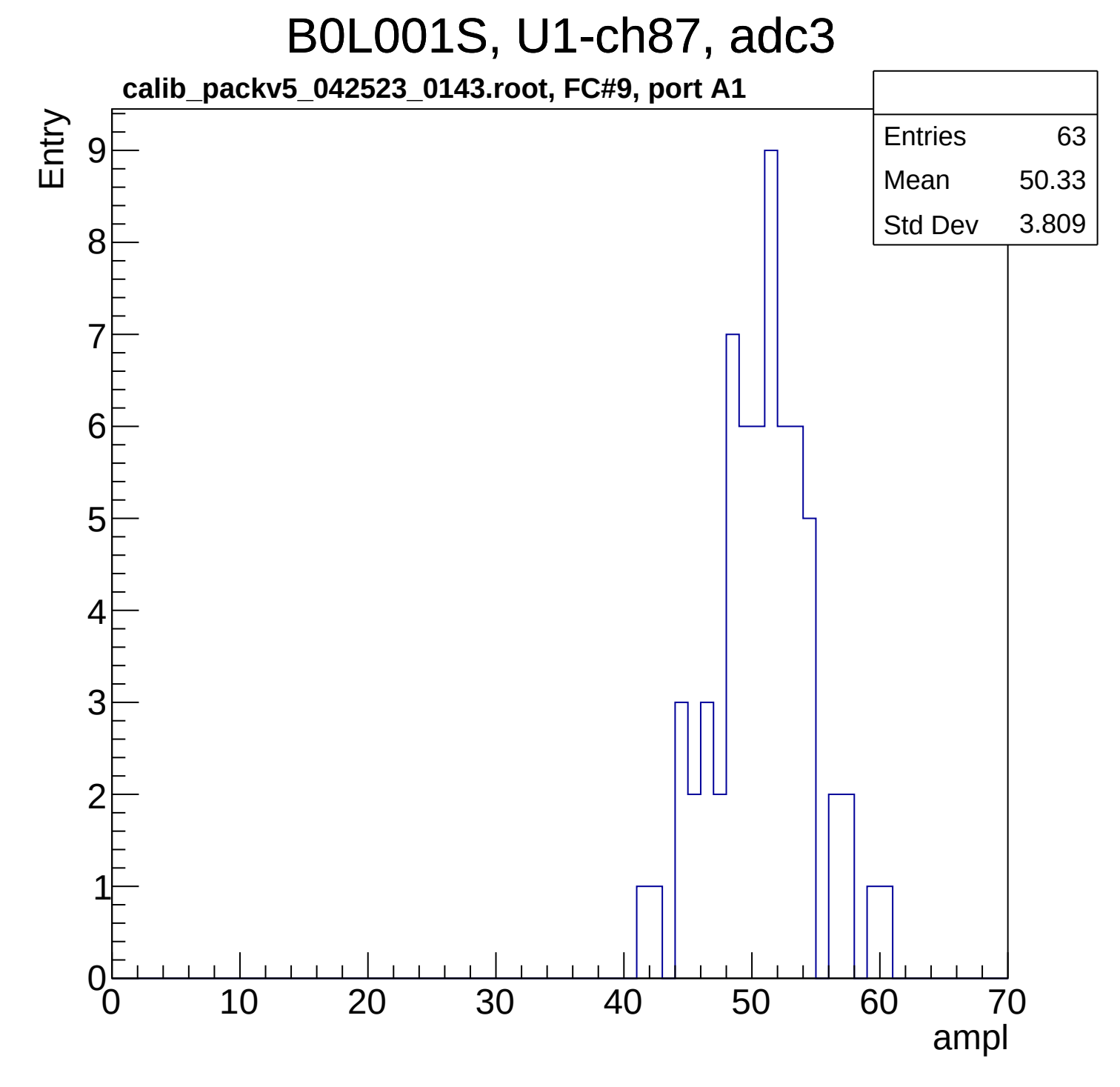
B0L001S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 50.33
Std Dev 3.809
Entry
ampl
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6
7
8
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0
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60
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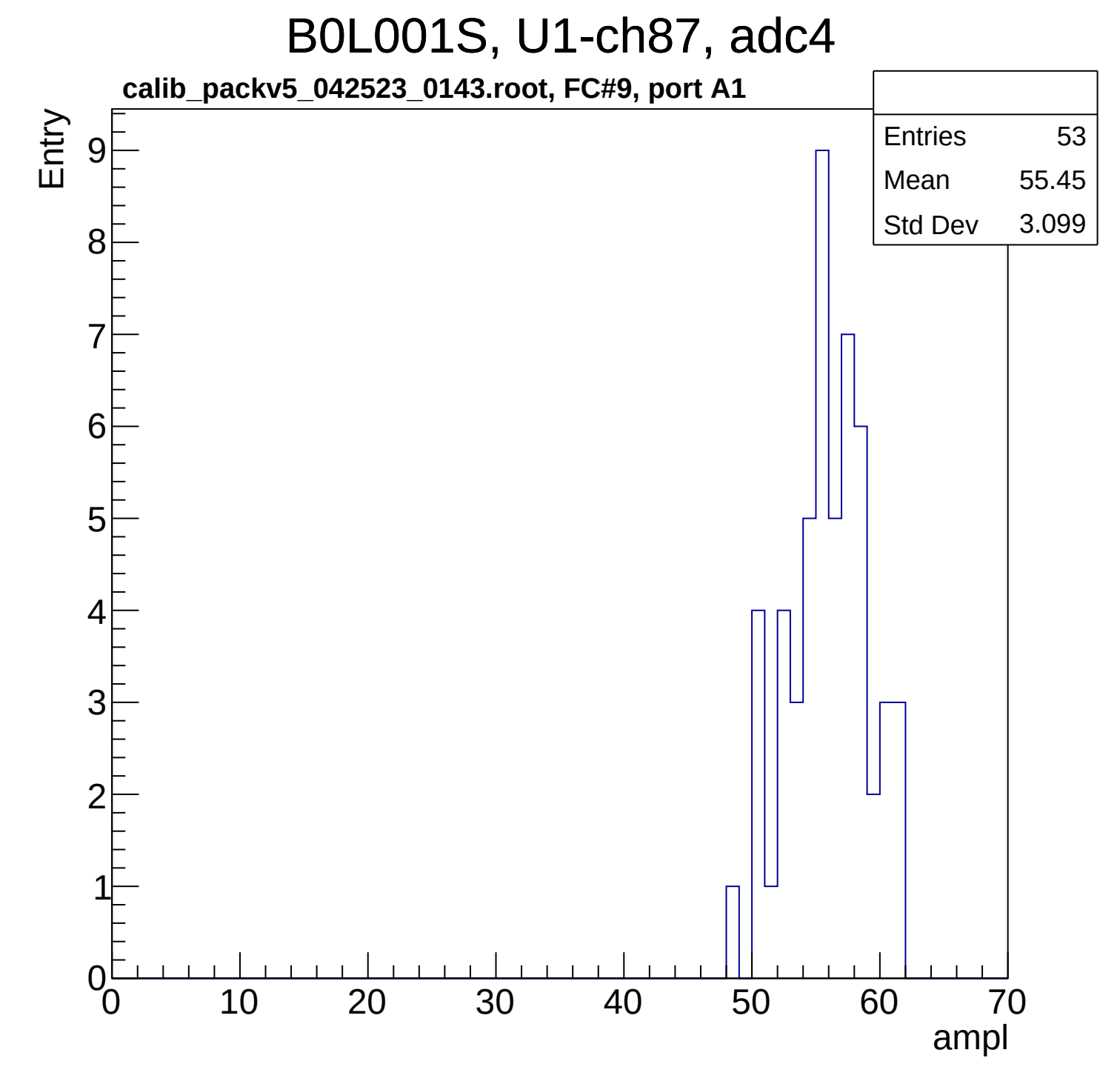
B0L001S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.45
Std Dev 3.099
Entry
ampl
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2
3
4
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6
7
8
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0
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40
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60
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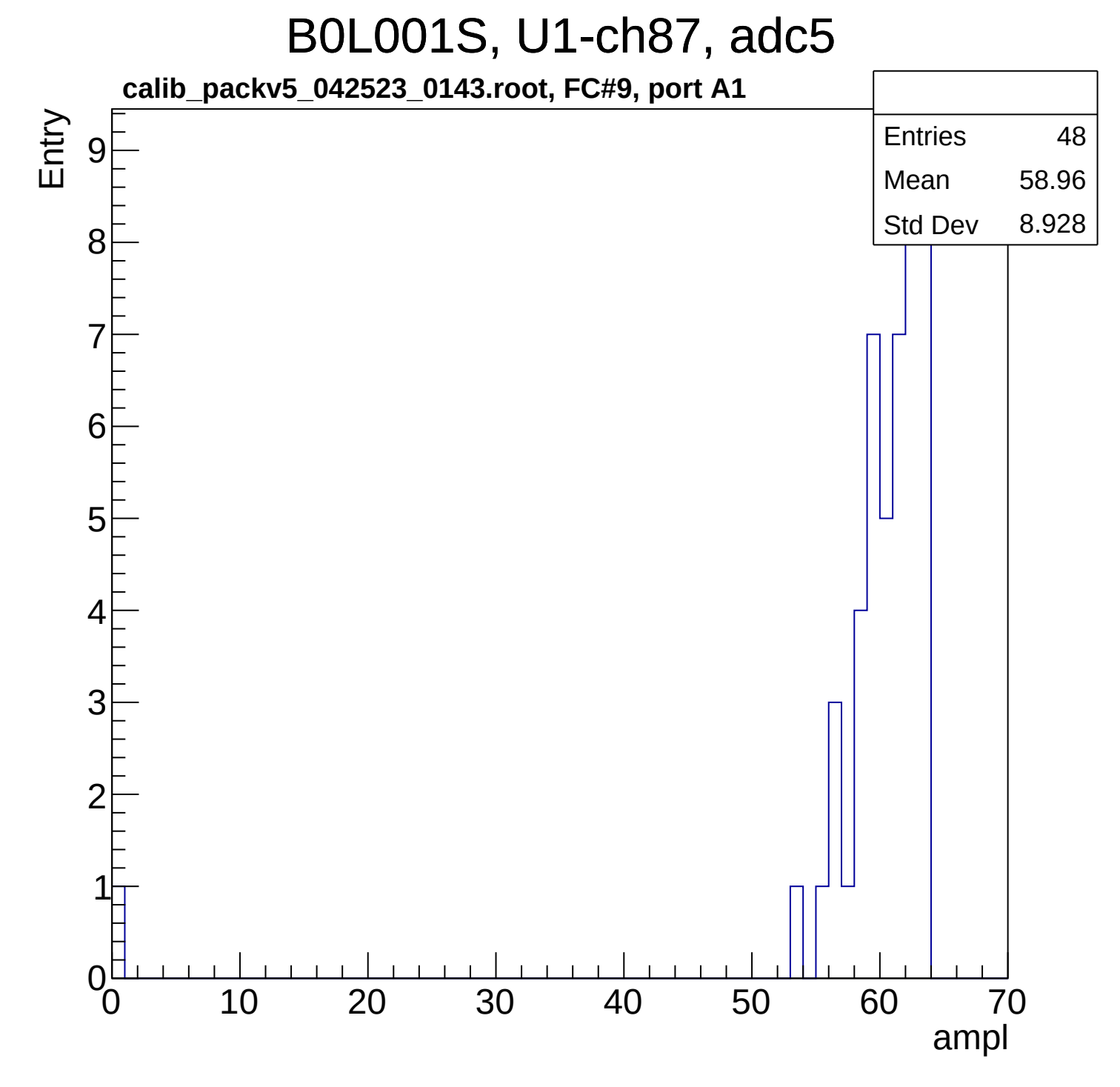
B0L001S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 58.96
Std Dev 8.928
Entry
ampl
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3
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5
6
7
8
9
0
10
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60
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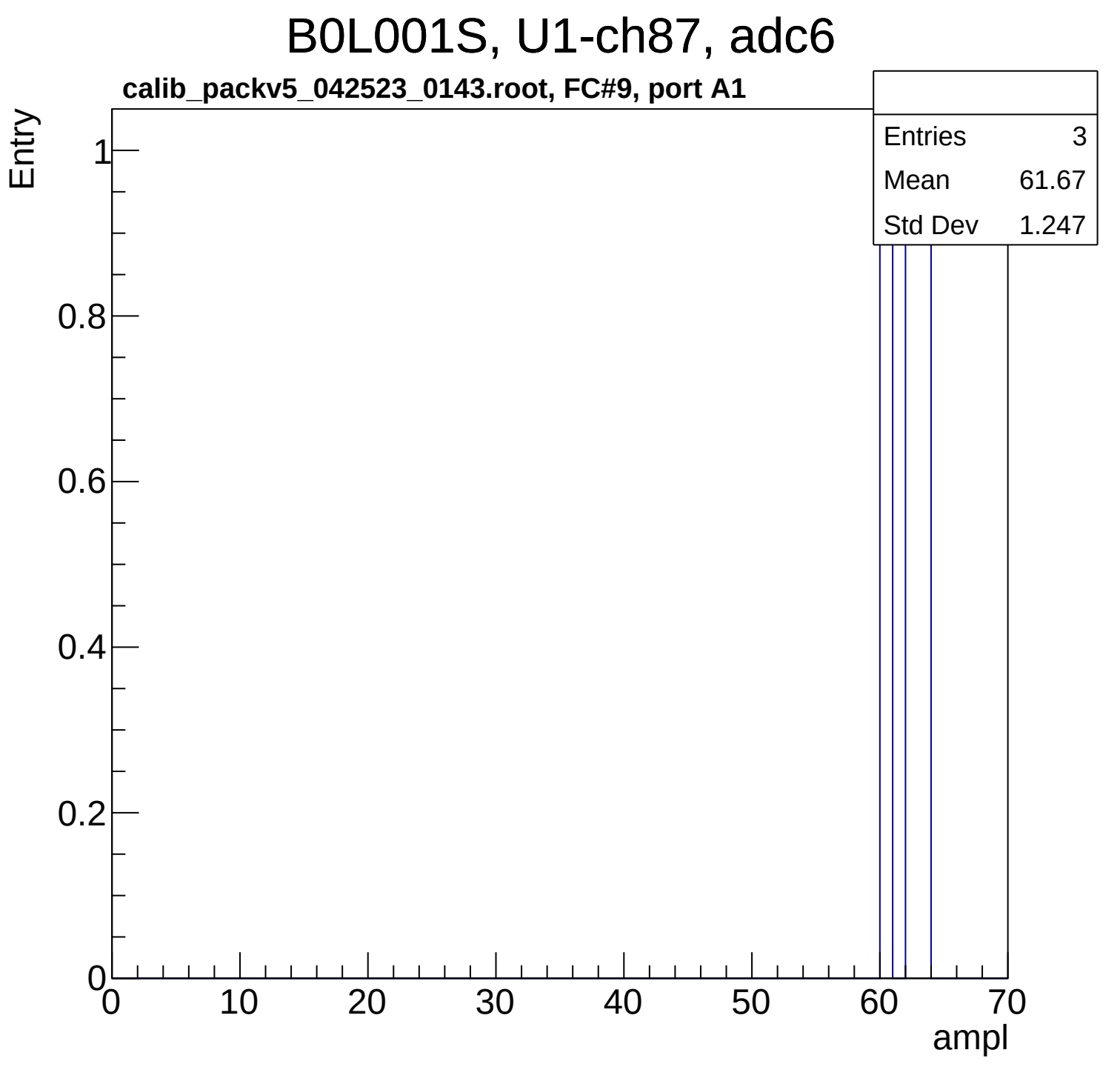
B0L001S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
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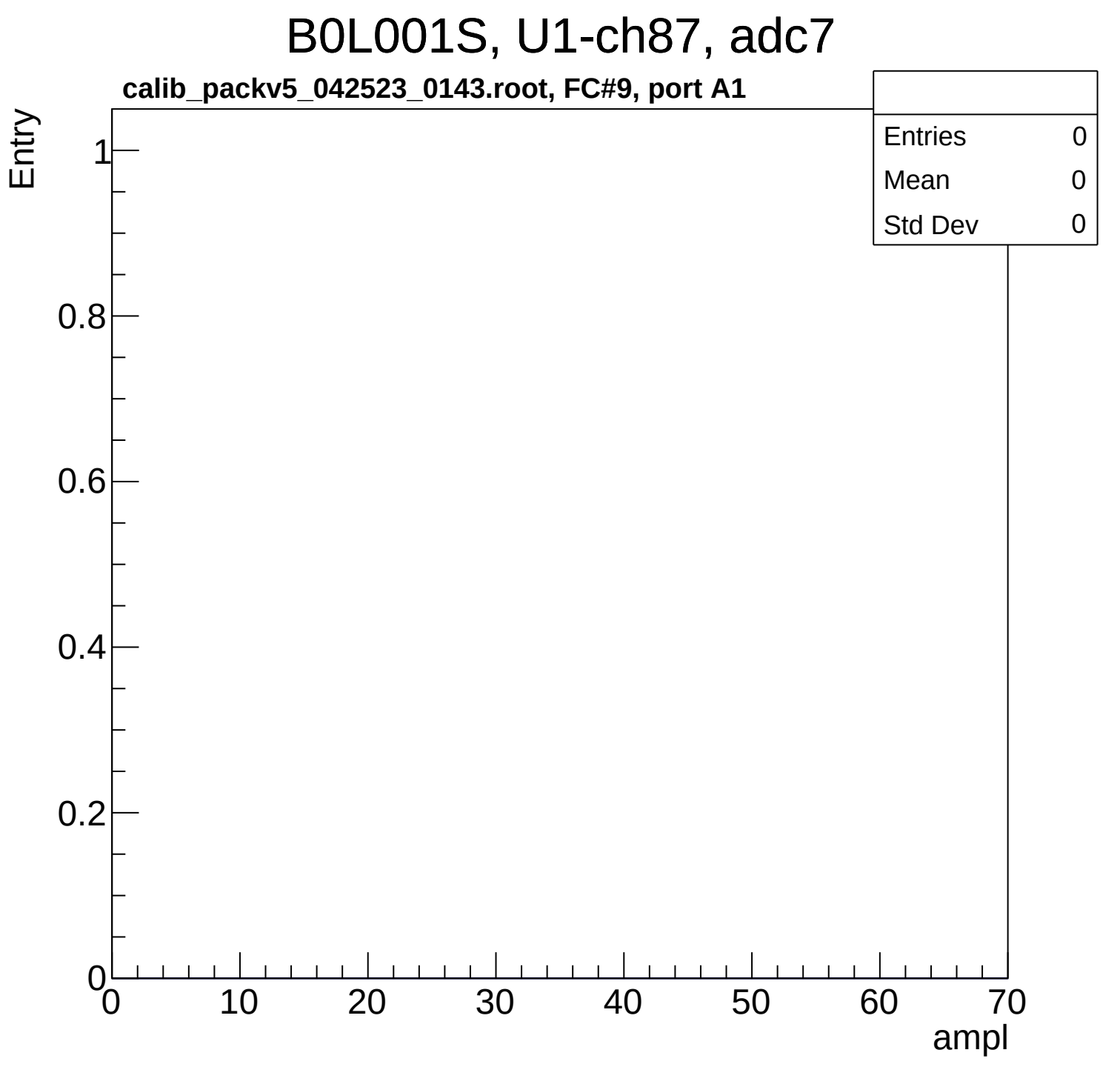
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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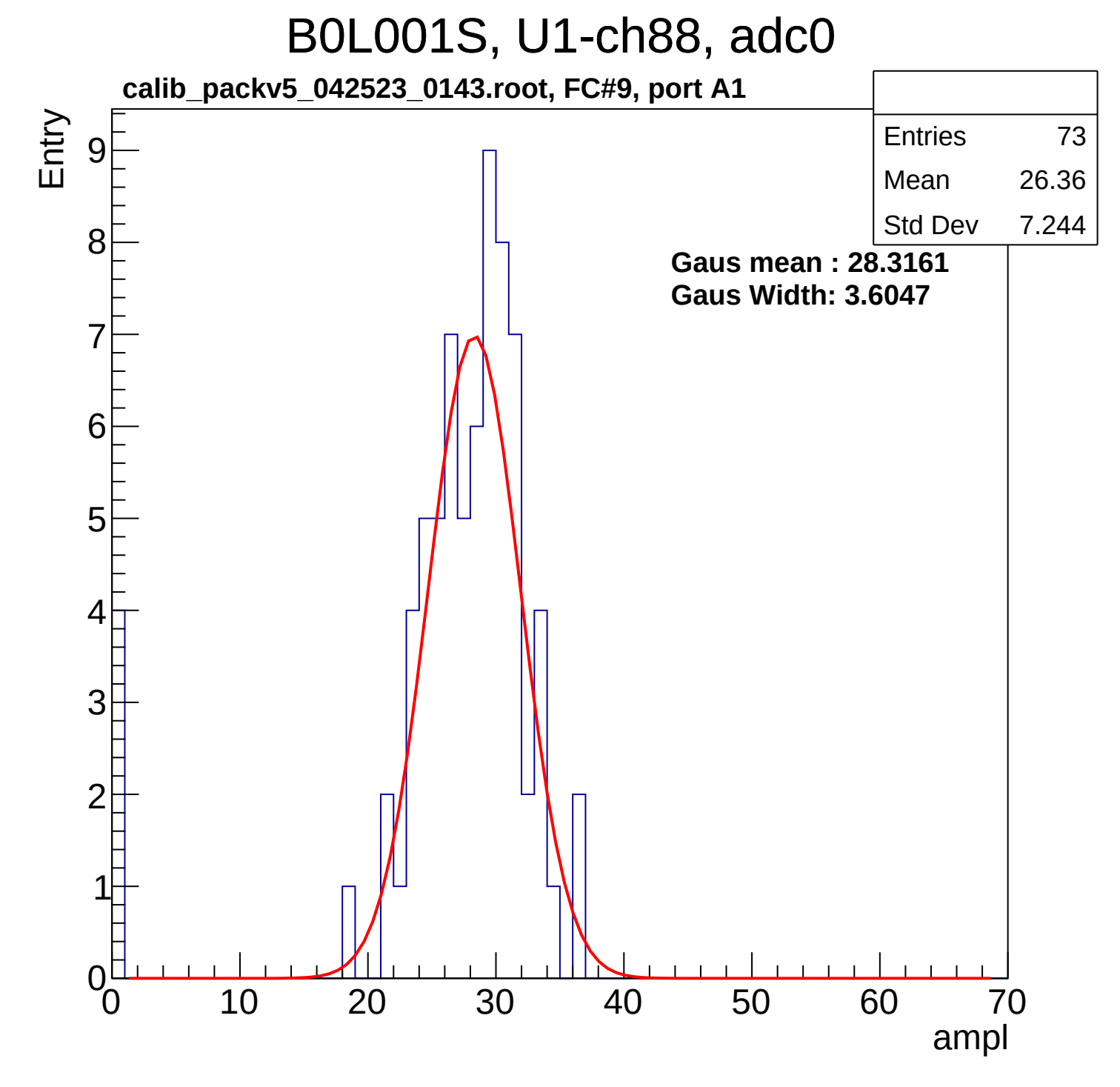
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 26.36
Std Dev 7.244
Gaus mean : 28.3161
Gaus Width: 3.6047
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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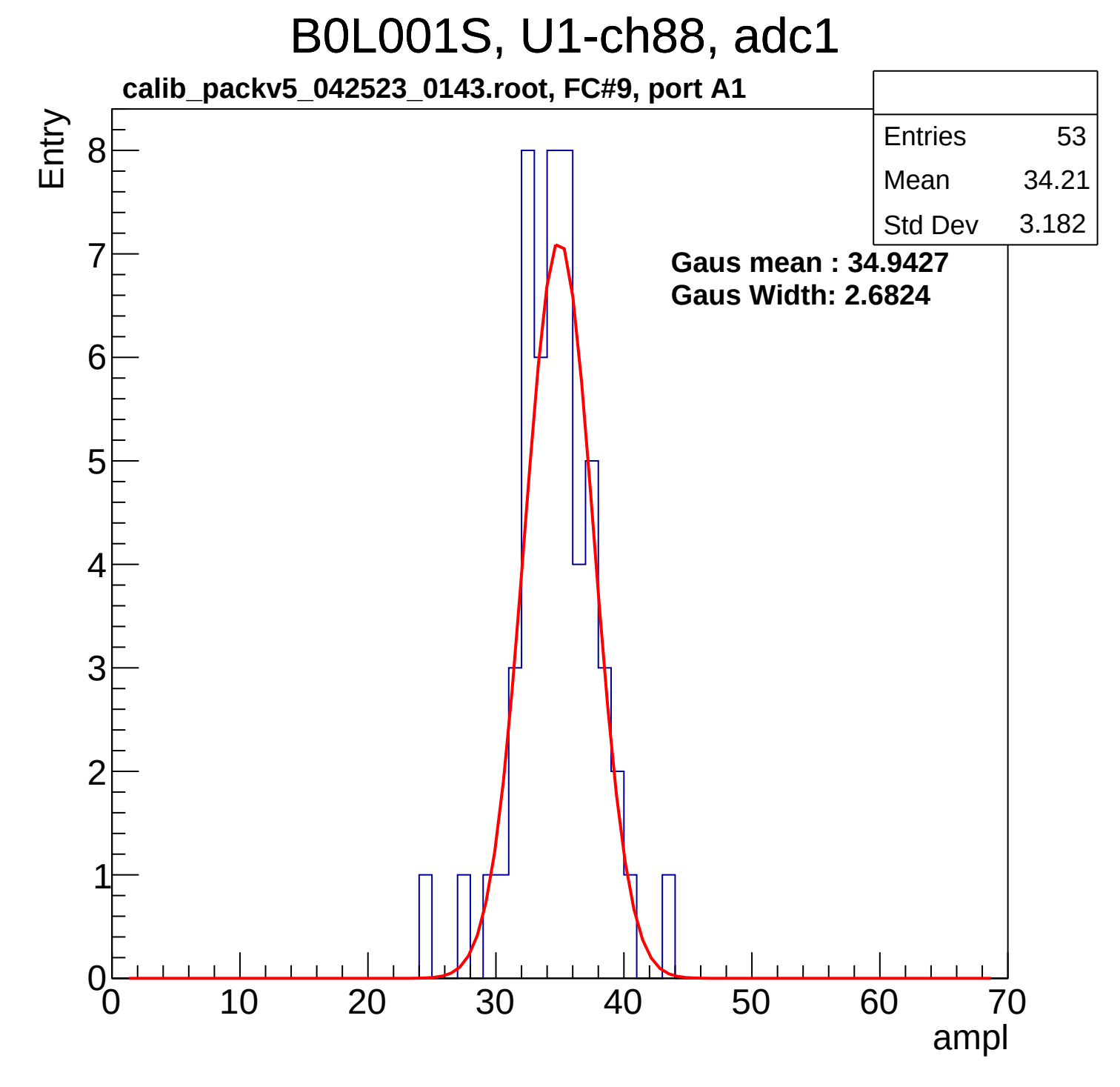
B0L001S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 34.21
Std Dev 3.182
Gaus mean : 34.9427
Gaus Width: 2.6824
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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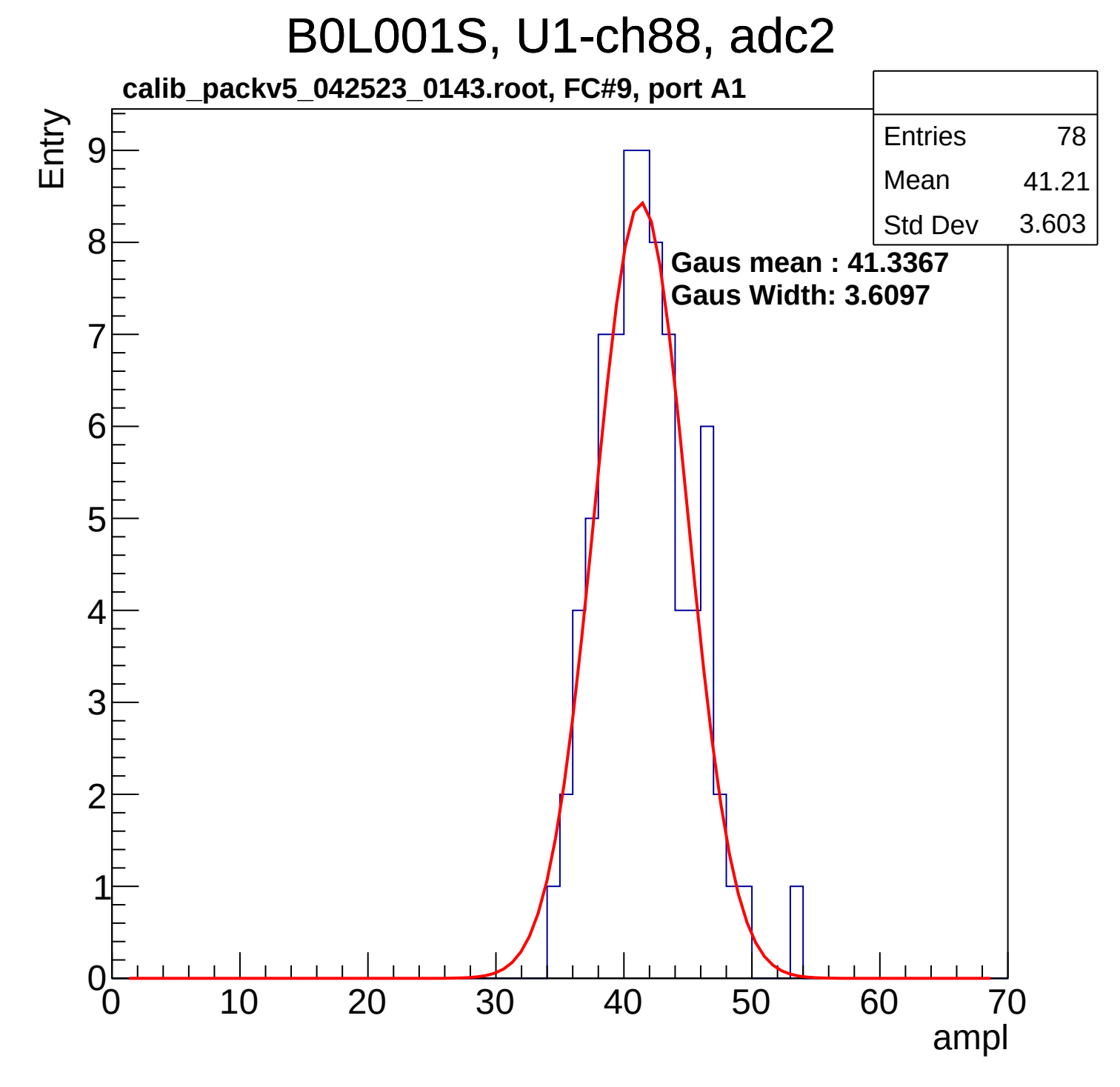
B0L001S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 41.21
Std Dev 3.603
Gaus mean : 41.3367
Gaus Width: 3.6097
Entry
ampl
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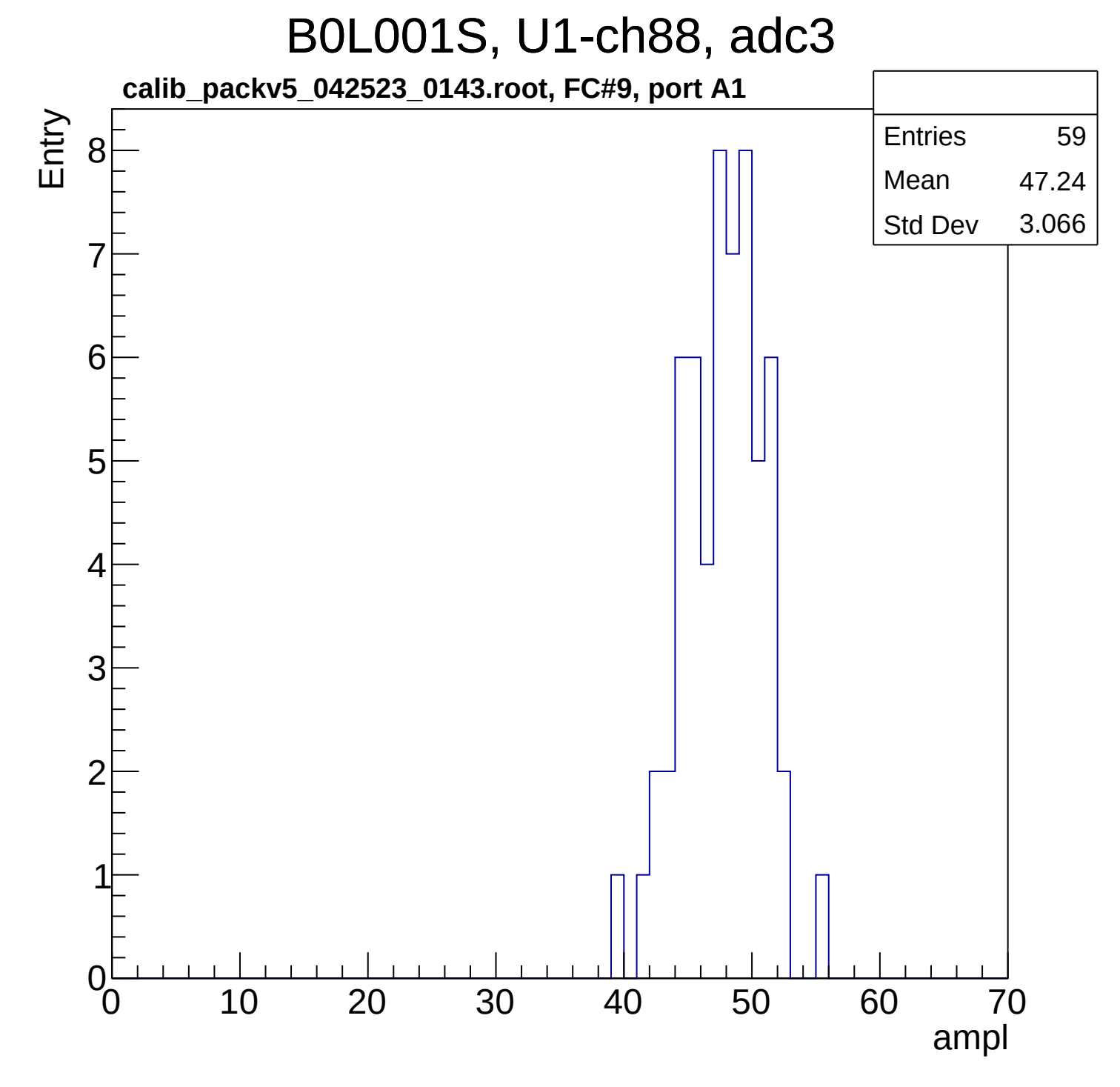
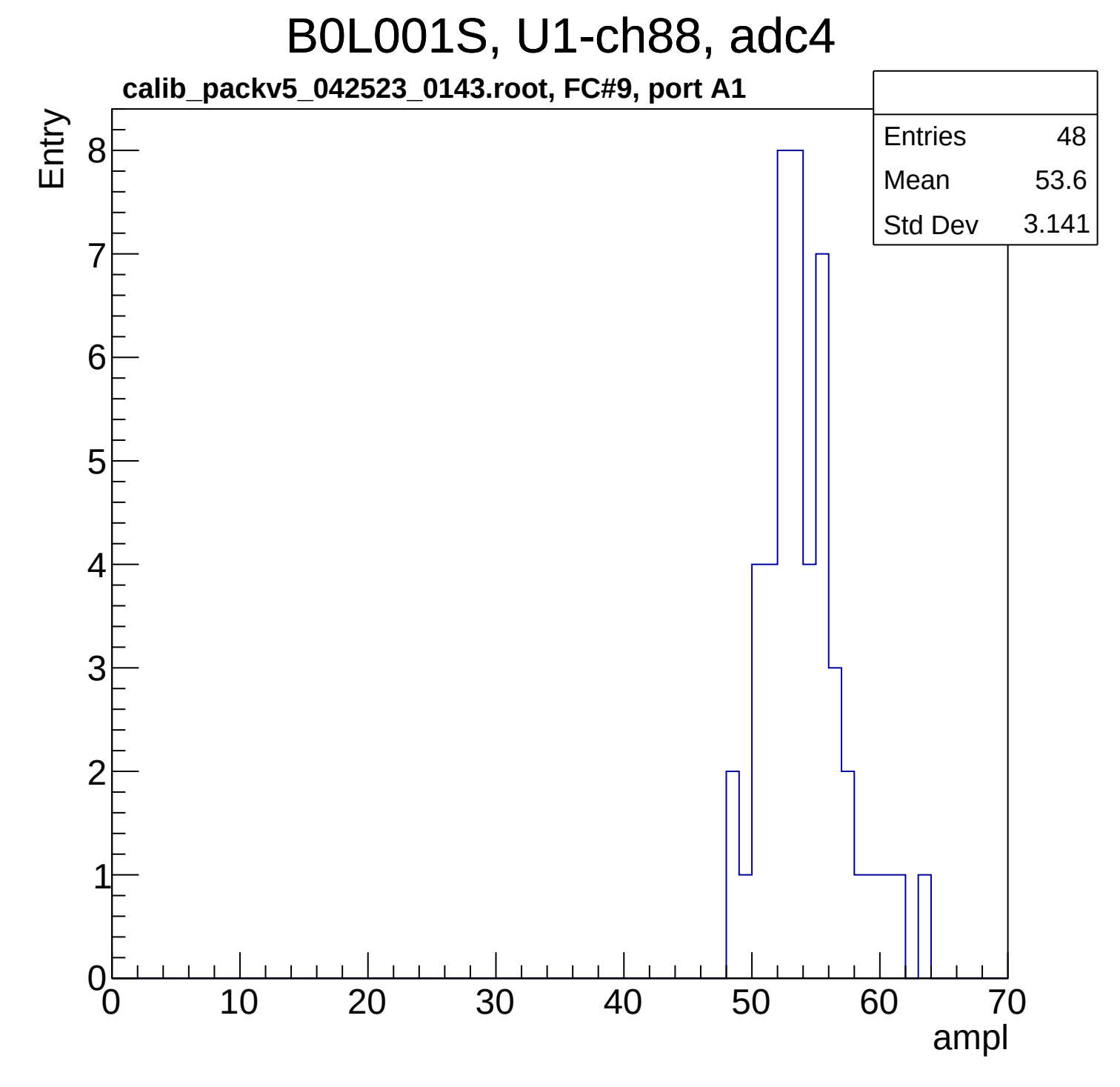
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 47.24
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 53.6
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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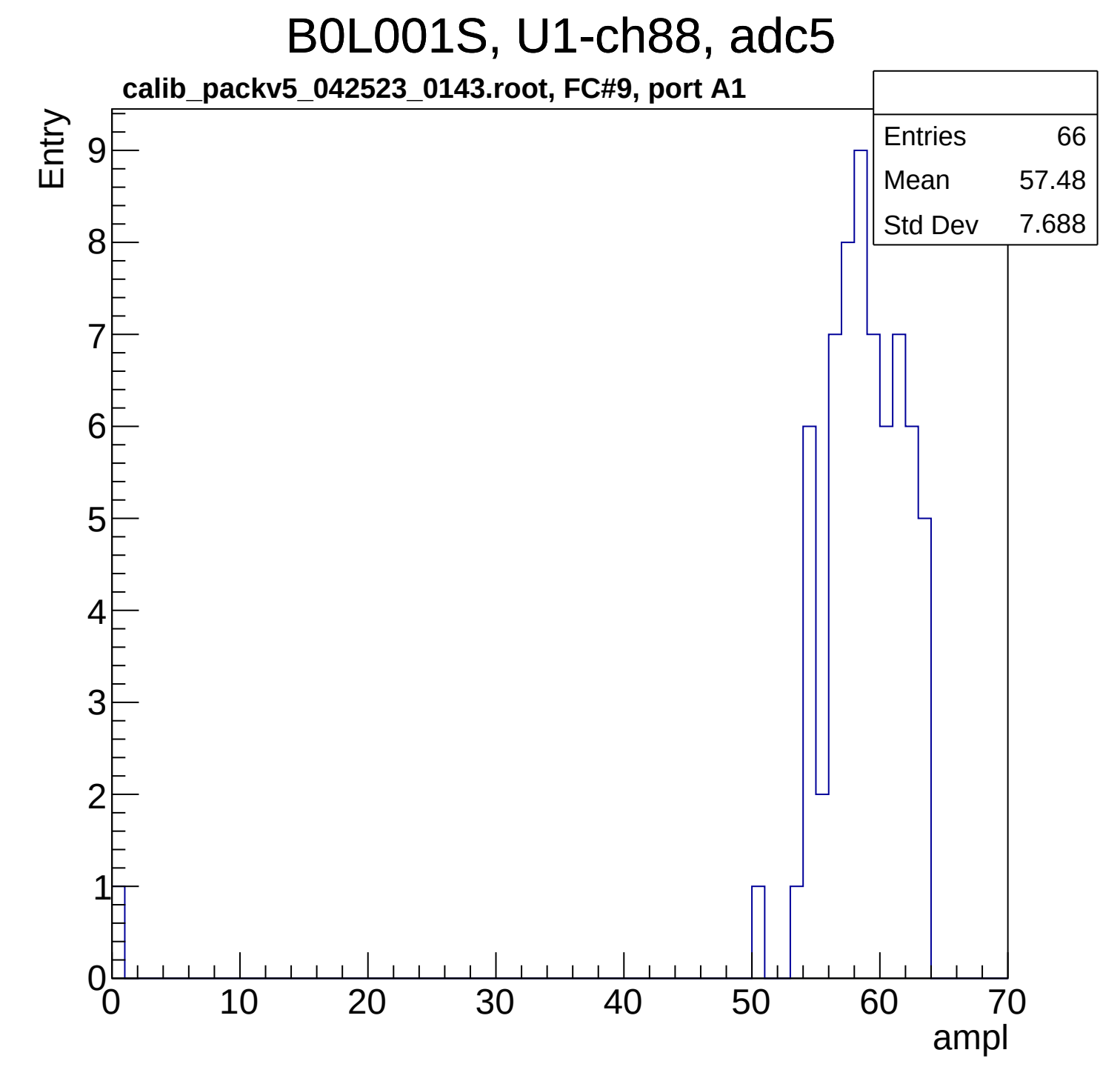
B0L001S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.48
Std Dev 7.688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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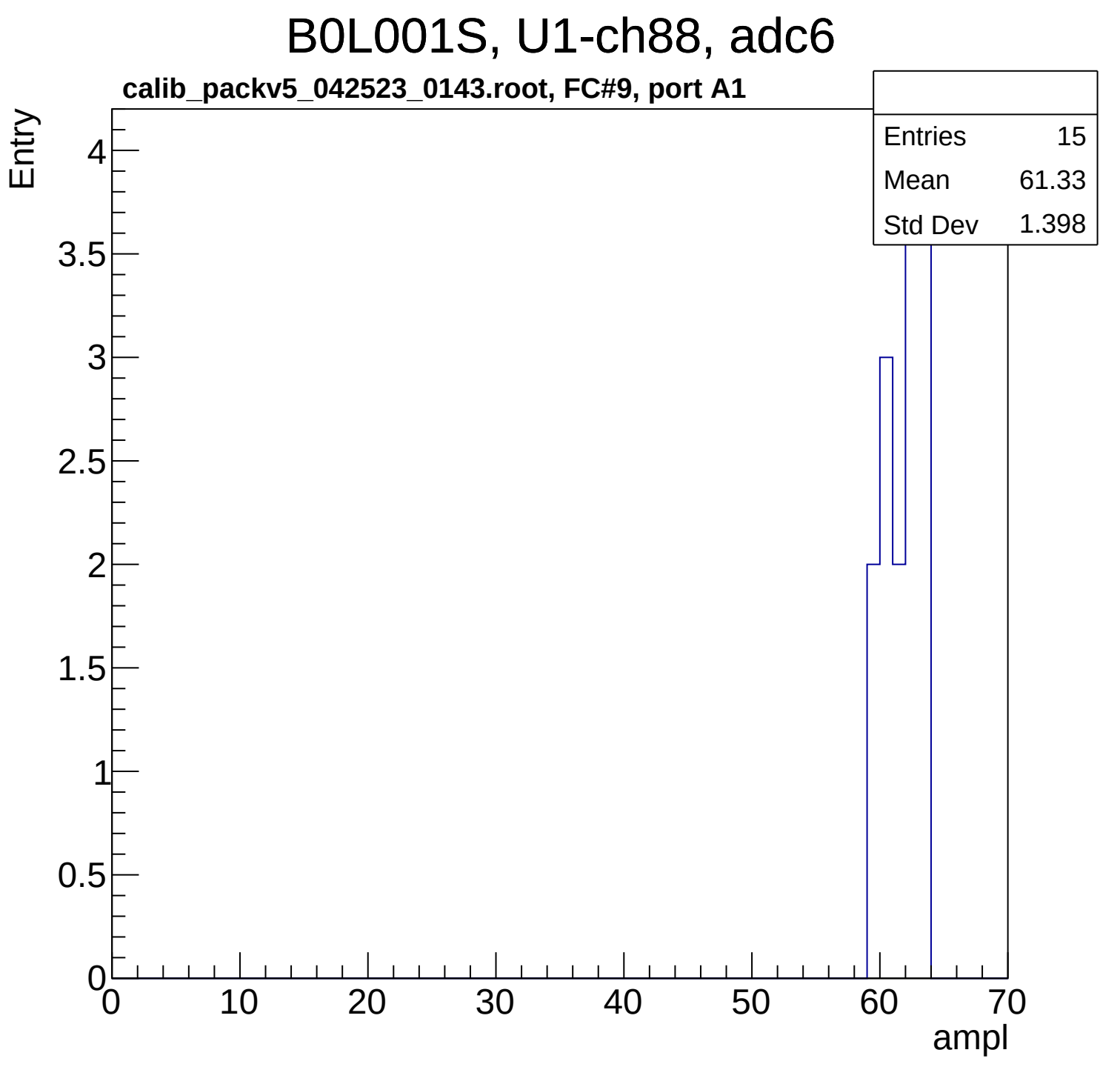
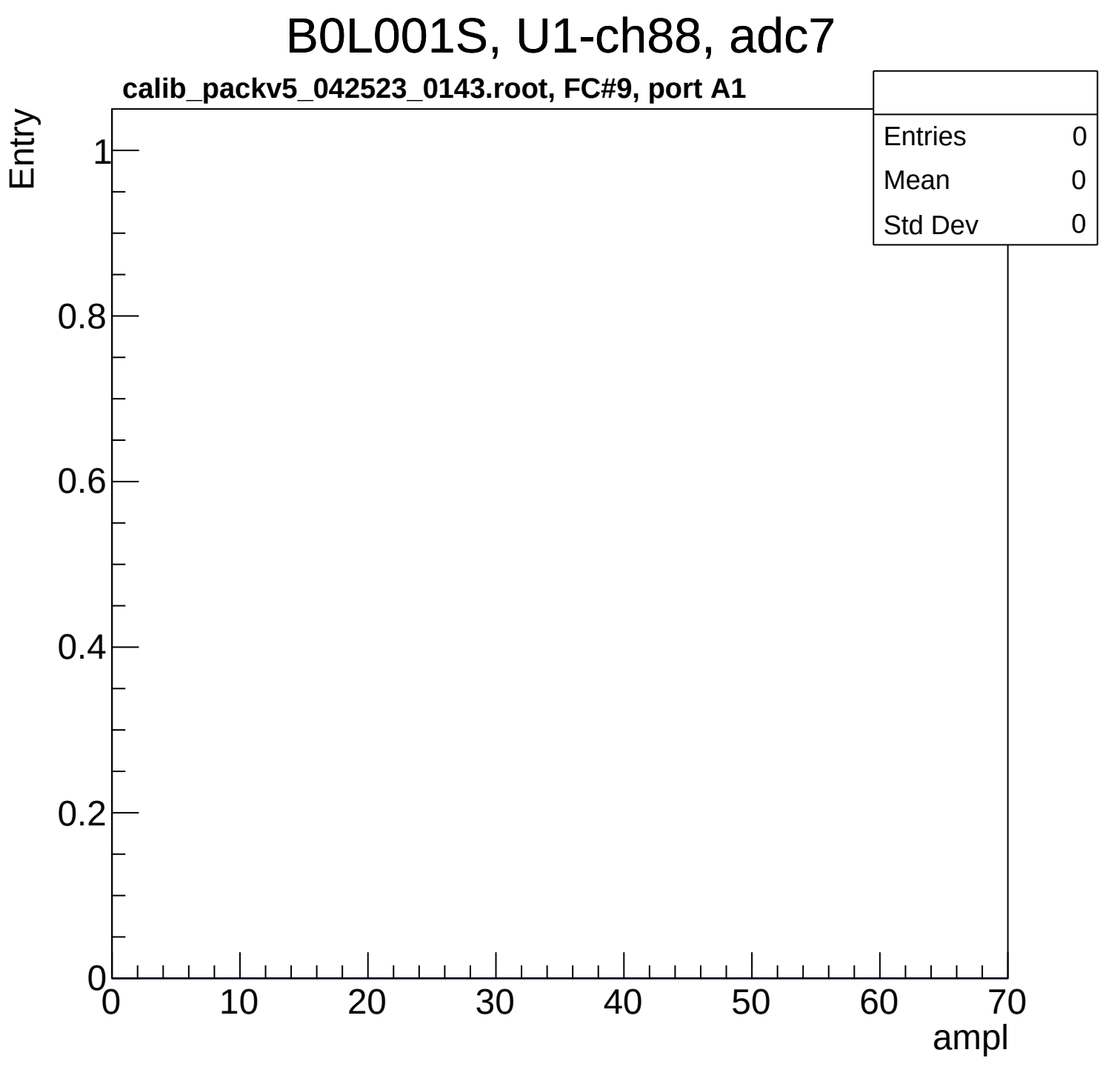
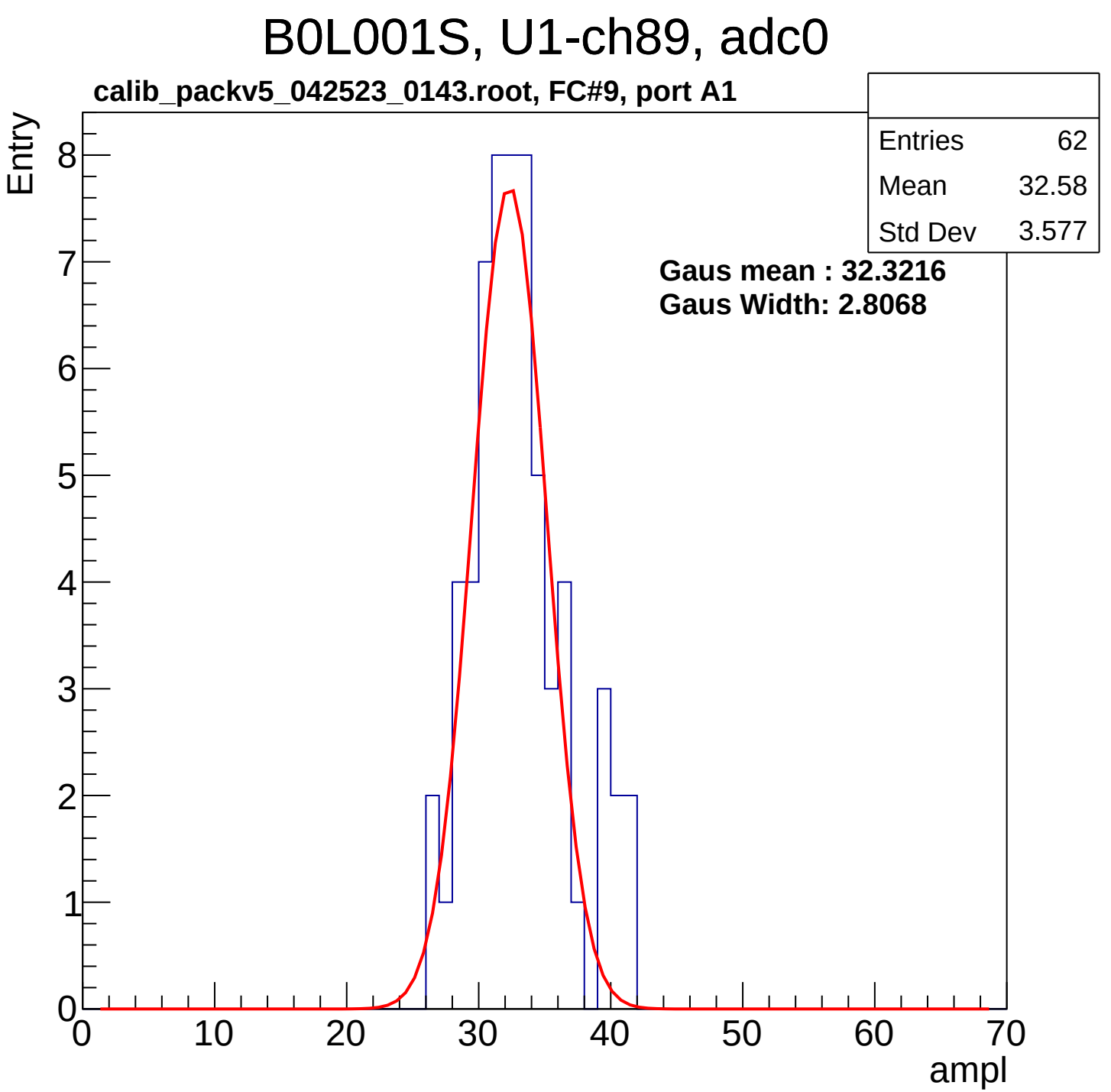
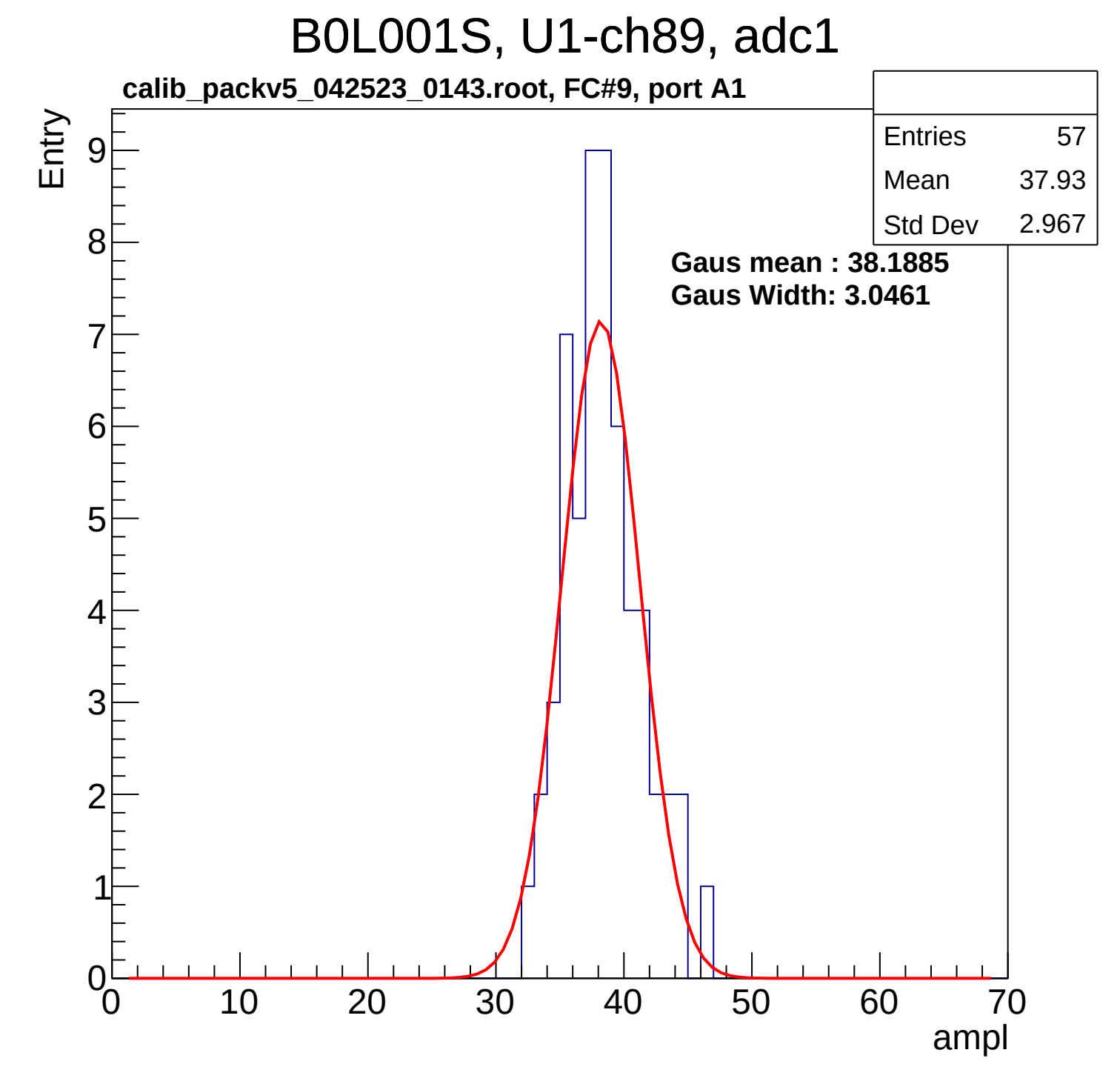
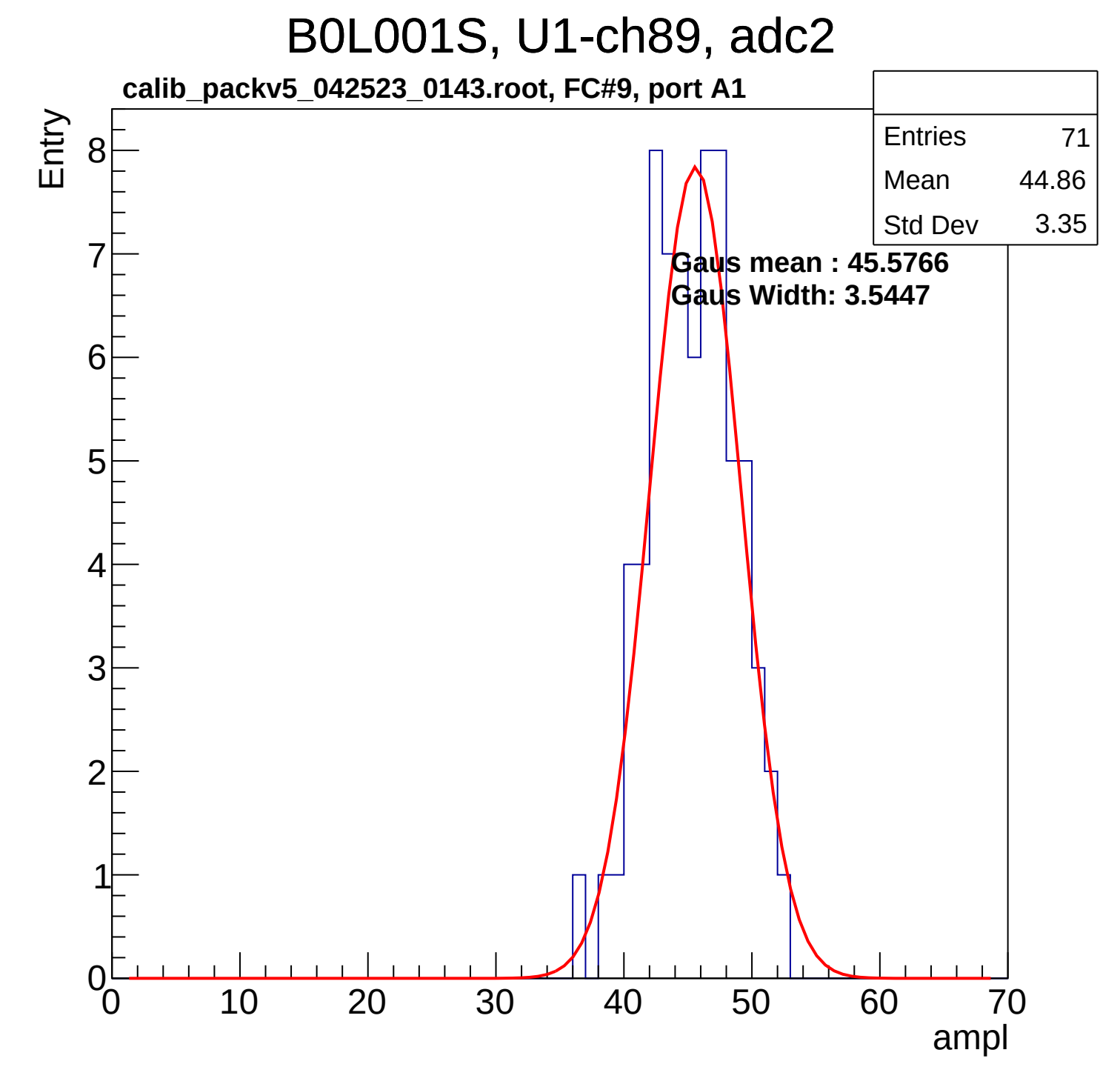
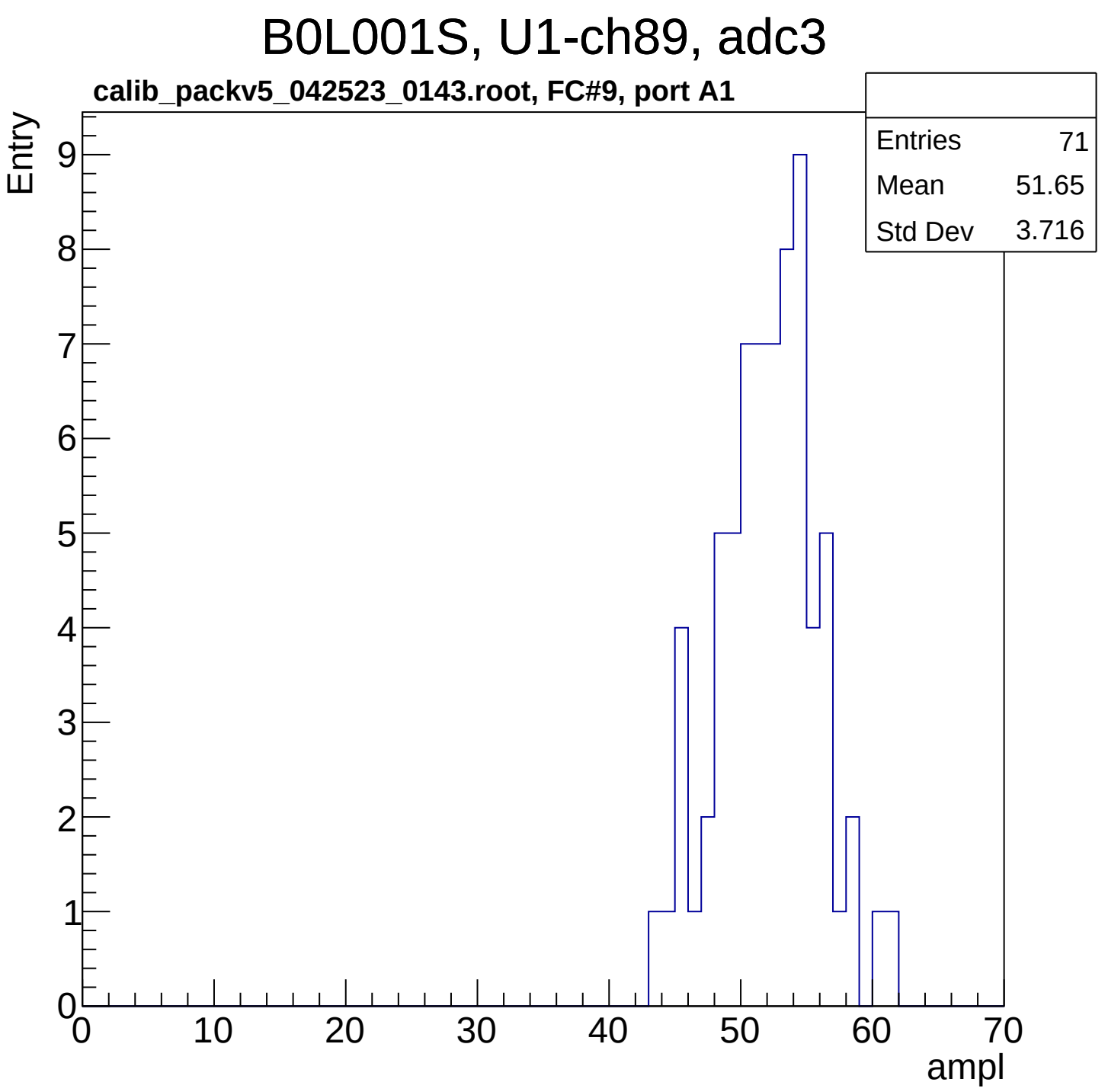

B0L001S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 61.33
Std Dev 1.398
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

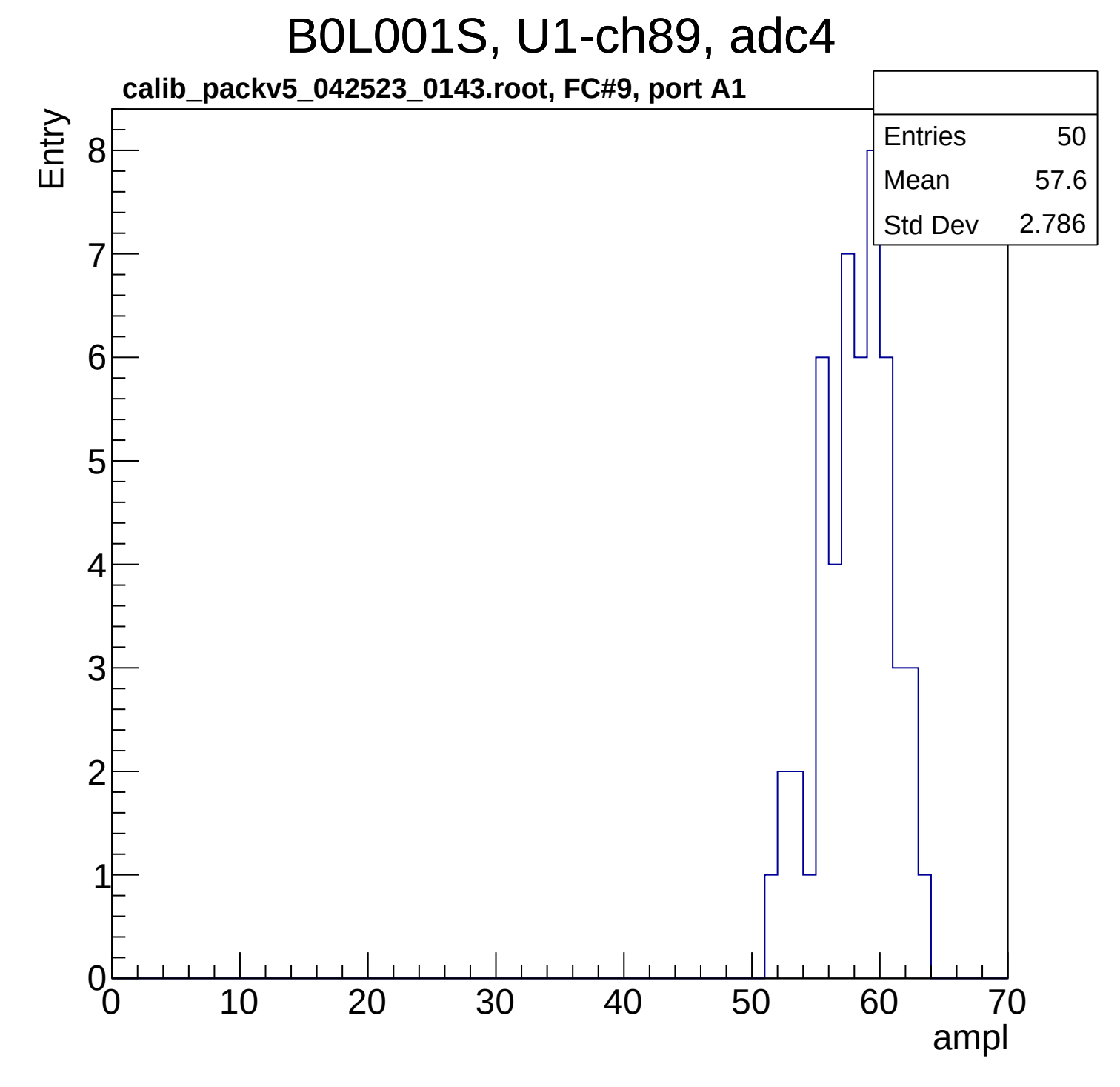

B0L001S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

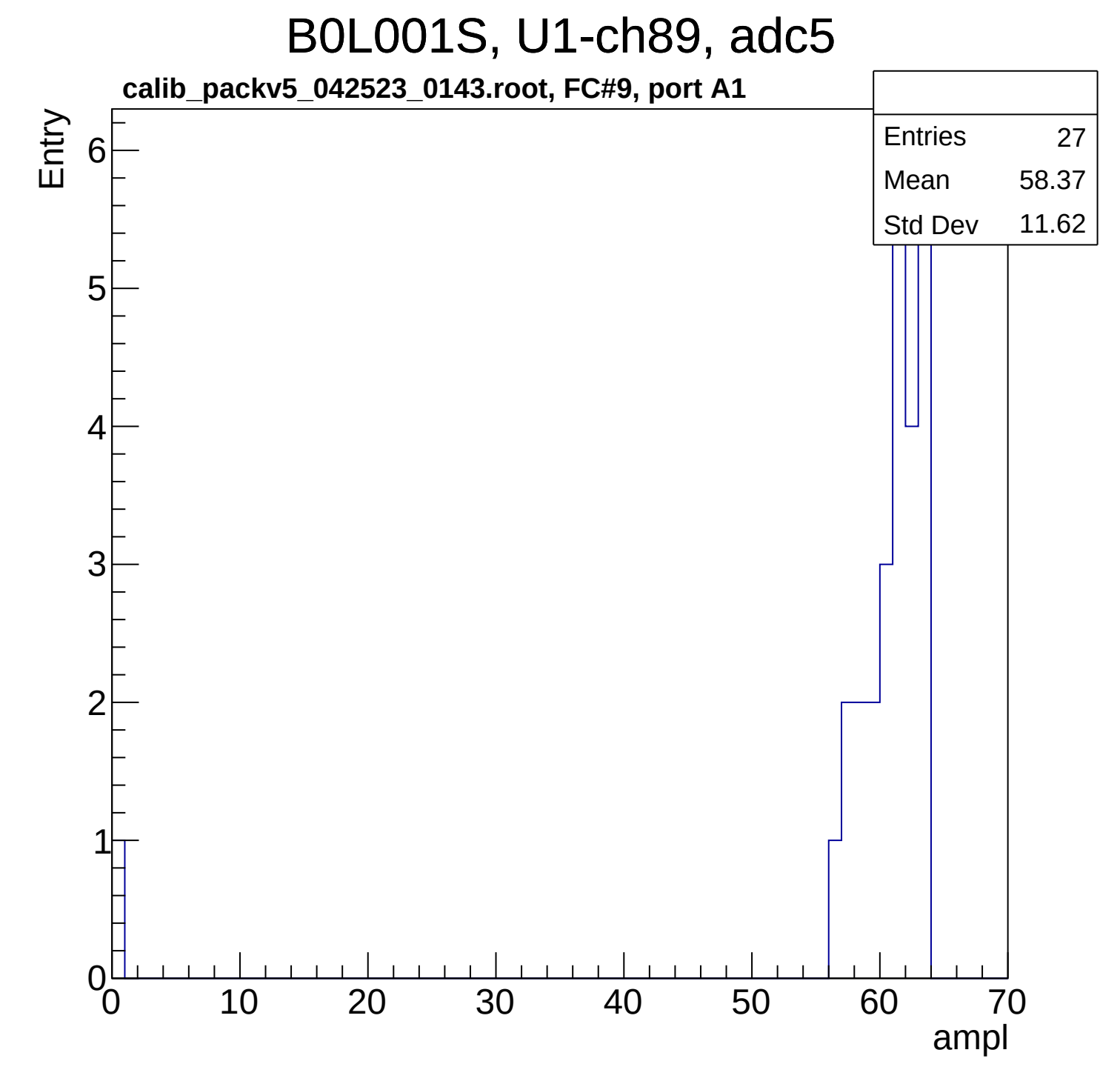

B0L001S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 32.58
Std Dev 3.577
Gaus mean : 32.3216
Gaus Width: 2.8068
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

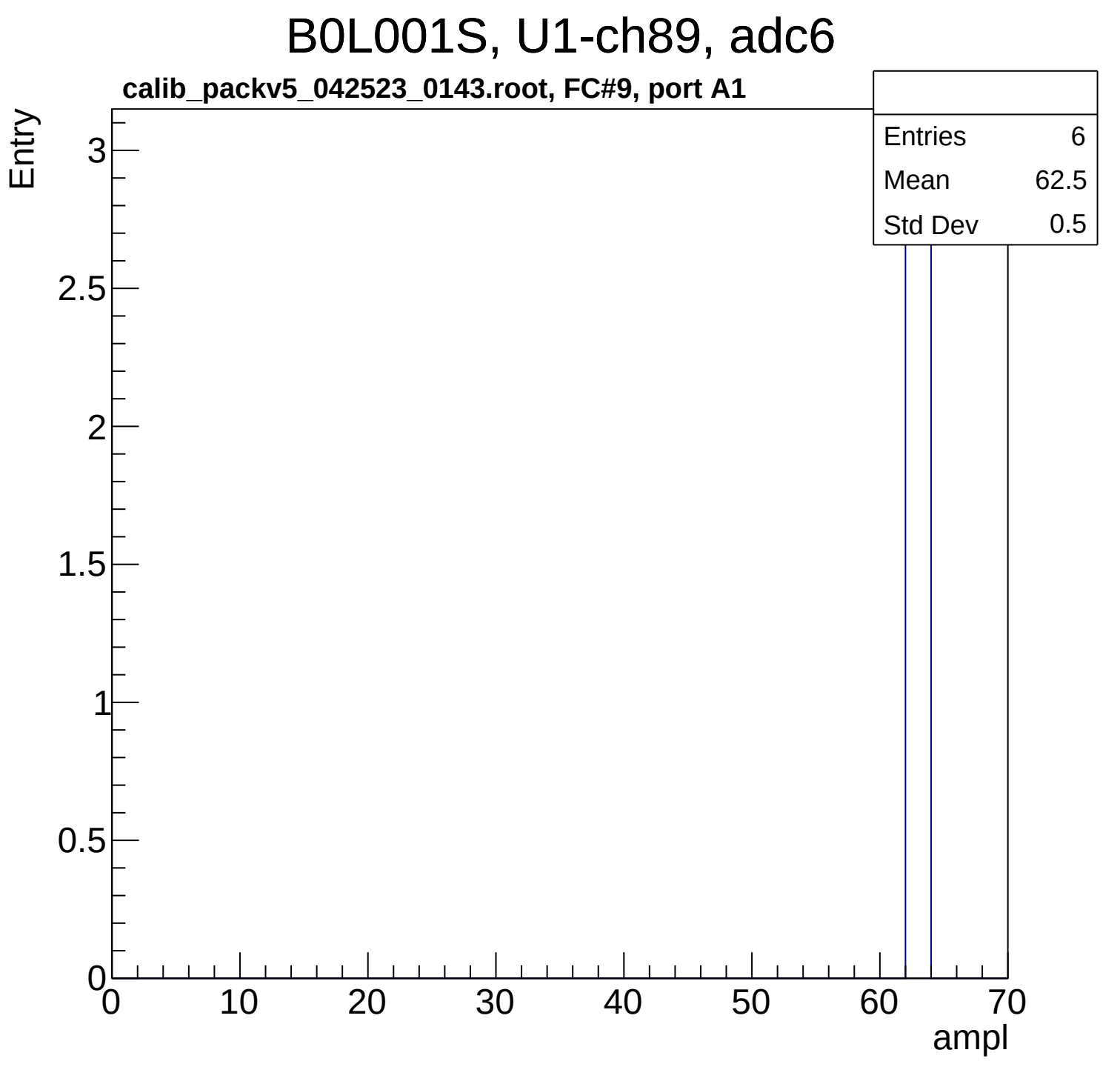

B0L001S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.93
Std Dev 2.967
Gaus mean : 38.1885
Gaus Width: 3.0461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

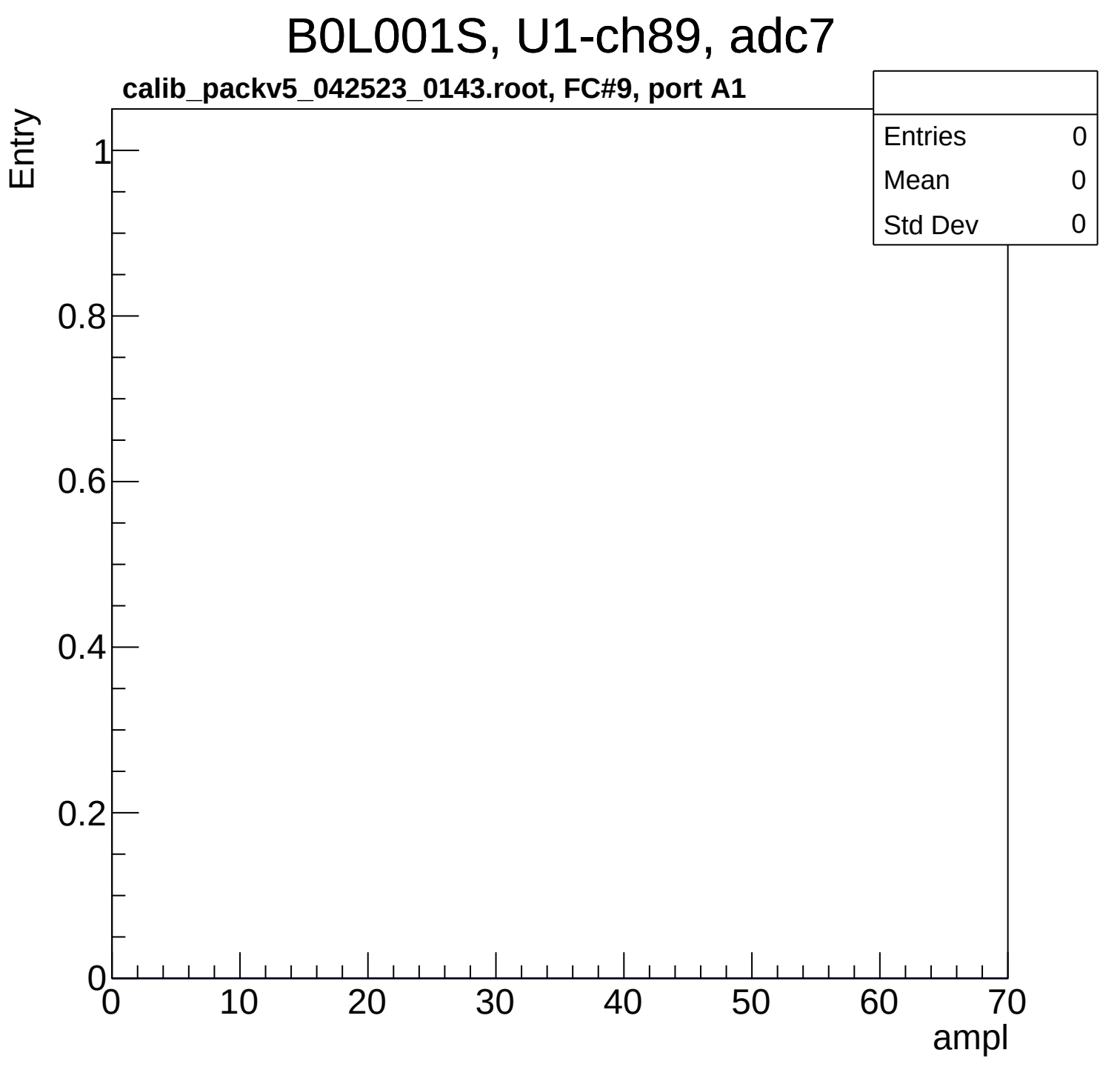

B0L001S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 44.86
Std Dev 3.35
Gaus mean : 45.5766
Gaus Width: 3.5447
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

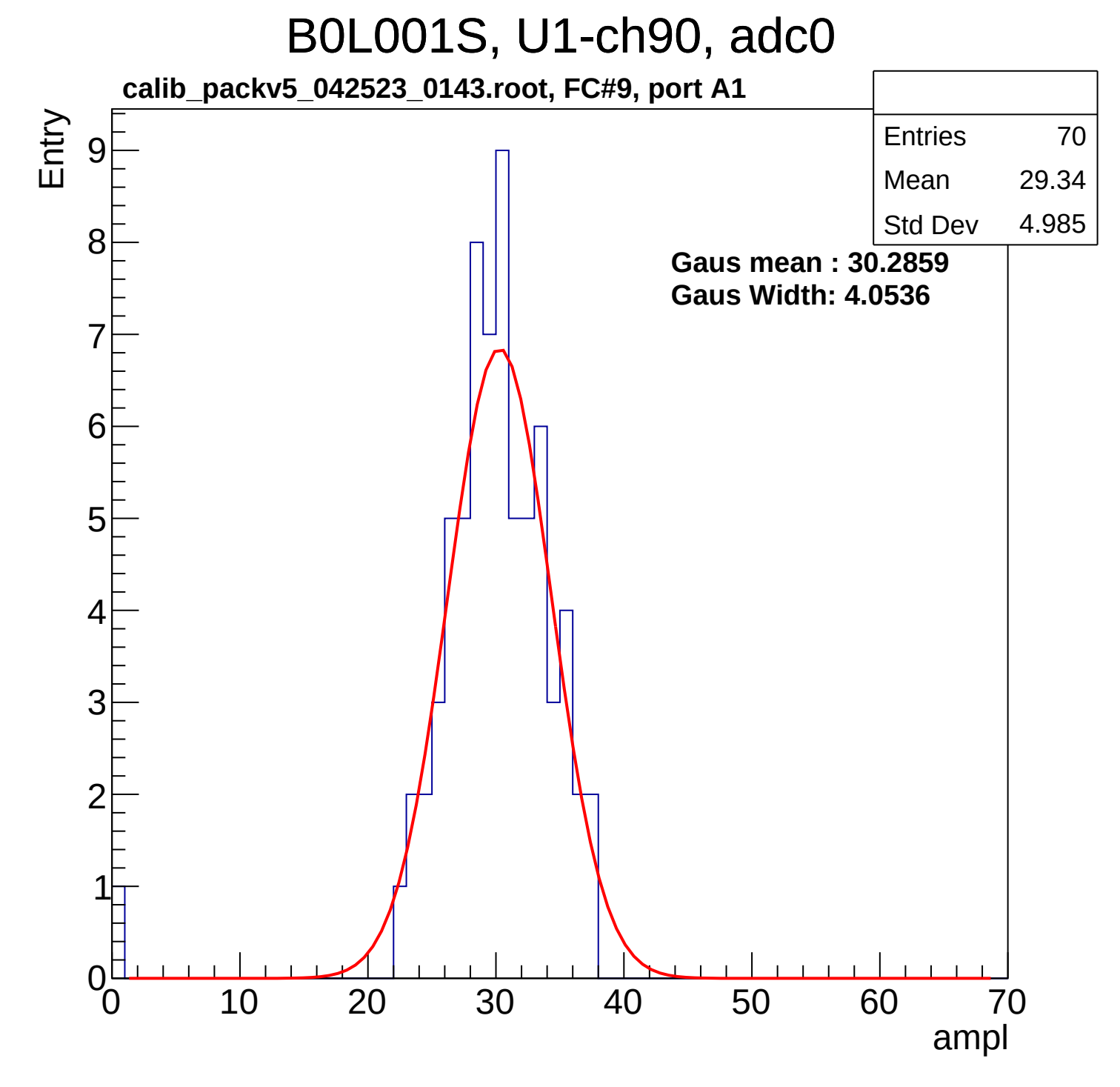

B0L001S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 51.65
Std Dev 3.716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

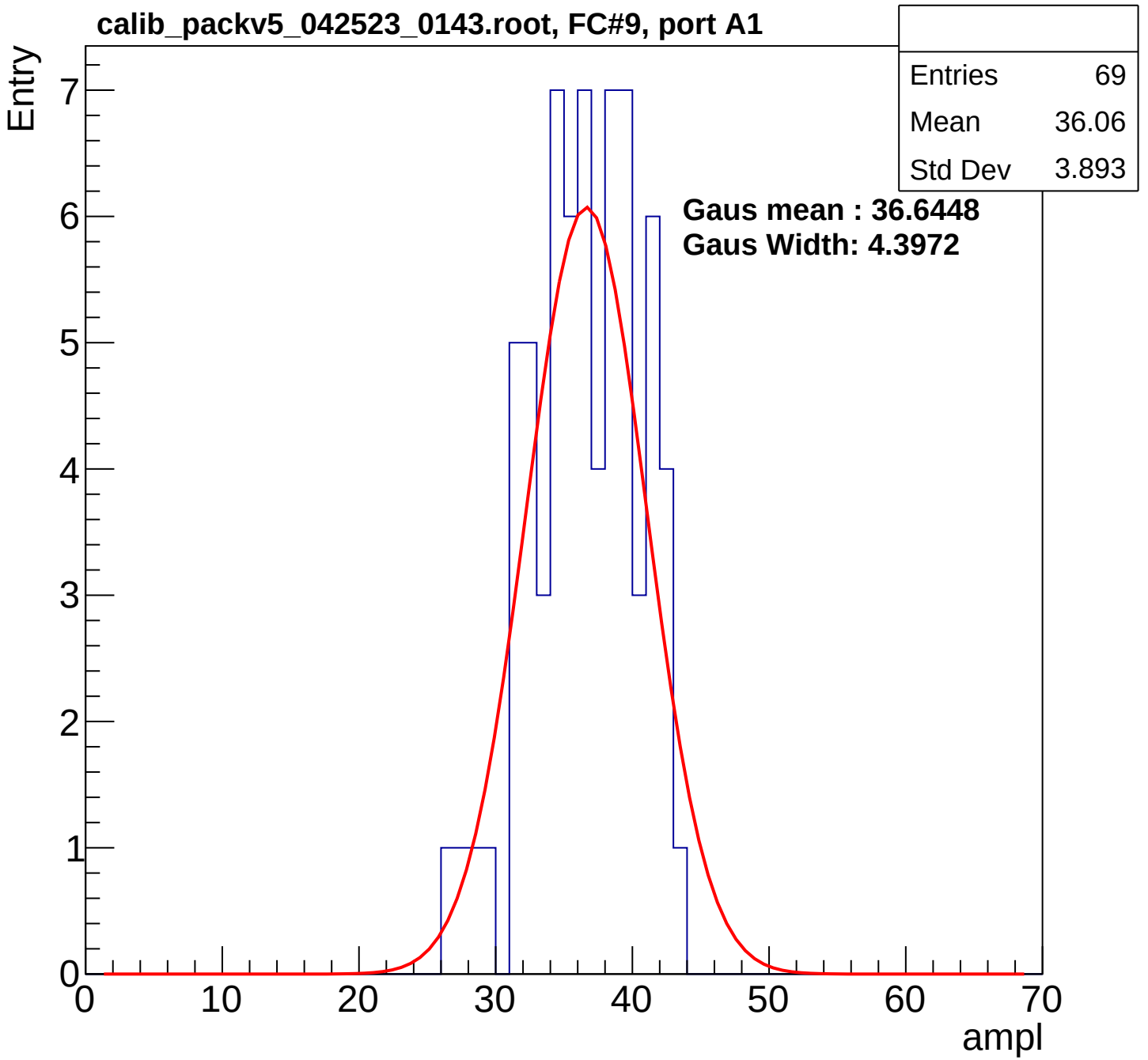

B0L001S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.6
Std Dev 2.786
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

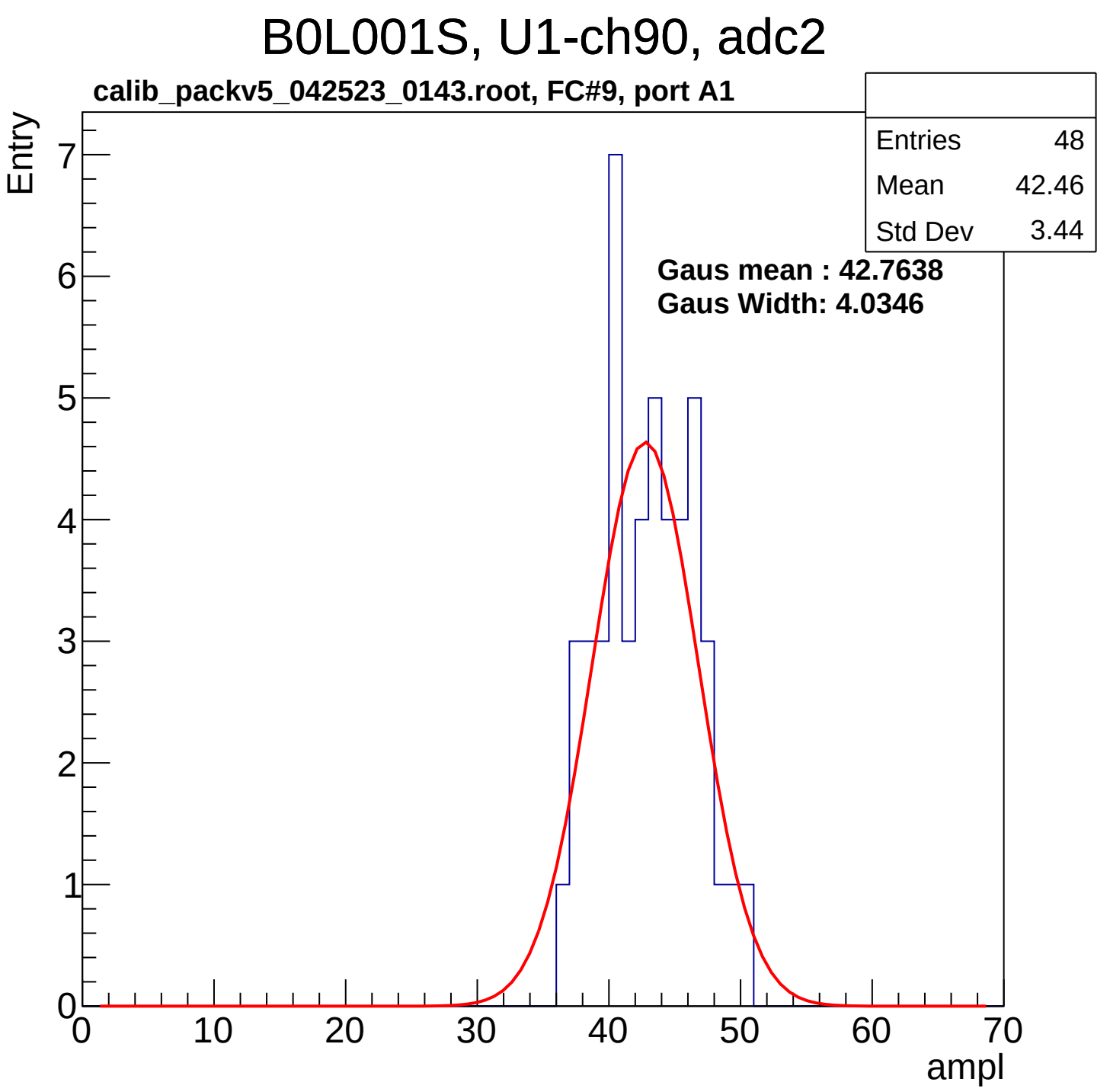

B0L001S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 27
Mean 58.37
Std Dev 11.62
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

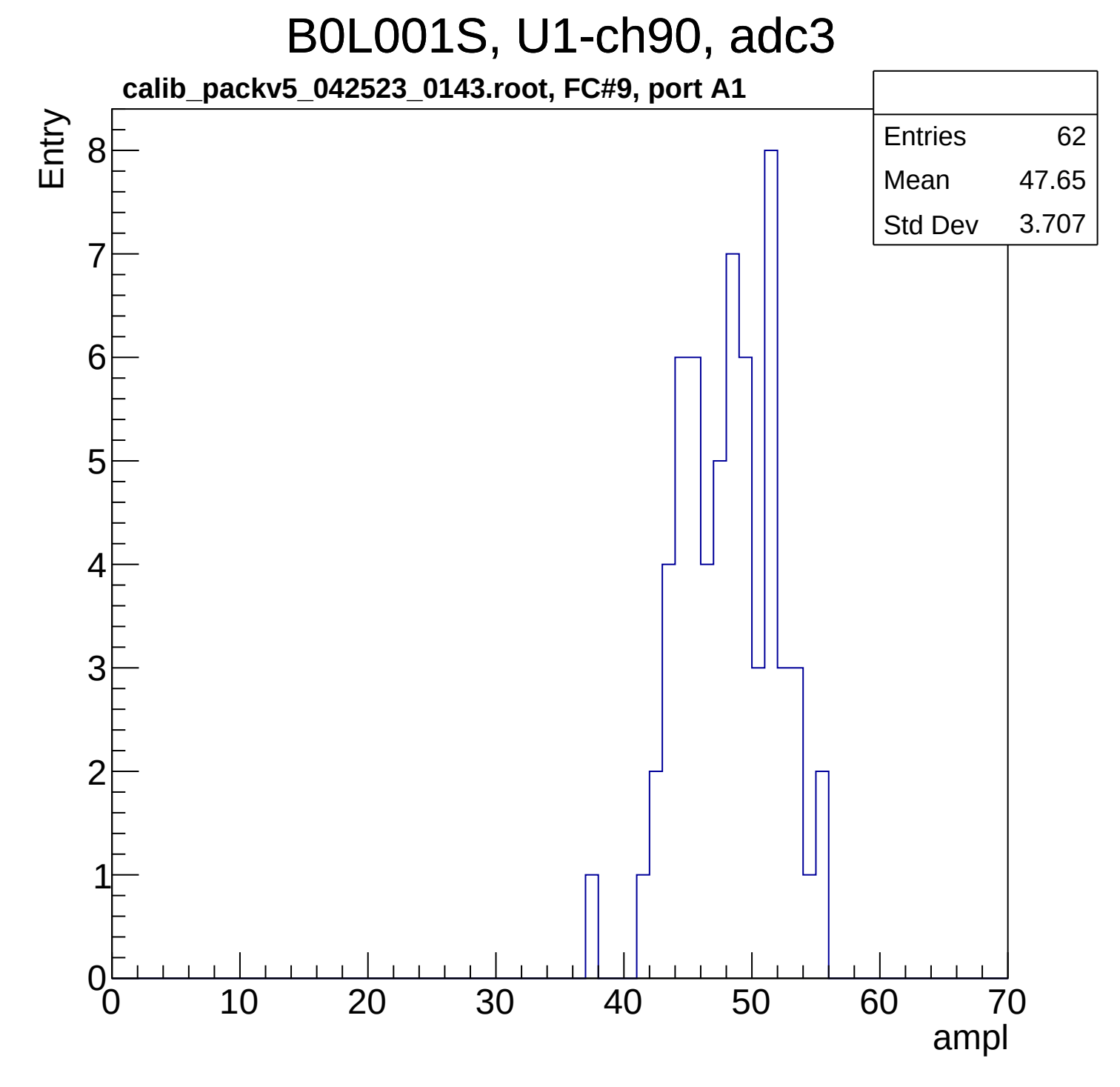

B0L001S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.5
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

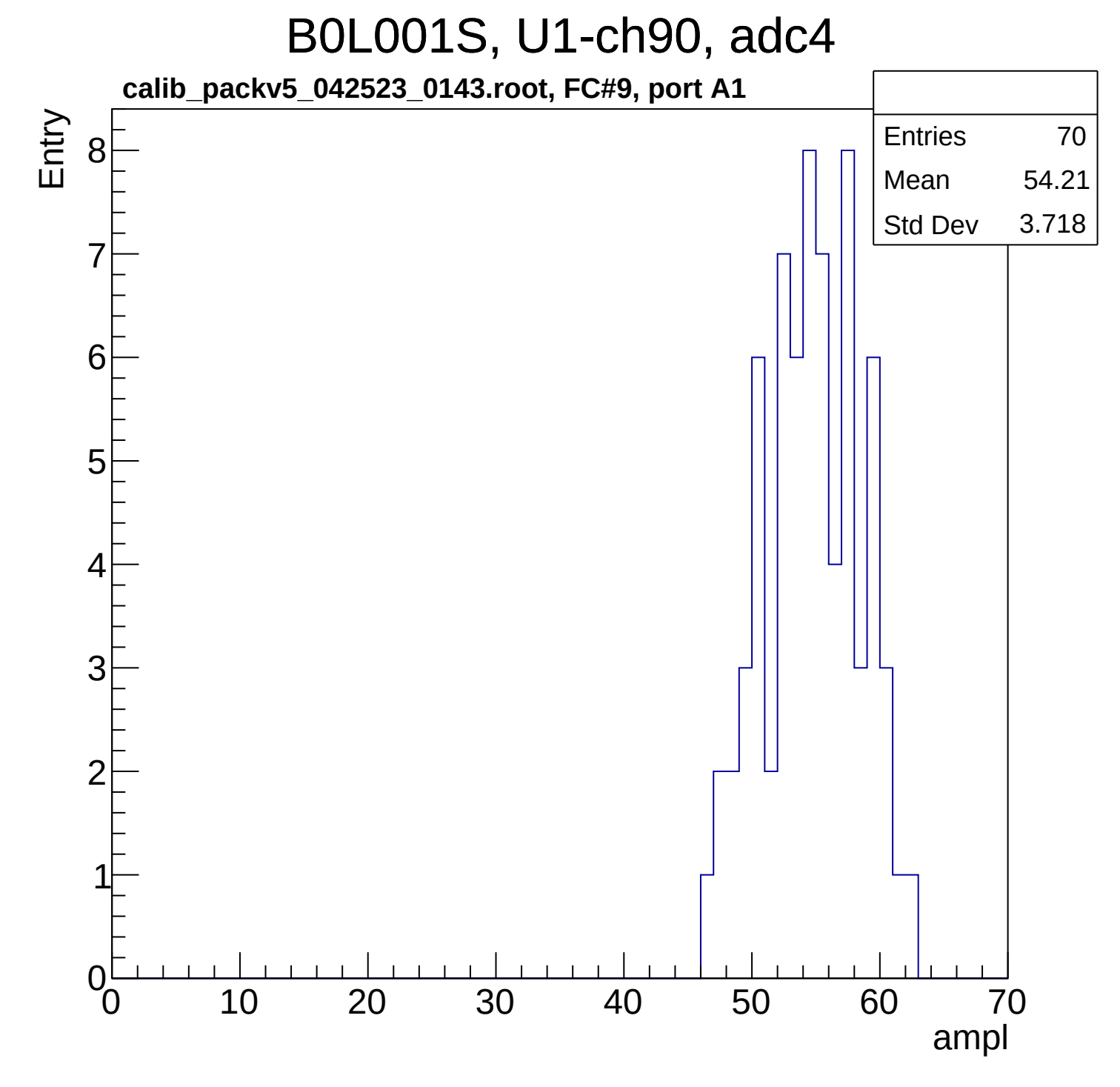

B0L001S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

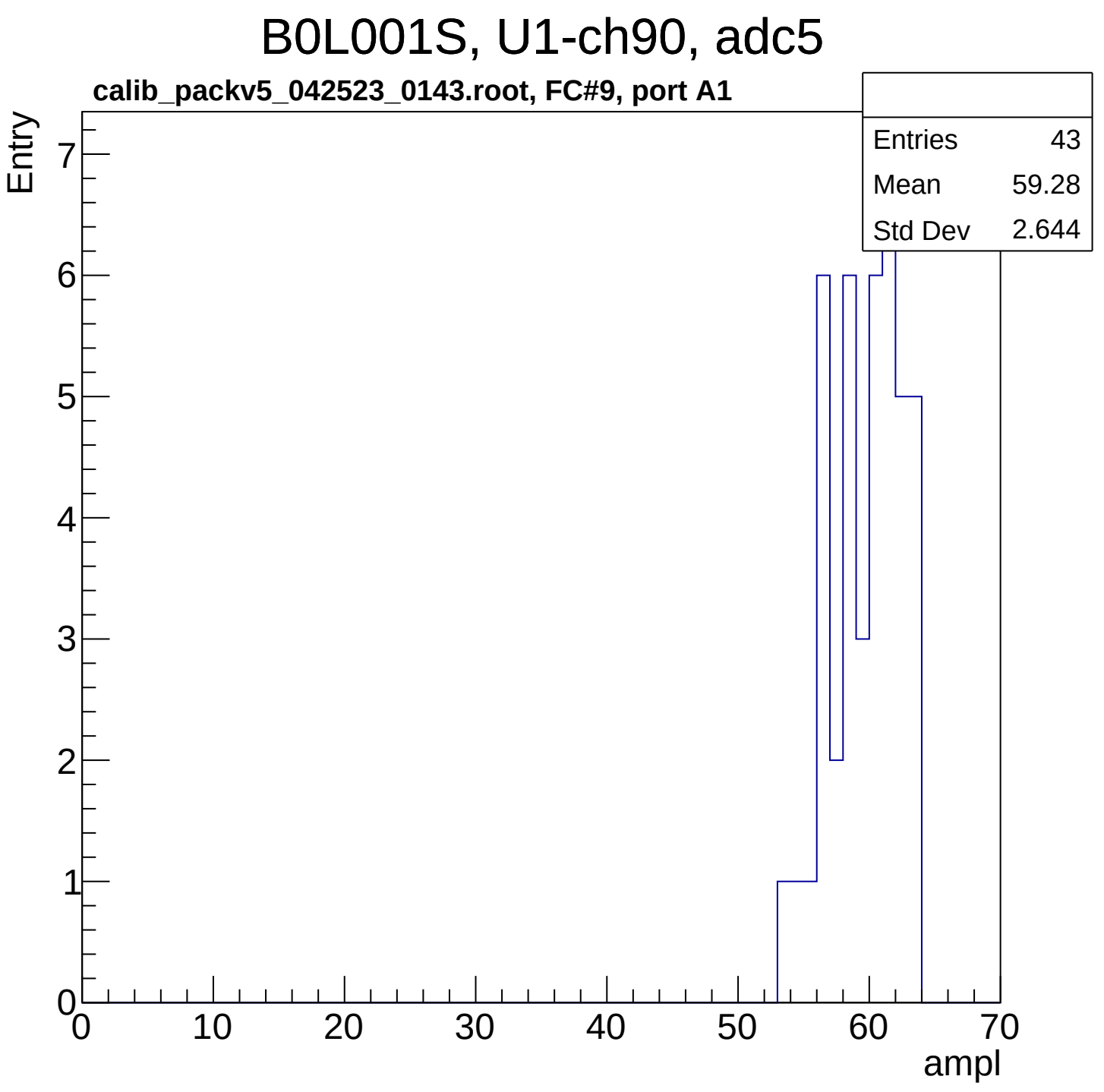

B0L001S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 29.34
Std Dev 4.985
Gaus mean : 30.2859
Gaus Width: 4.0536
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

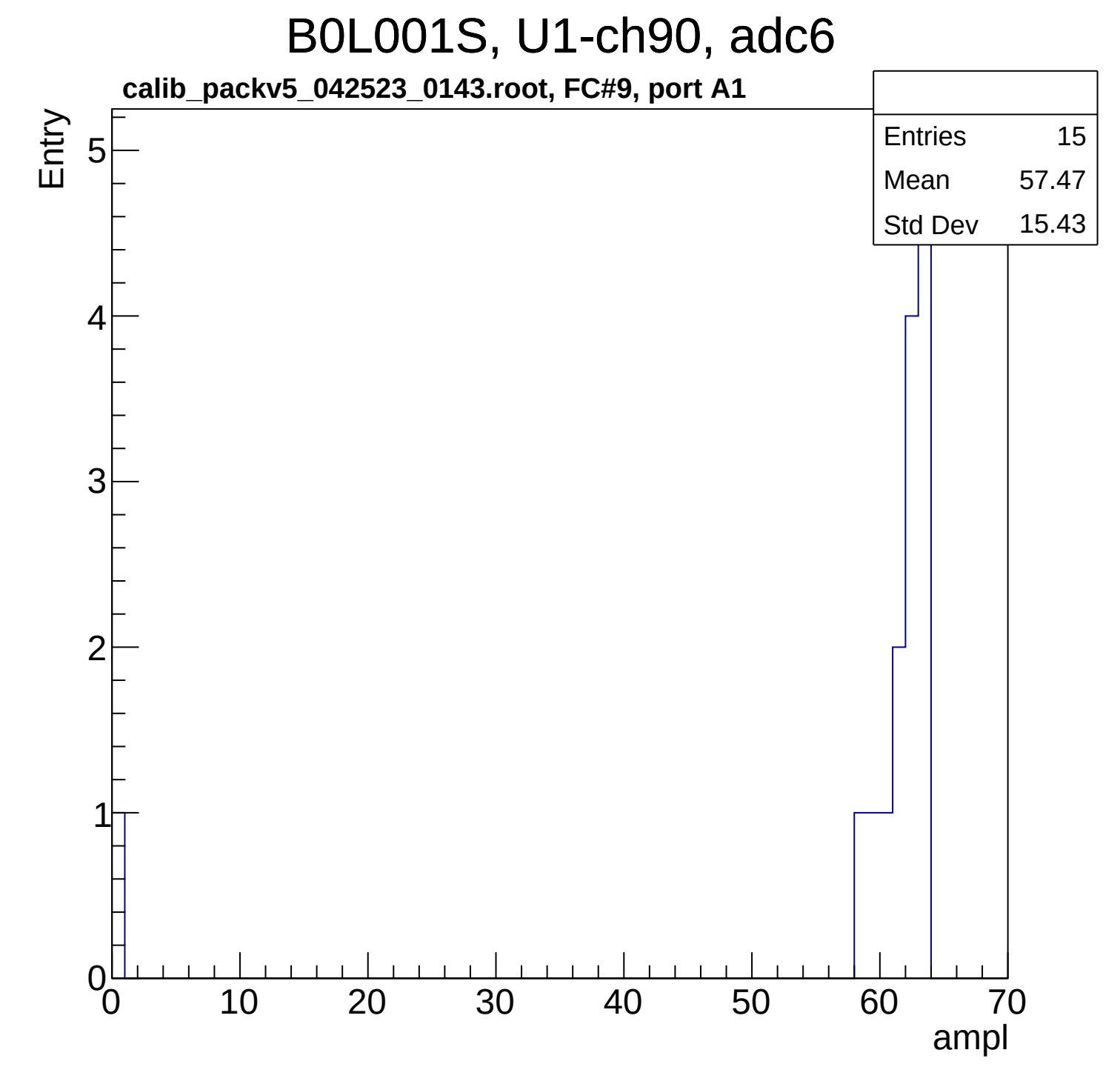

B0L001S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.06
Std Dev 3.893
Gaus mean : 36.6448
Gaus Width: 4.3972
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

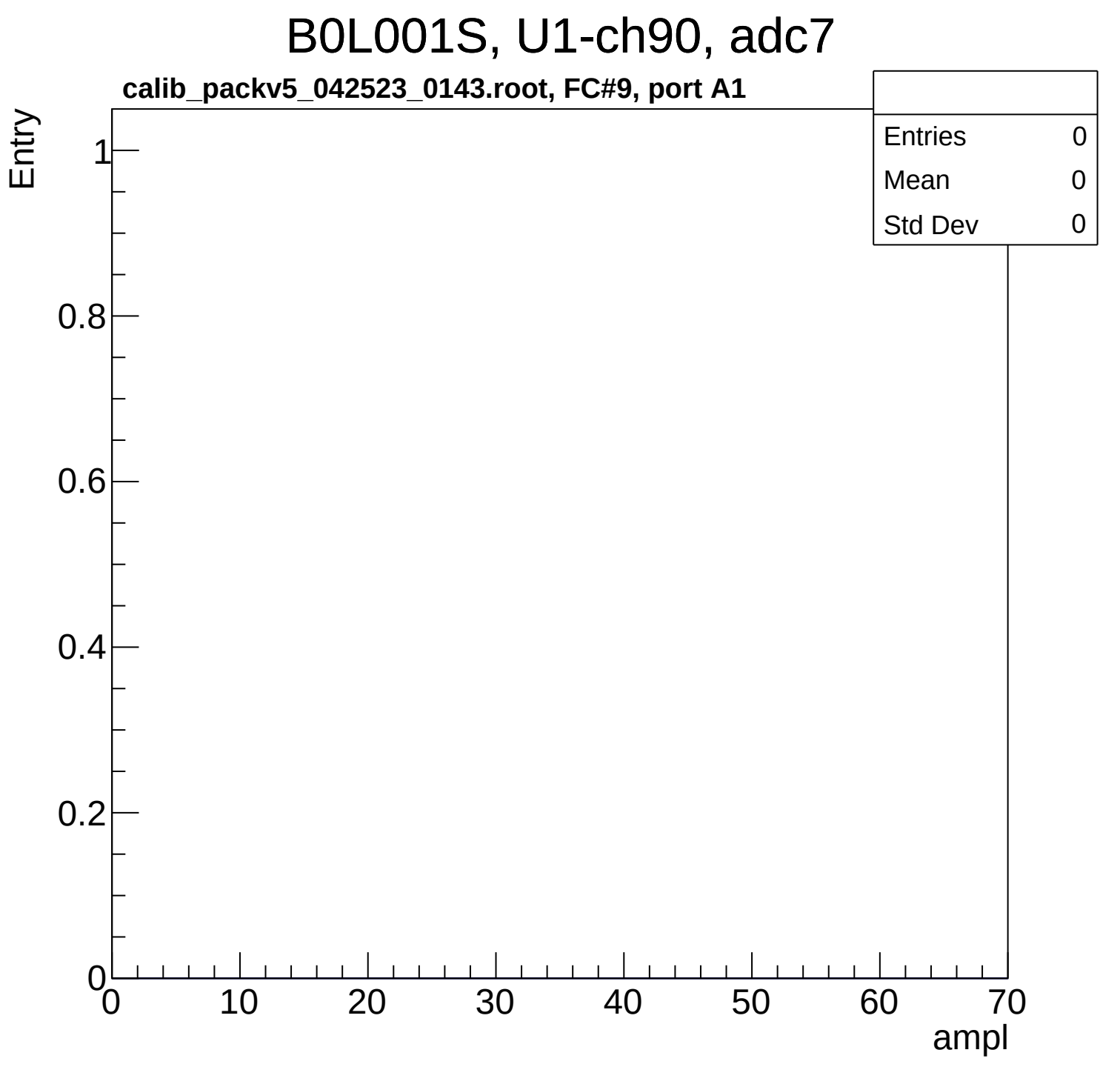

B0L001S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 42.46
Std Dev 3.44
Gaus mean : 42.7638
Gaus Width: 4.0346
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

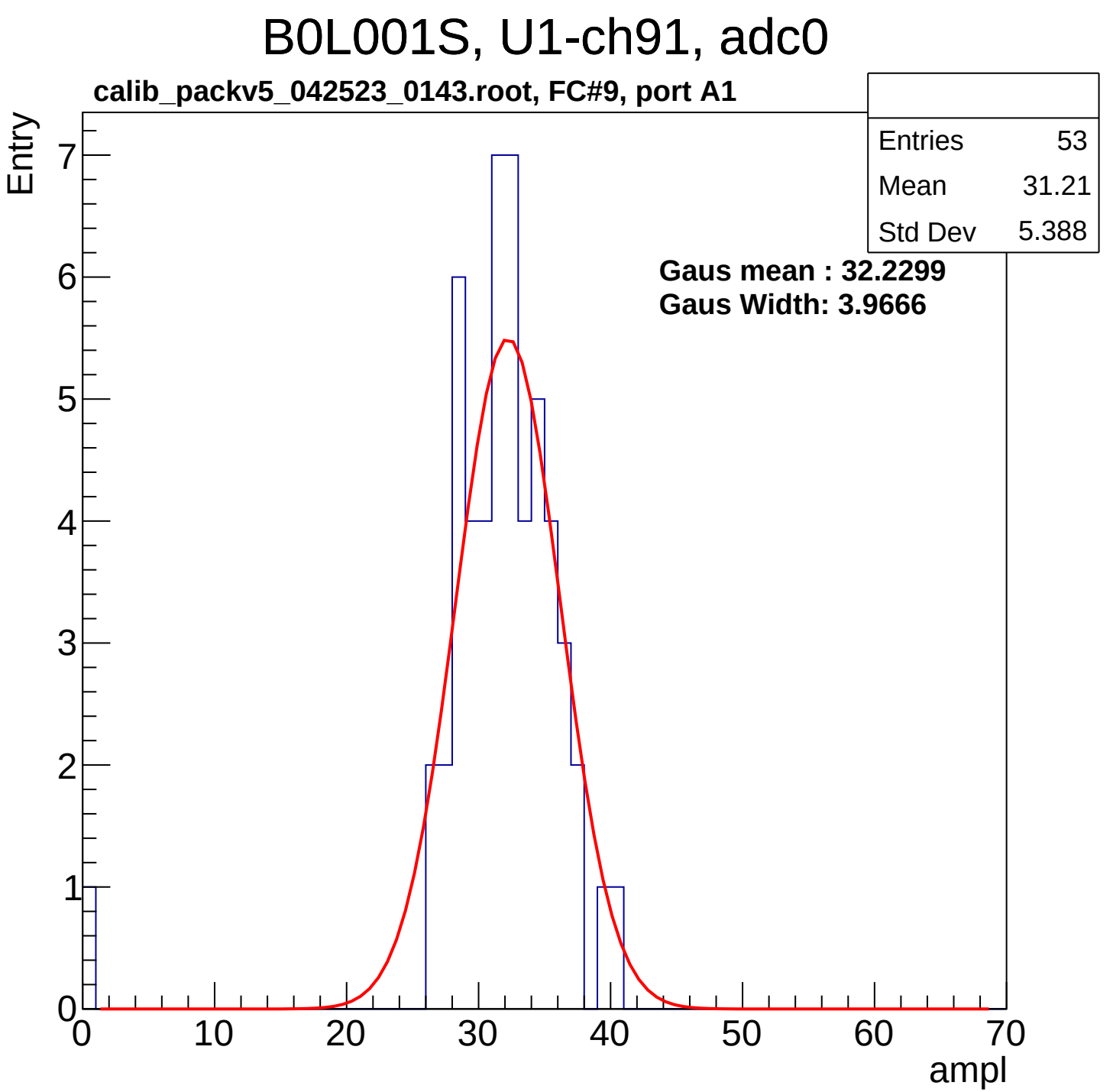

B0L001S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 47.65
Std Dev 3.707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

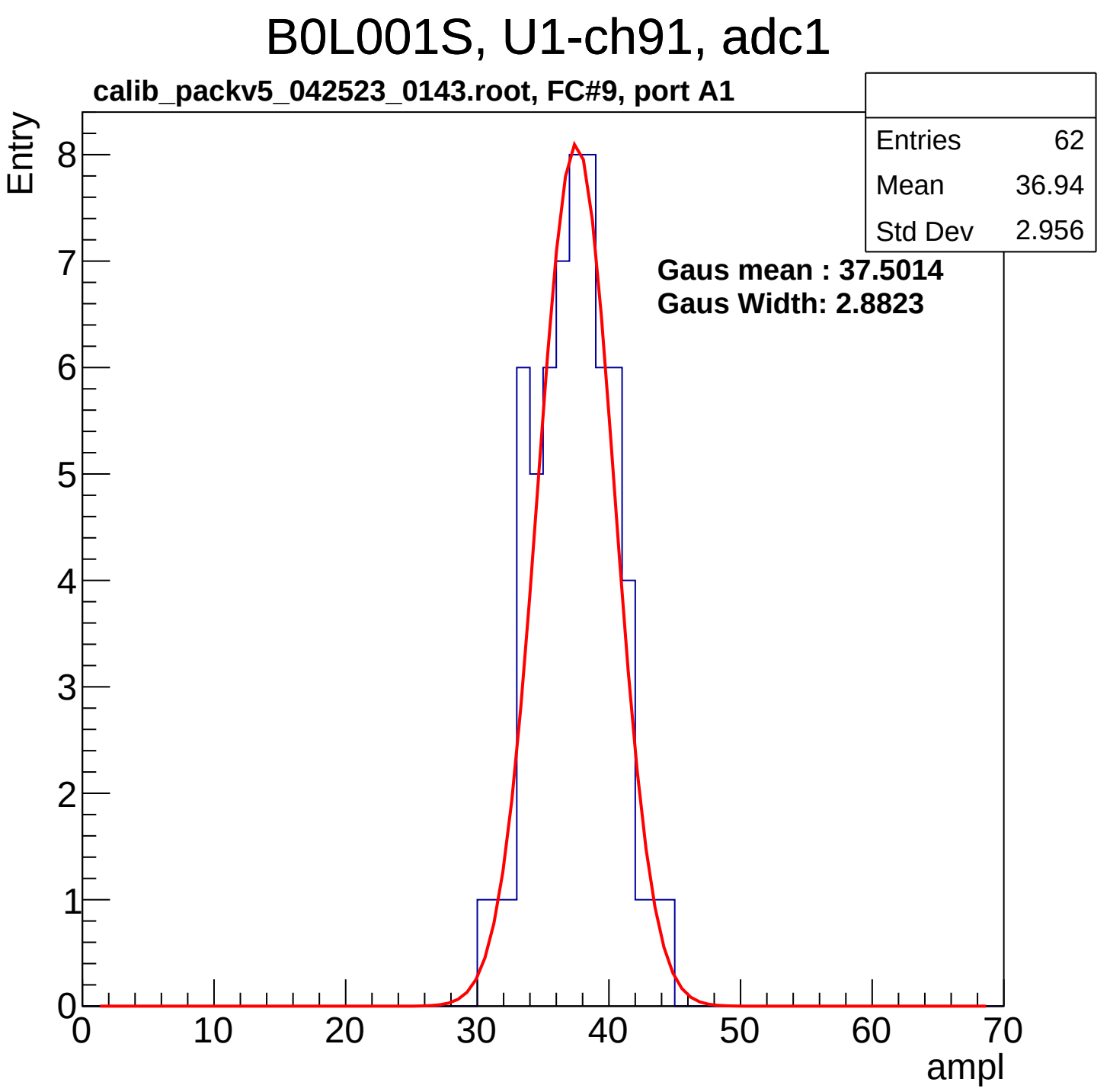

B0L001S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.21
Std Dev 3.718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

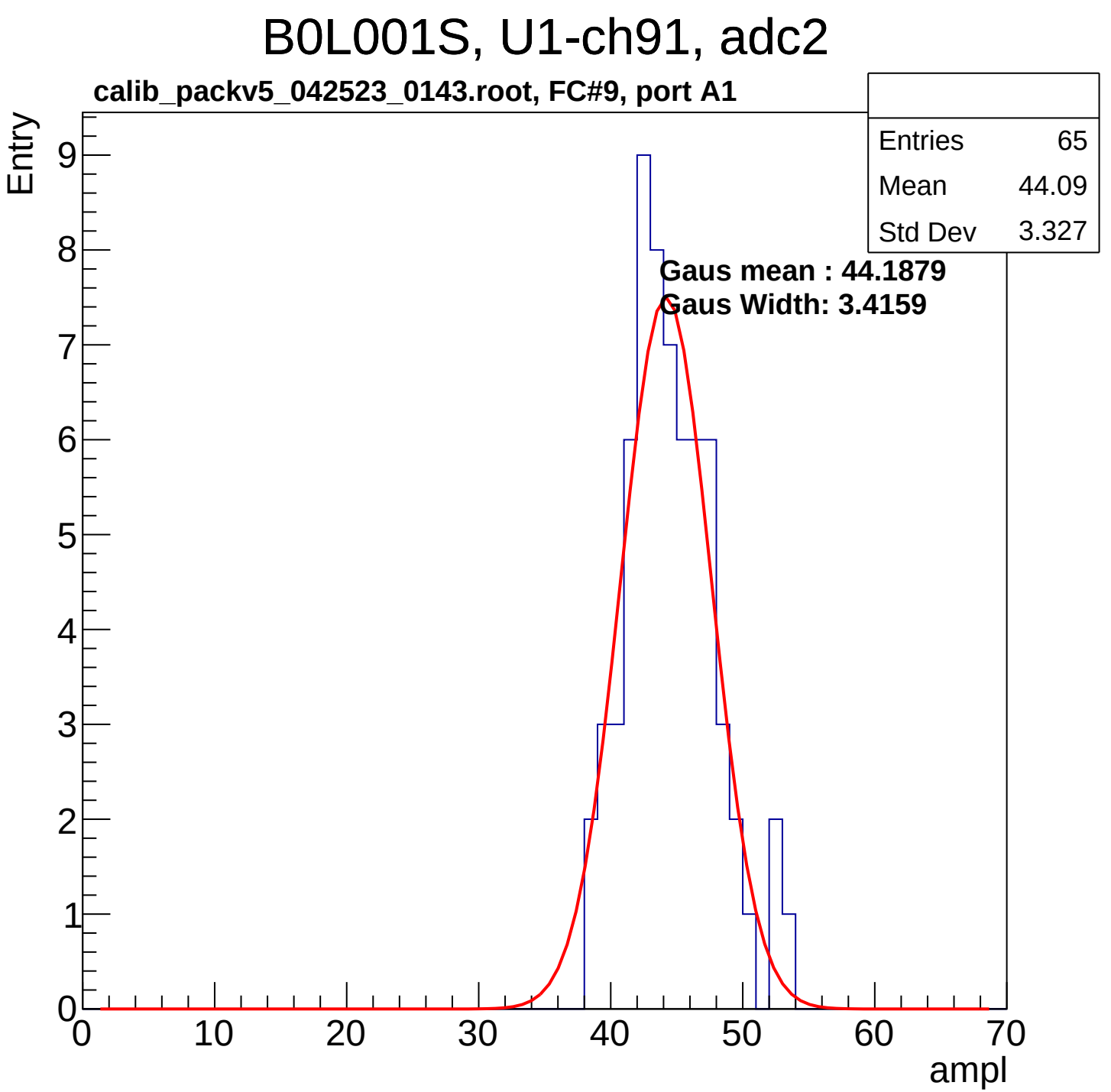

B0L001S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 59.28
Std Dev 2.644
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

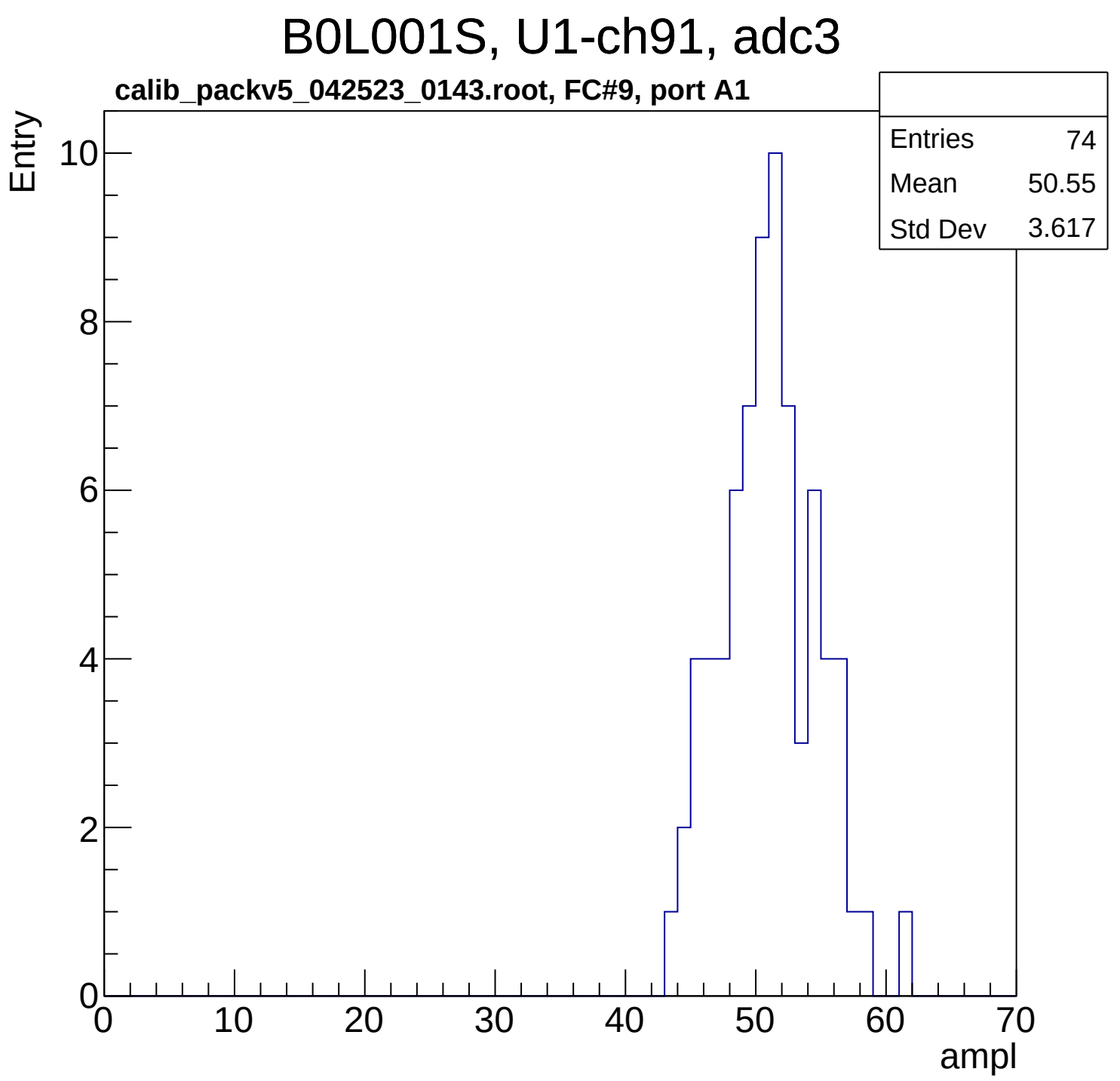

B0L001S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 15
Mean 57.47
Std Dev 15.43
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

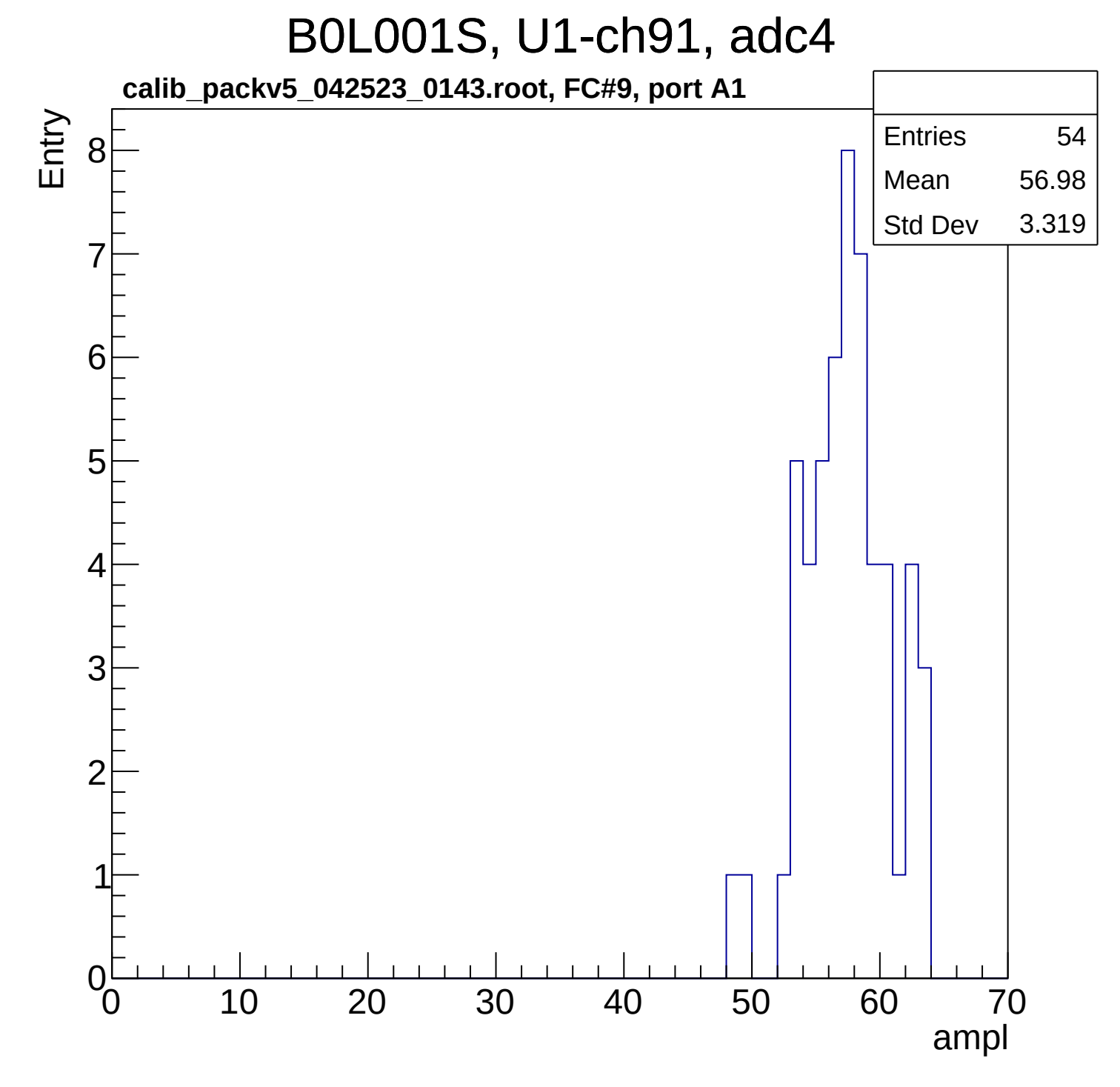

B0L001S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

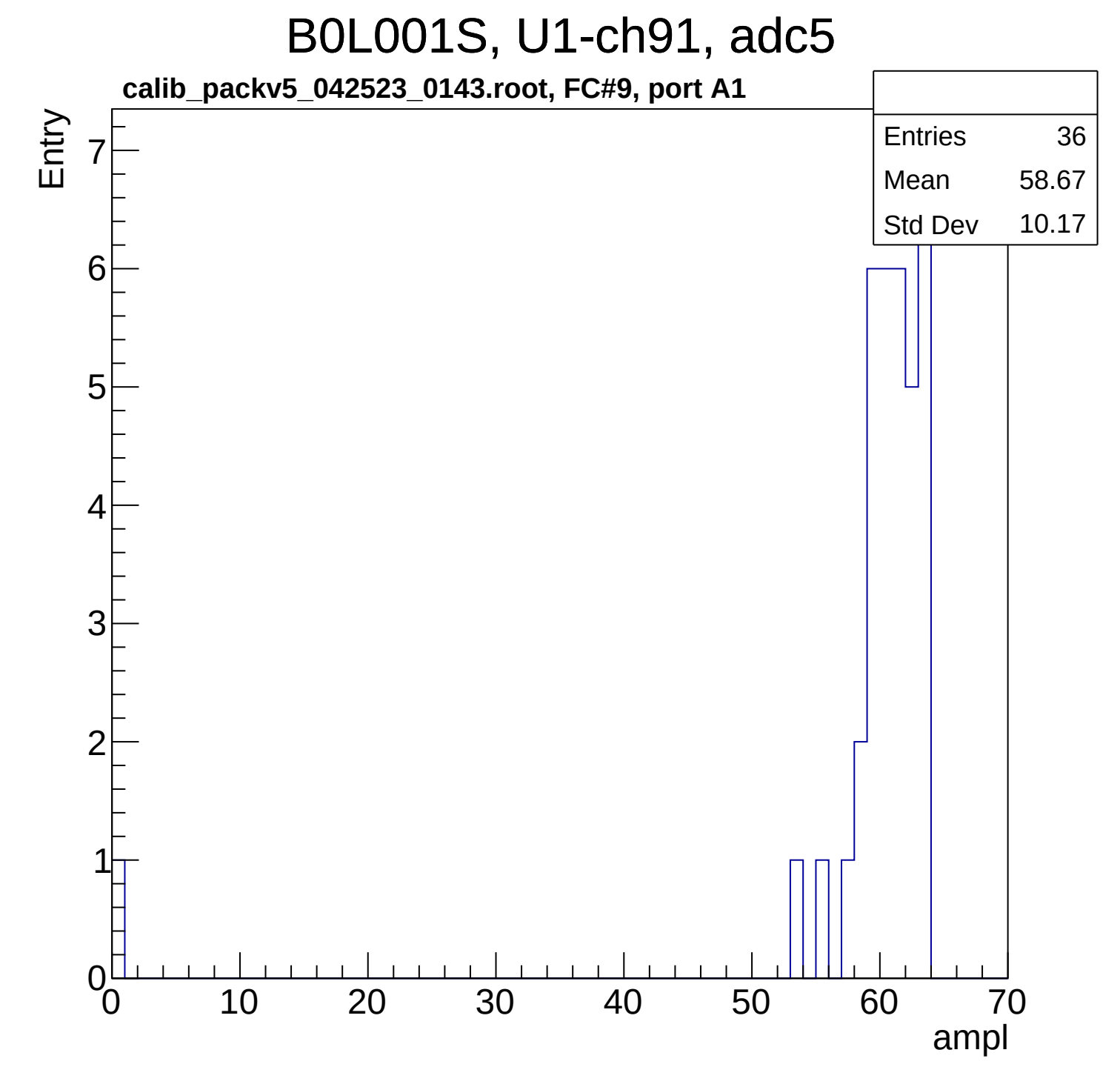

B0L001S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 31.21
Std Dev 5.388
Gaus mean : 32.2299
Gaus Width: 3.9666
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

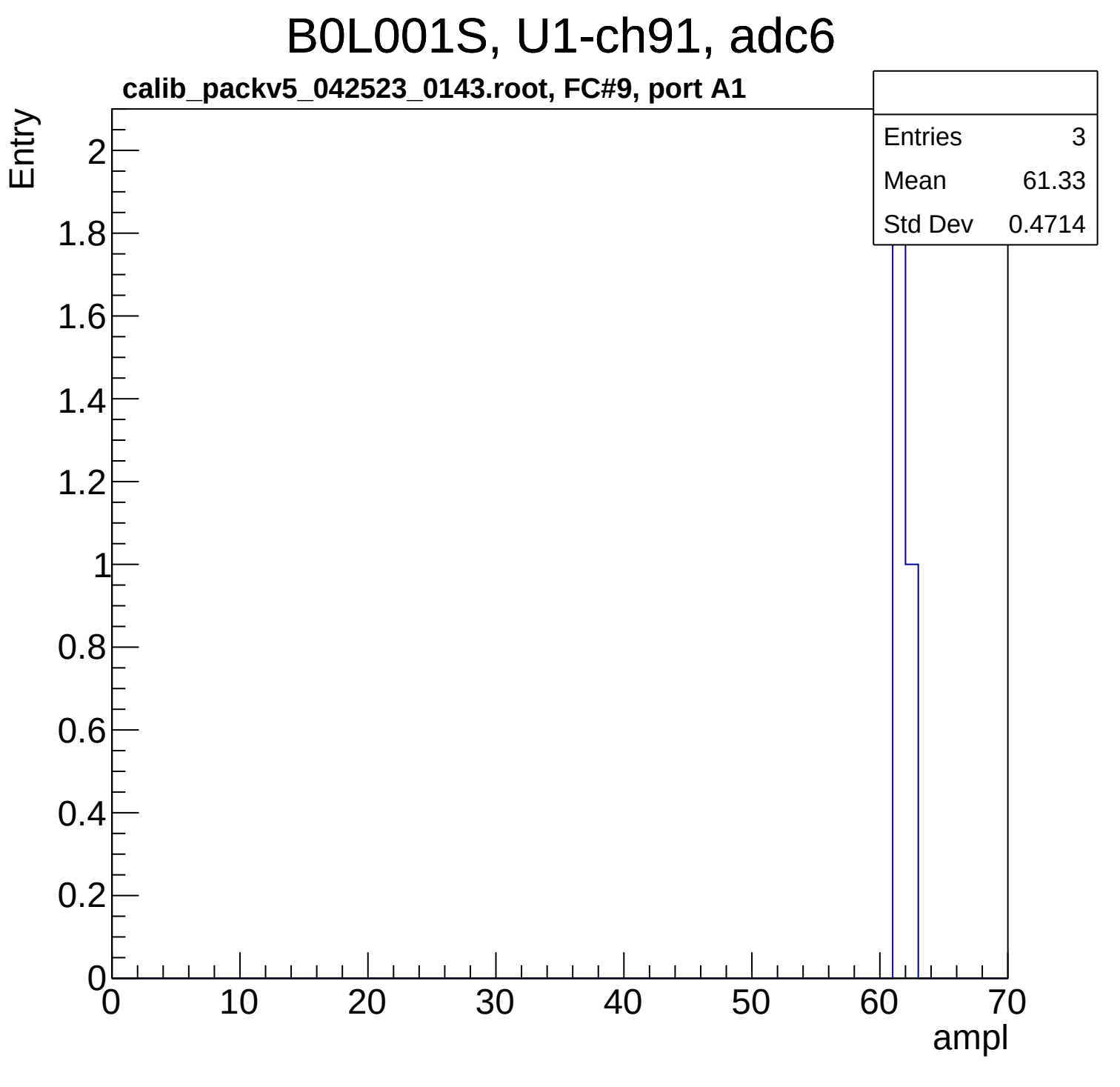

B0L001S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 36.94
Std Dev 2.956
Gaus mean : 37.5014
Gaus Width: 2.8823
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

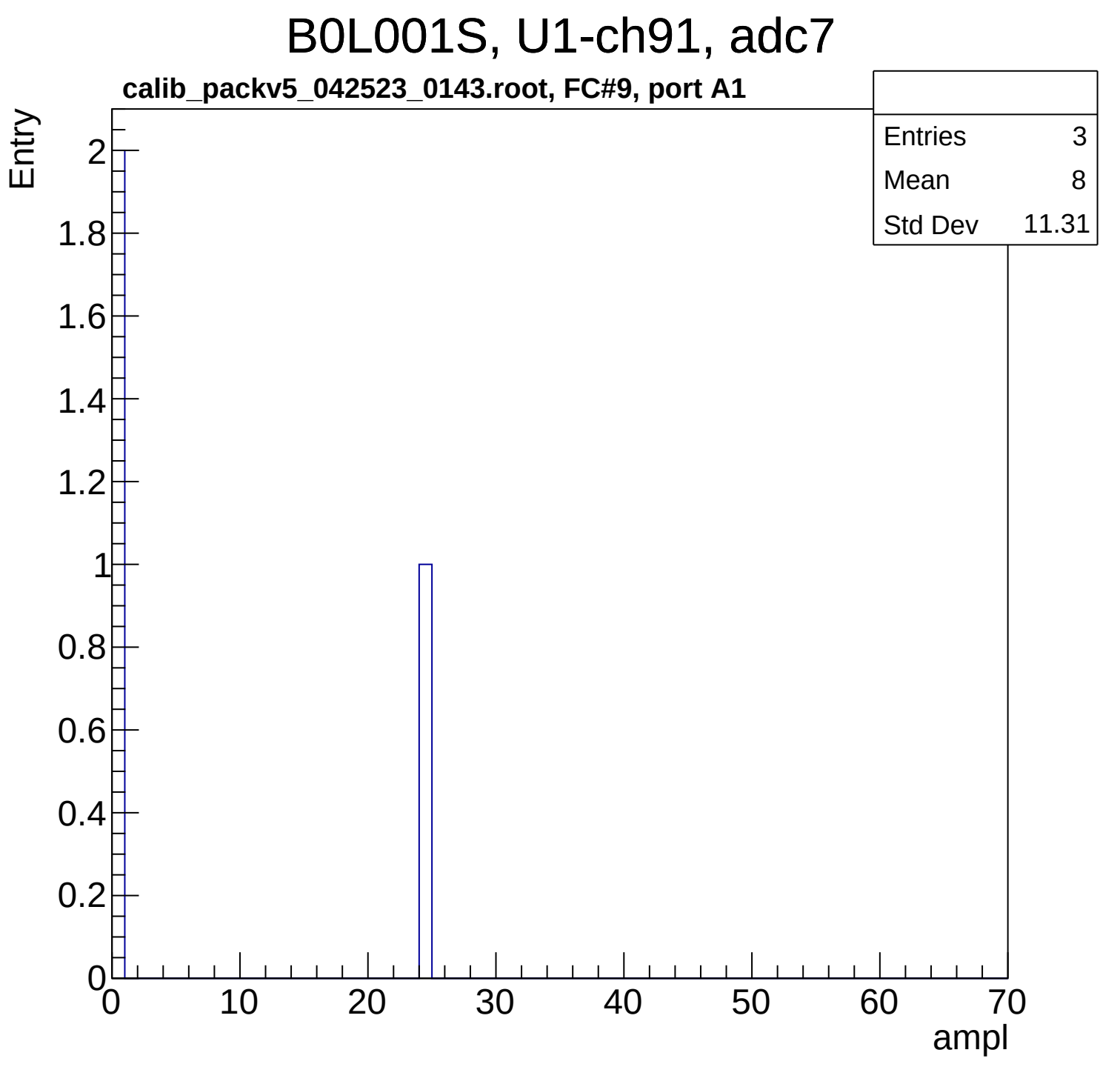

B0L001S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 44.09
Std Dev 3.327
Gaus mean : 44.1879
Gaus Width: 3.4159
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B0L001S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 50.55
Std Dev 3.617
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 56.98
Std Dev 3.319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 58.67
Std Dev 10.17
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 61.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 8
Std Dev 11.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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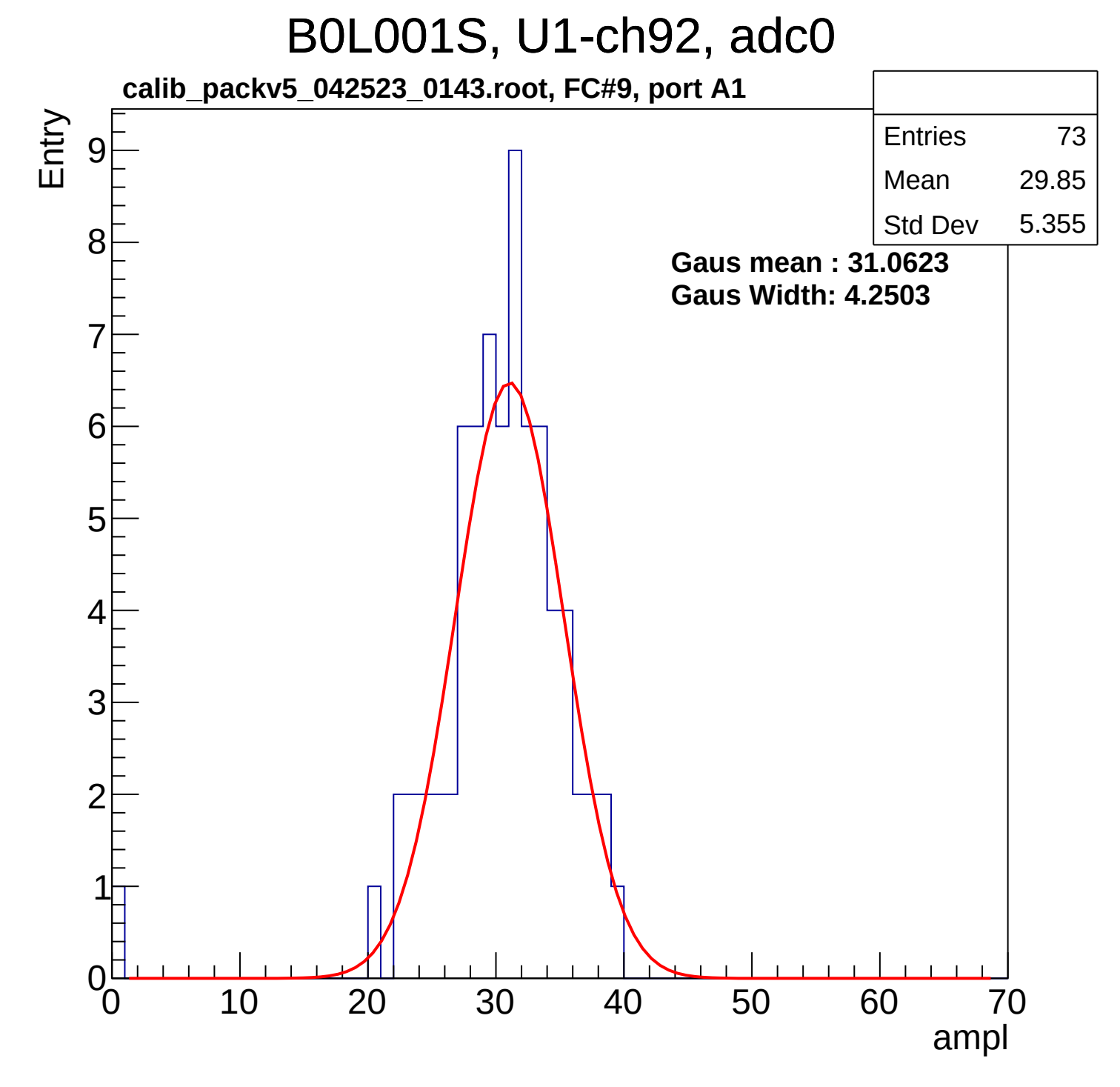
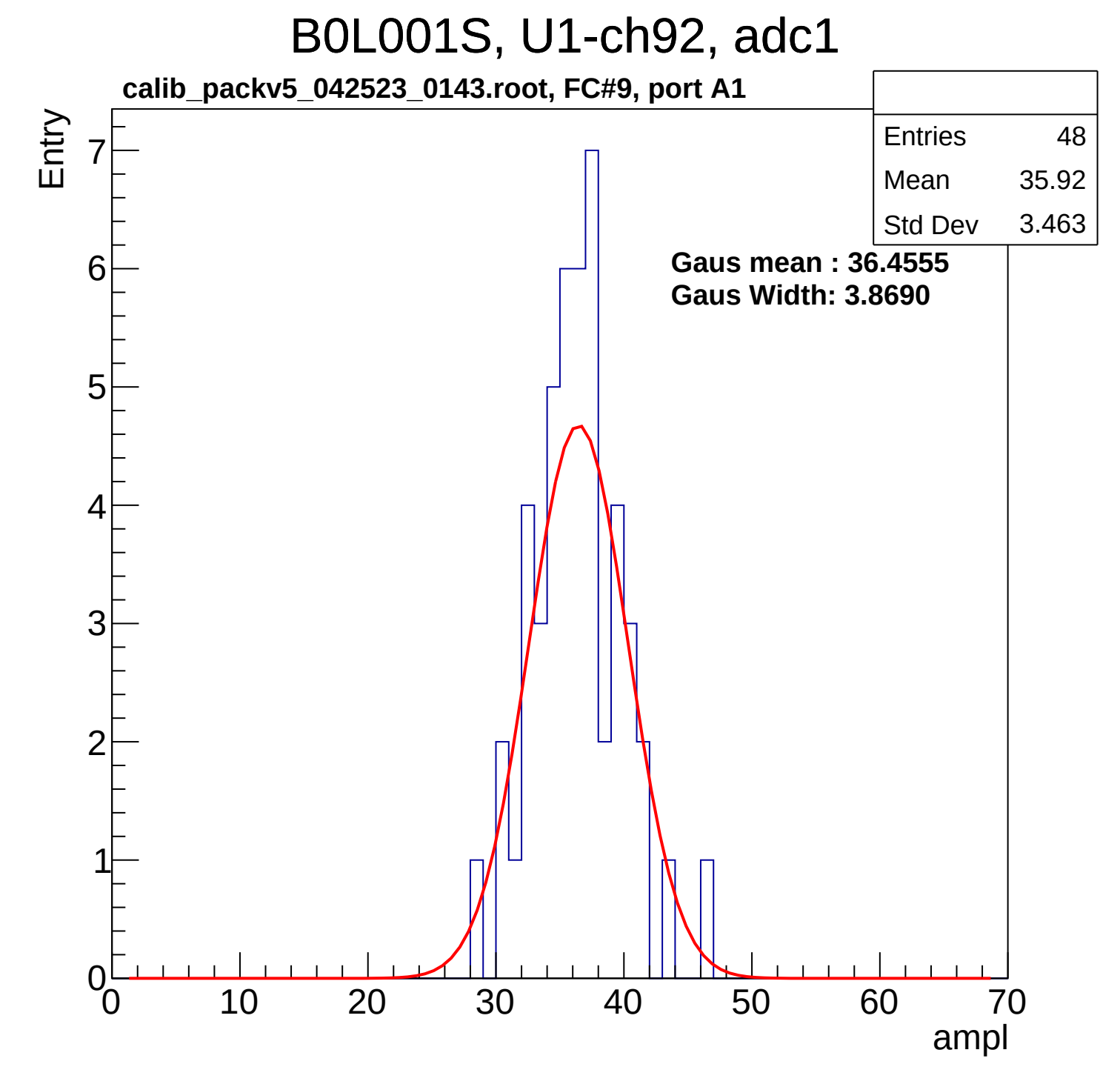
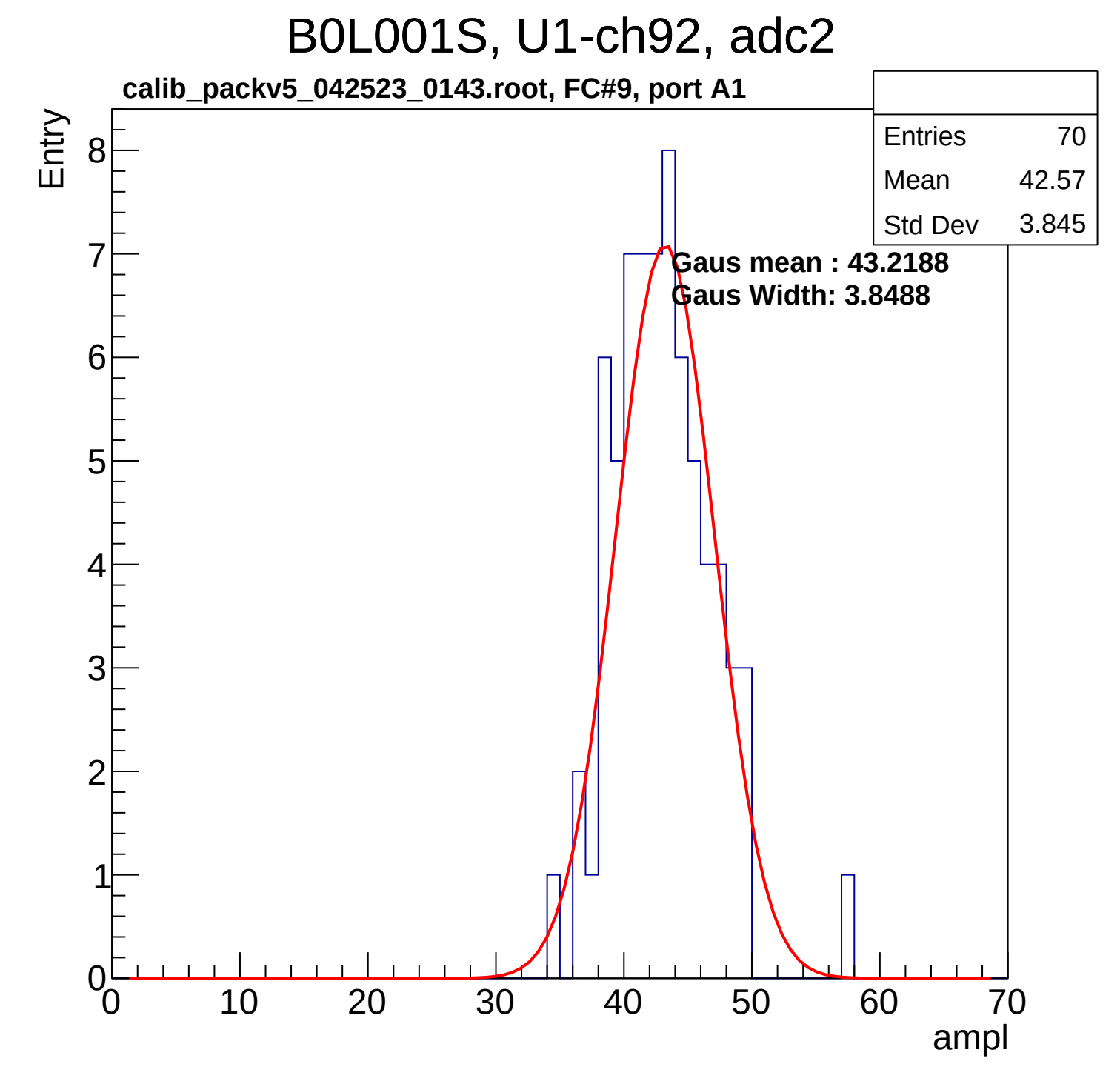
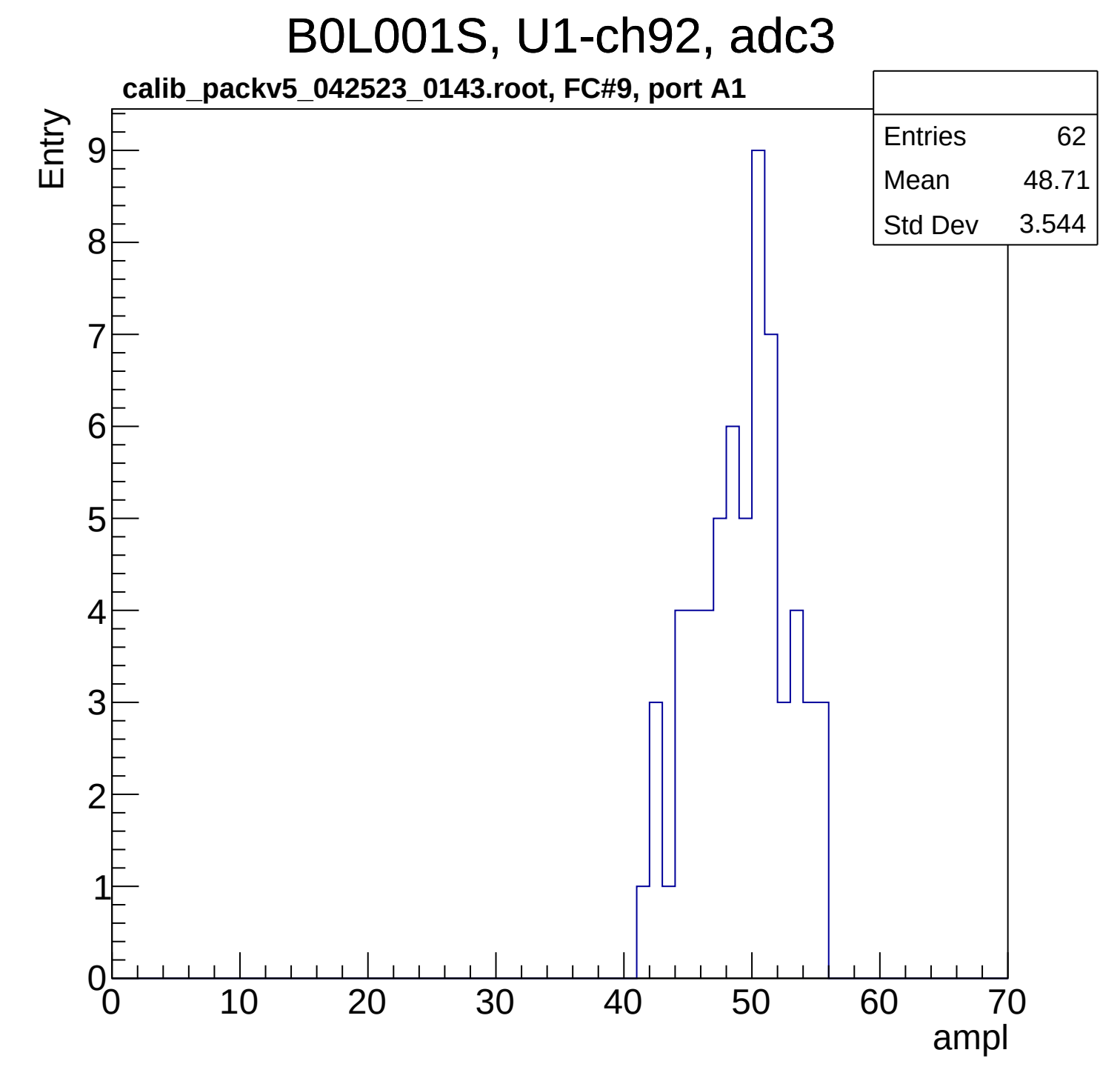

B0L001S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.85
Std Dev 5.355
Gaus mean : 31.0623
Gaus Width: 4.2503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 35.92
Std Dev 3.463
Gaus mean : 36.4555
Gaus Width: 3.8690
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 42.57
Std Dev 3.845
Gaus mean : 43.2188
Gaus Width: 3.8488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 48.71
Std Dev 3.544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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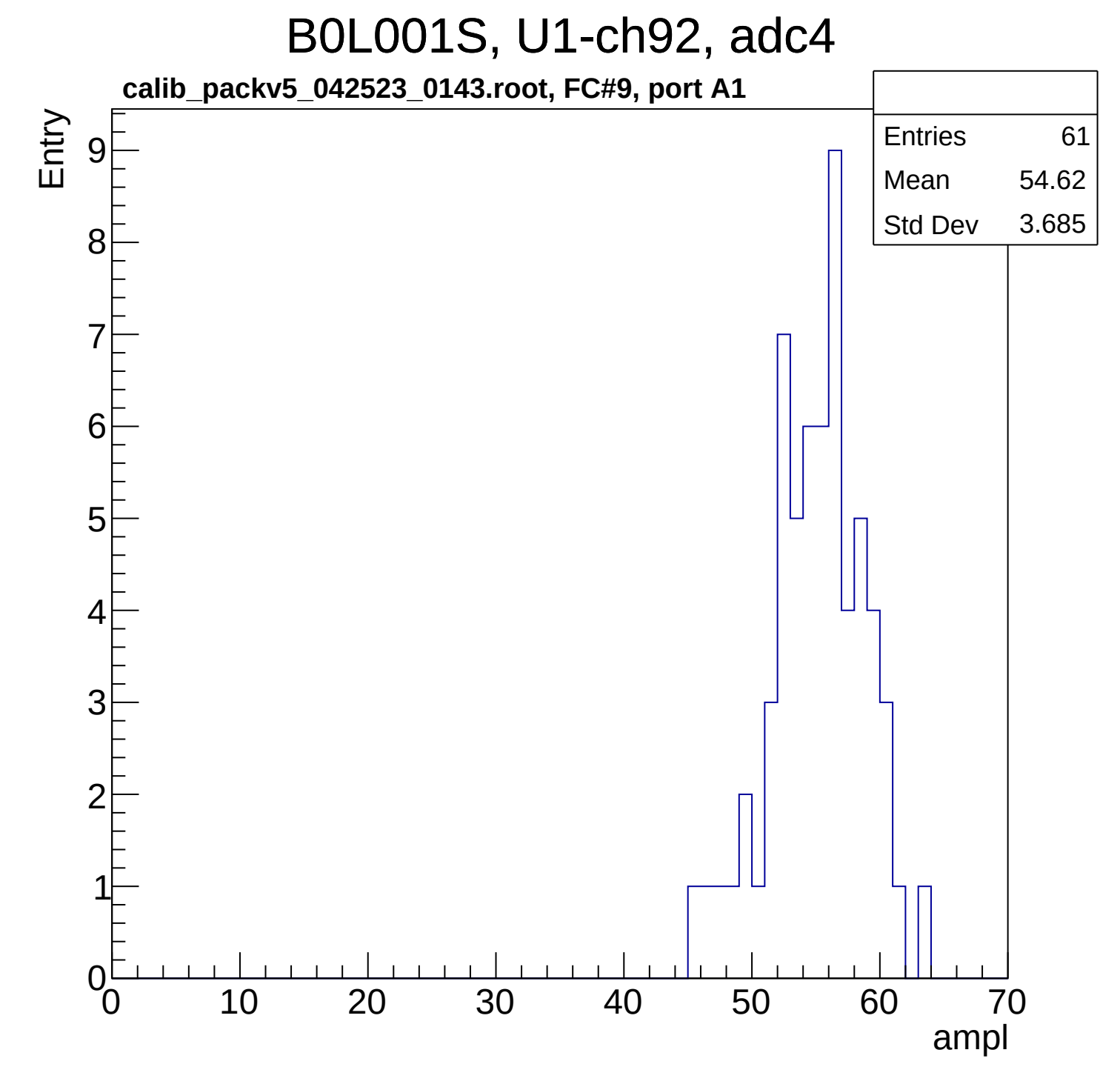
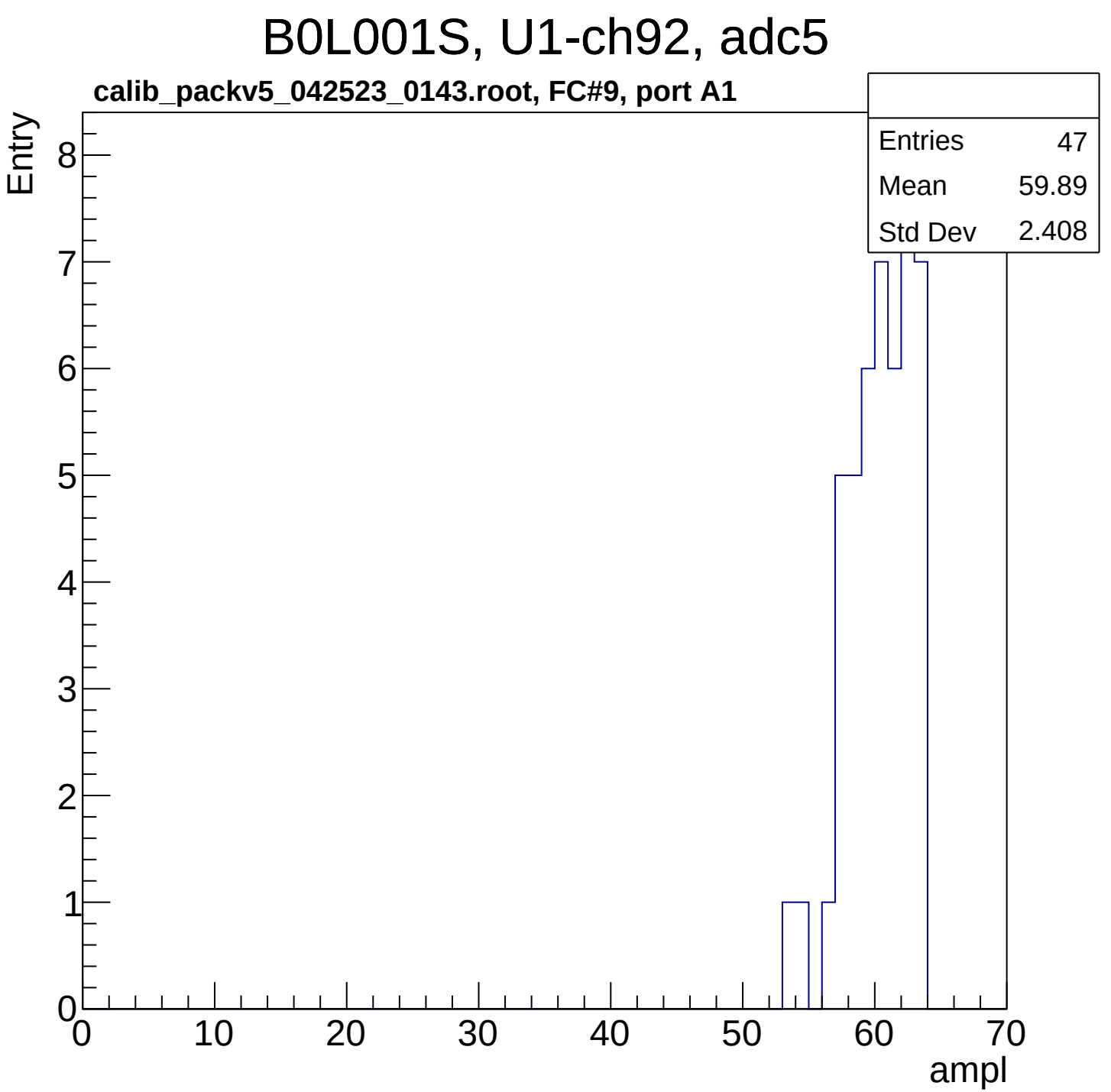
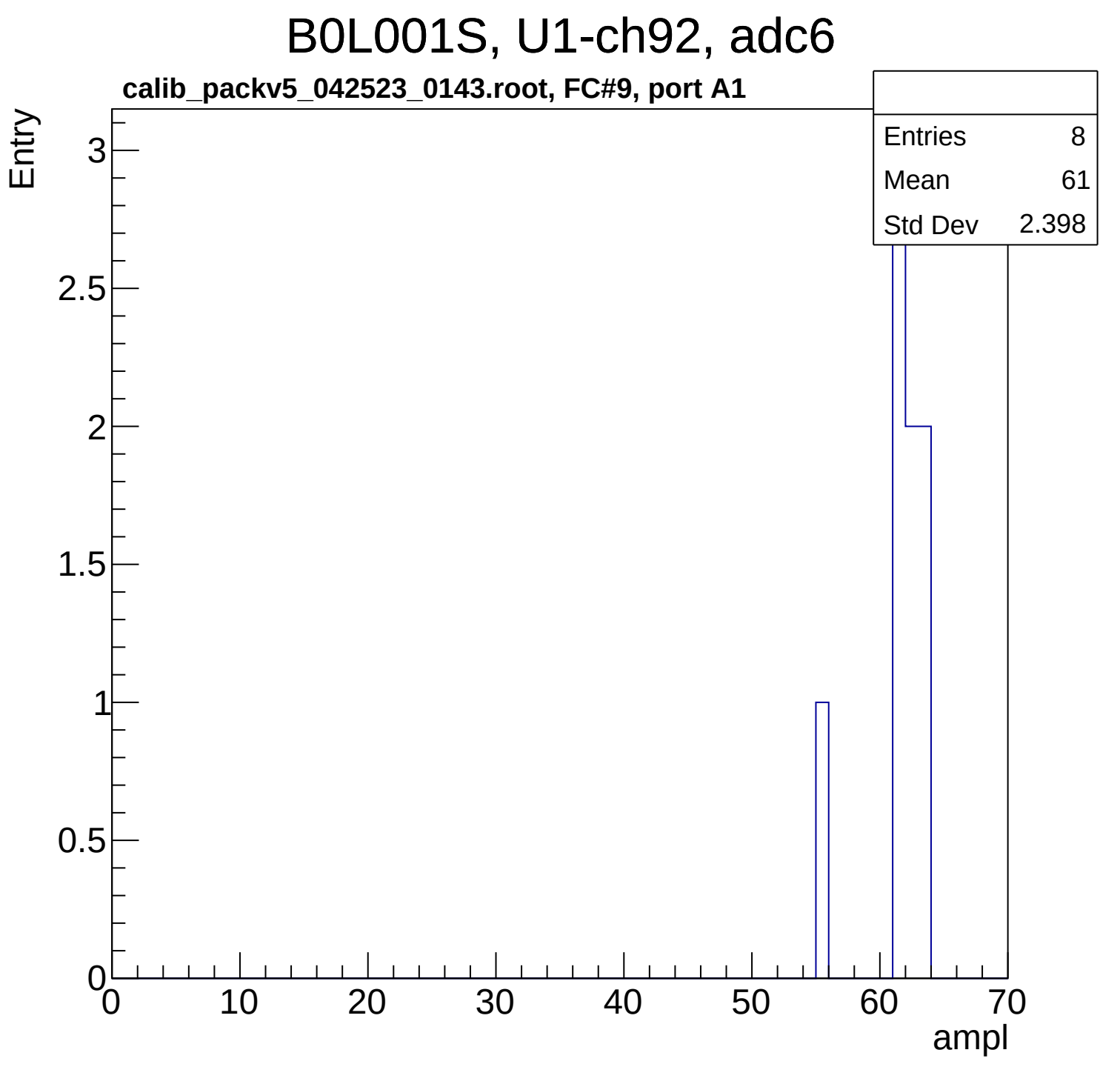
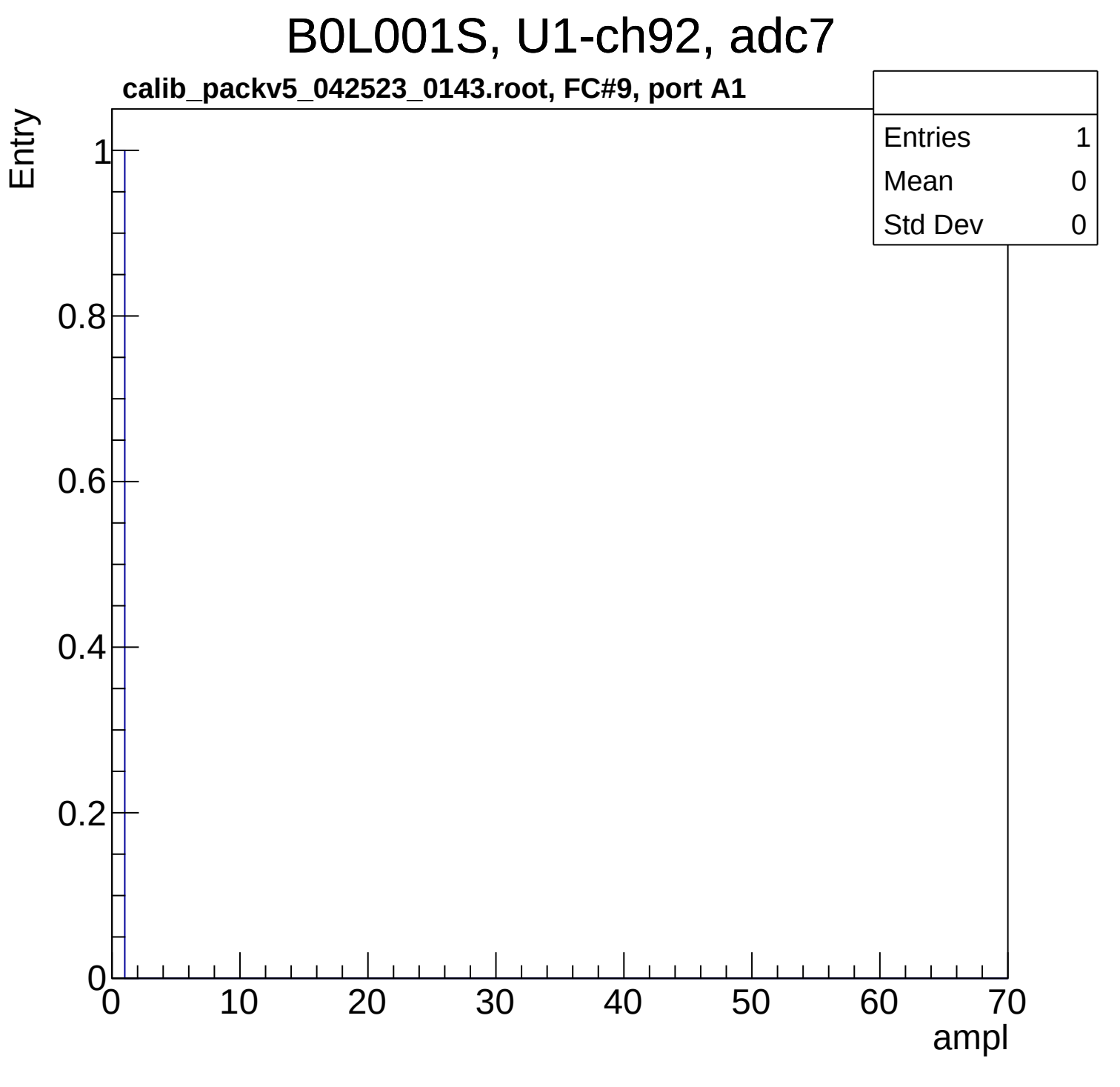
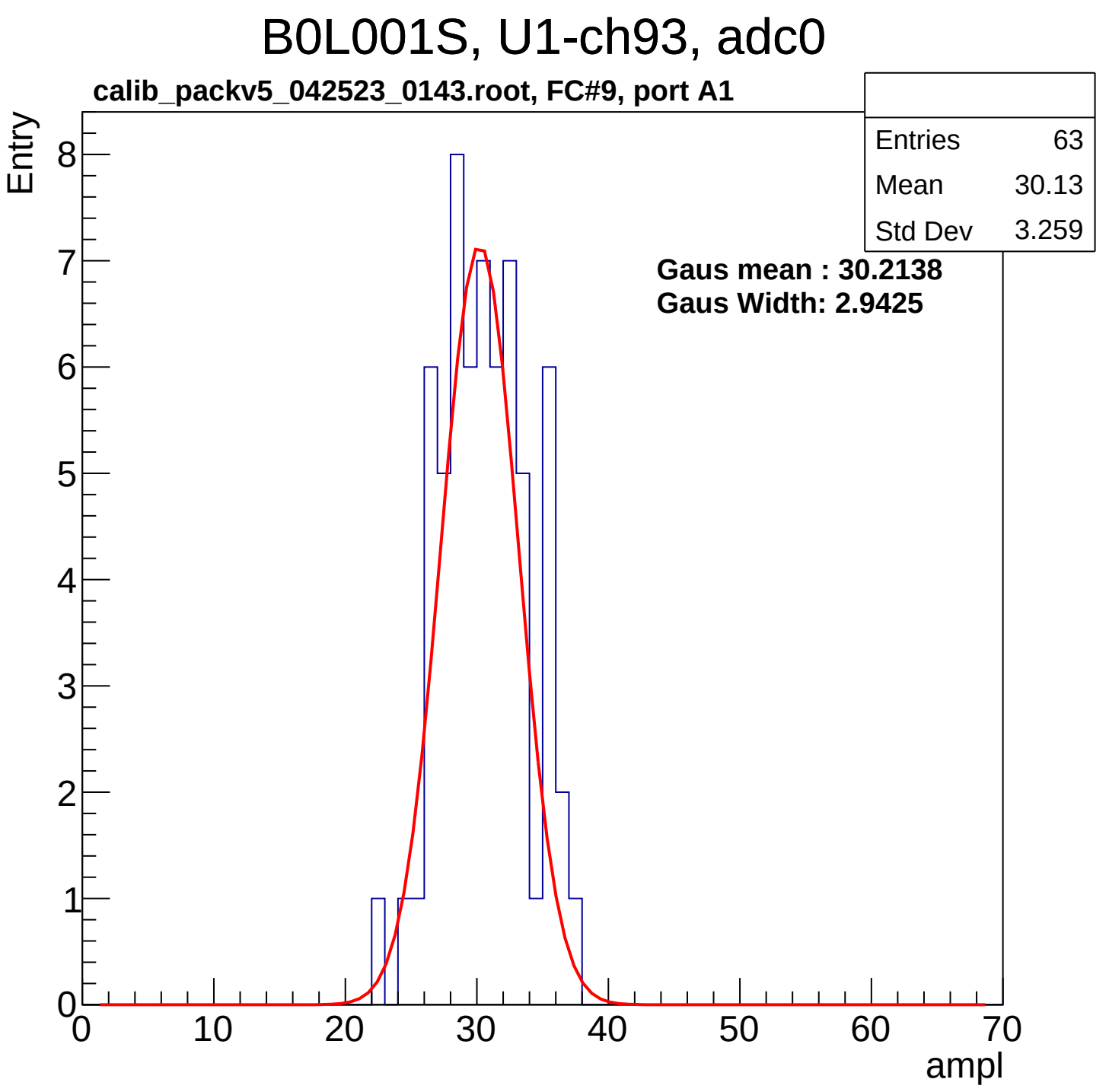
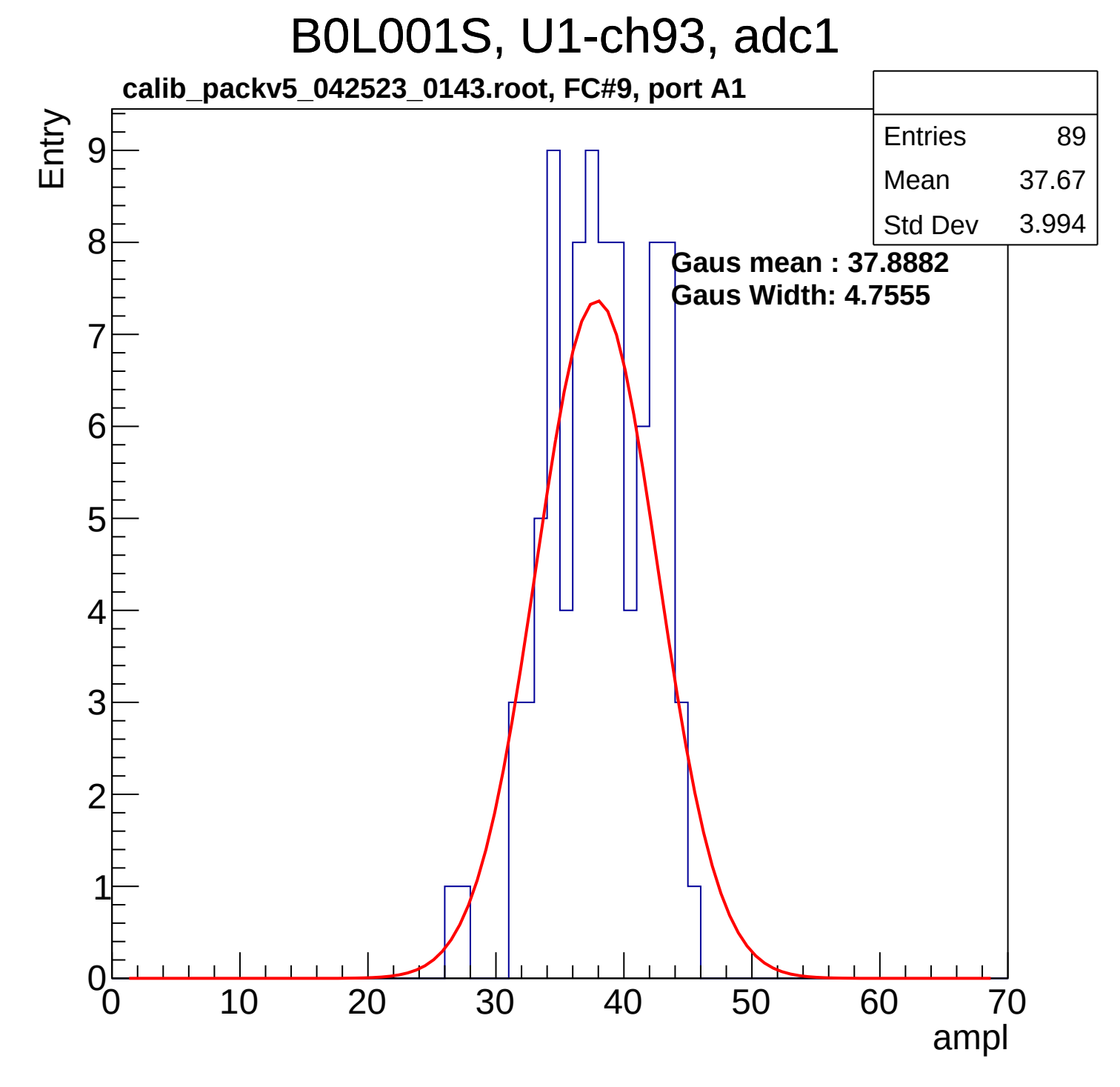

B0L001S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 54.62
Std Dev 3.685
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

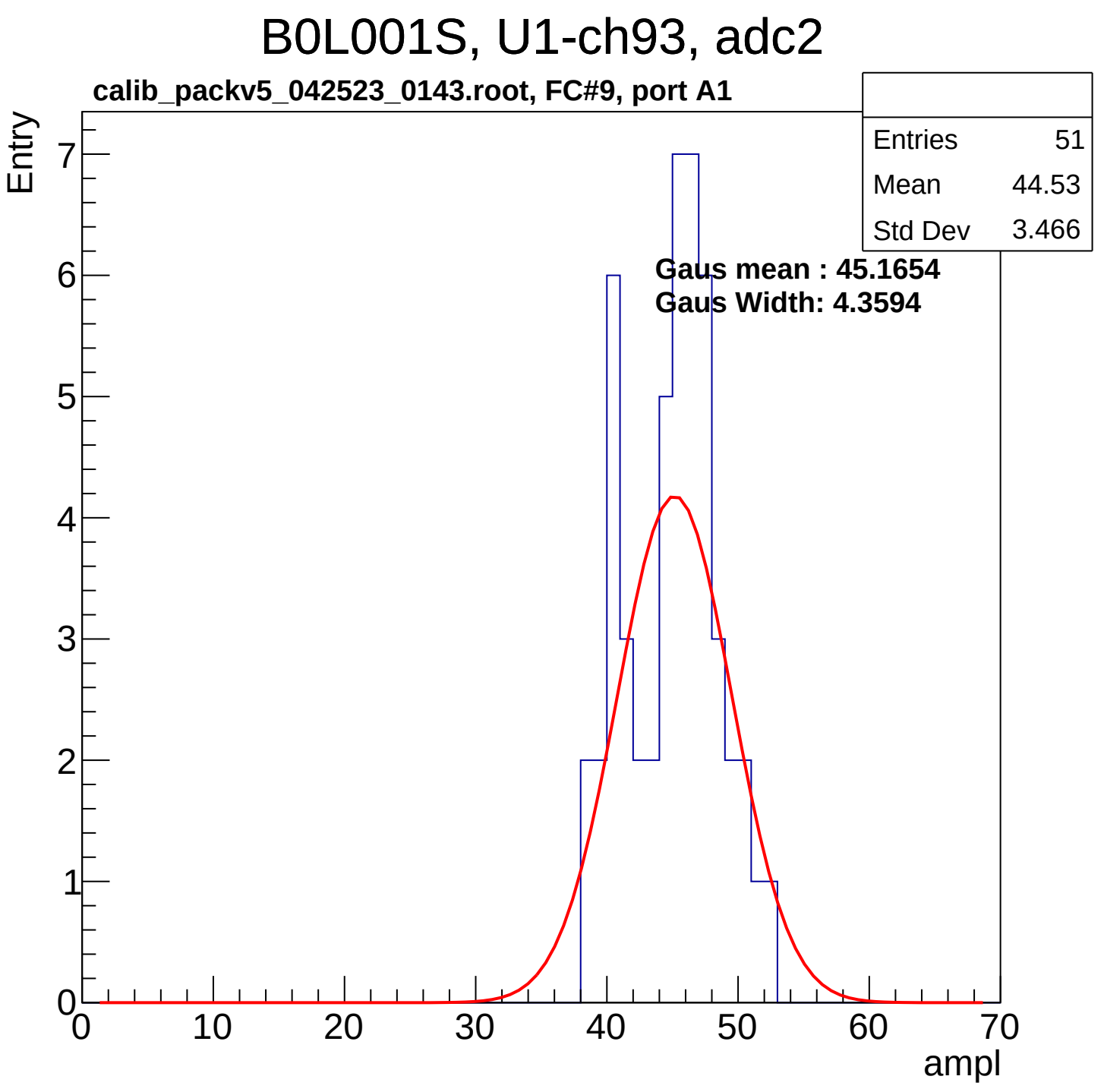

B0L001S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 59.89
Std Dev 2.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

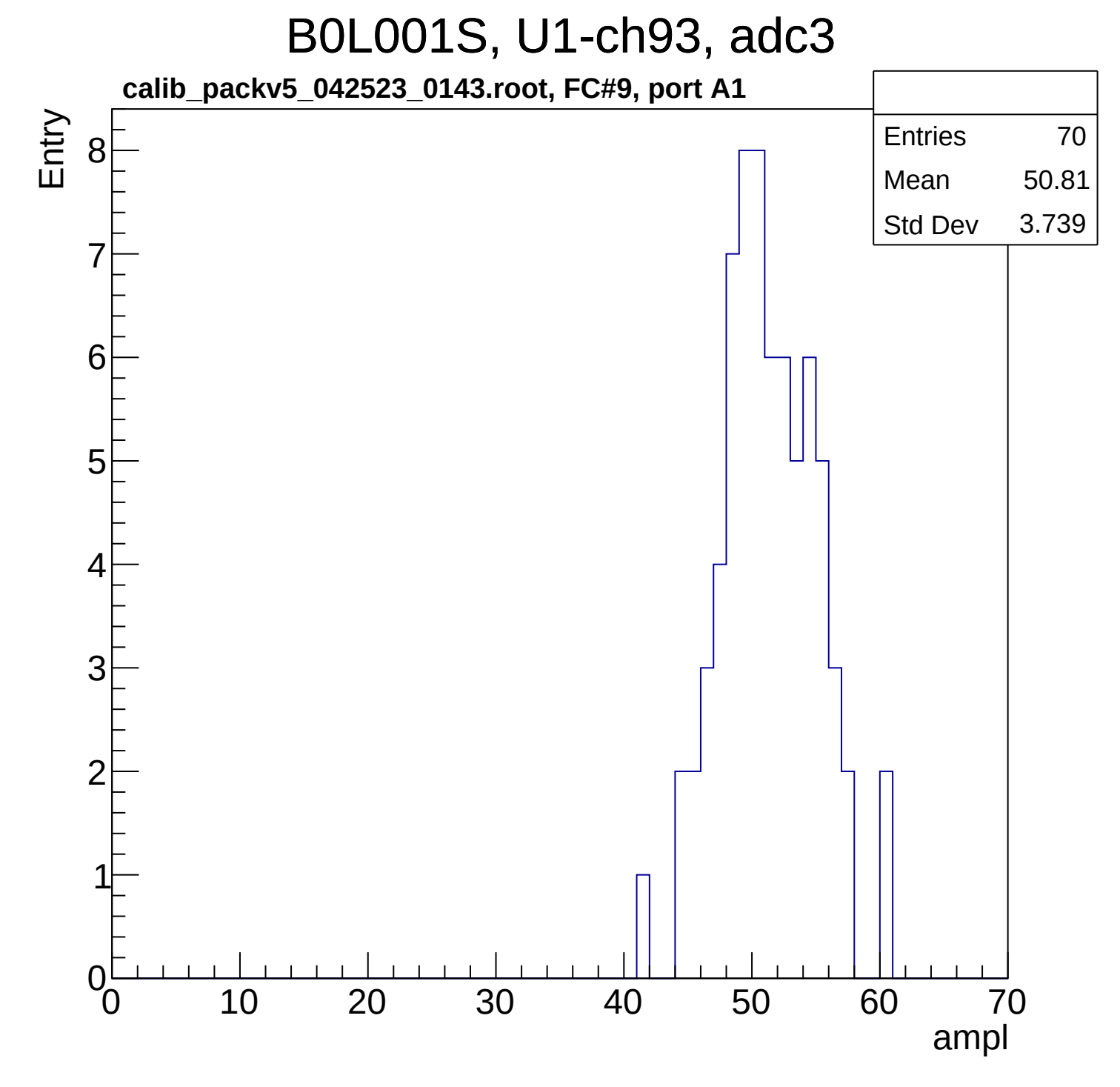

B0L001S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61
Std Dev 2.398
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

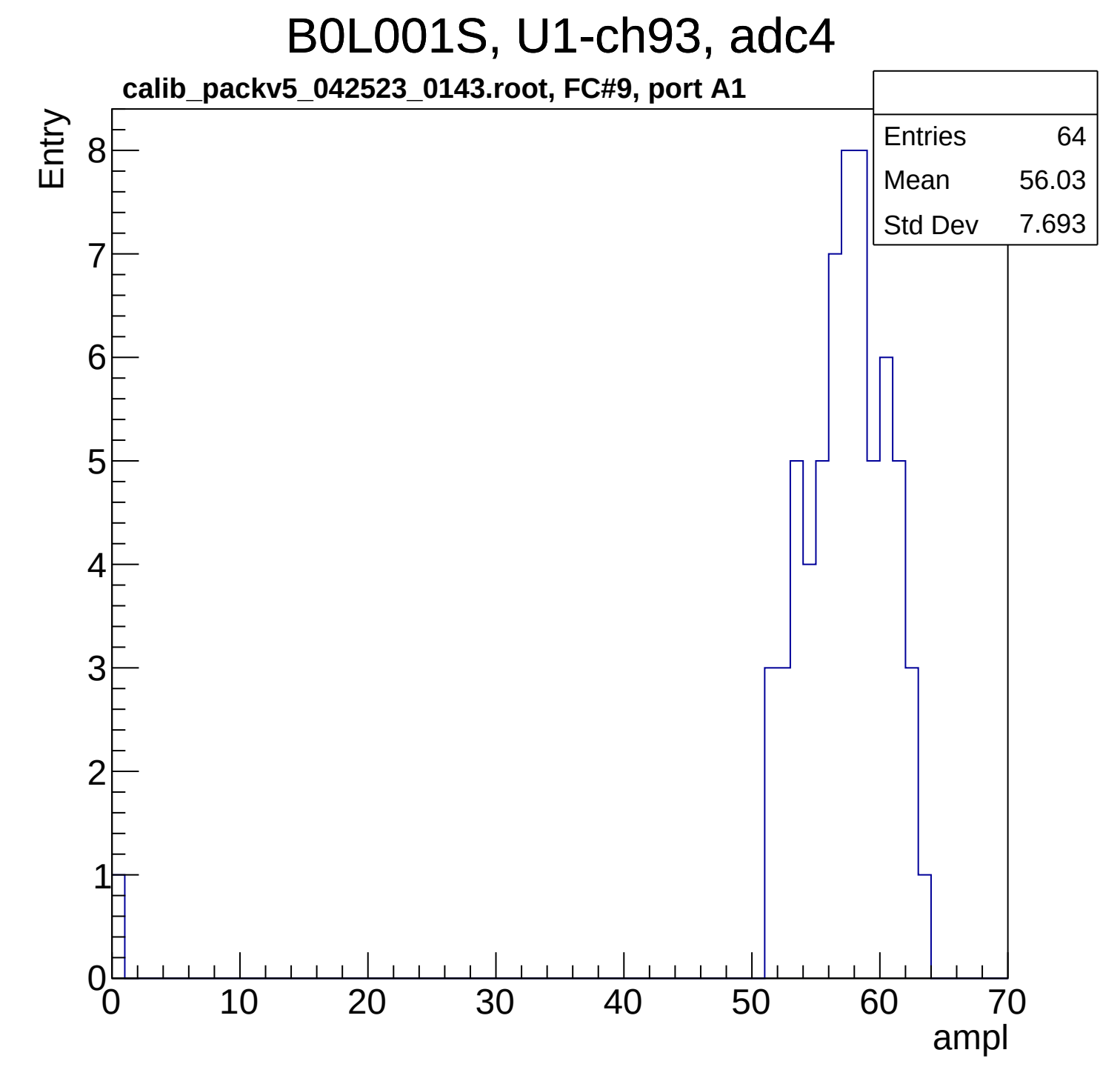

B0L001S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

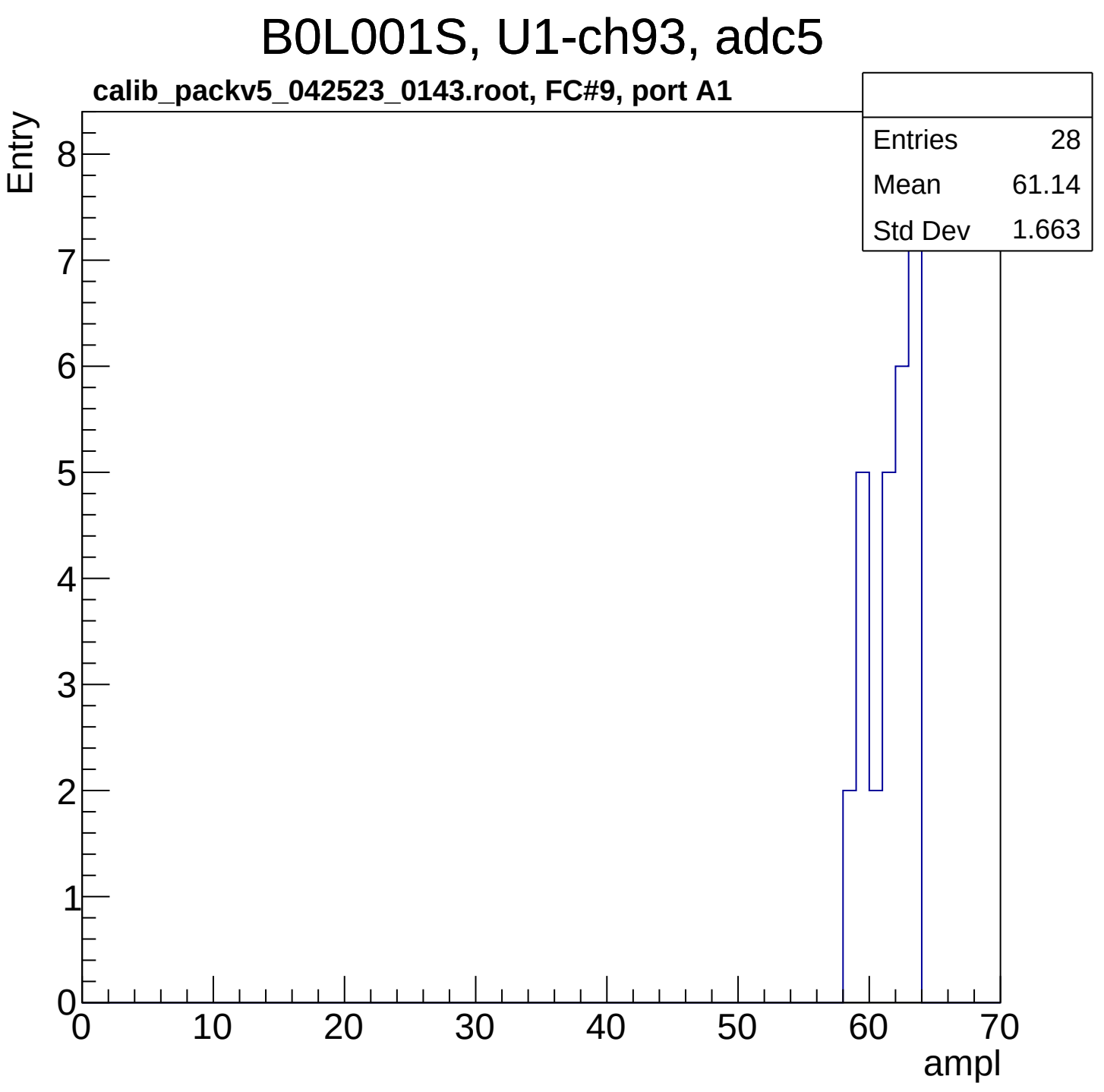

B0L001S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 30.13
Std Dev 3.259
Gaus mean : 30.2138
Gaus Width: 2.9425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

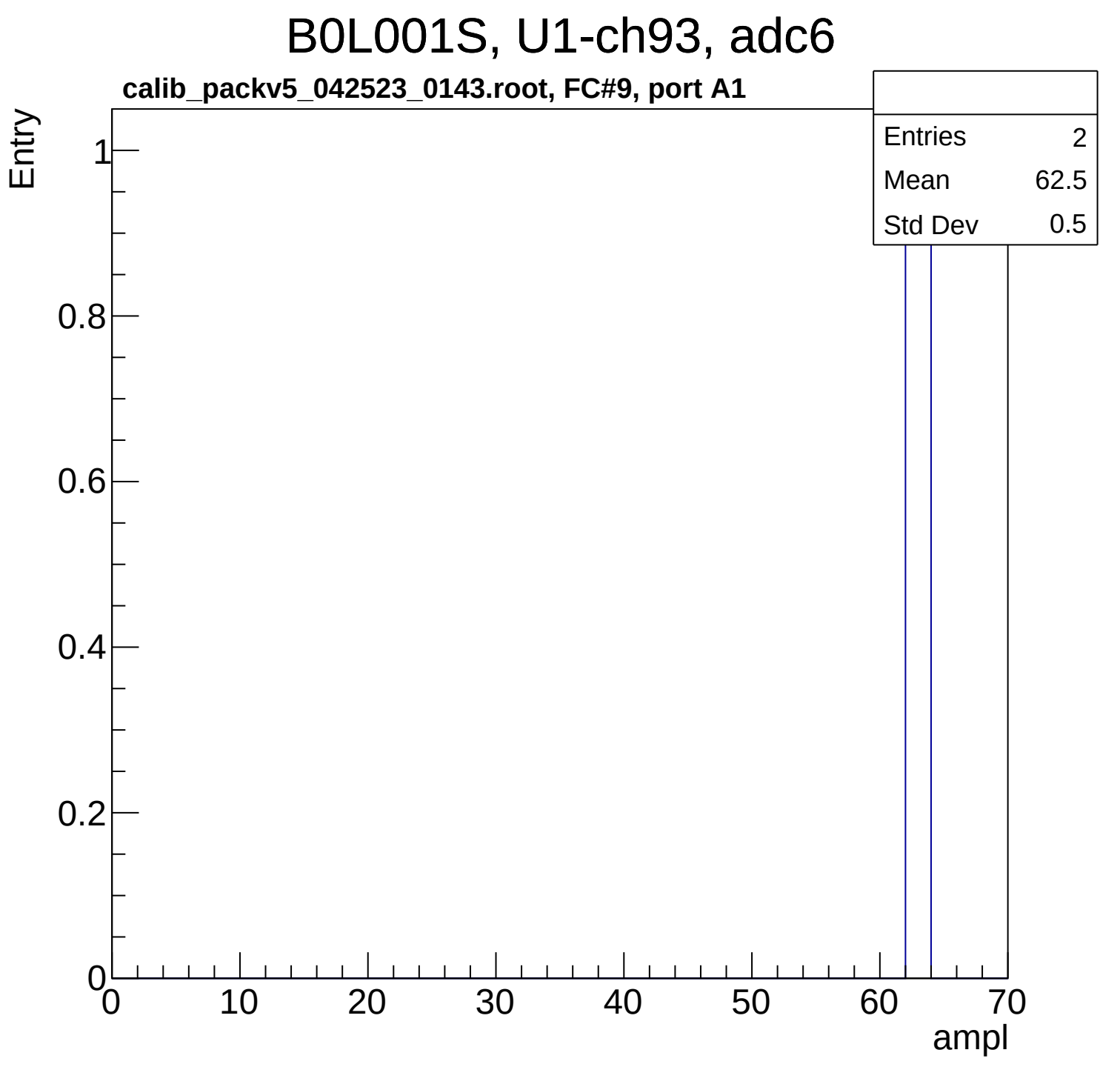

B0L001S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 89
Mean 37.67
Std Dev 3.994
Gaus mean : 37.8882
Gaus Width: 4.7555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

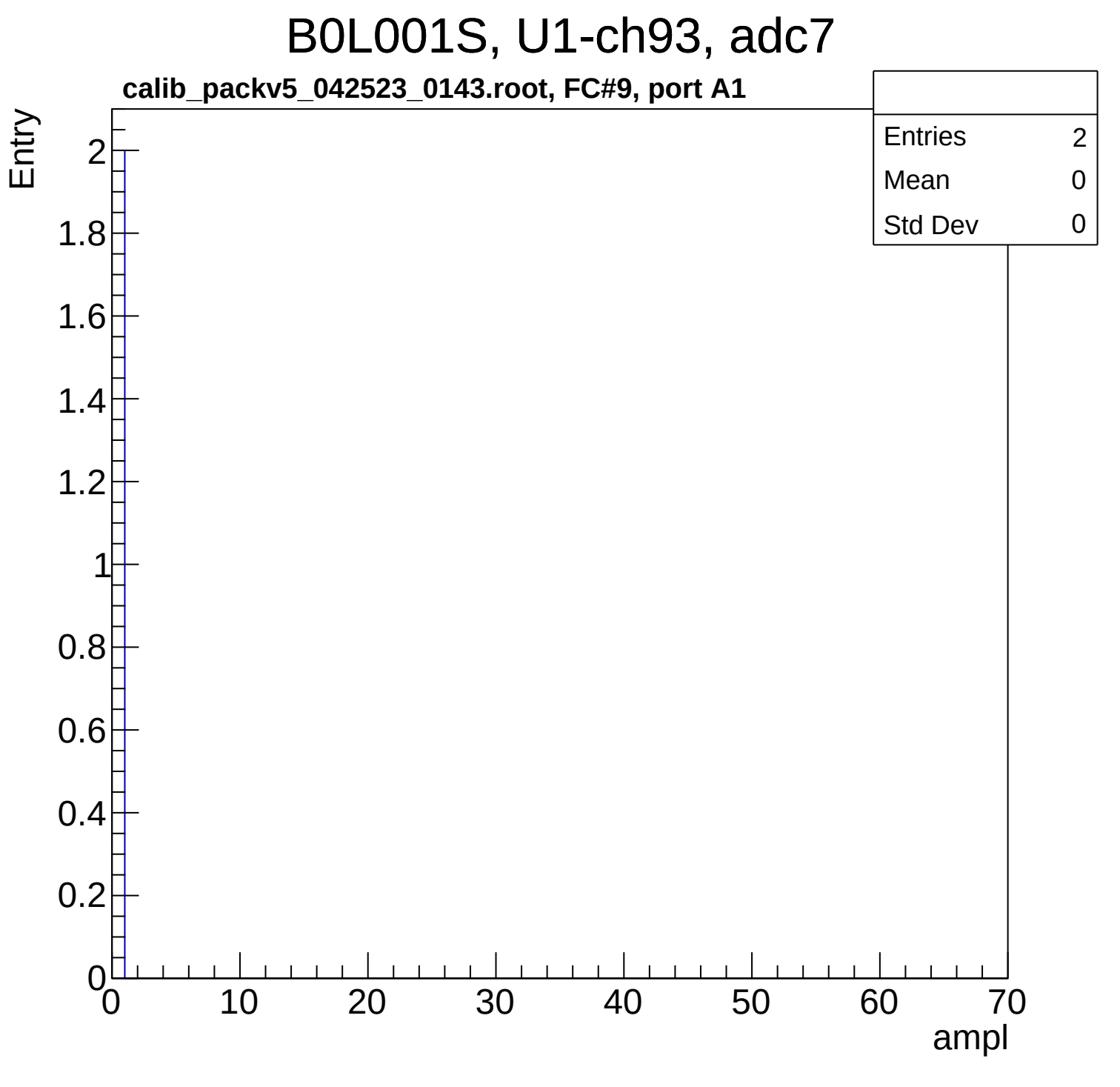

B0L001S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 44.53
Std Dev 3.466
Gaus mean : 45.1654
Gaus Width: 4.3594
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

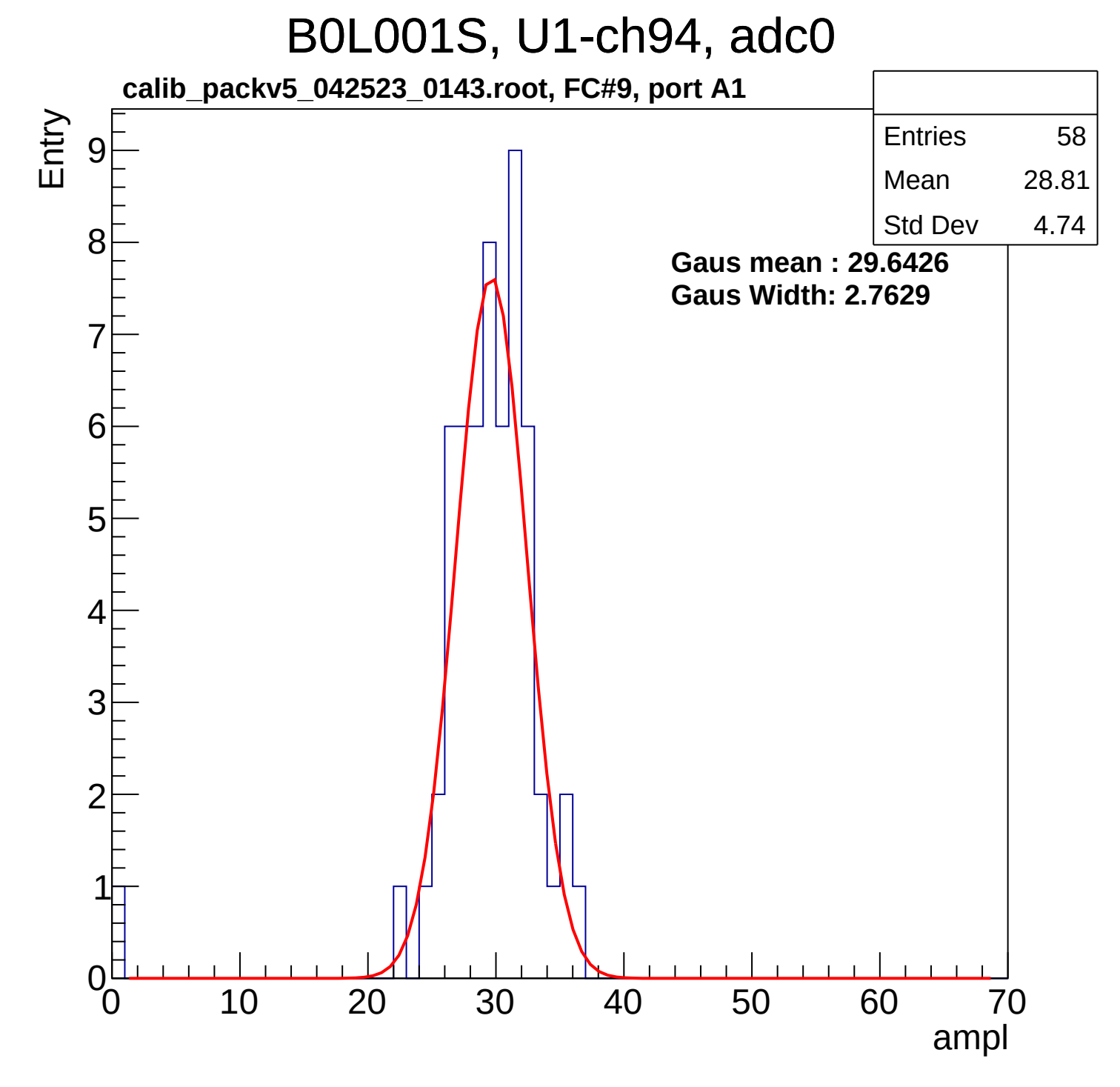

B0L001S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.81
Std Dev 3.739
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

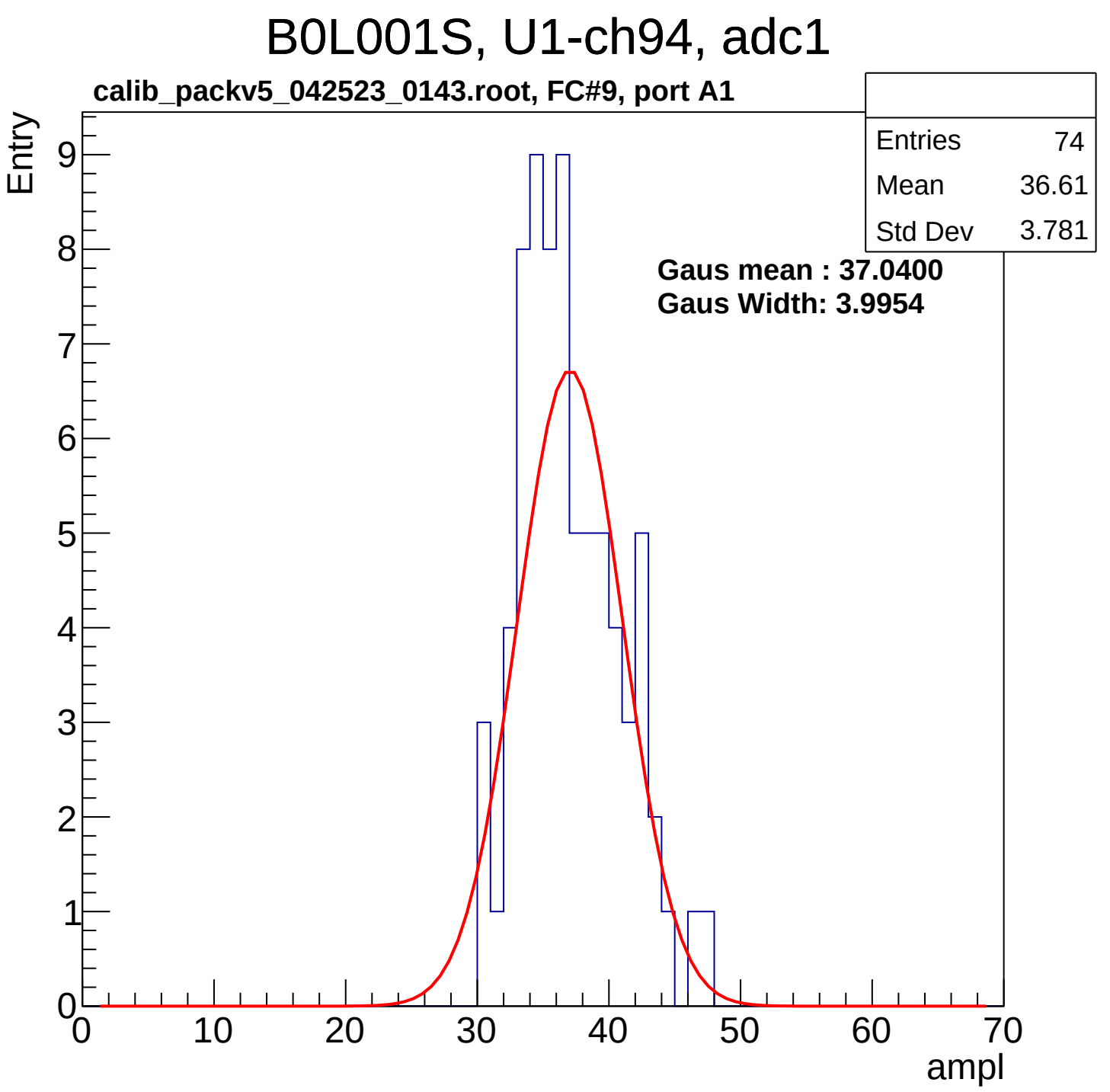

B0L001S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.03
Std Dev 7.693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

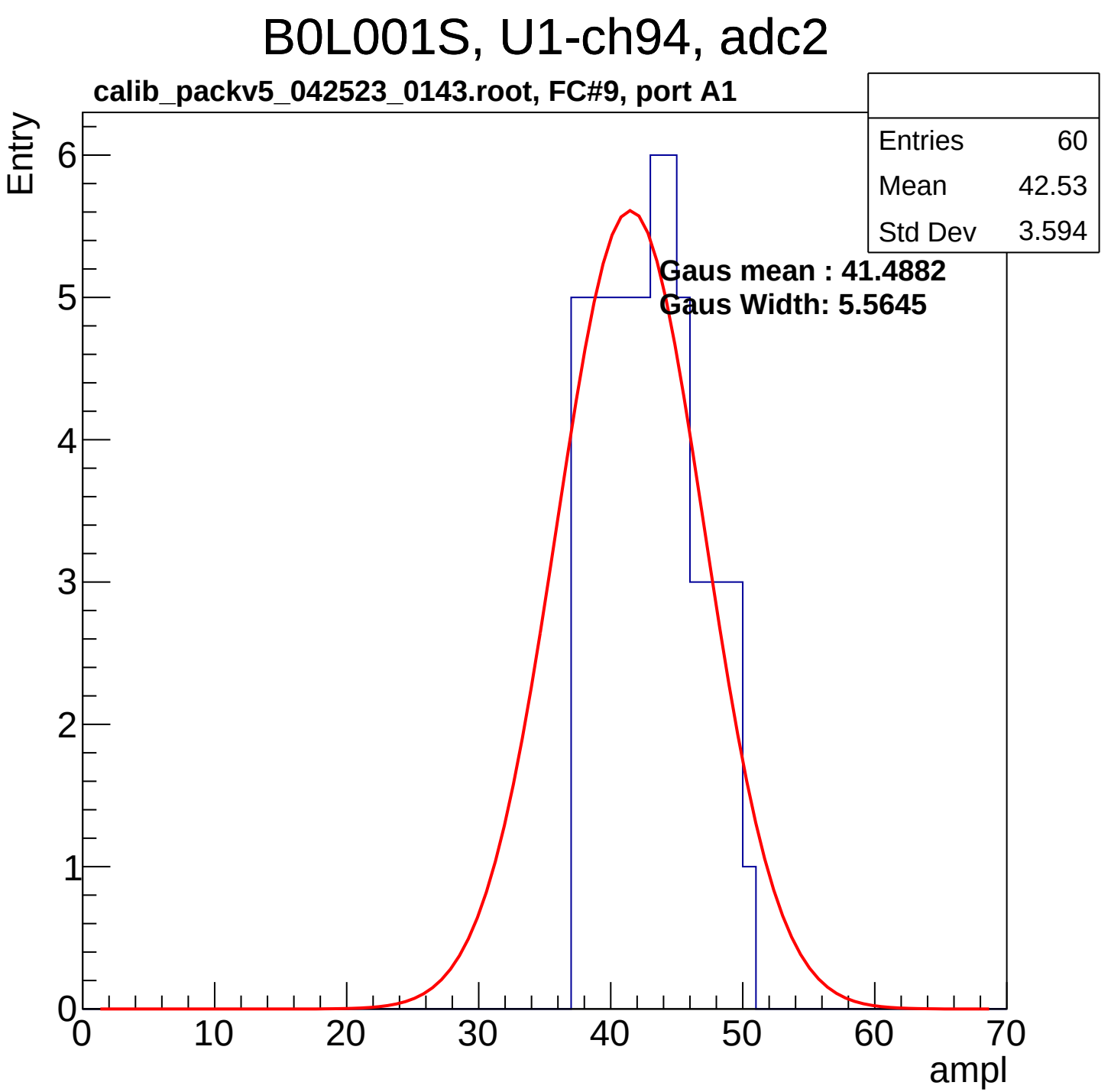

B0L001S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 28
Mean 61.14
Std Dev 1.663
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

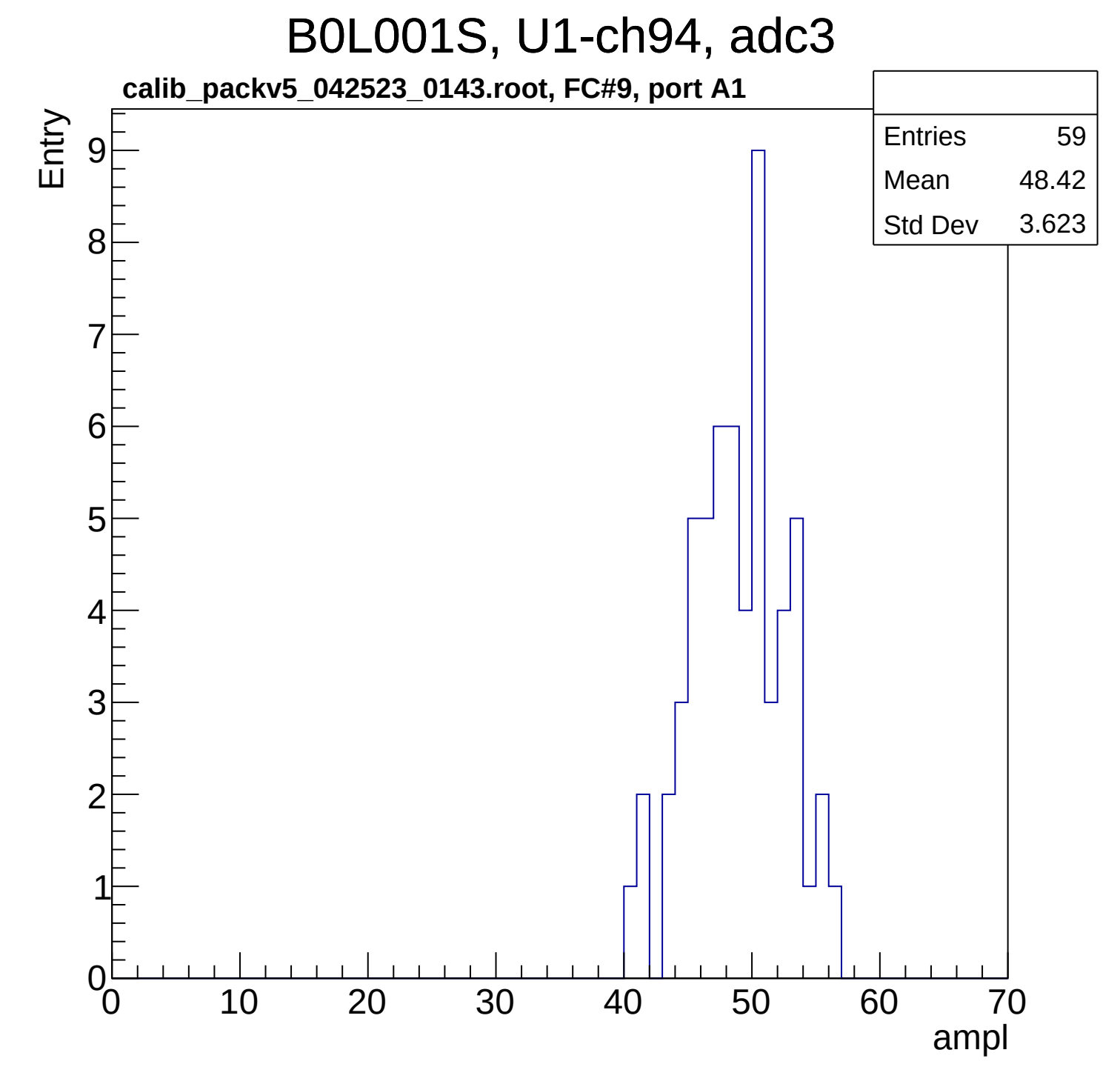

B0L001S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

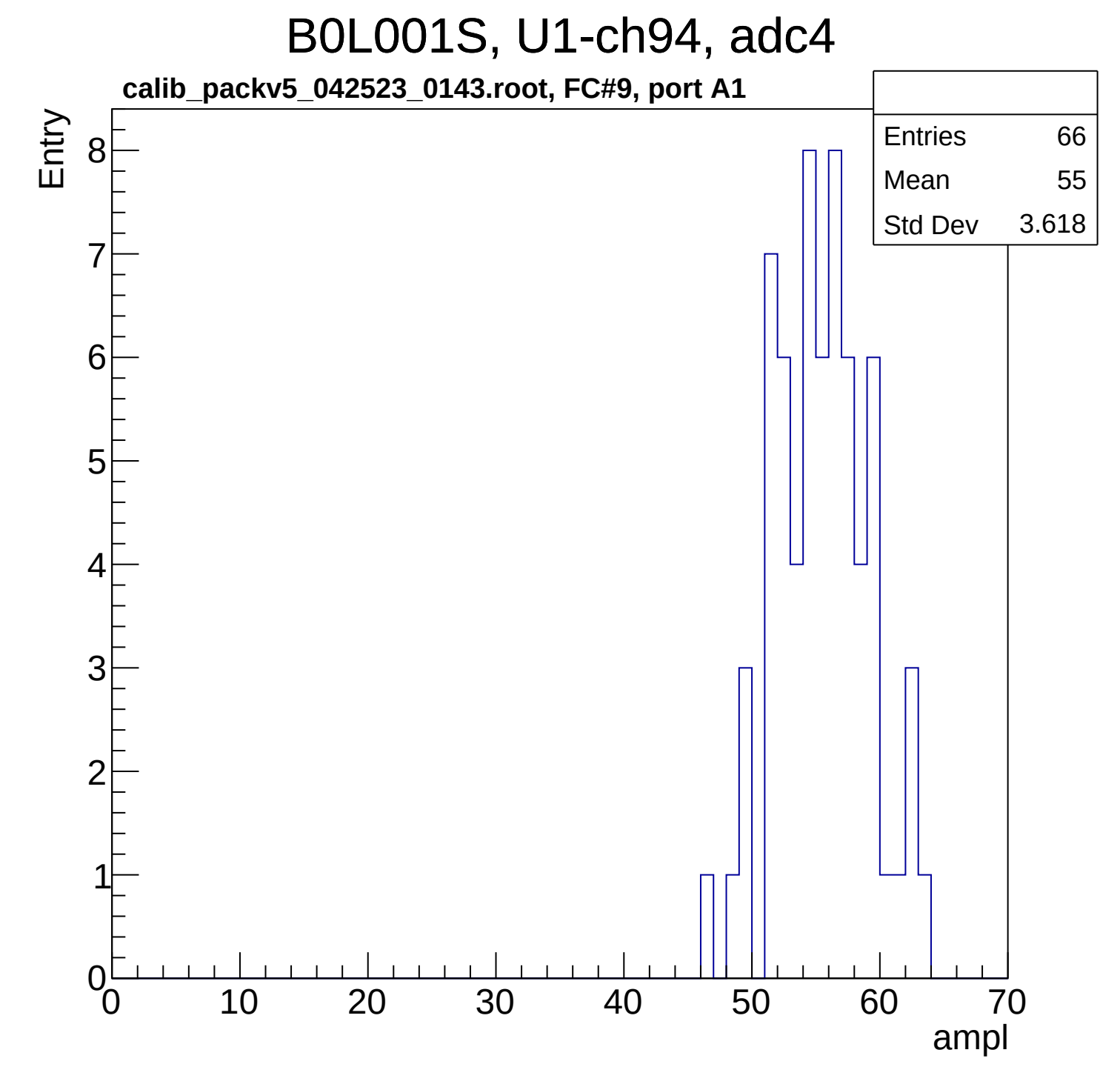

B0L001S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 28.81
Std Dev 4.74
Gaus mean : 29.6426
Gaus Width: 2.7629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.61
Std Dev 3.781
Gaus mean : 37.0400
Gaus Width: 3.9954
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 42.53
Std Dev 3.594
Gaus mean : 41.4882
Gaus Width: 5.5645
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 48.42
Std Dev 3.623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55
Std Dev 3.618
Entry
ampl
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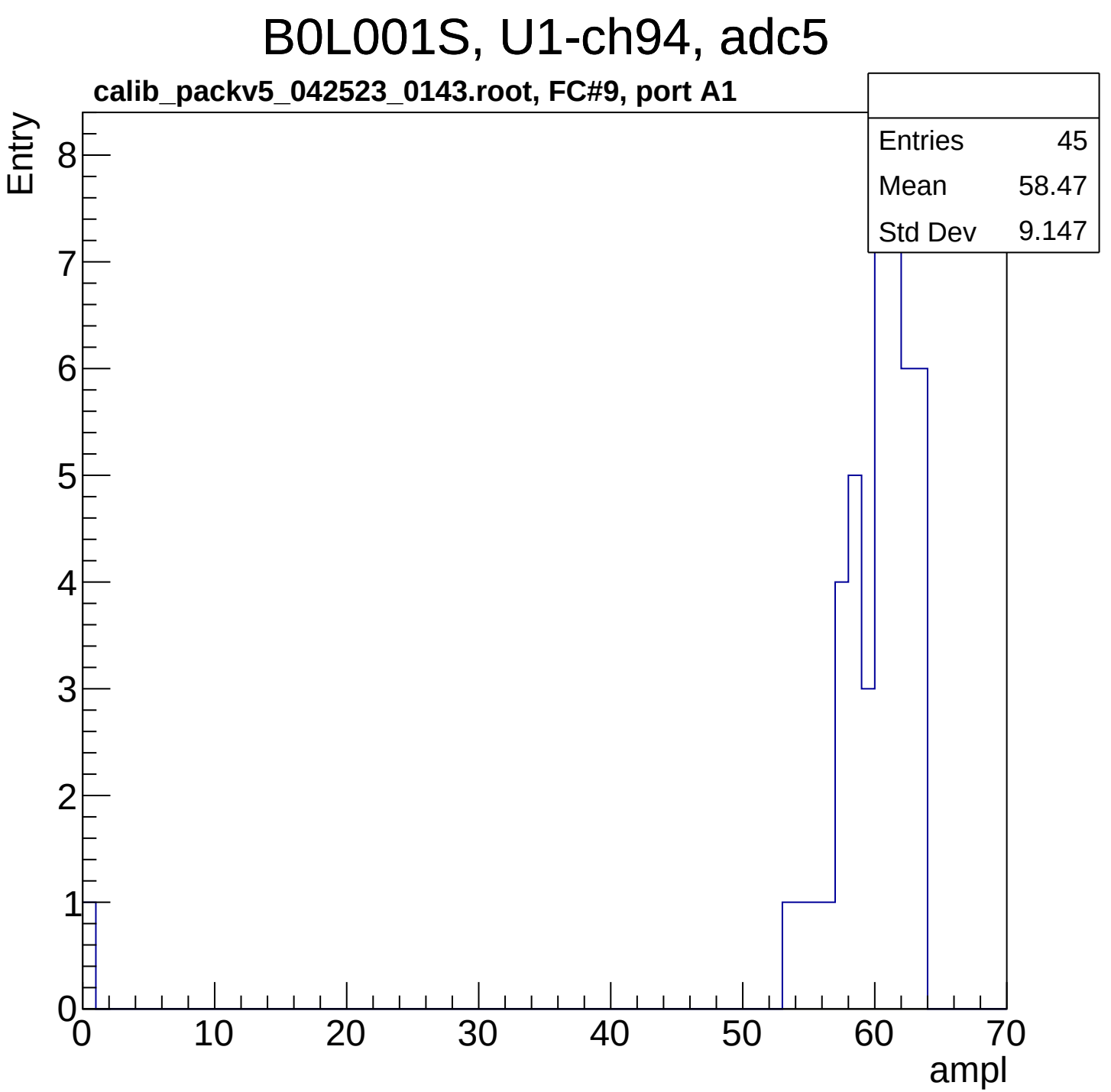
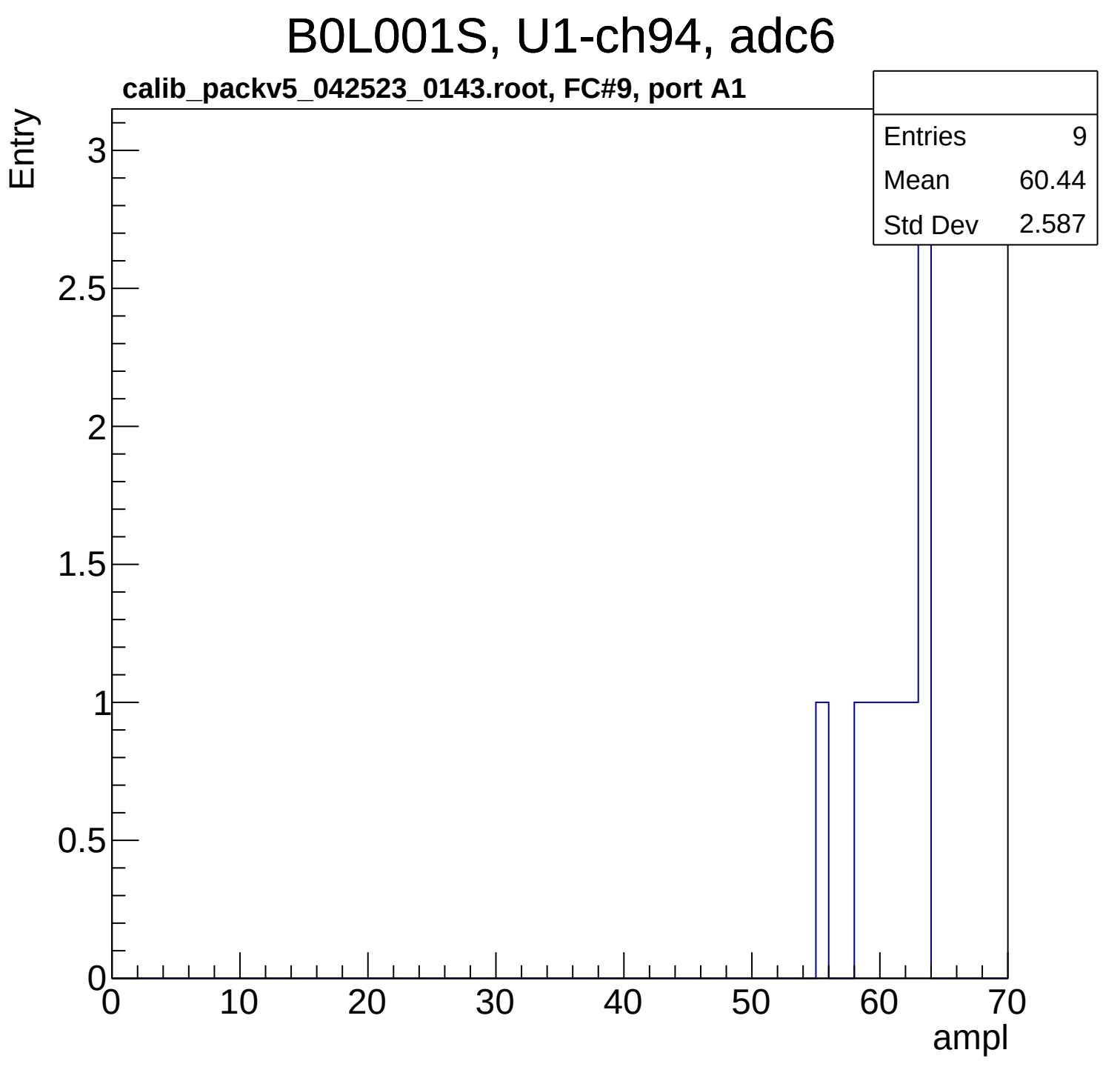
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 58.47
Std Dev 9.147
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 60.44
Std Dev 2.587
Entry
ampl
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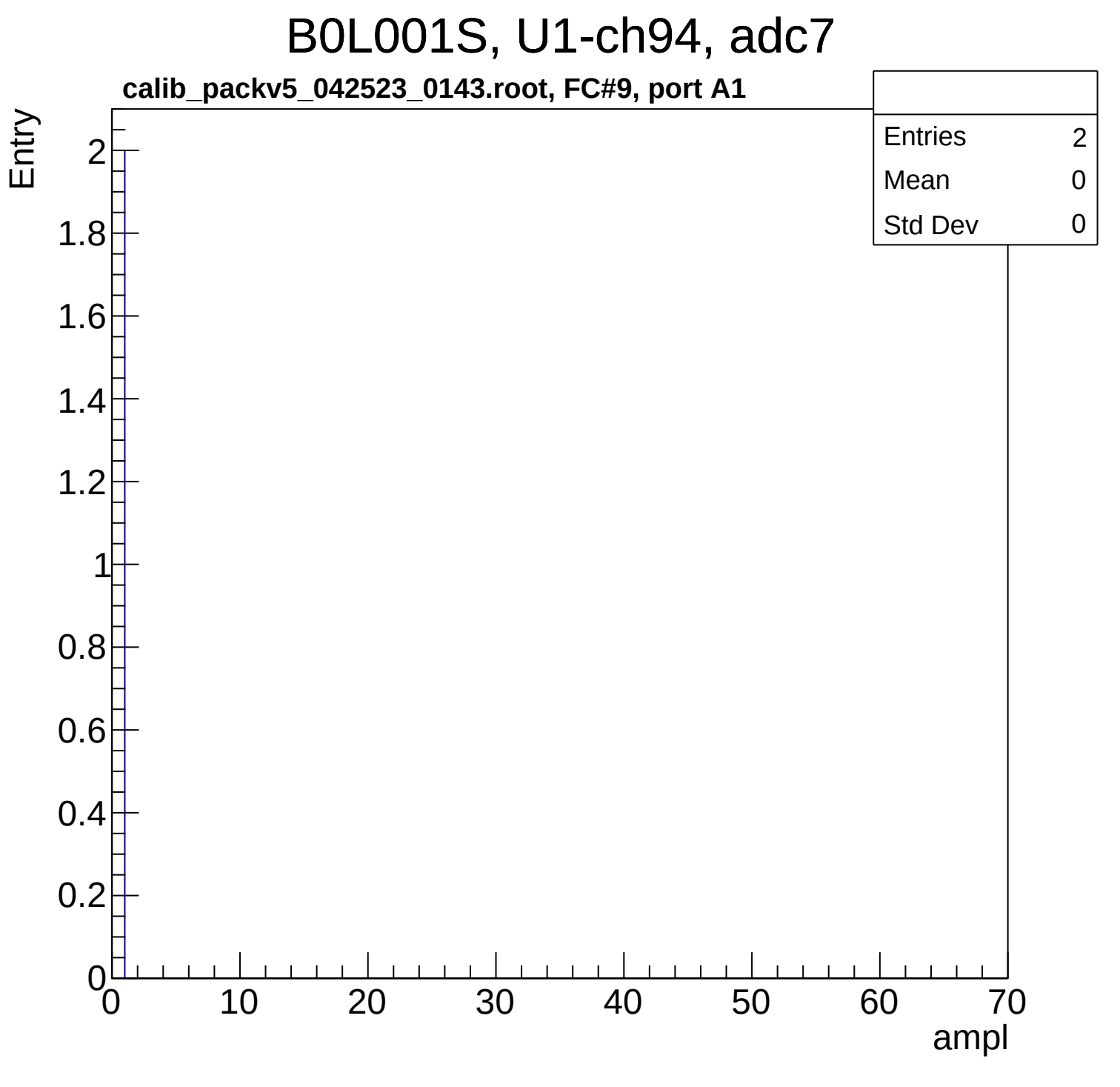
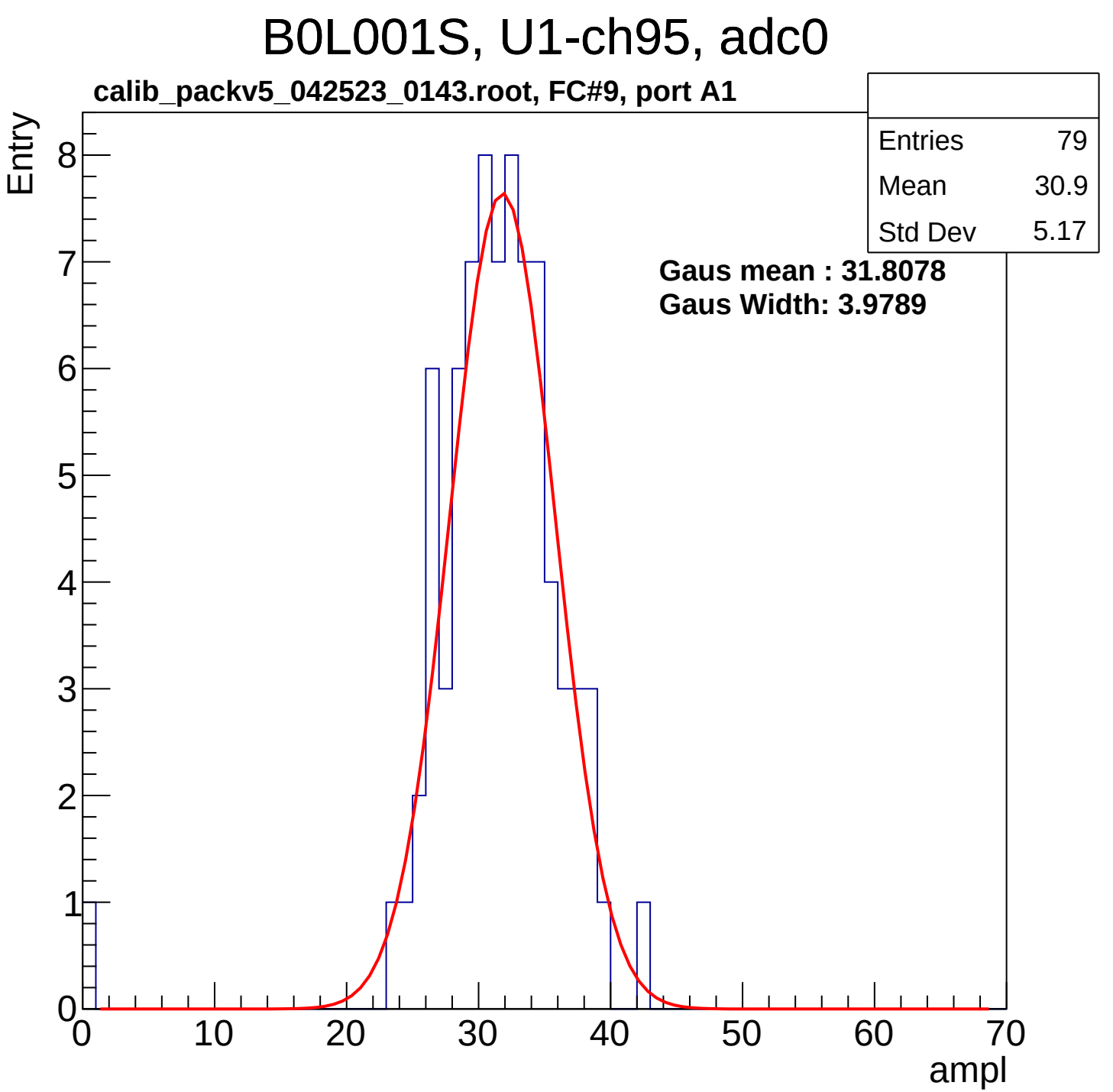
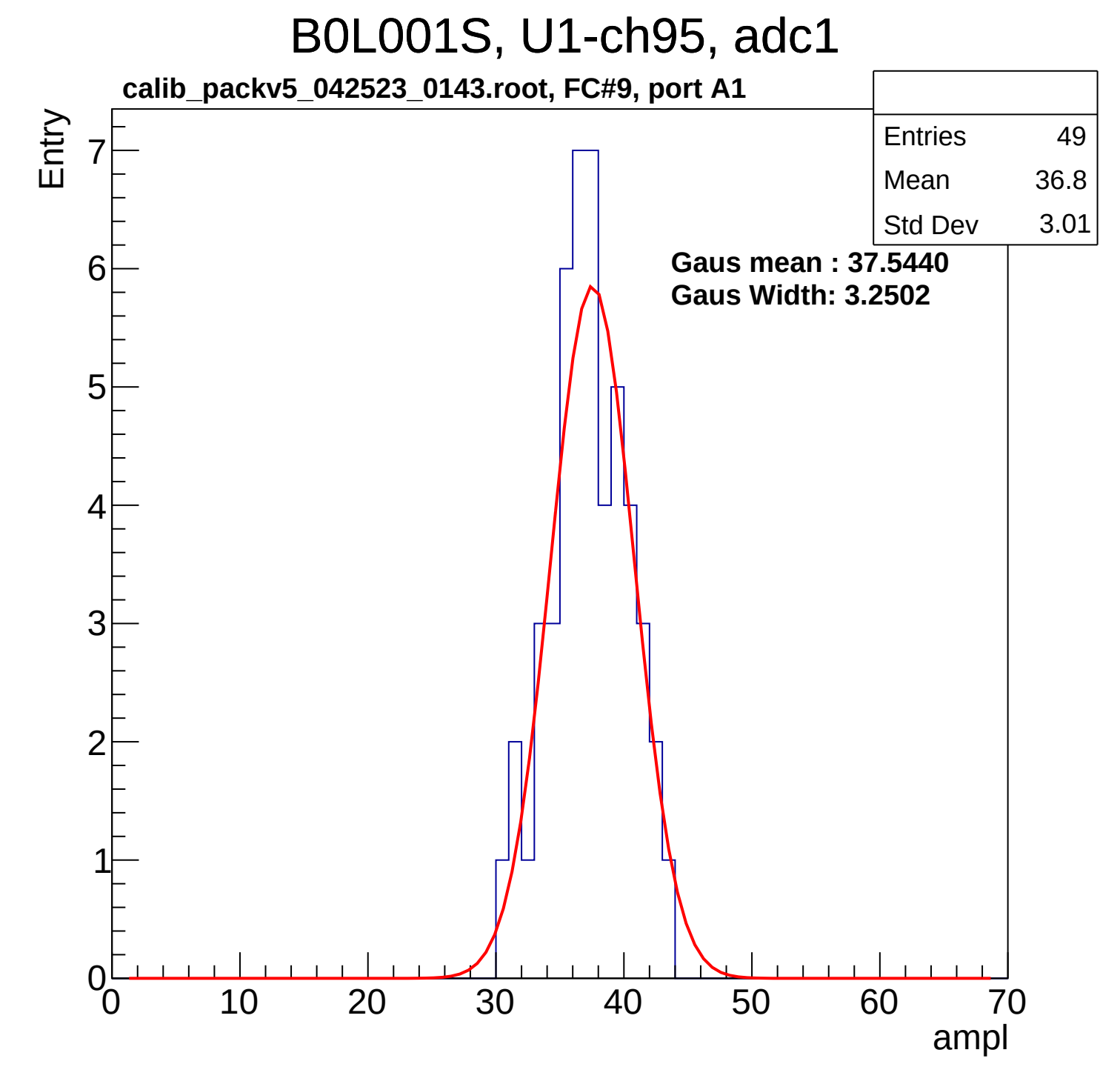
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 30.9
Std Dev 5.17
Gaus mean : 31.8078
Gaus Width: 3.9789
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 36.8
Std Dev 3.01
Gaus mean : 37.5440
Gaus Width: 3.2502
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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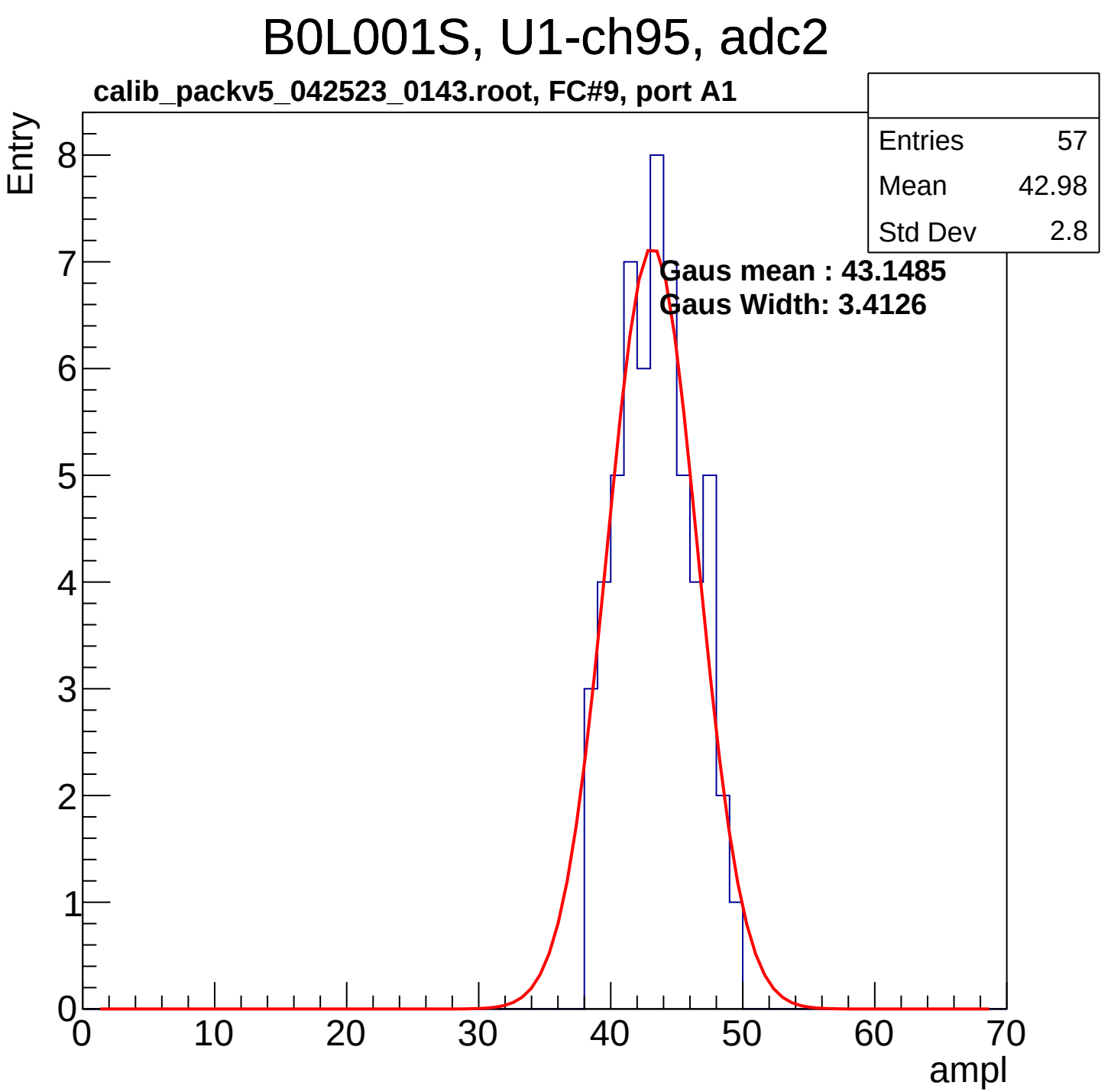
B0L001S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 42.98
Std Dev 2.8
Gaus mean : 43.1485
Gaus Width: 3.4126
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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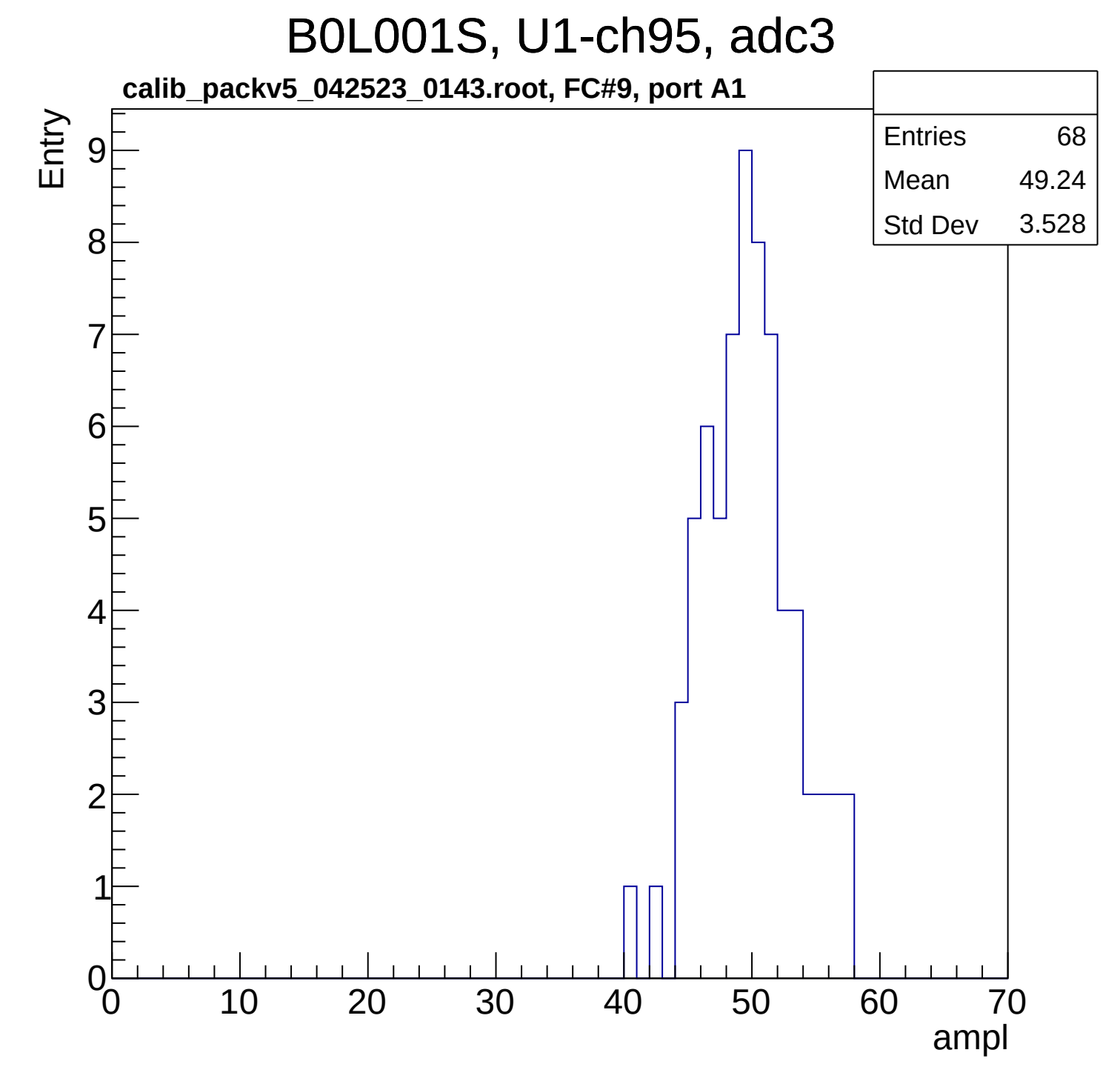
40
50
60
70


B0L001S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 49.24
Std Dev 3.528
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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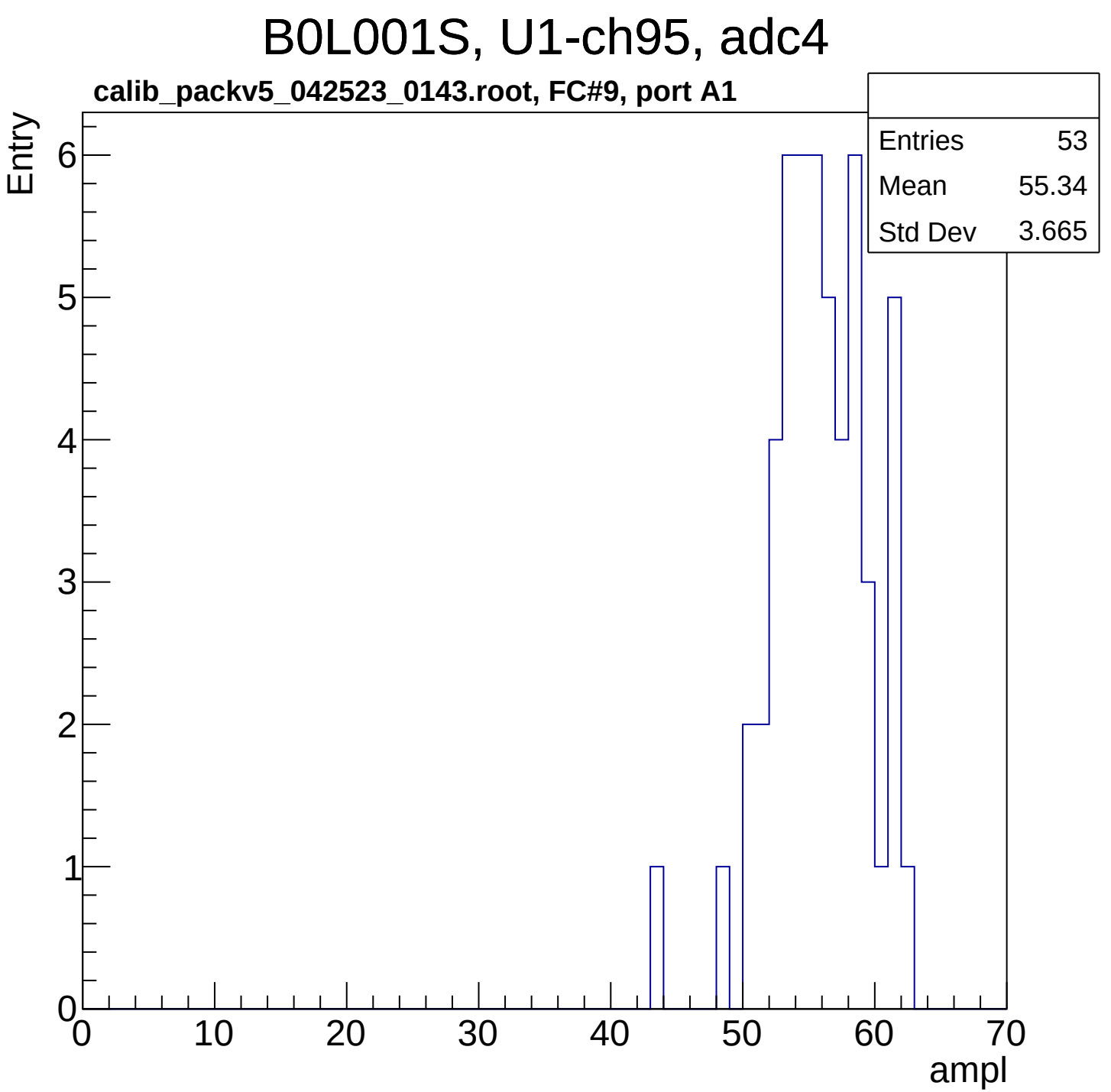
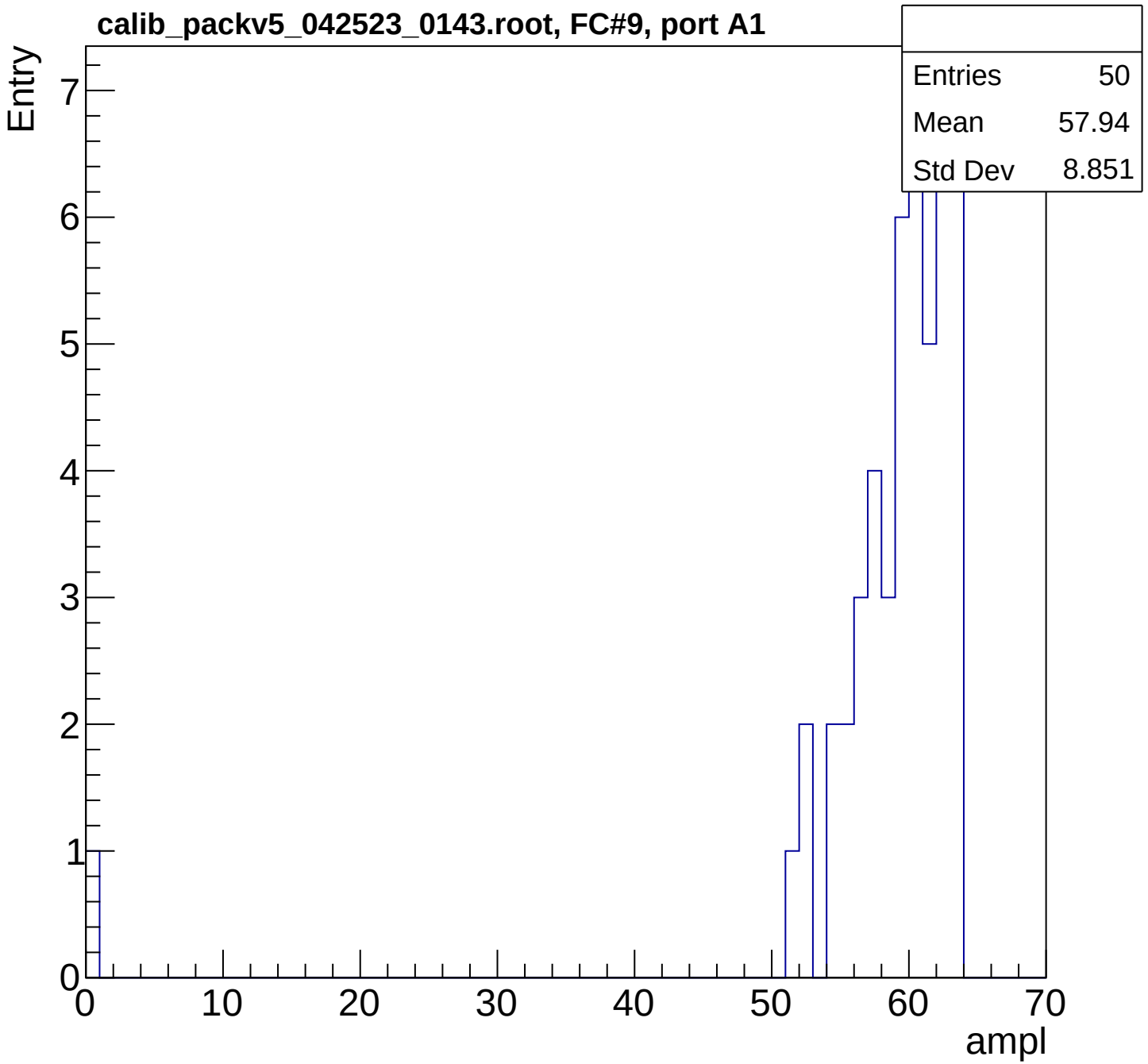
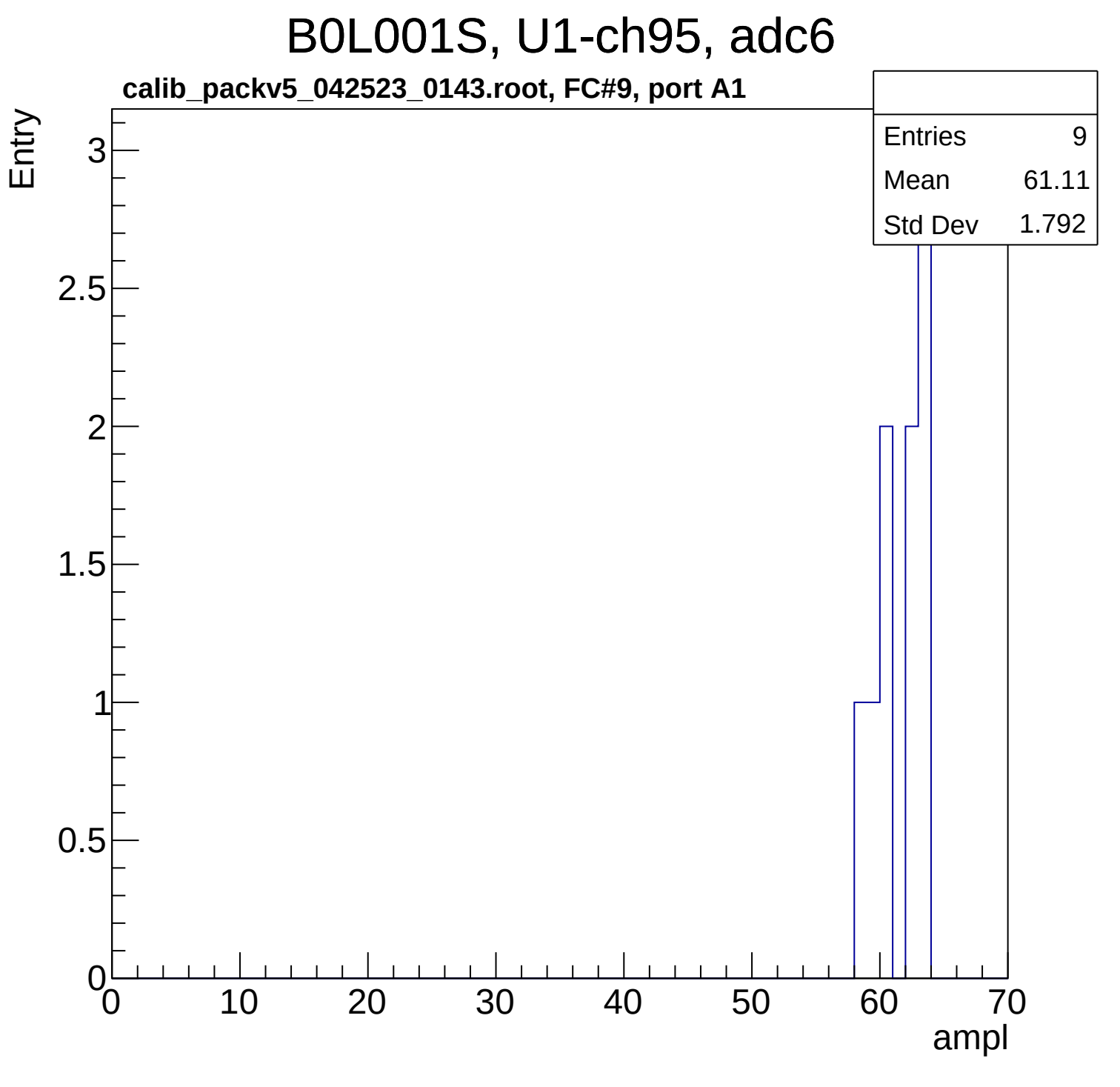
40
50
60
70


B0L001S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 55.34
Std Dev 3.665
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 57.94
Std Dev 8.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.11
Std Dev 1.792
Entry
ampl
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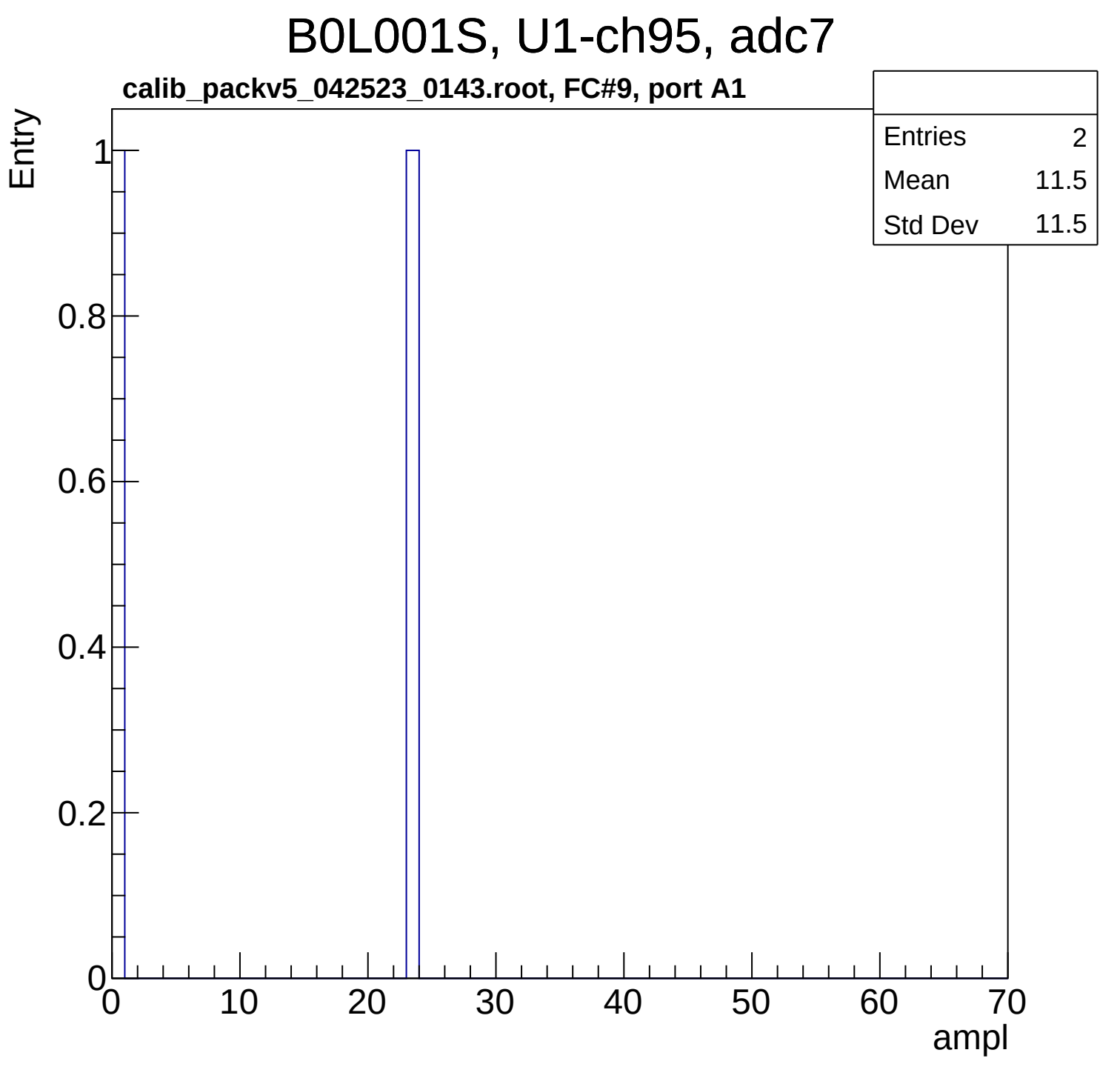
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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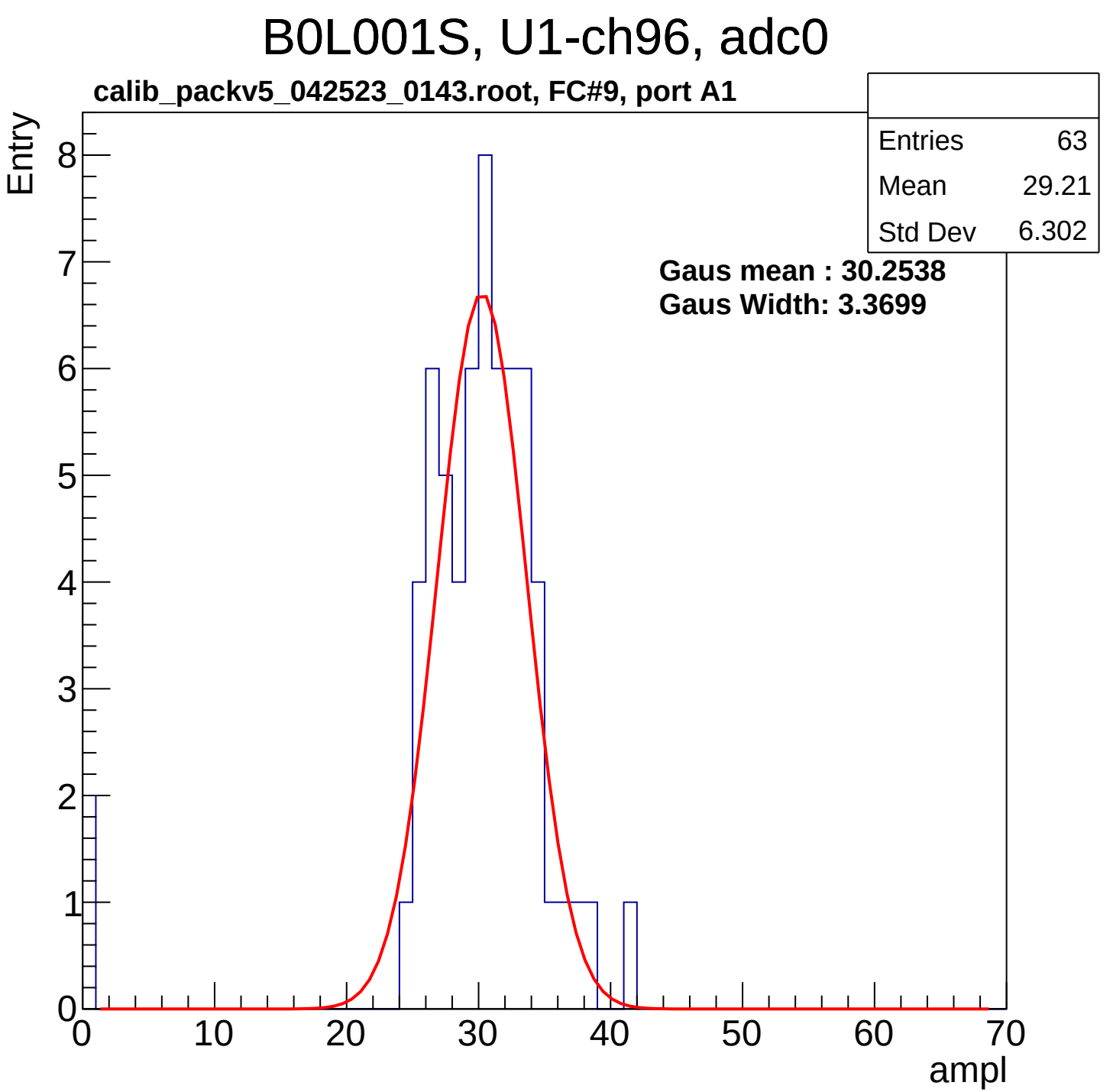
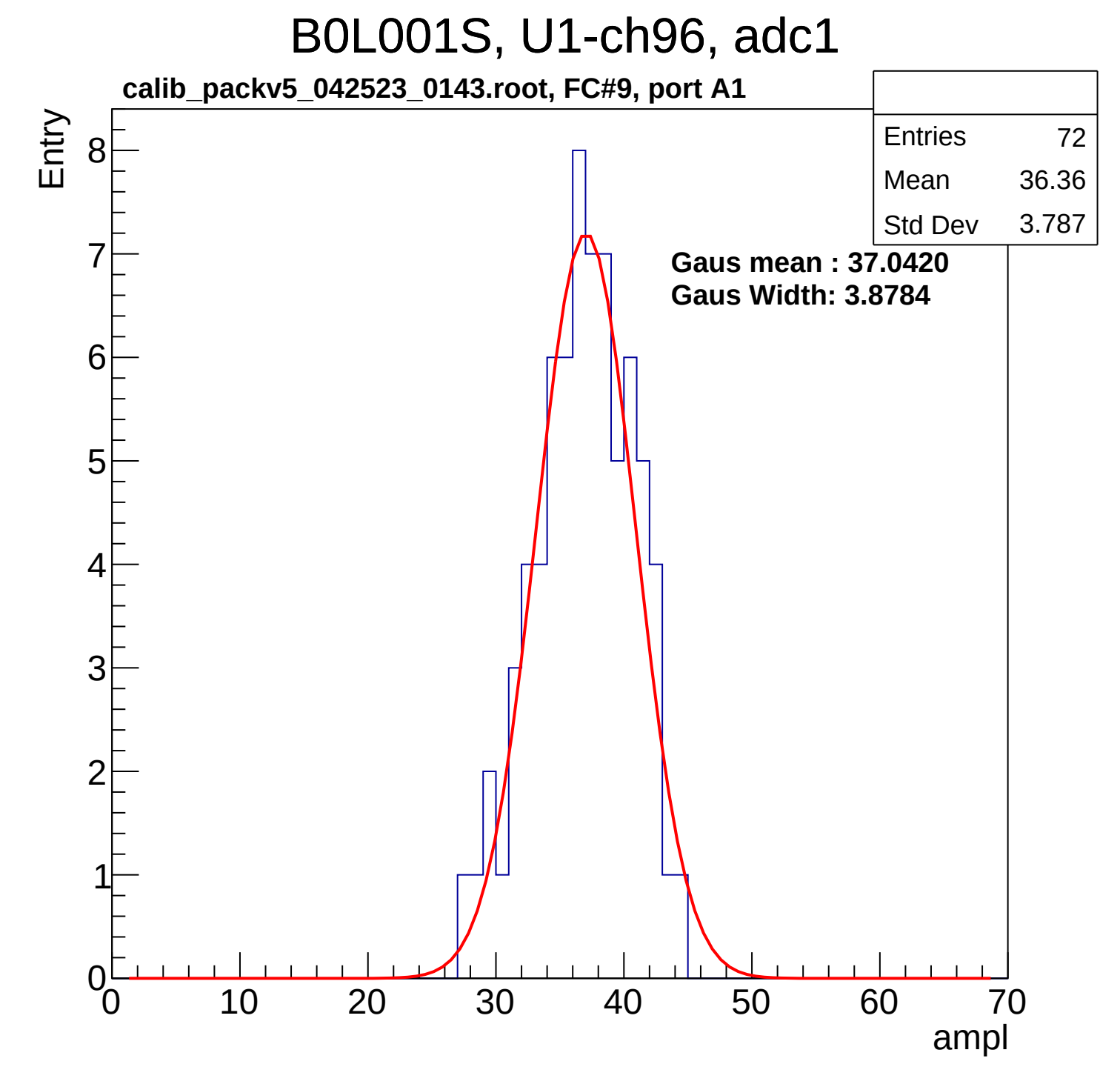
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 29.21
Std Dev 6.302
Gaus mean : 30.2538
Gaus Width: 3.3699
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 36.36
Std Dev 3.787
Gaus mean : 37.0420
Gaus Width: 3.8784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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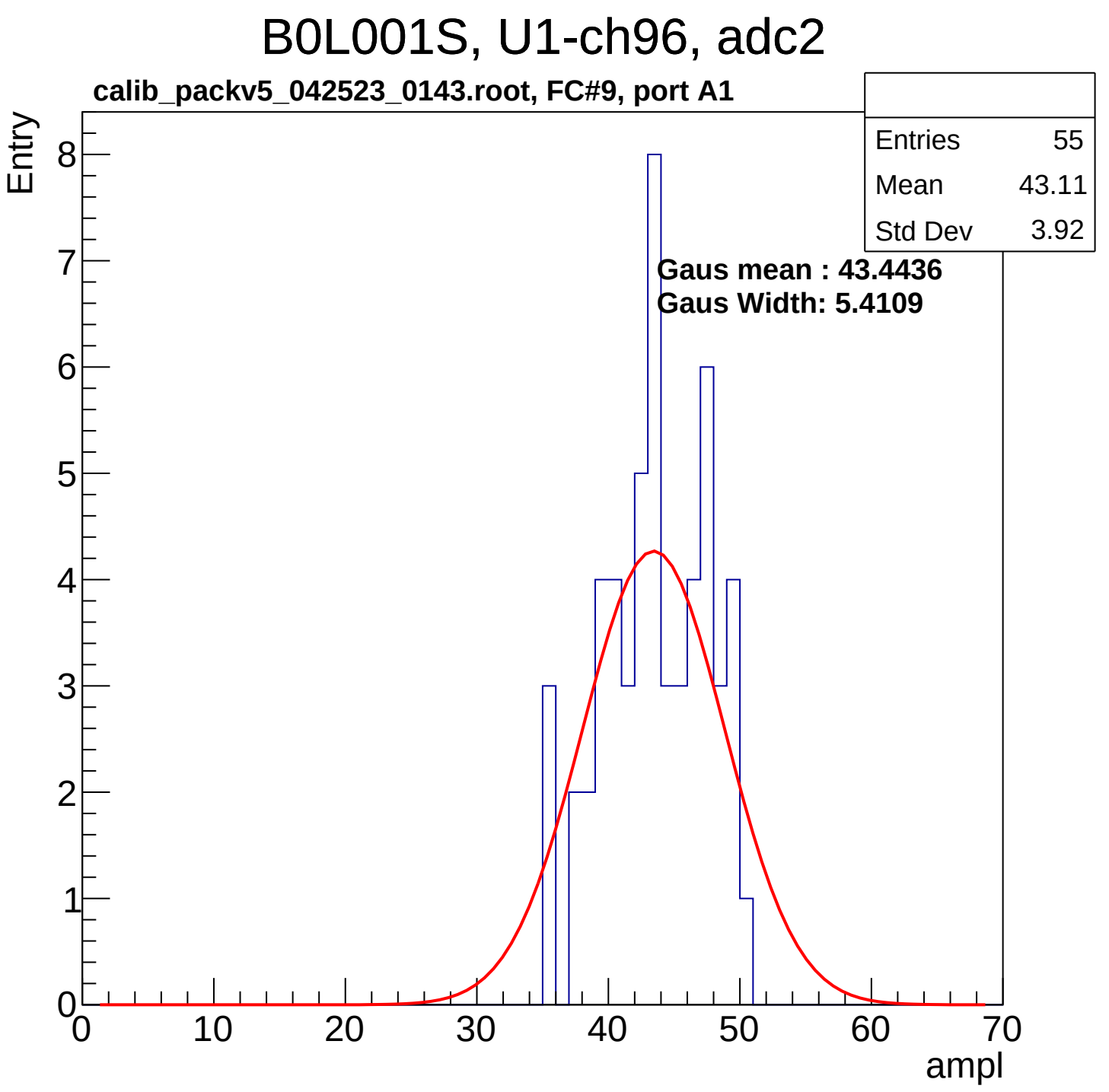
B0L001S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 43.11
Std Dev 3.92
Gaus mean : 43.4436
Gaus Width: 5.4109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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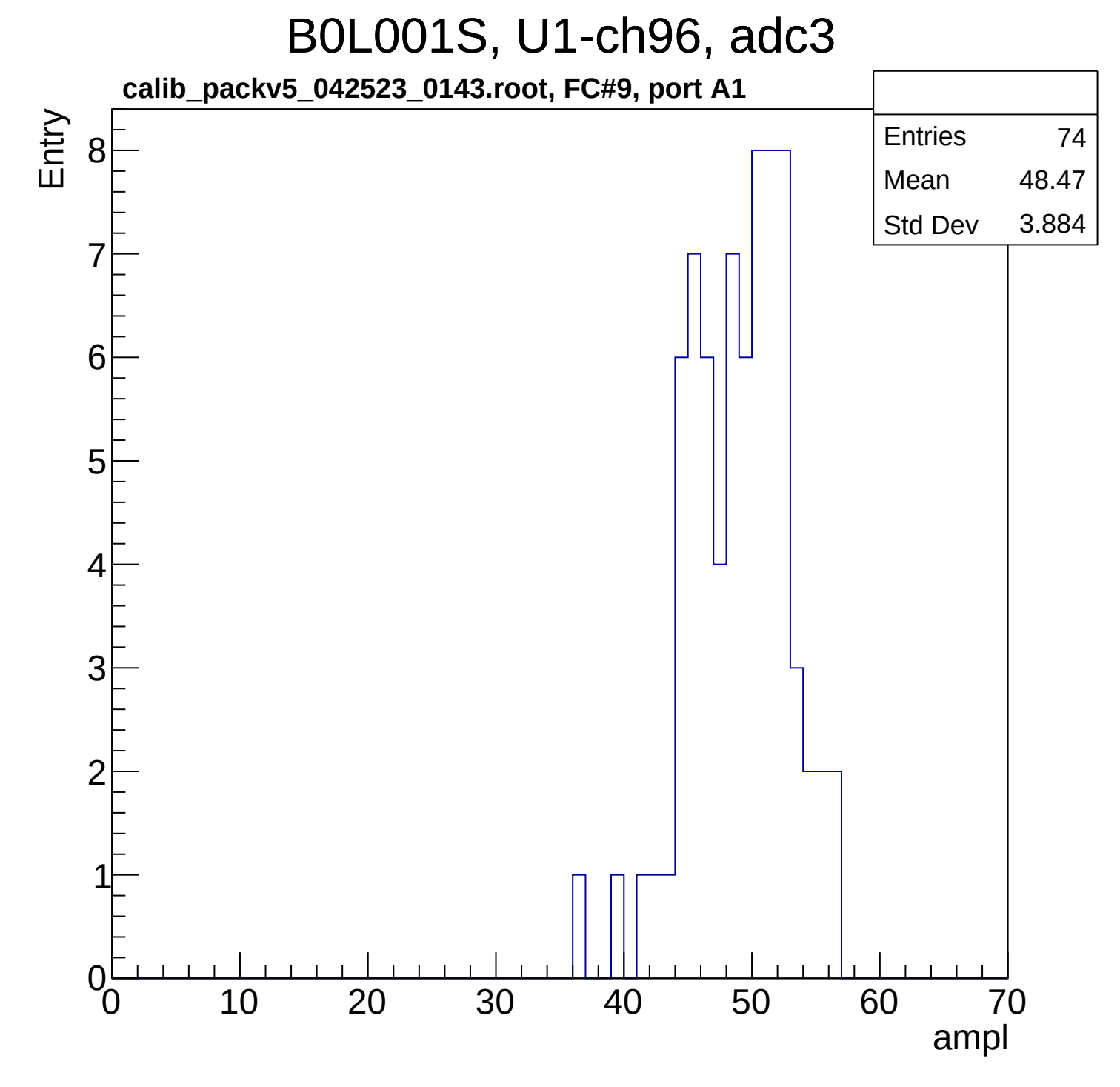
B0L001S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 48.47
Std Dev 3.884
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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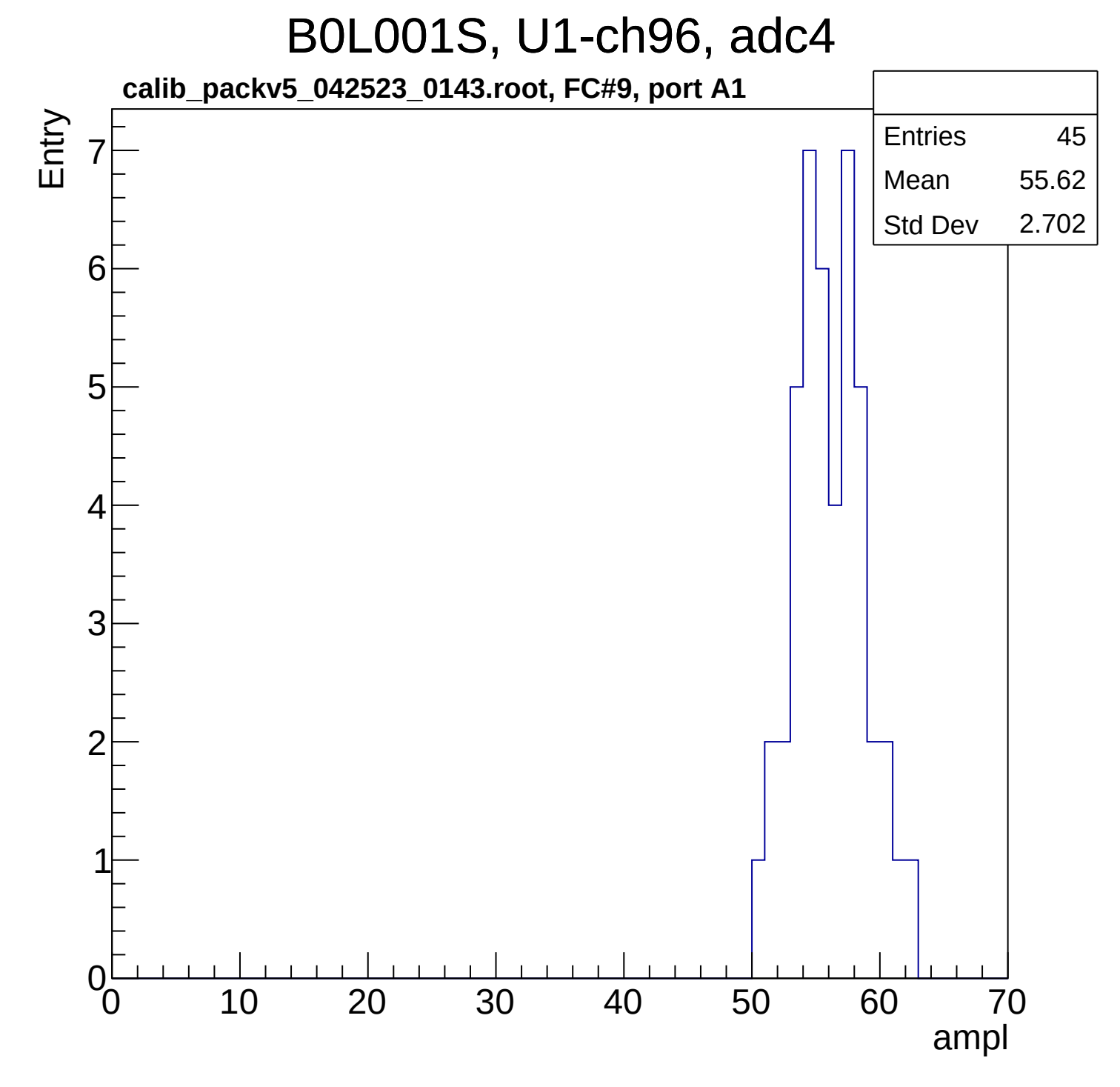
B0L001S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.62
Std Dev 2.702
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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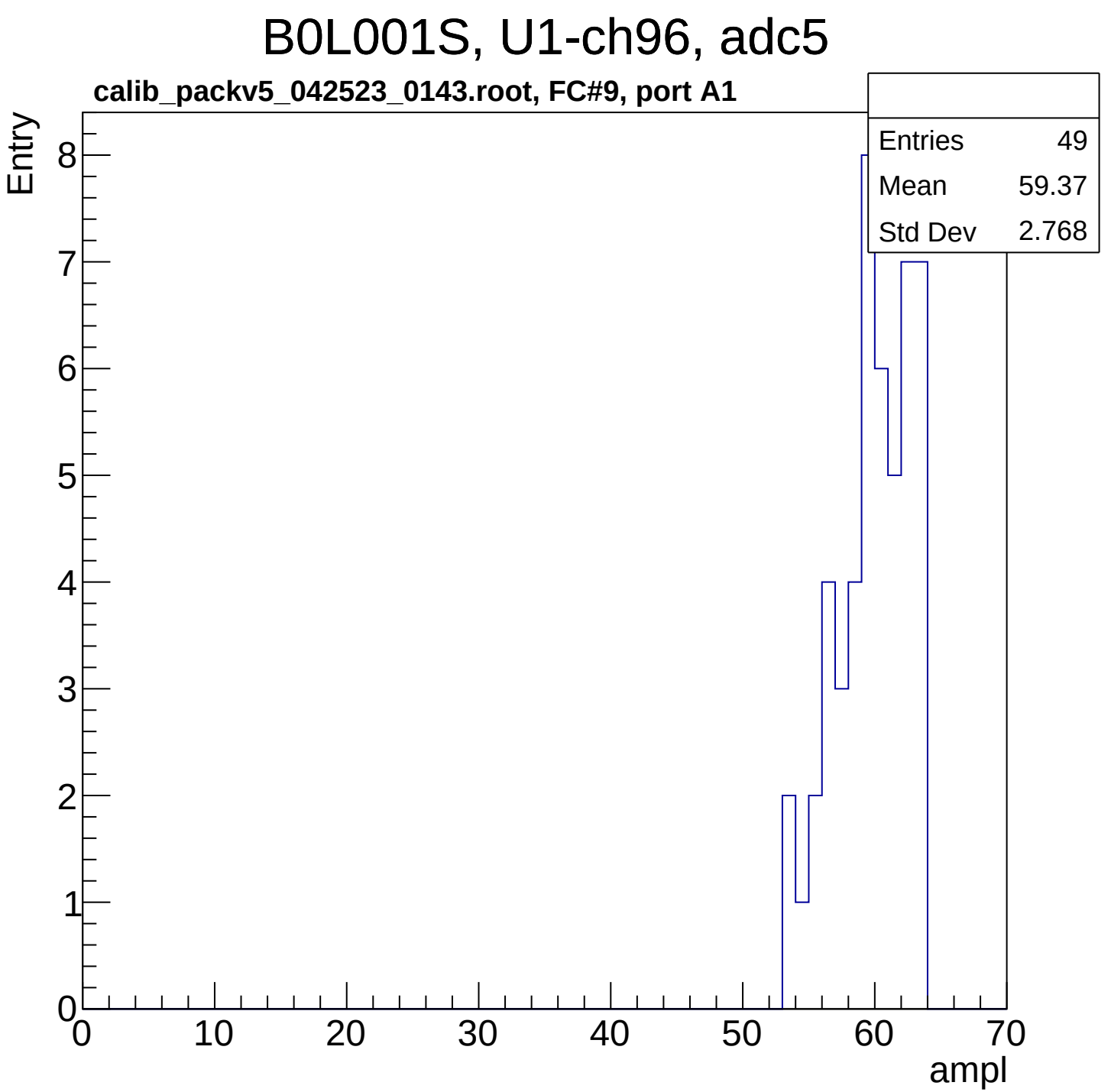
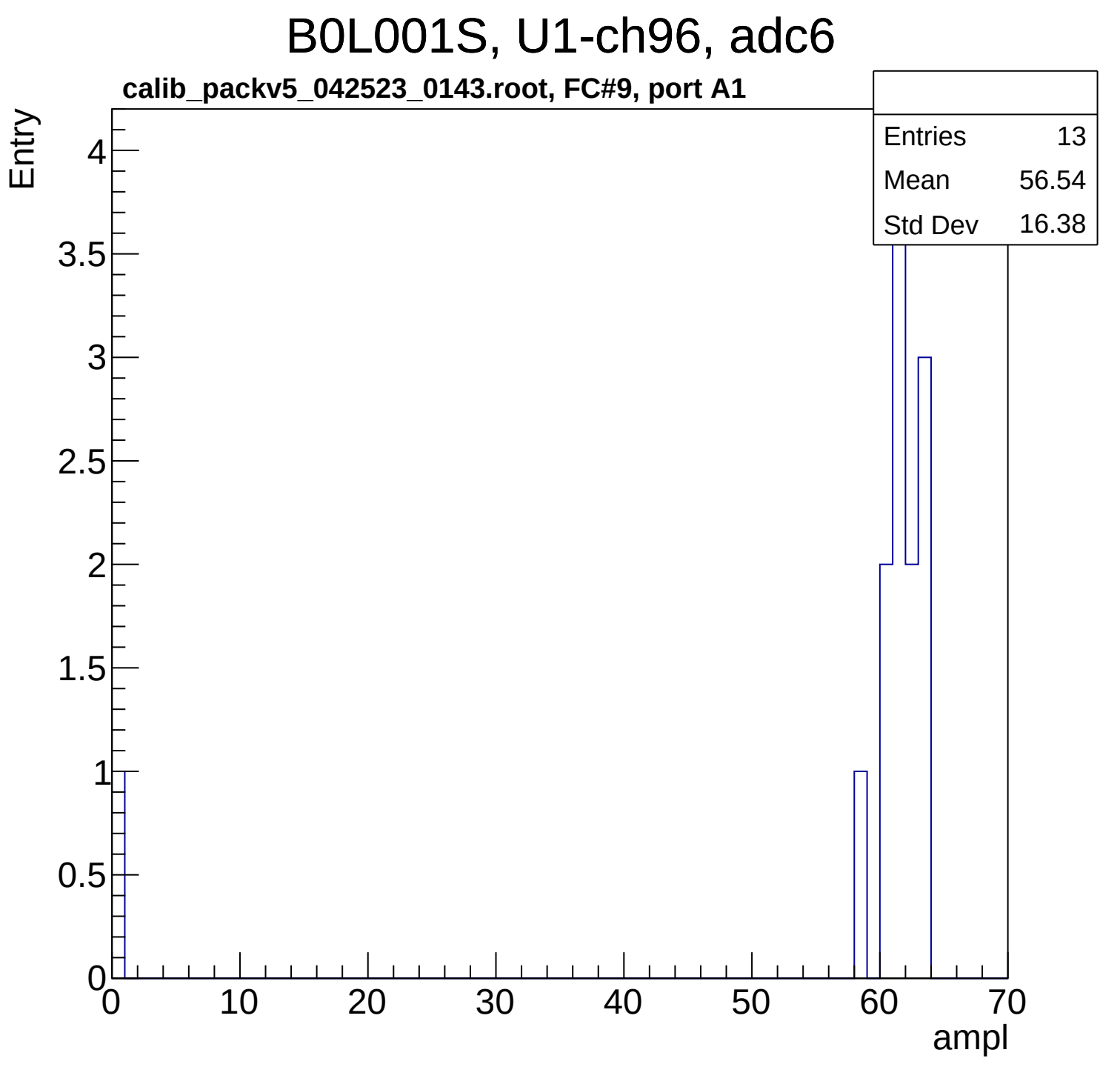

B0L001S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 59.37
Std Dev 2.768
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 56.54
Std Dev 16.38
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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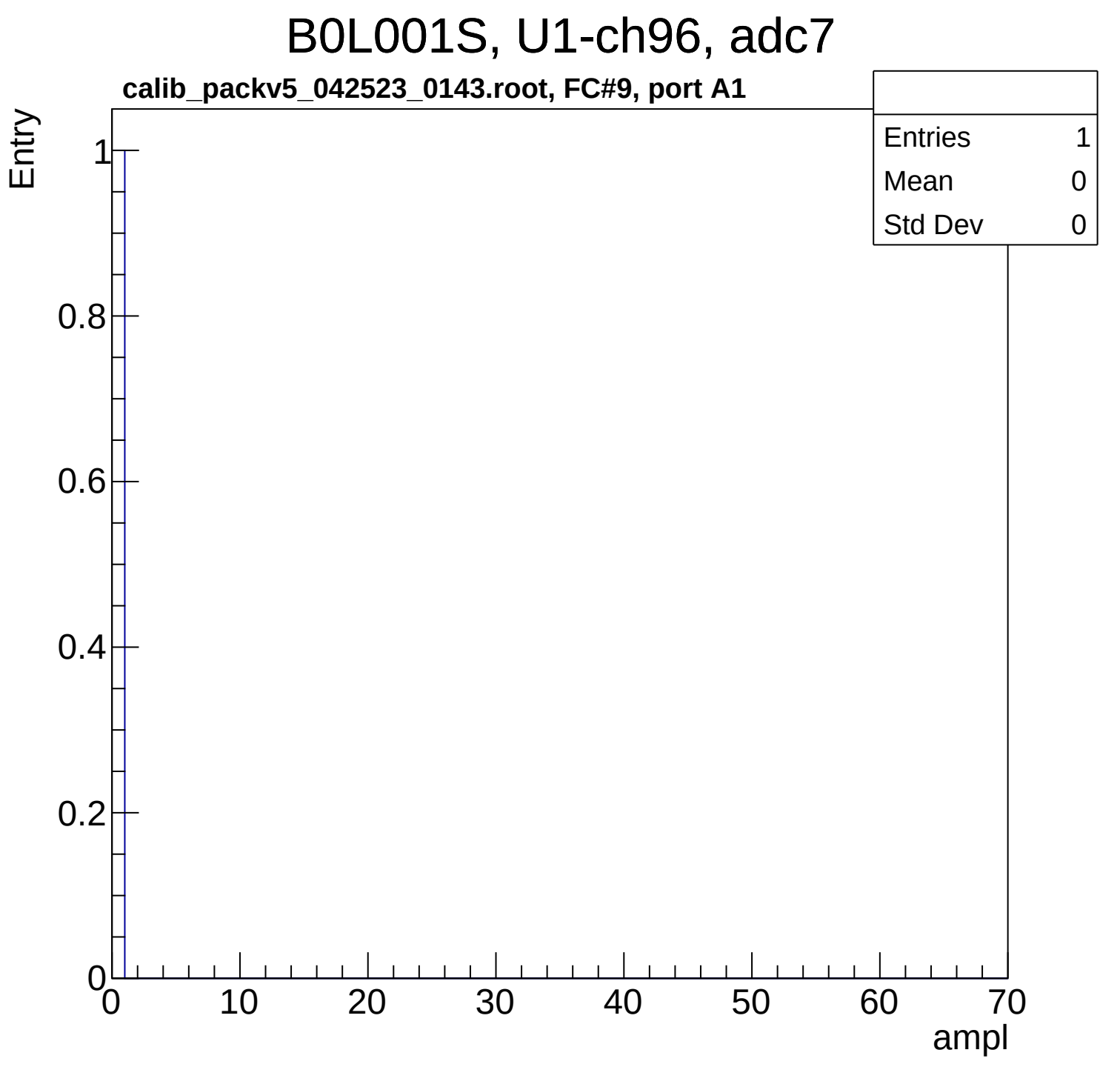
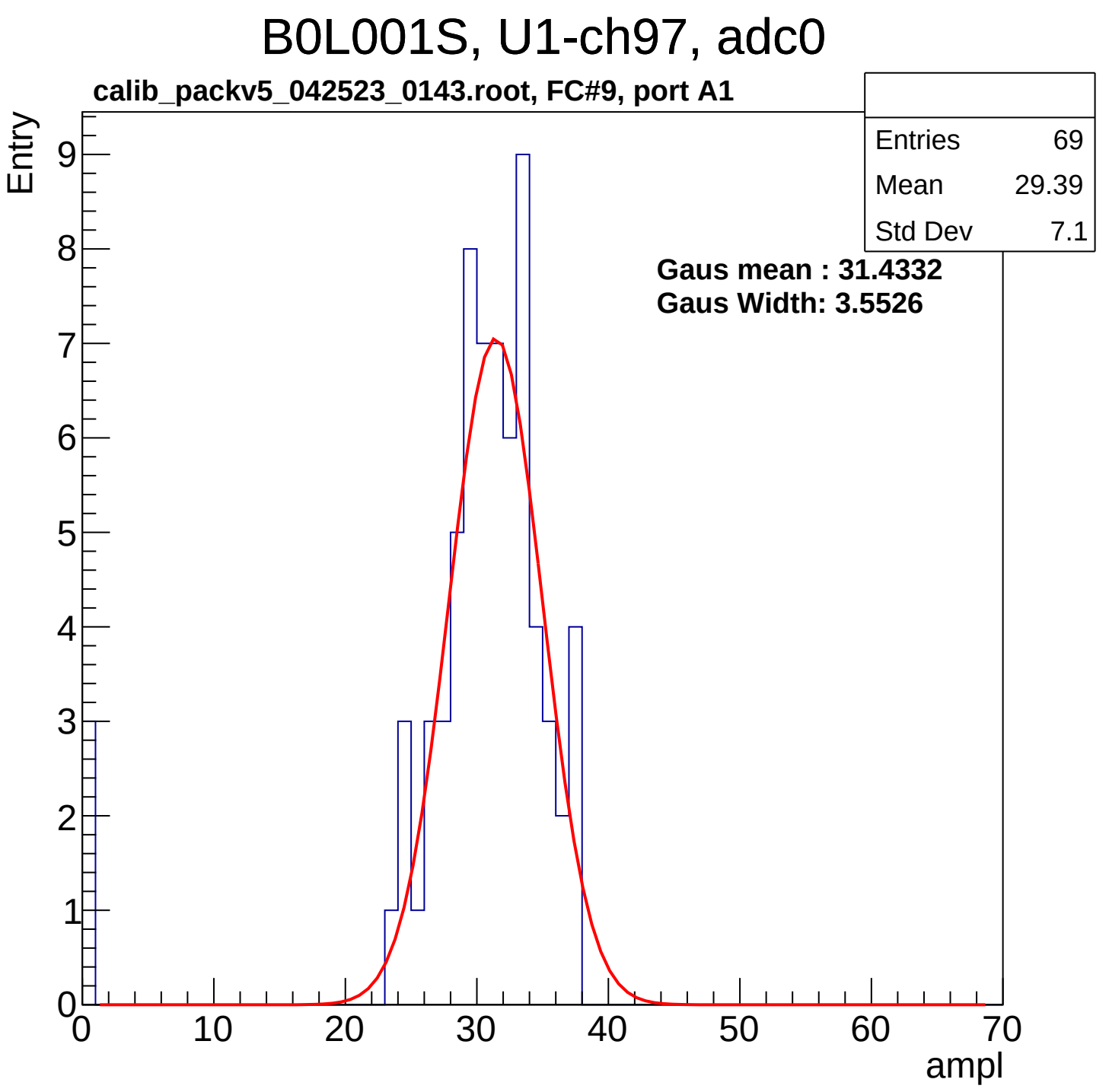
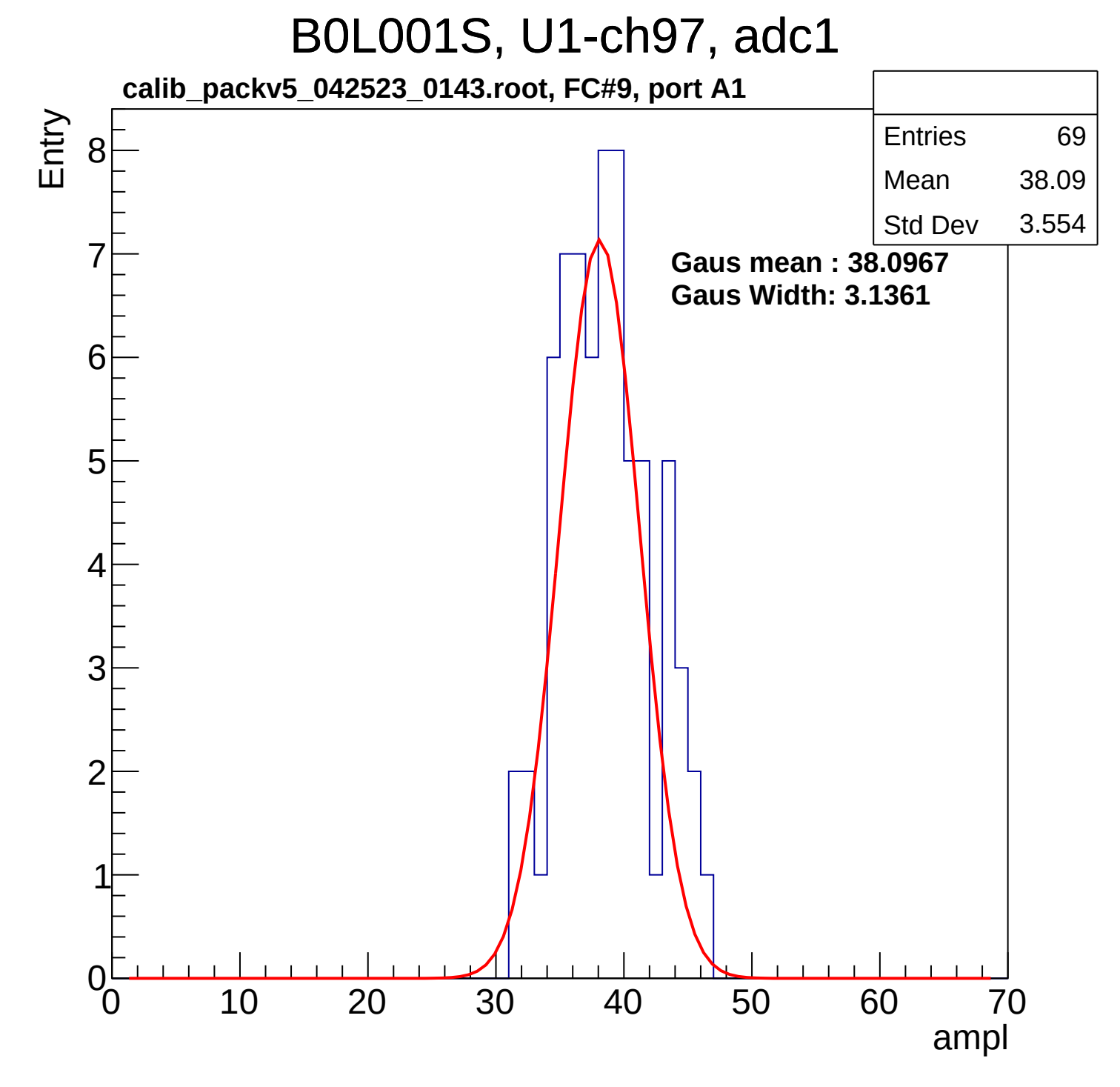
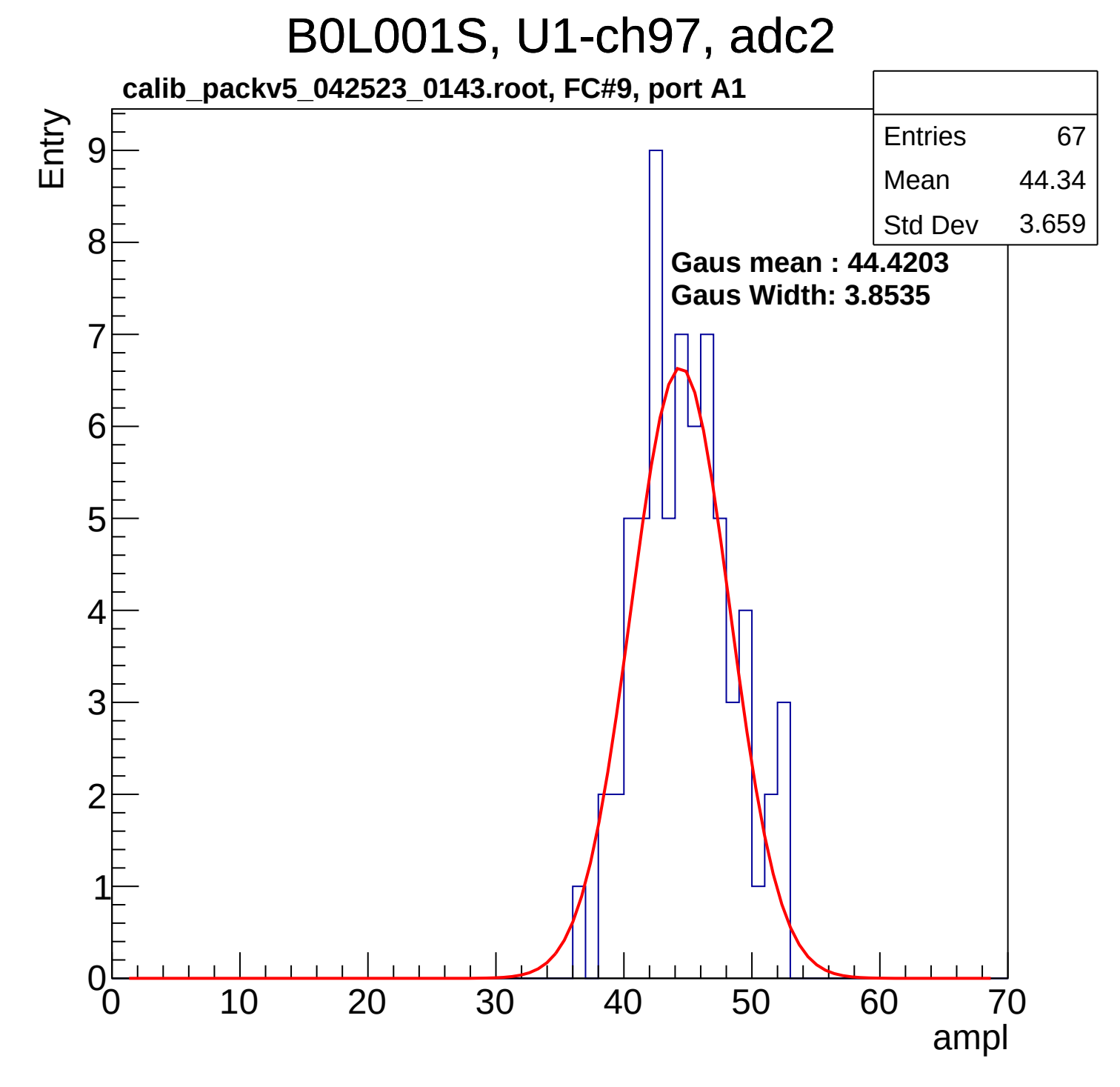
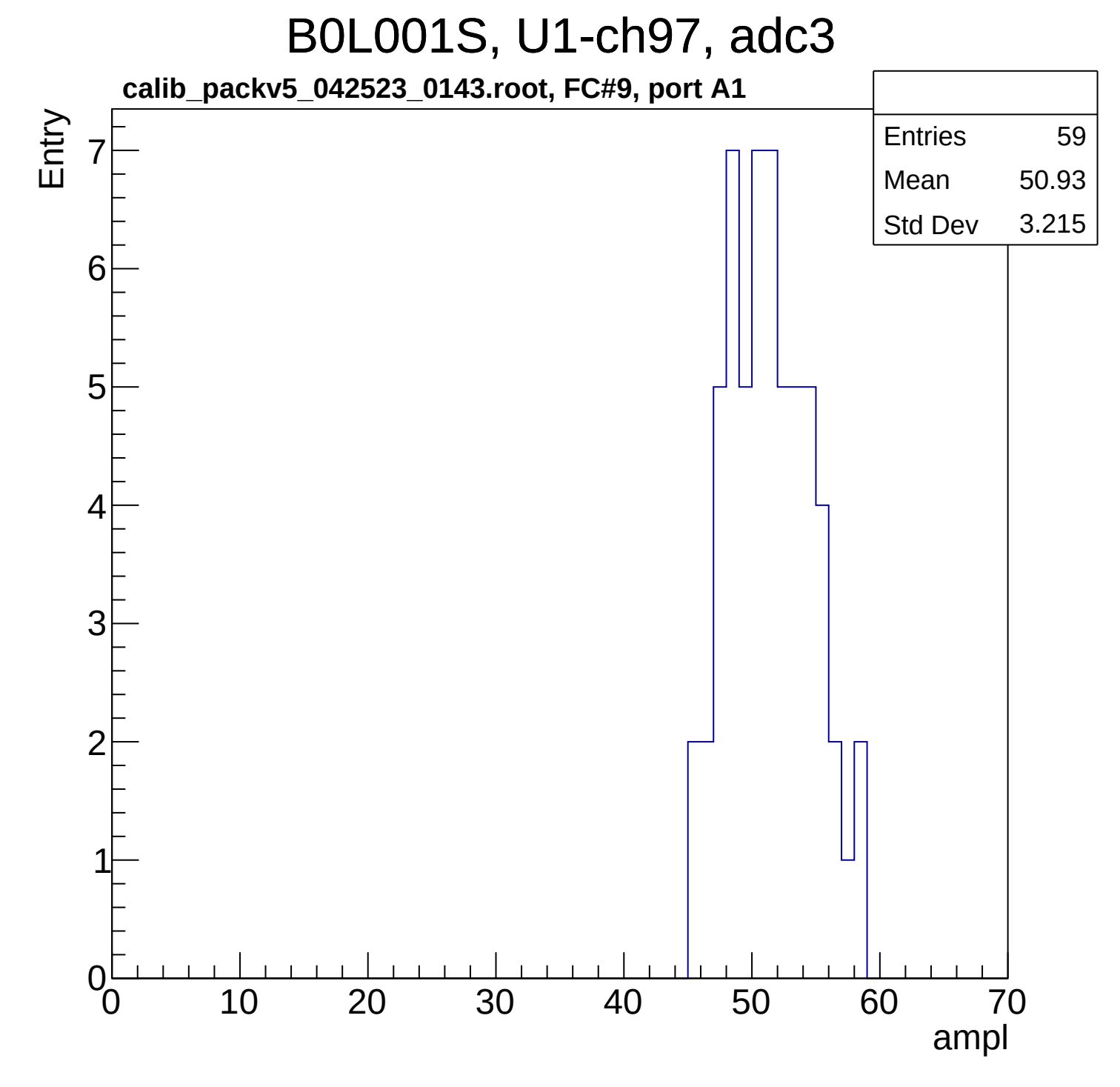
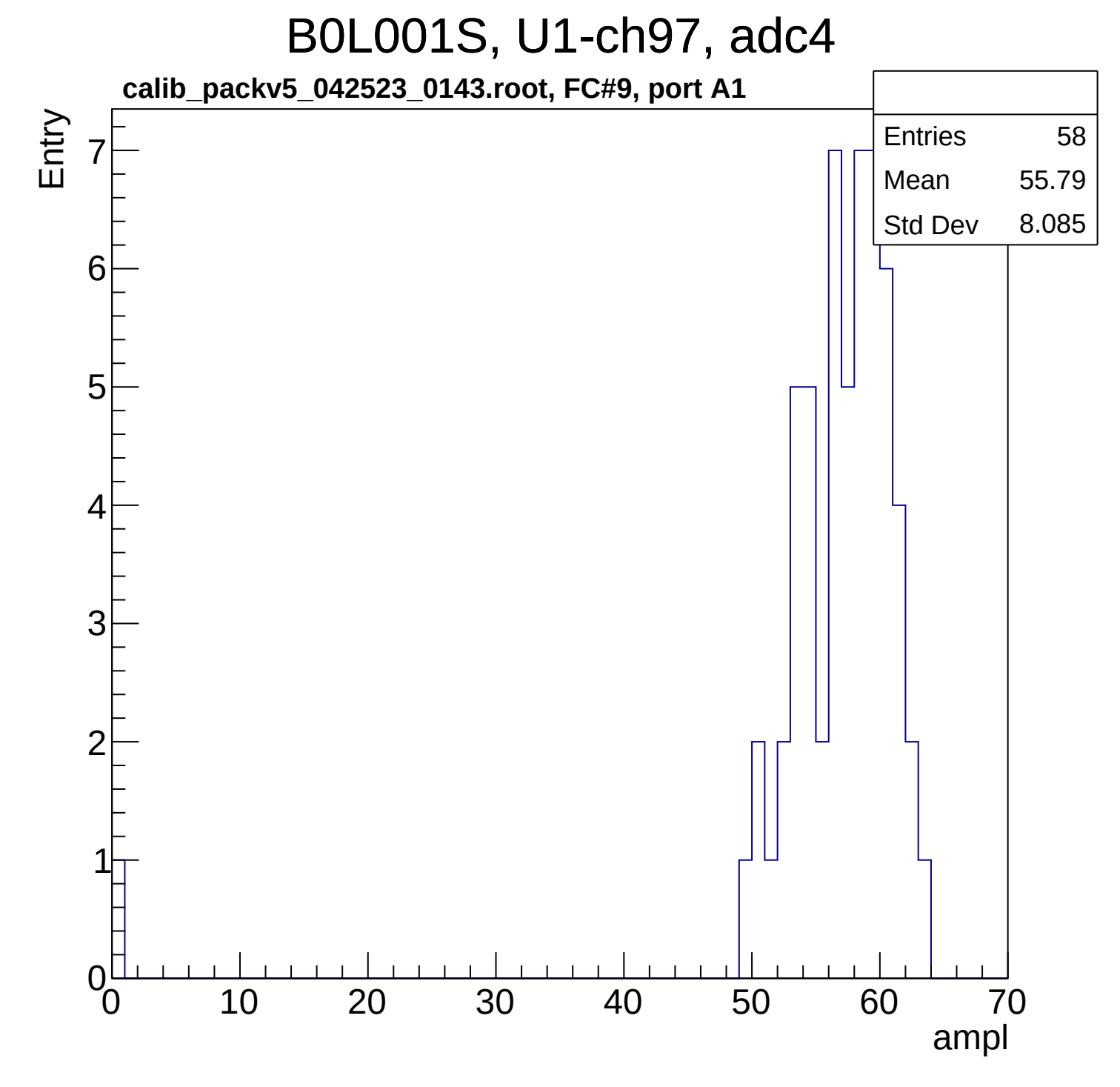

B0L001S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 29.39
Std Dev 7.1
Gaus mean : 31.4332
Gaus Width: 3.5526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 38.09
Std Dev 3.554
Gaus mean : 38.0967
Gaus Width: 3.1361
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 44.34
Std Dev 3.659
Gaus mean : 44.4203
Gaus Width: 3.8535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 50.93
Std Dev 3.215
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 55.79
Std Dev 8.085
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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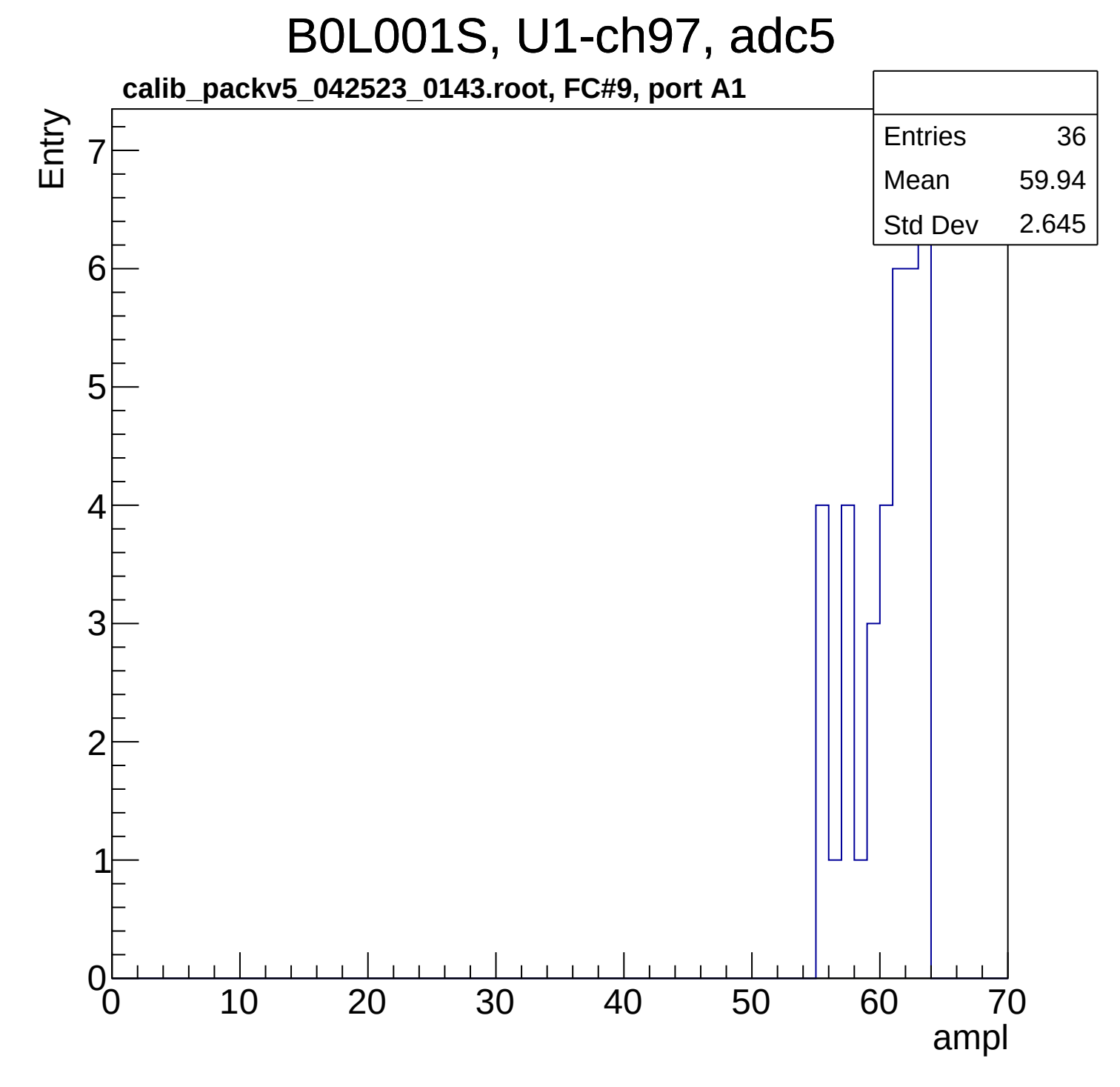
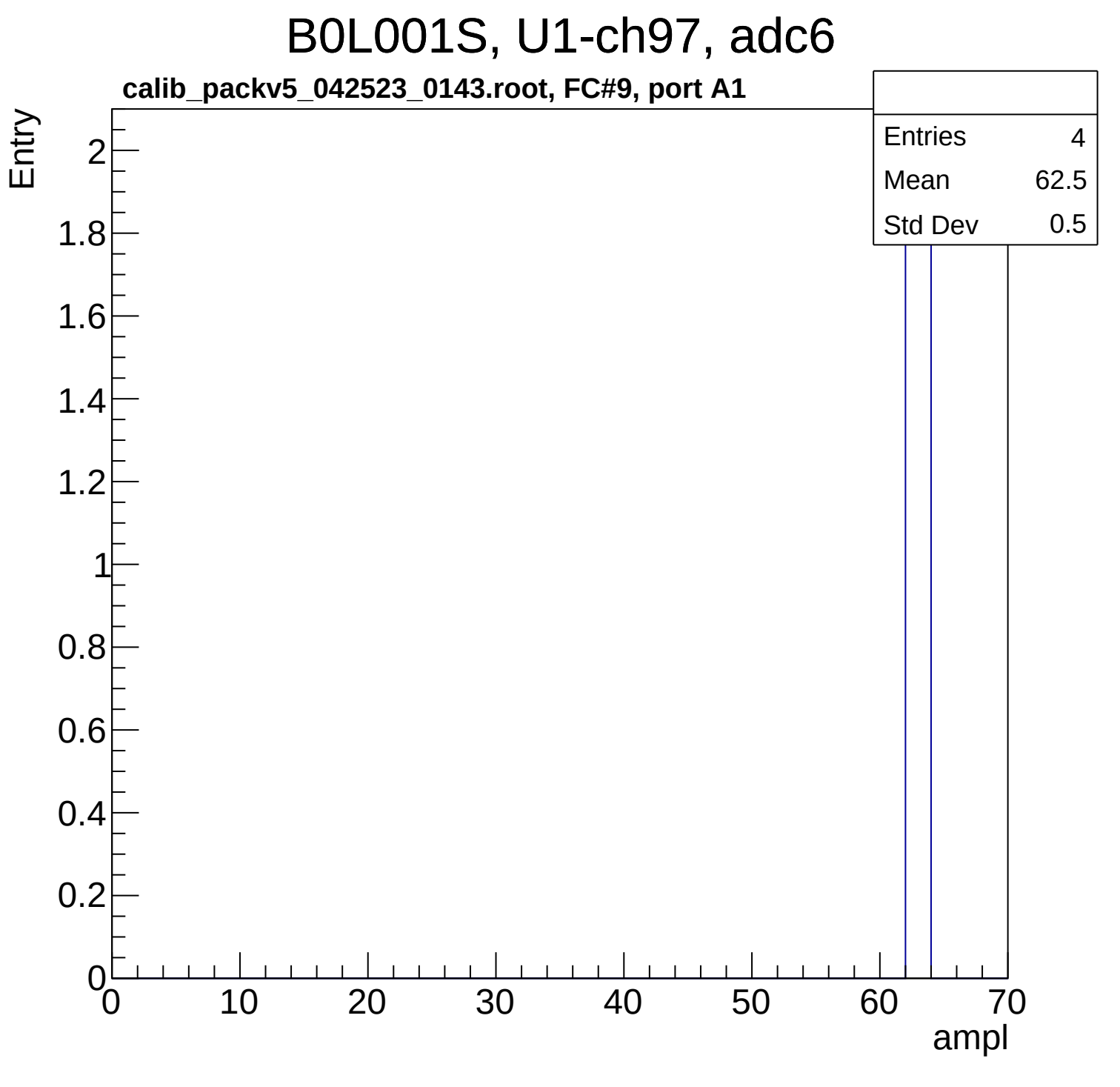
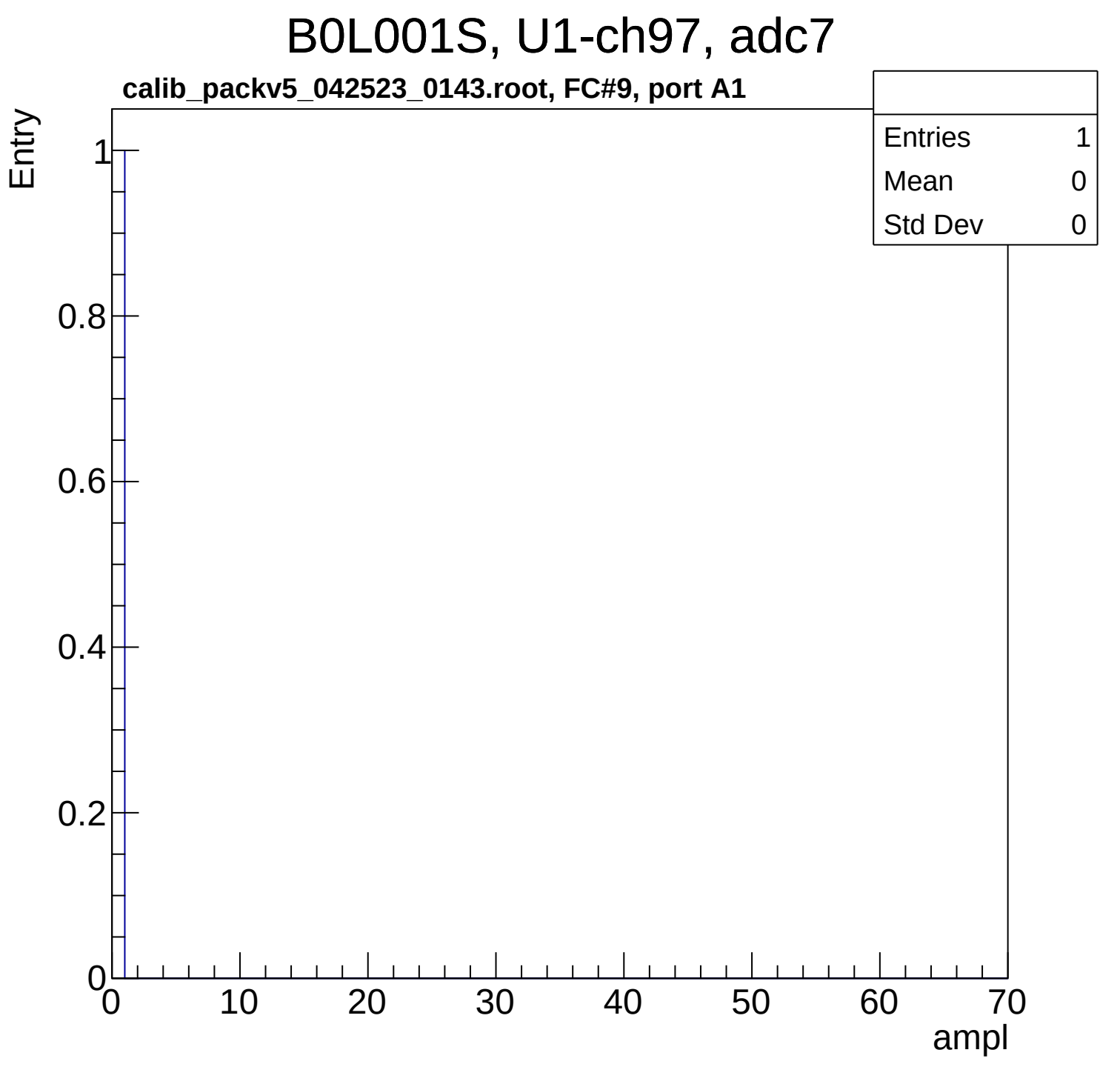
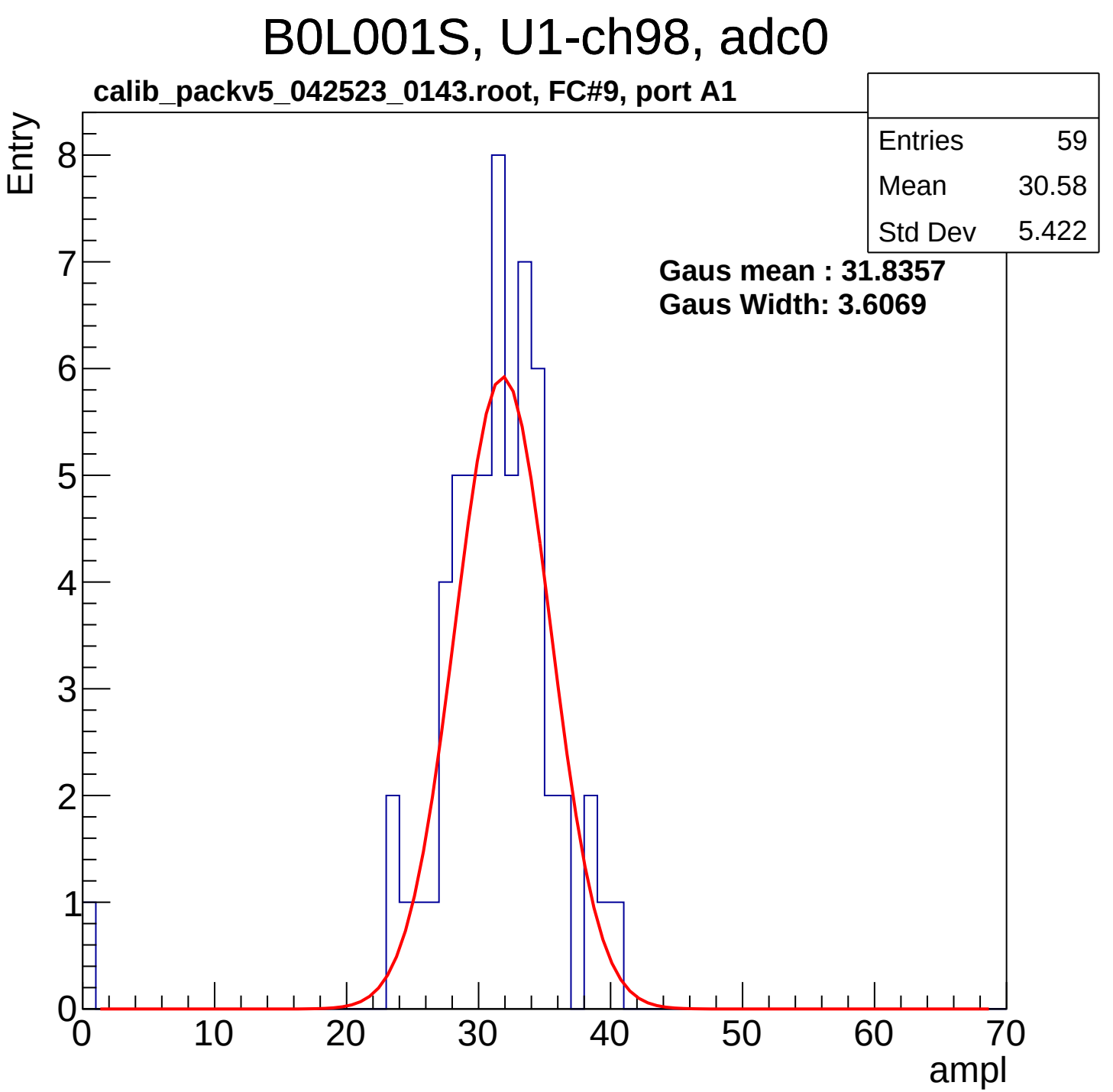
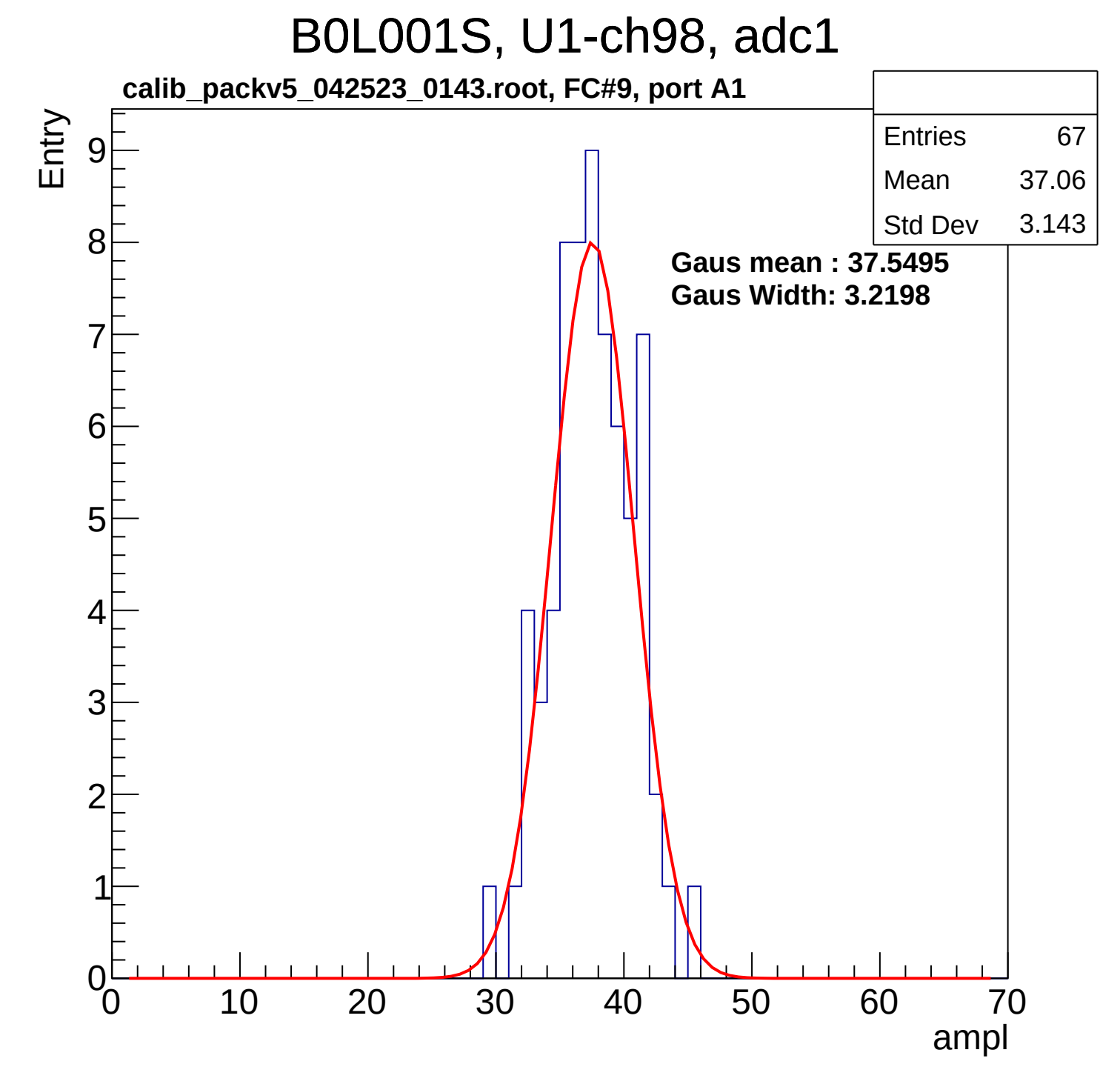
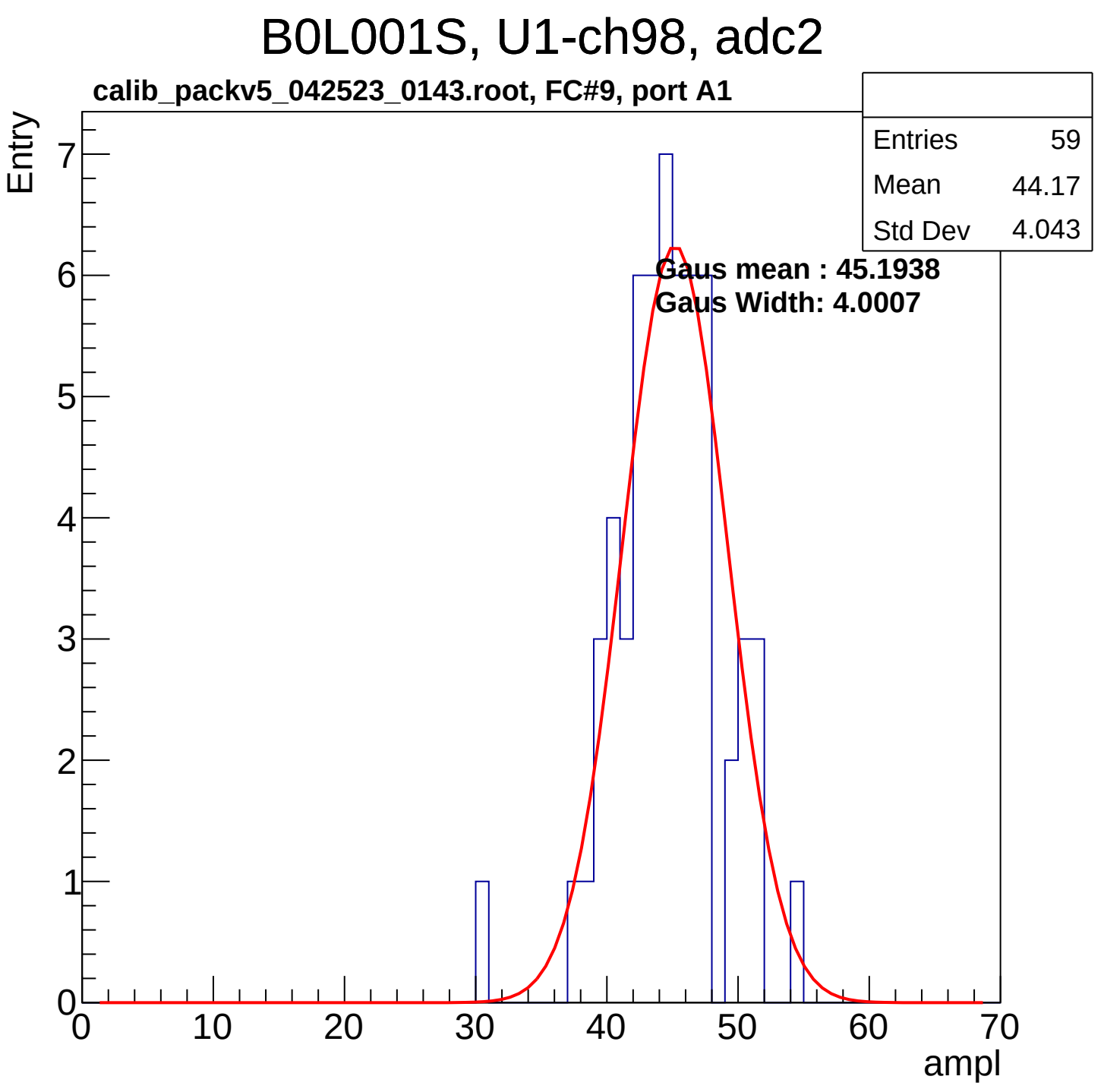

B0L001S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 36
Mean 59.94
Std Dev 2.645
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

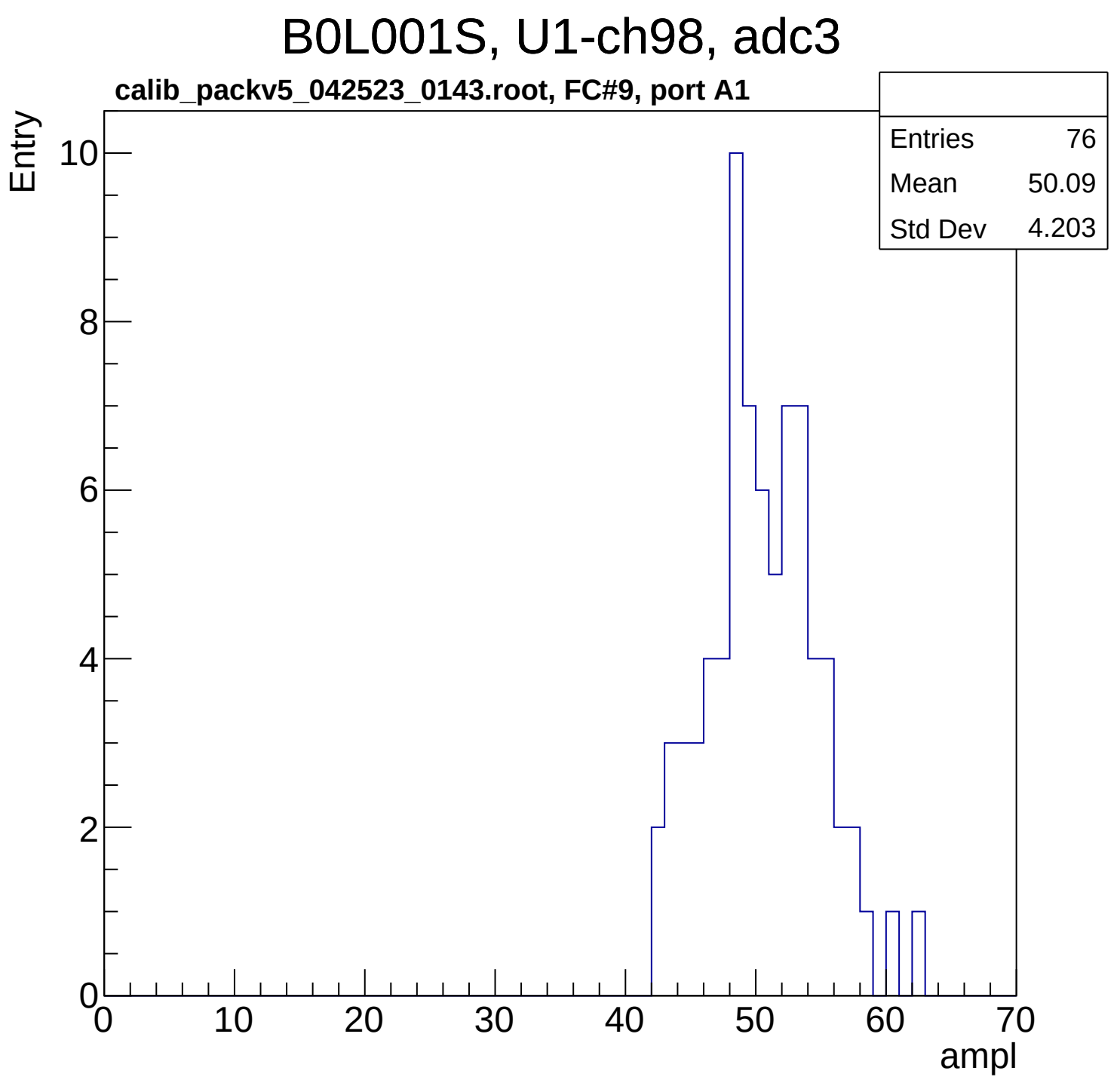

B0L001S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

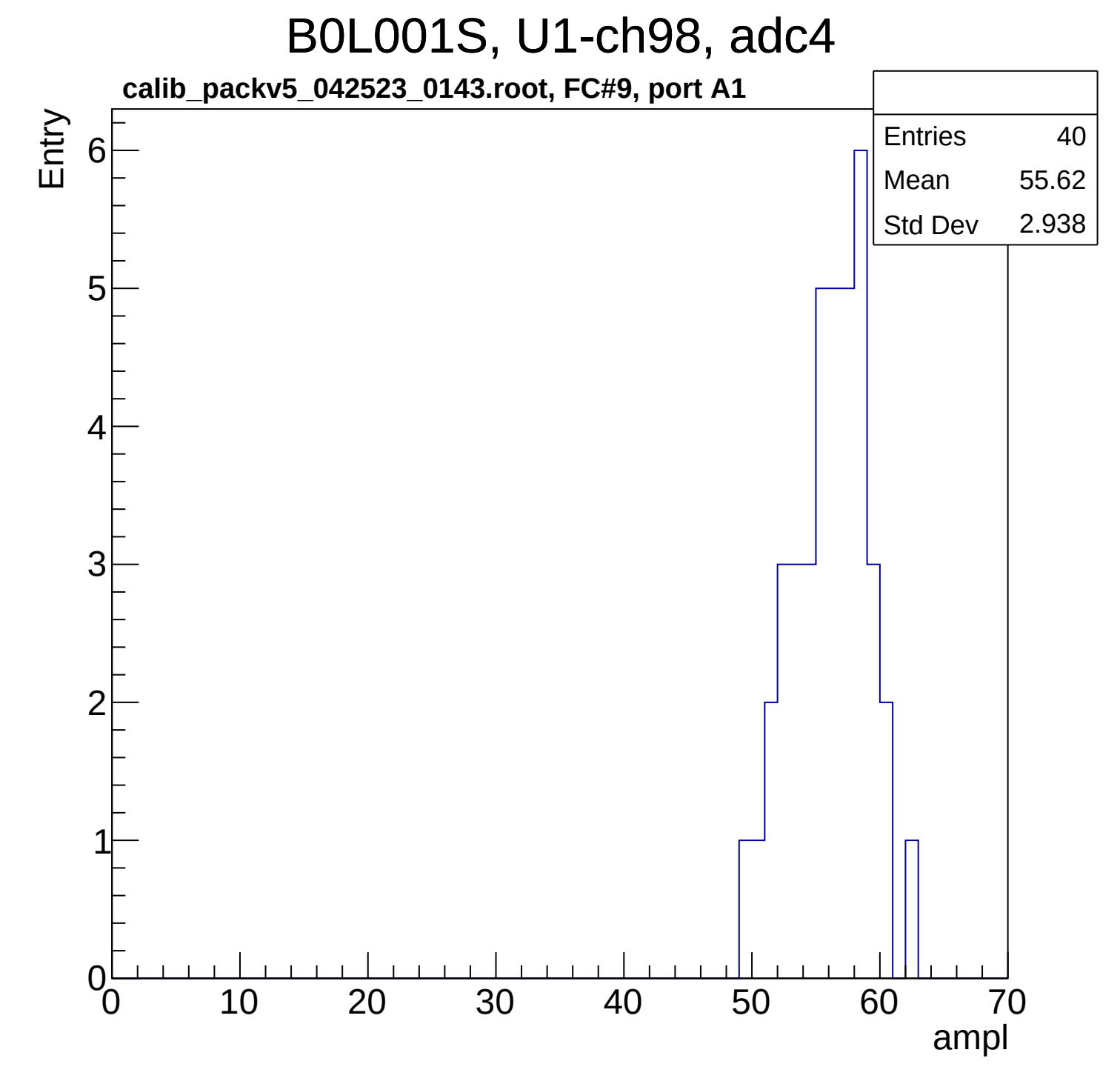

B0L001S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

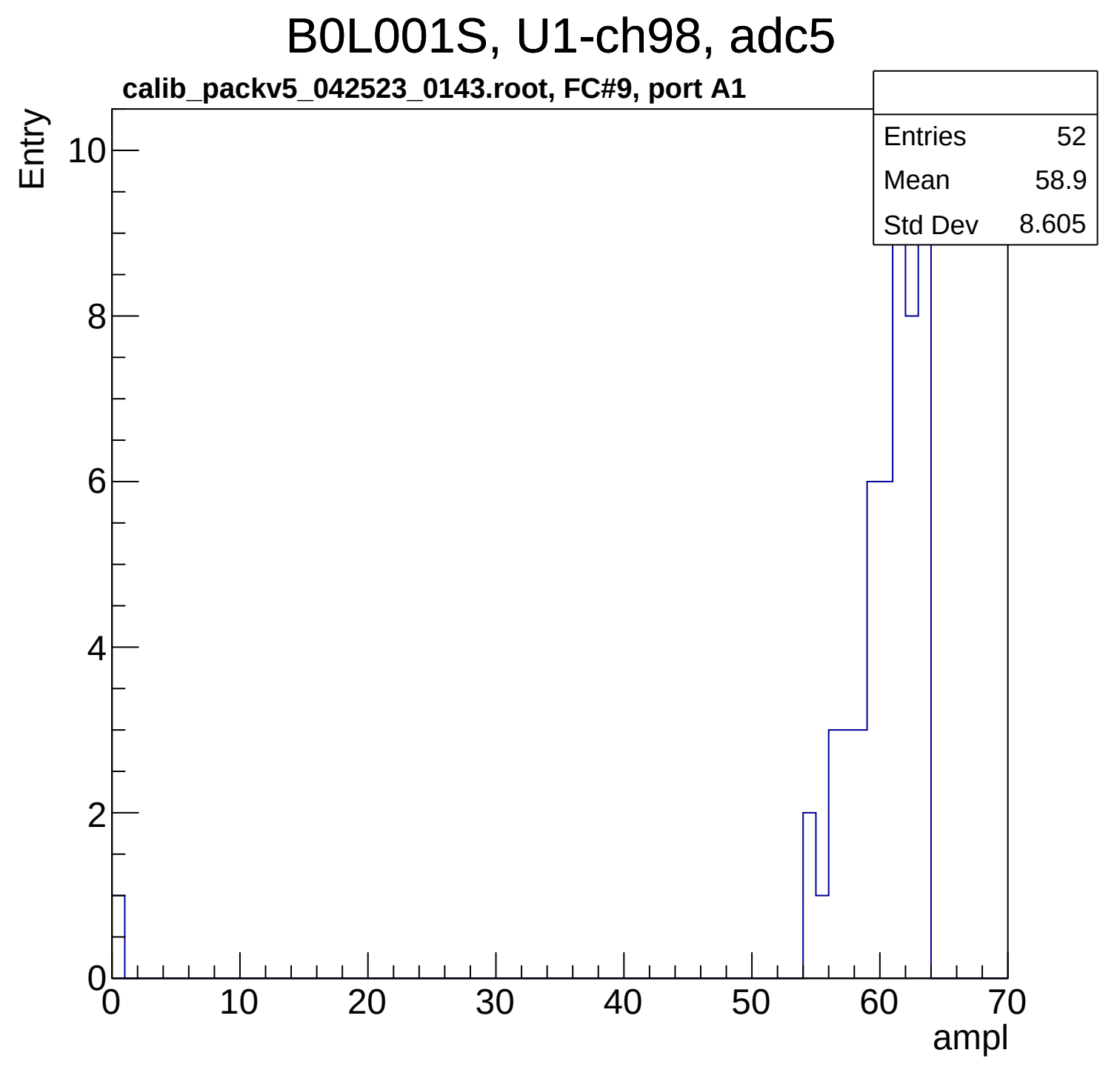

B0L001S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.58
Std Dev 5.422
Gaus mean : 31.8357
Gaus Width: 3.6069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 37.06
Std Dev 3.143
Gaus mean : 37.5495
Gaus Width: 3.2198
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.17
Std Dev 4.043
Gaus mean : 45.1938
Gaus Width: 4.0007
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 50.09
Std Dev 4.203
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 55.62
Std Dev 2.938
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 58.9
Std Dev 8.605
Entry
ampl
0
2
4
6
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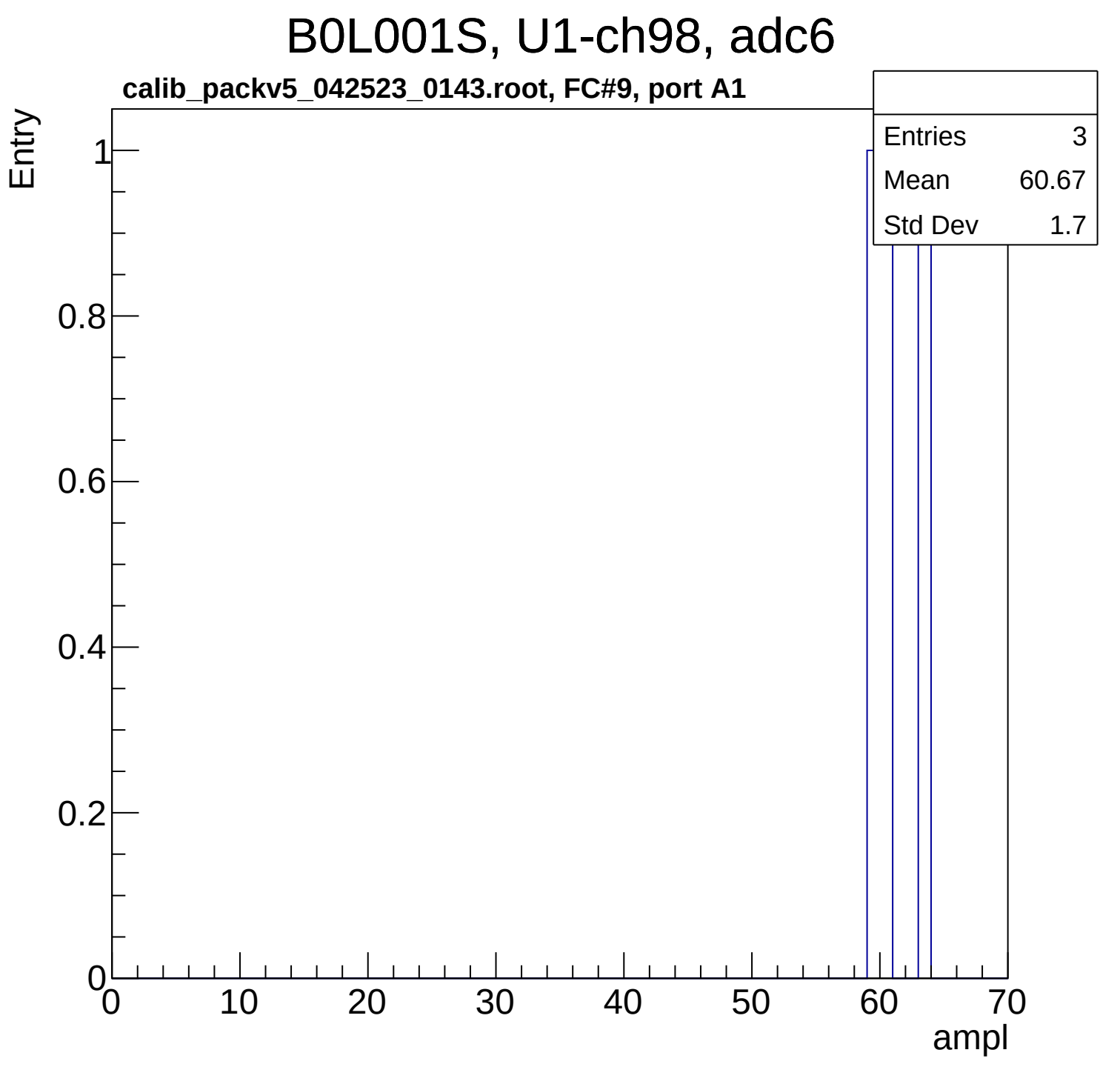
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 60.67
Std Dev 1.7
Entry
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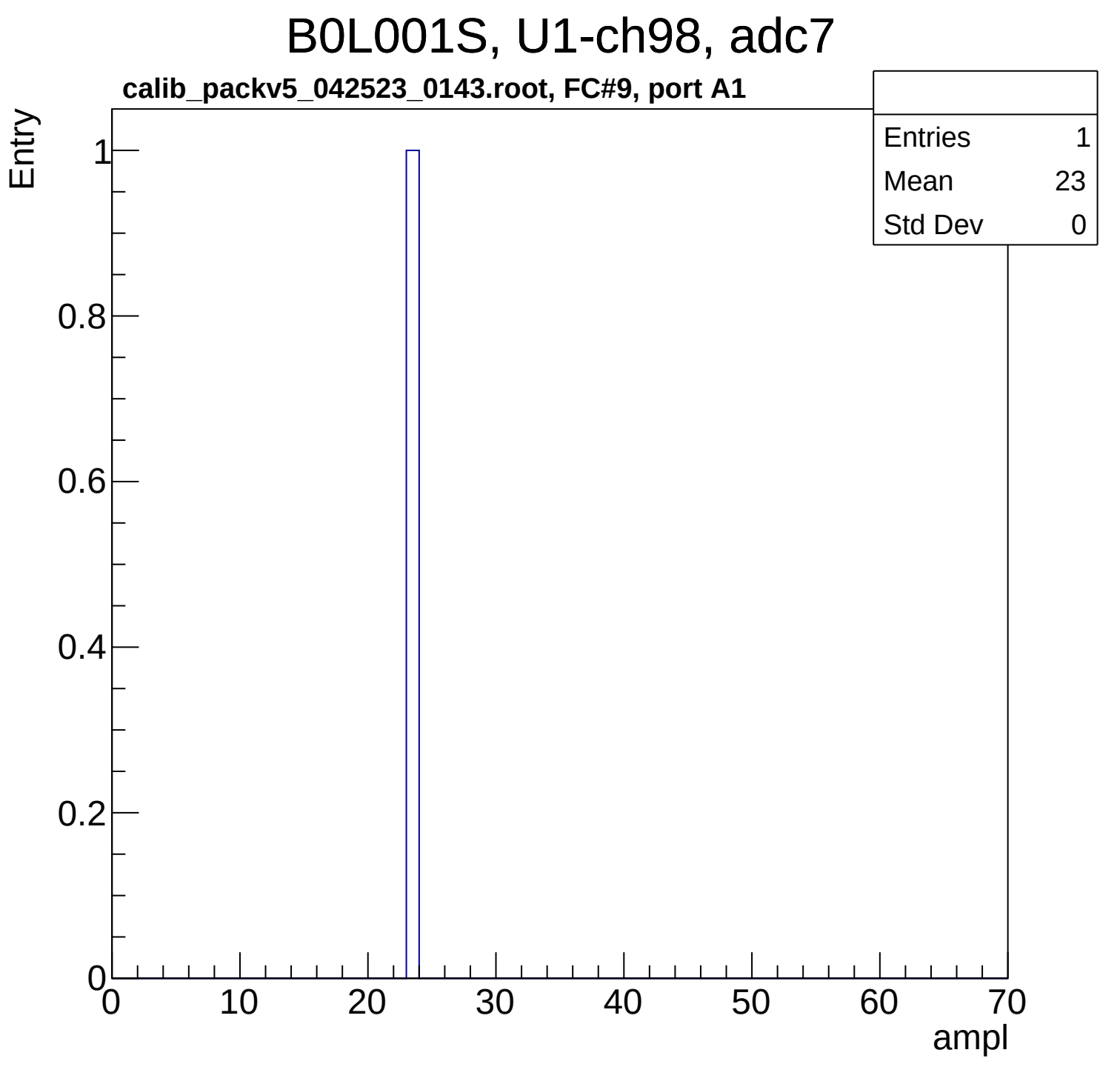
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 23
Std Dev 0
Entry
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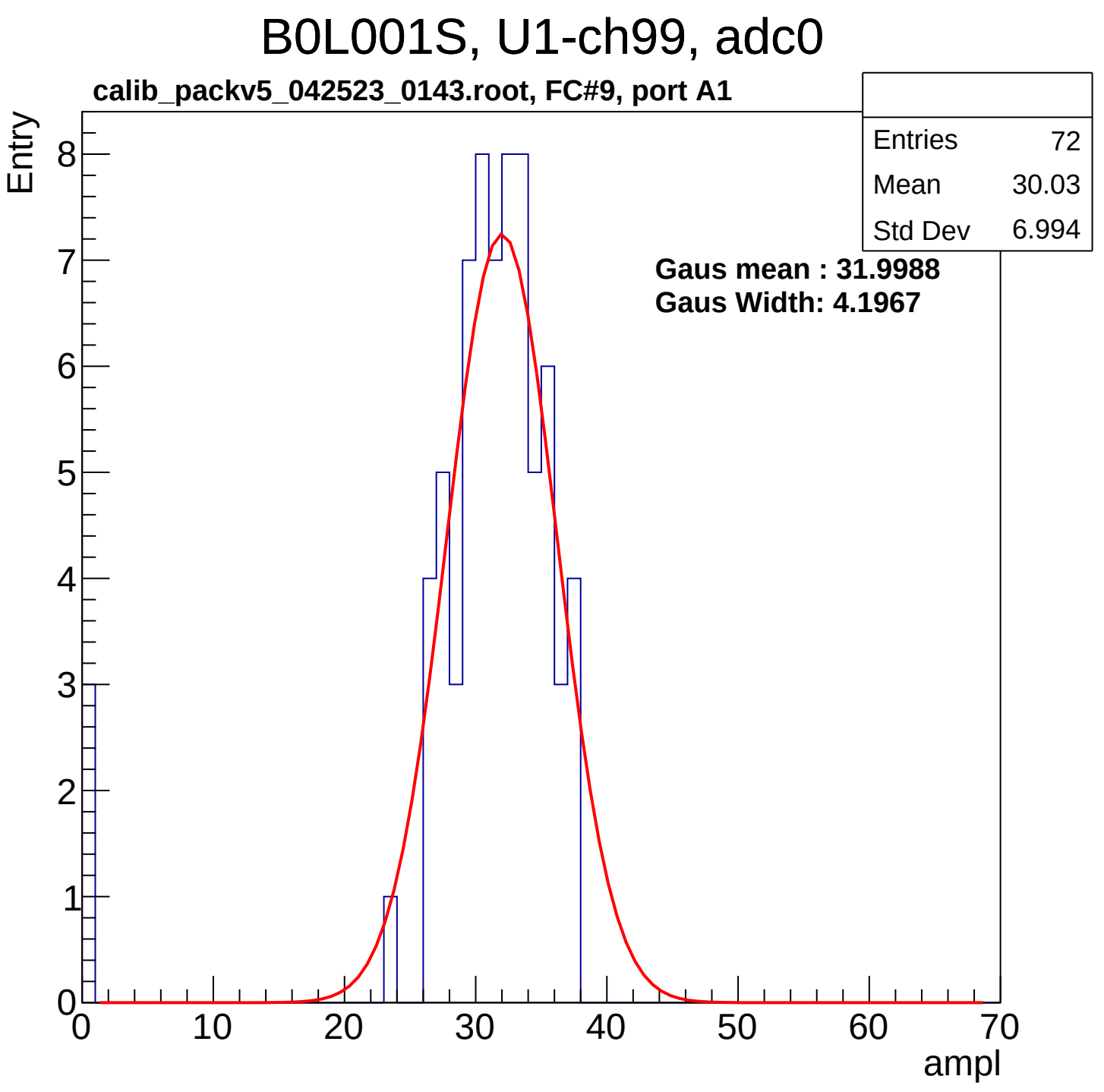
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 72
Mean 30.03
Std Dev 6.994
Gaus mean : 31.9988
Gaus Width: 4.1967
Entry
ampl
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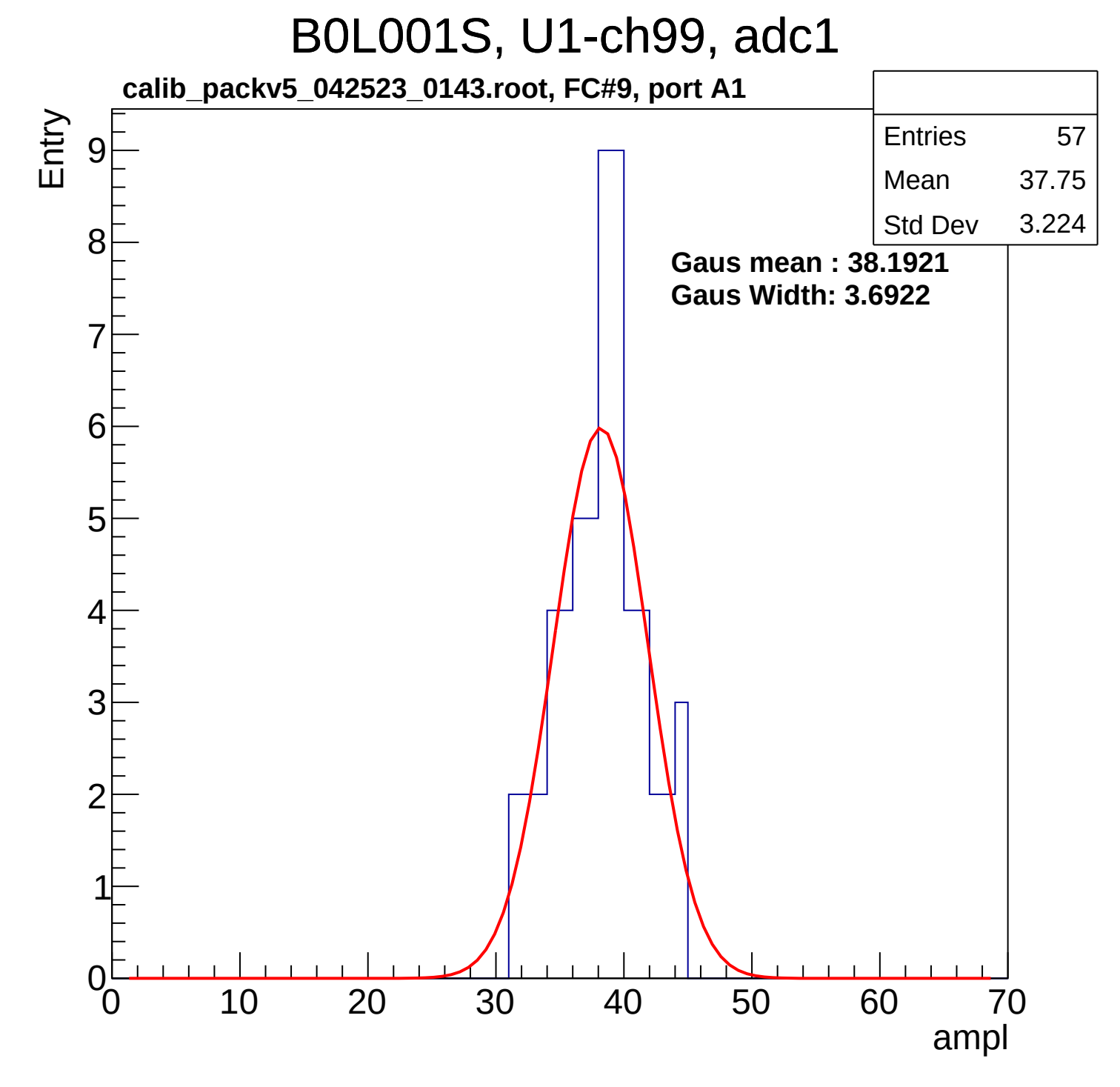
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 37.75
Std Dev 3.224
Gaus mean : 38.1921
Gaus Width: 3.6922
Entry
ampl
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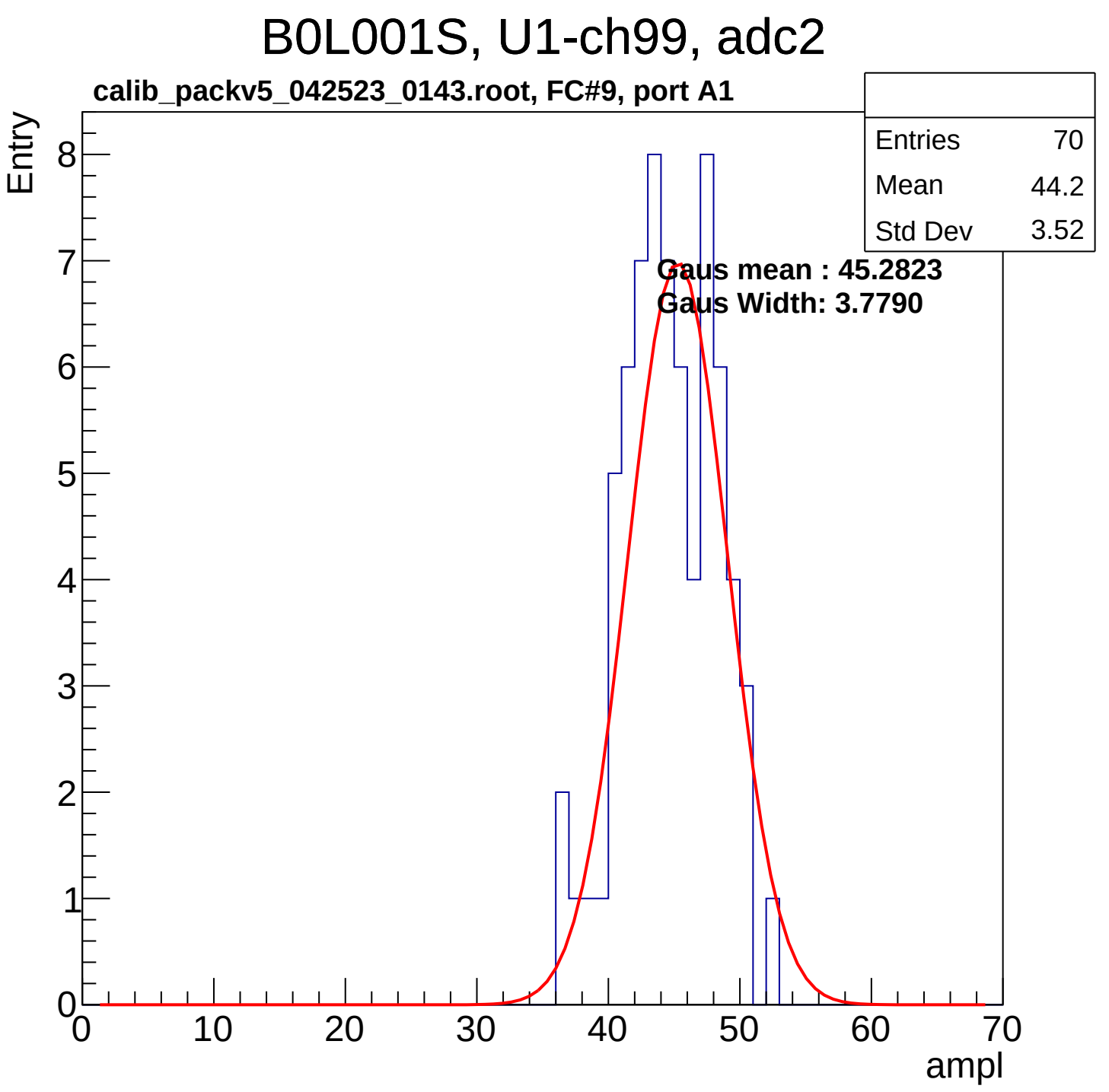
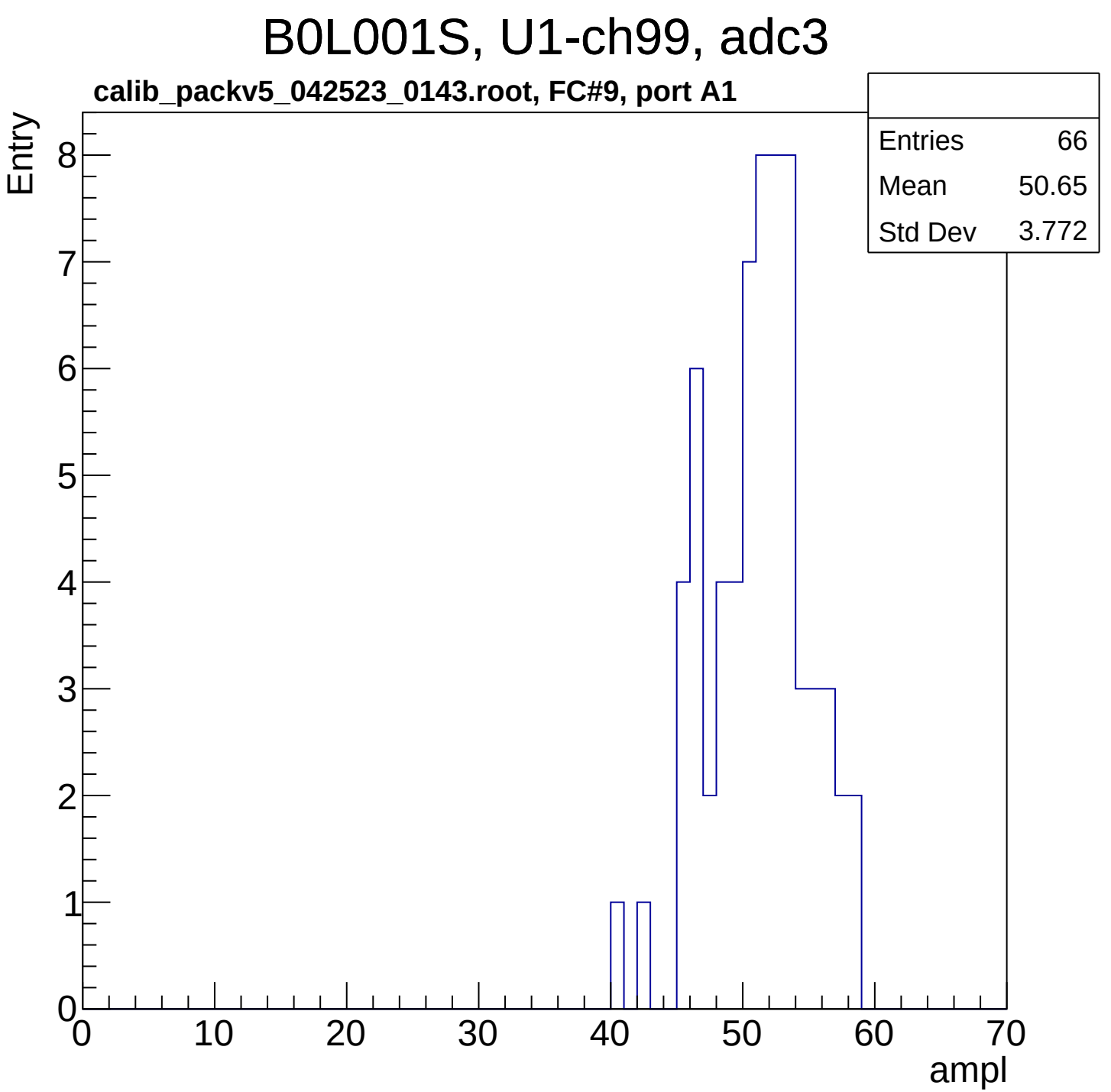
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.2
Std Dev 3.52
Gaus mean : 45.2823
Gaus Width: 3.7790
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.65
Std Dev 3.772
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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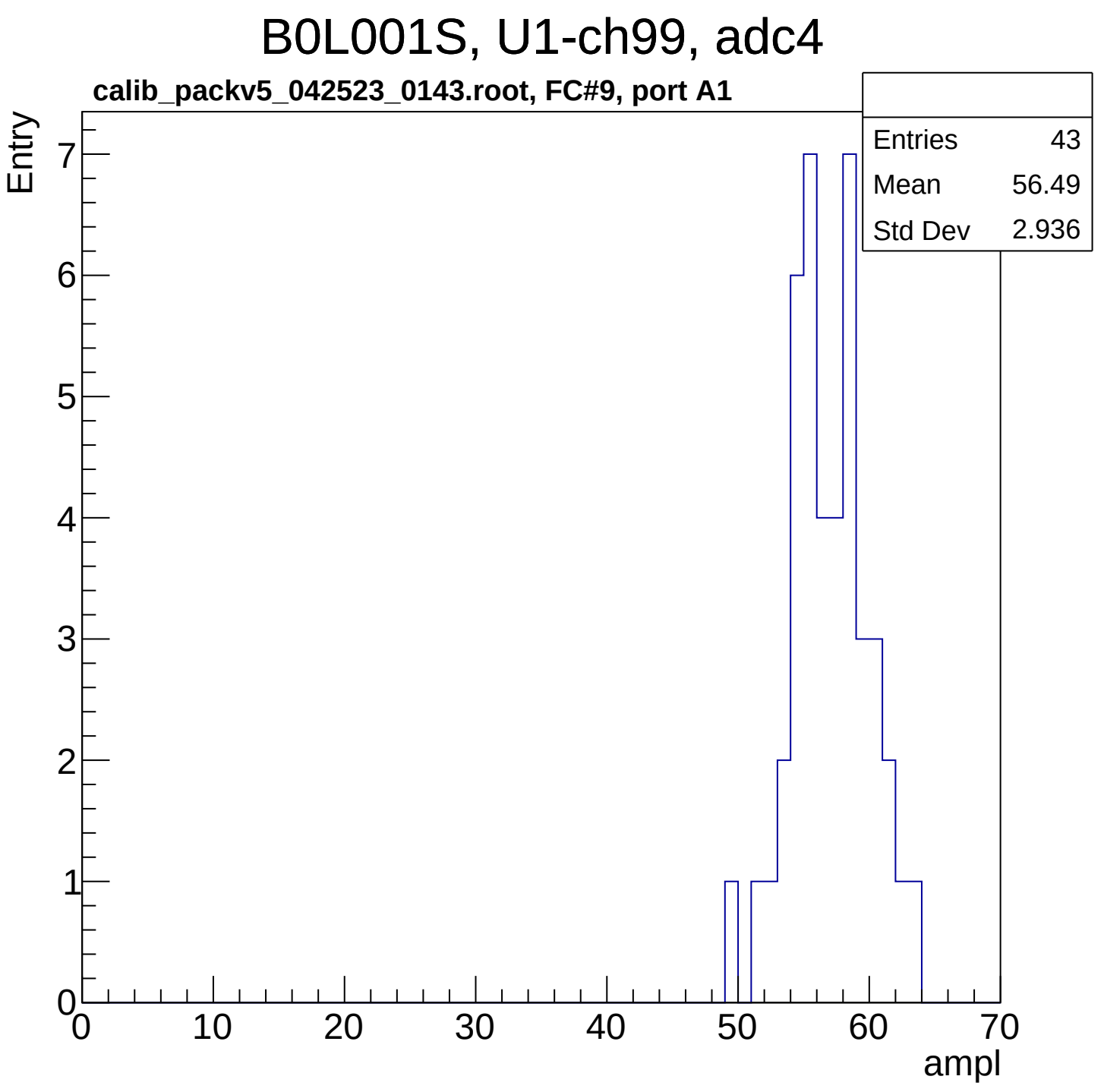
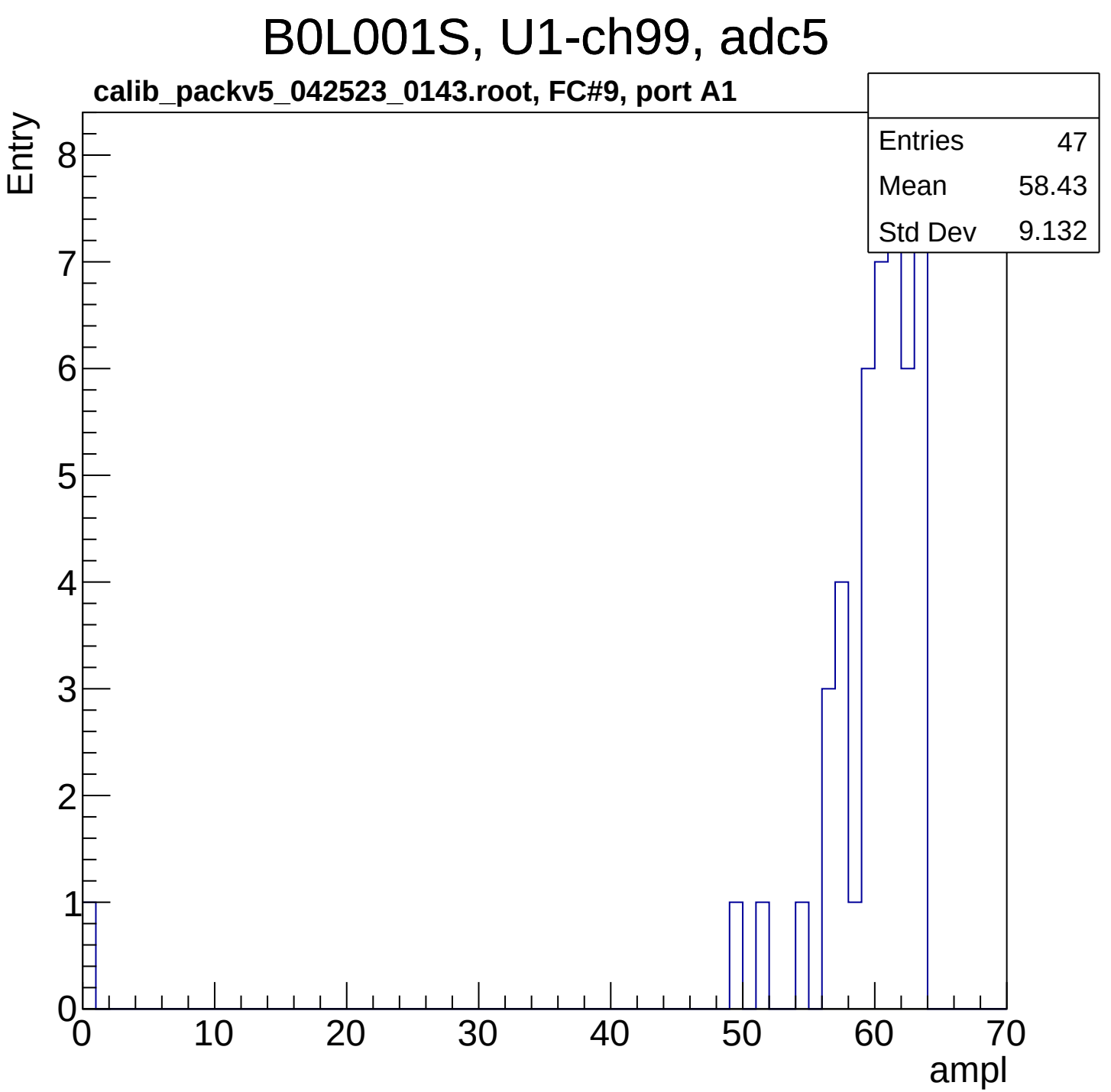

B0L001S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 56.49
Std Dev 2.936
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.43
Std Dev 9.132
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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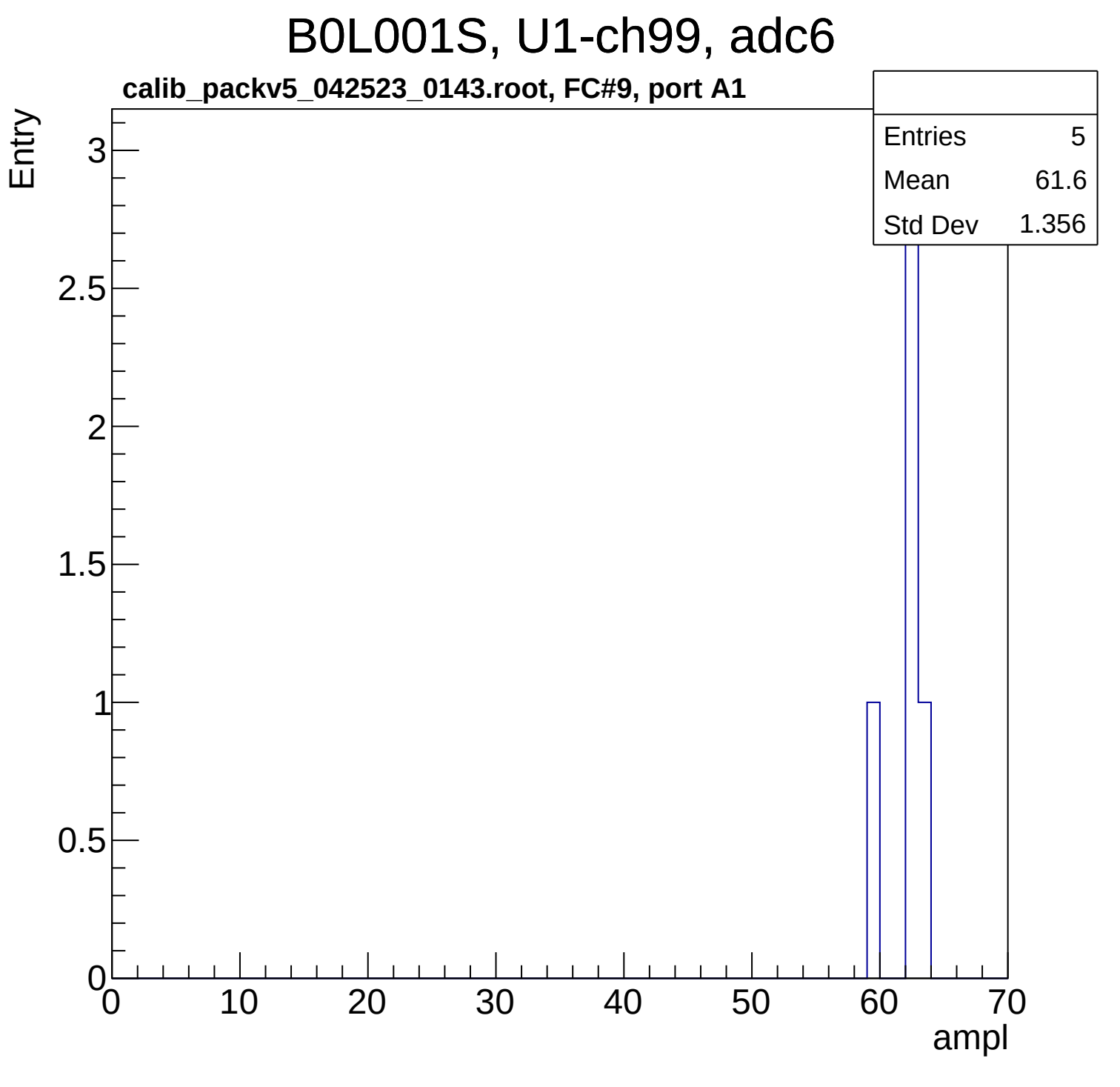
B0L001S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.6
Std Dev 1.356
Entry
ampl
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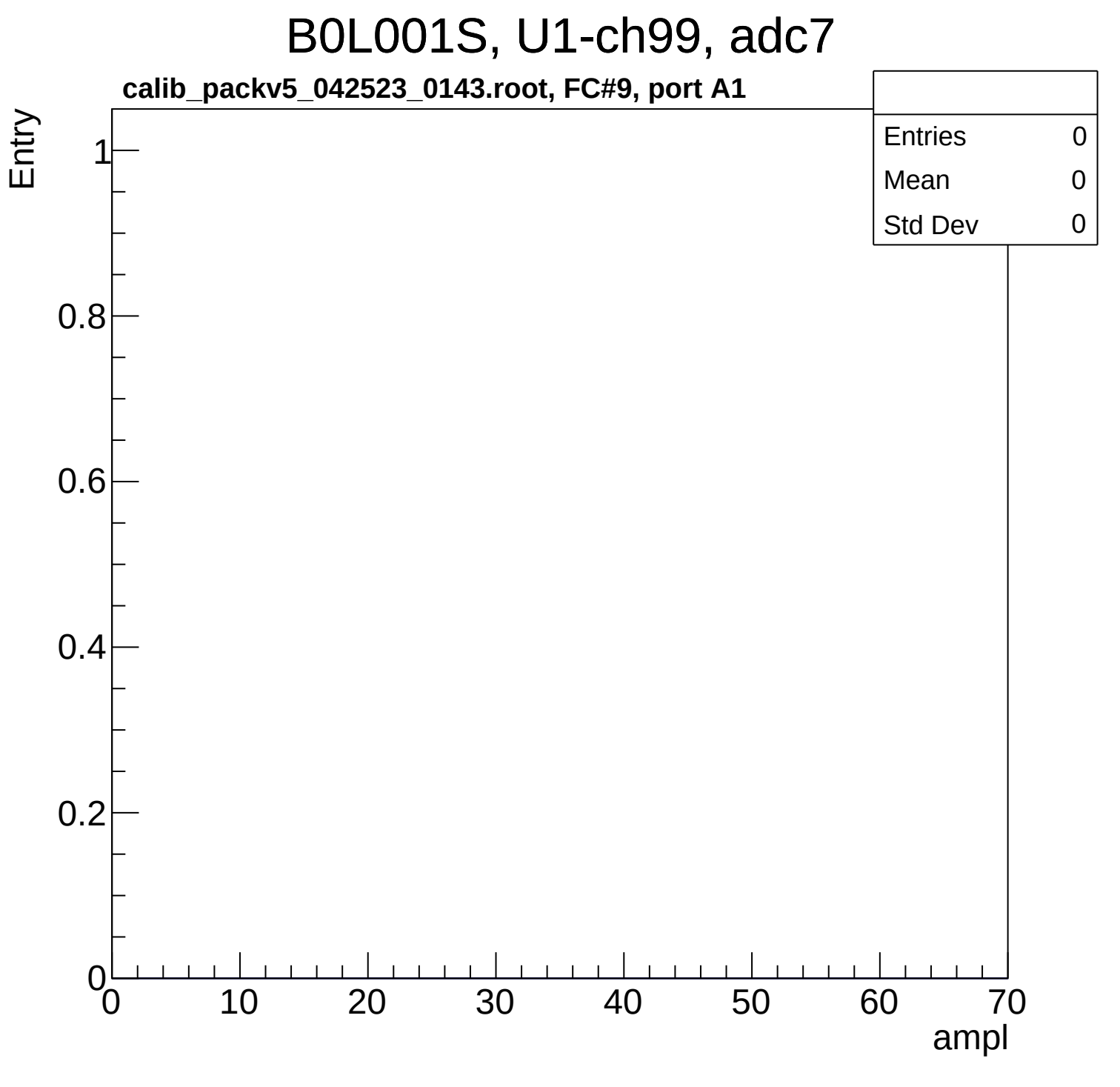
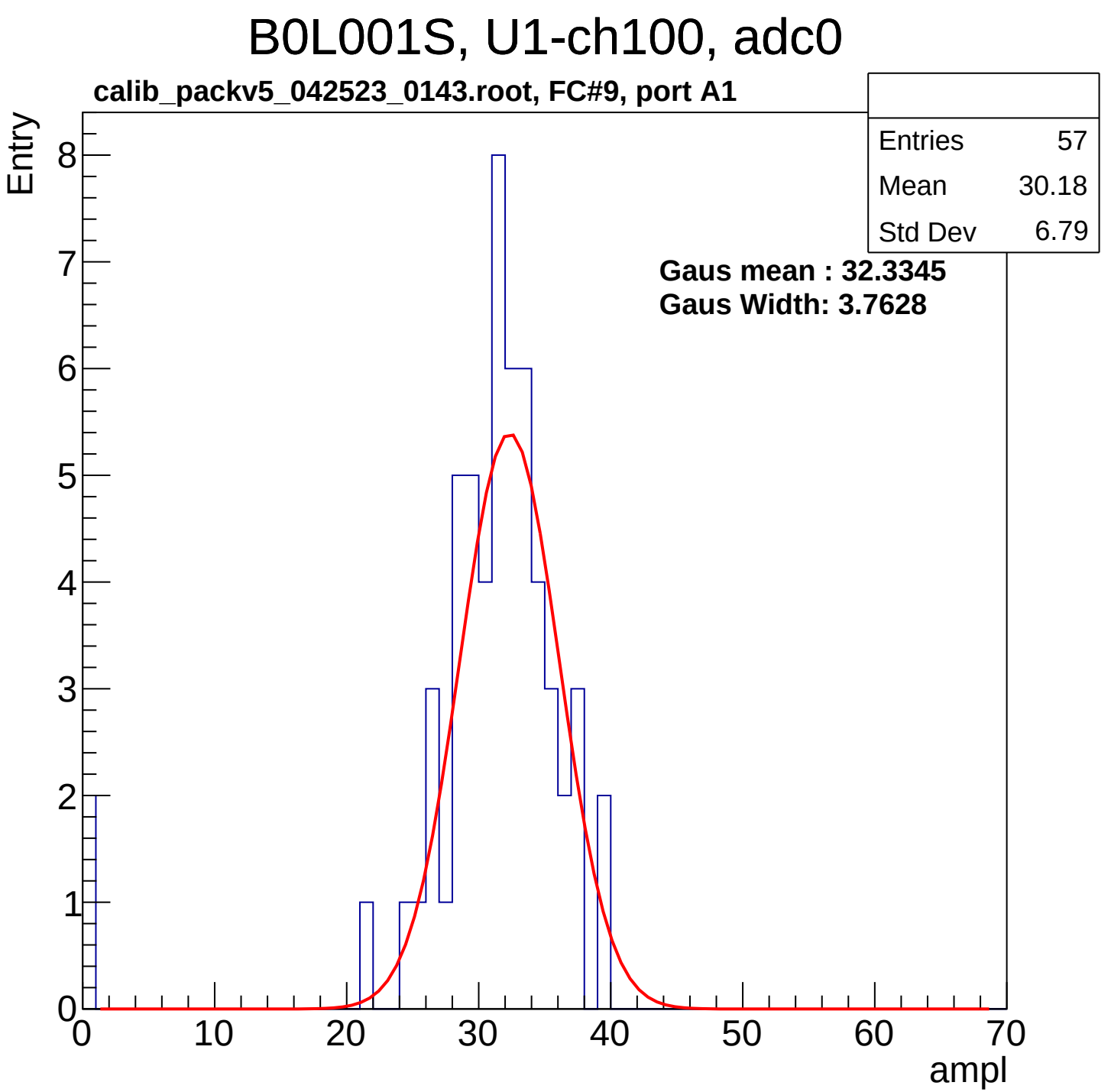
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 30.18
Std Dev 6.79
Gaus mean : 32.3345
Gaus Width: 3.7628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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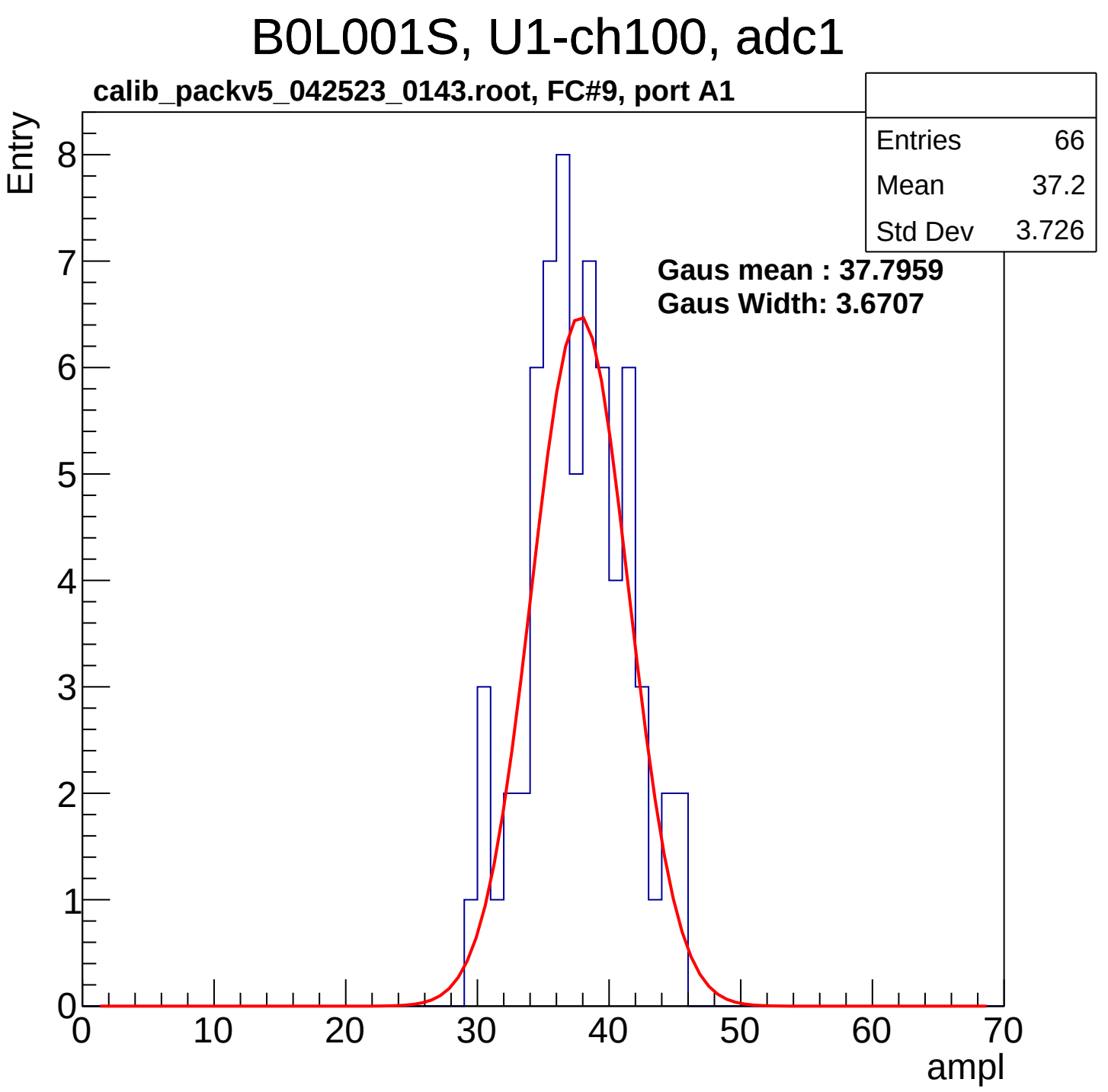
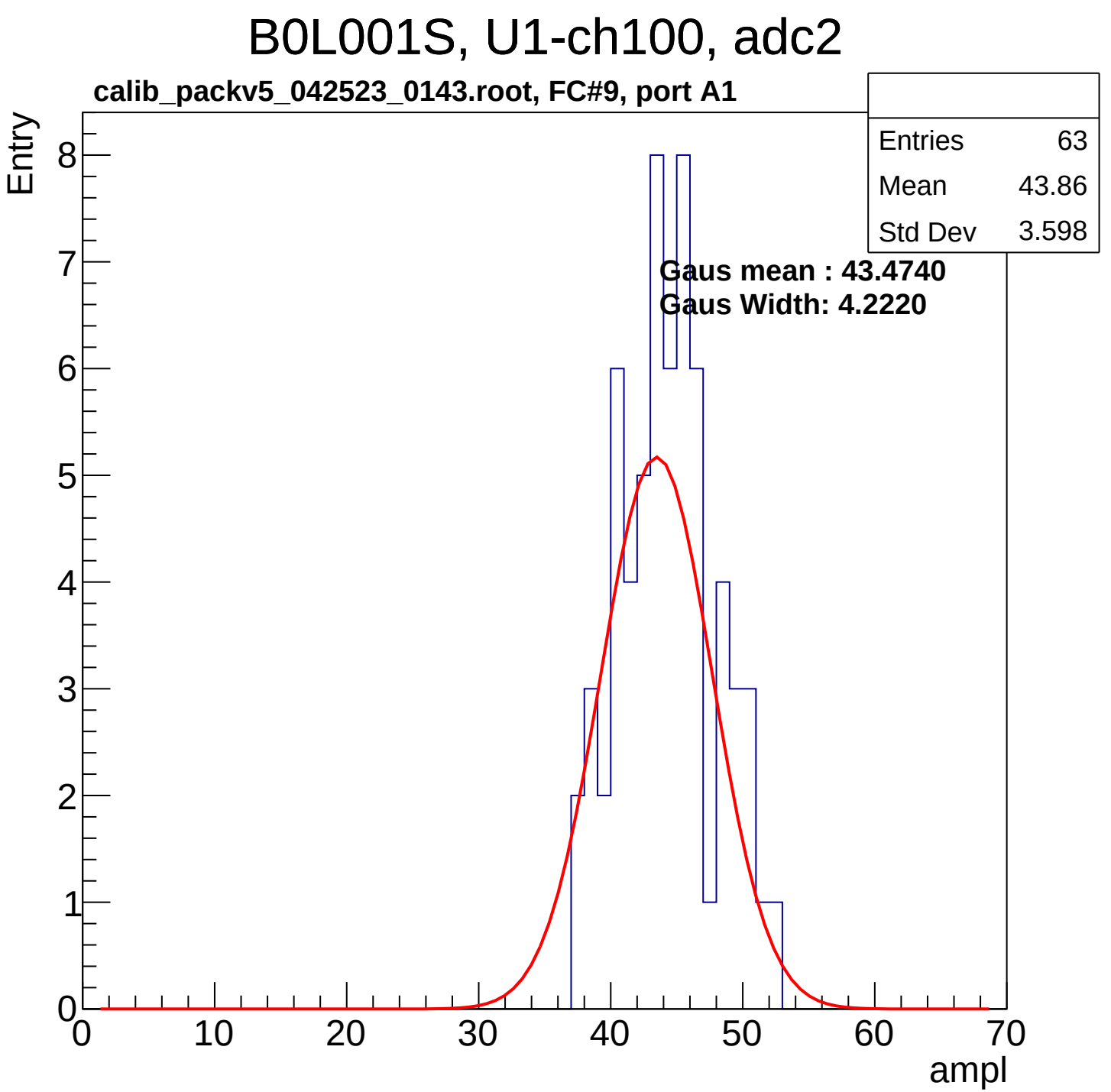

B0L001S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.2
Std Dev 3.726
Gaus mean : 37.7959
Gaus Width: 3.6707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 43.86
Std Dev 3.598
Gaus mean : 43.4740
Gaus Width: 4.2220
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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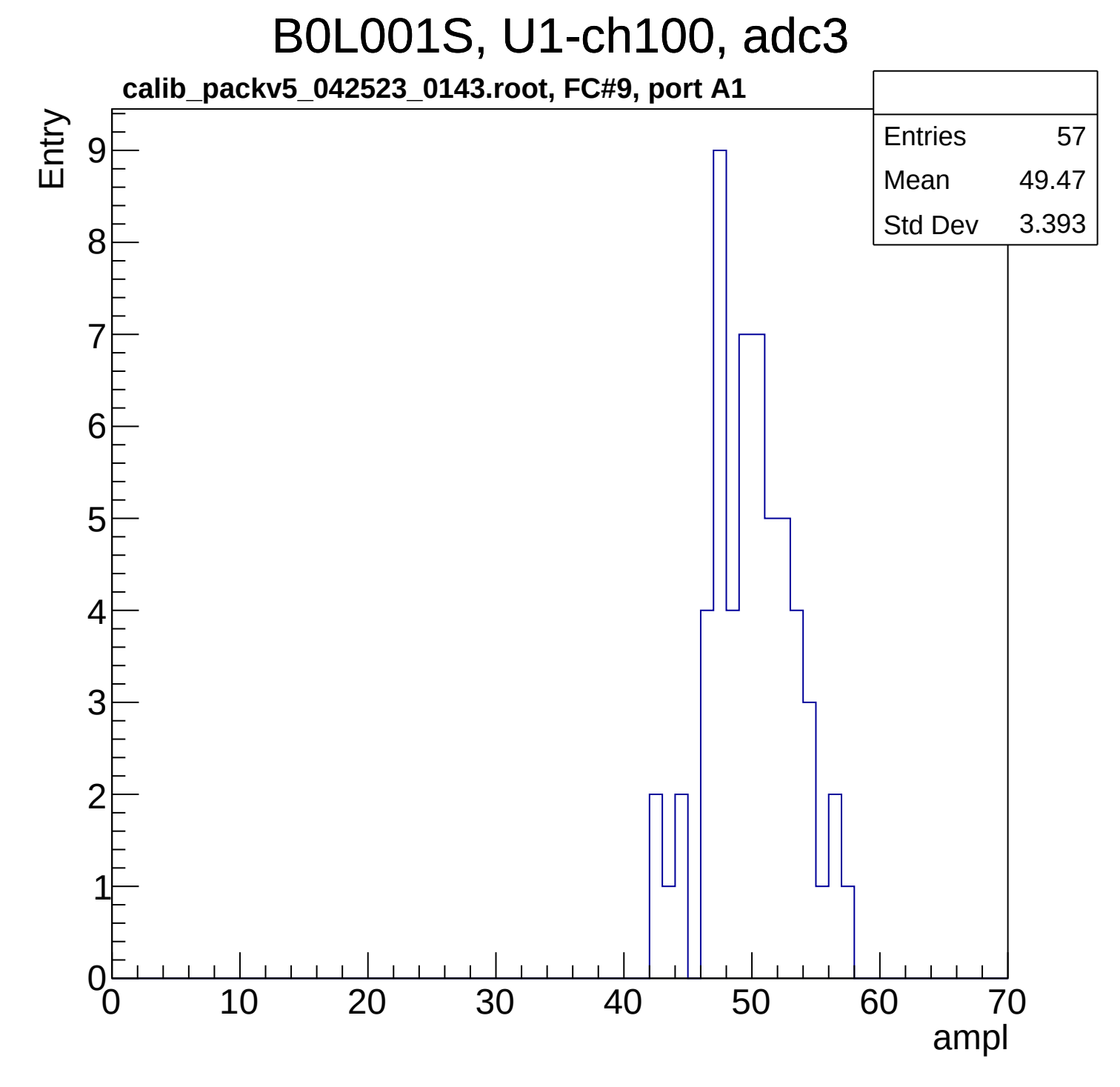
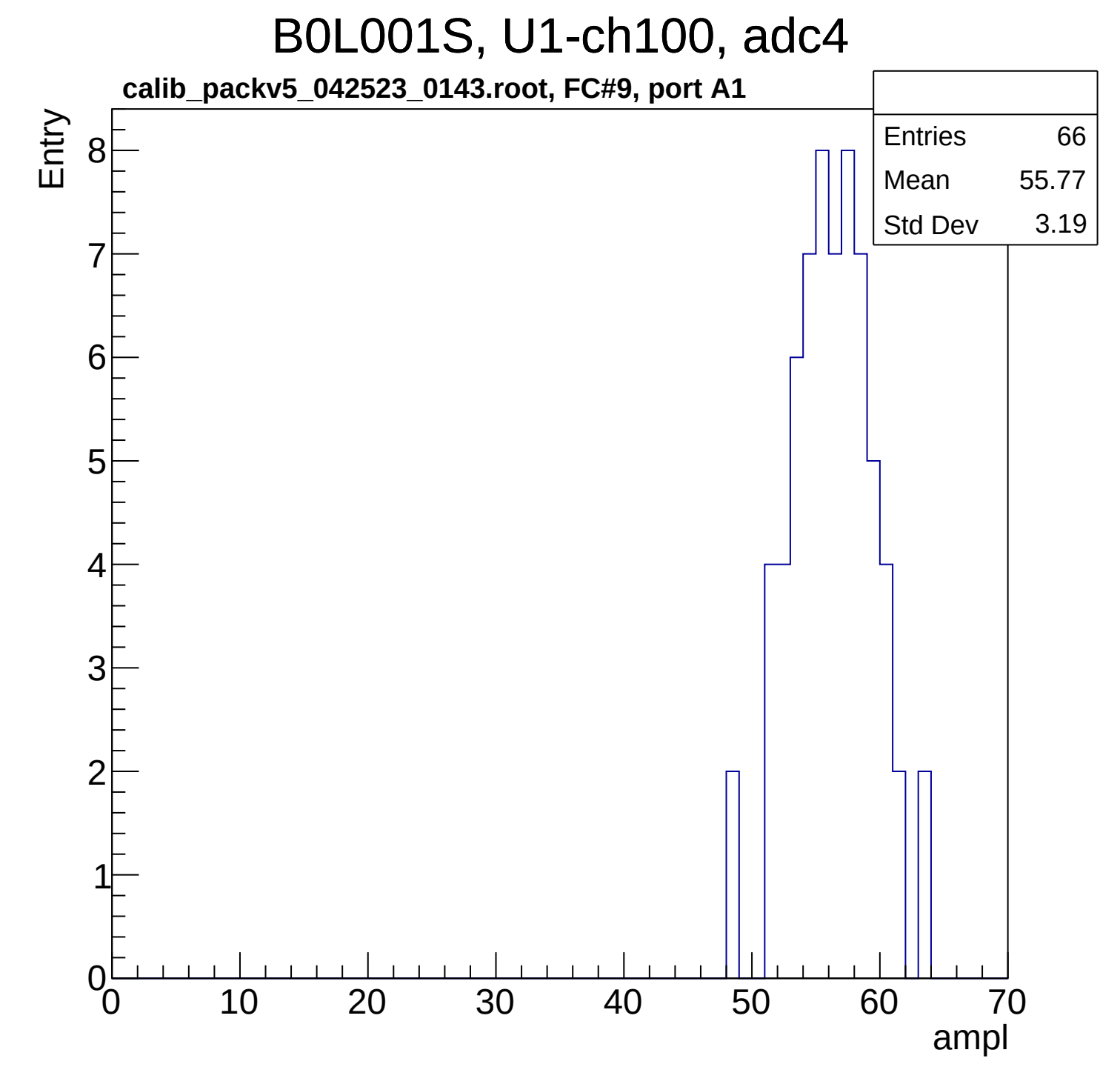

B0L001S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 49.47
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.77
Std Dev 3.19
Entry
ampl
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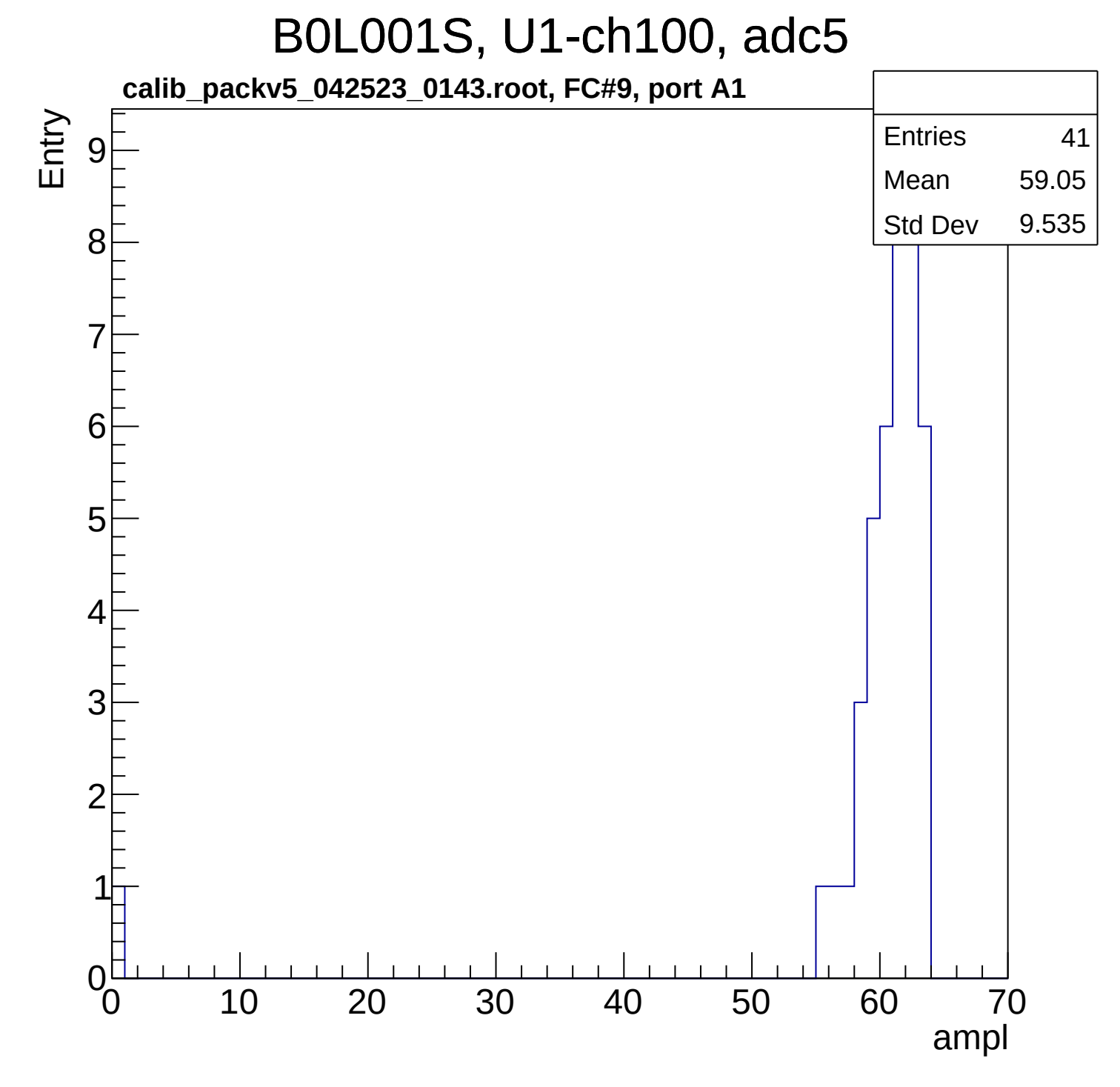
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 59.05
Std Dev 9.535
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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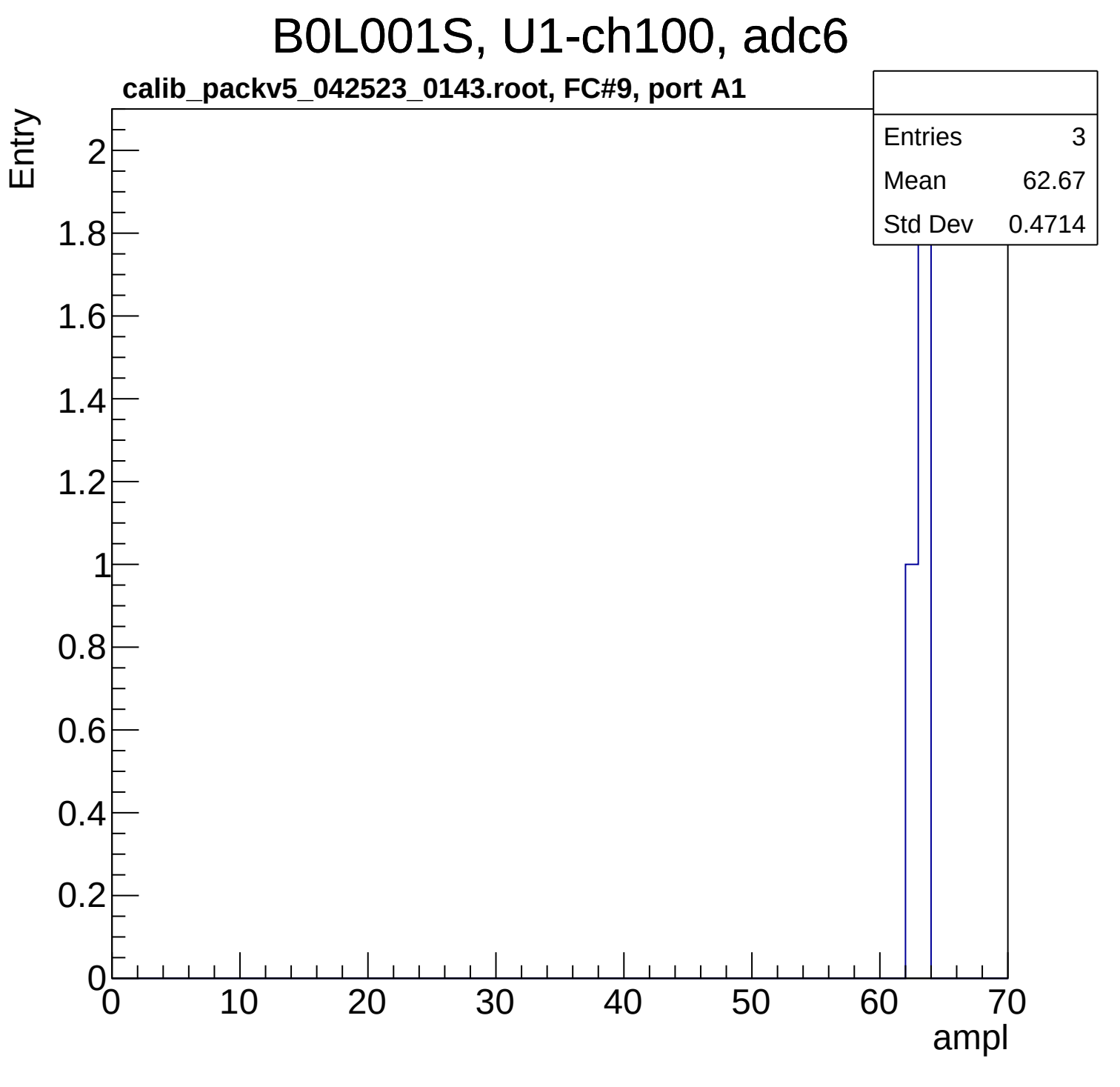
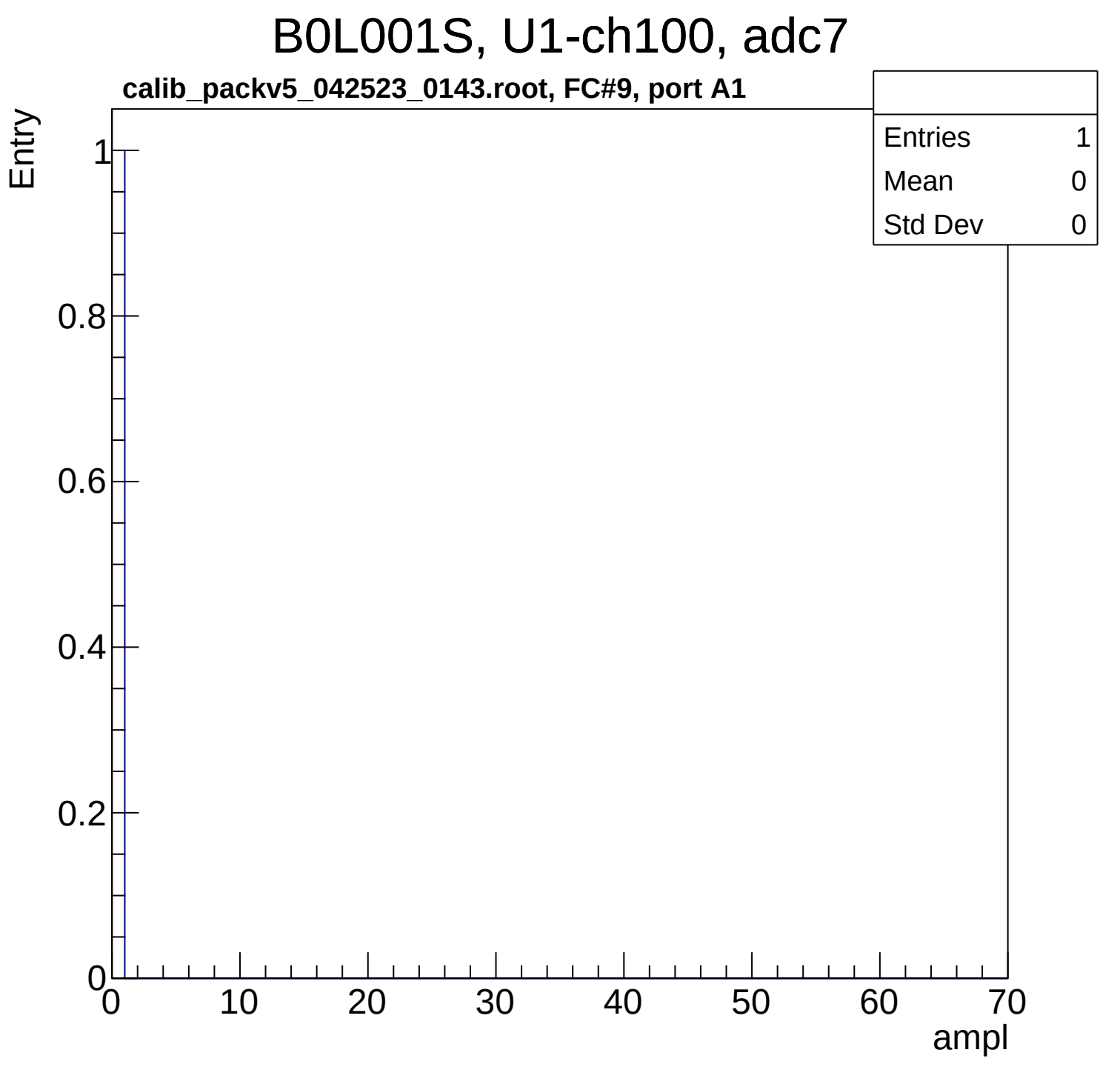
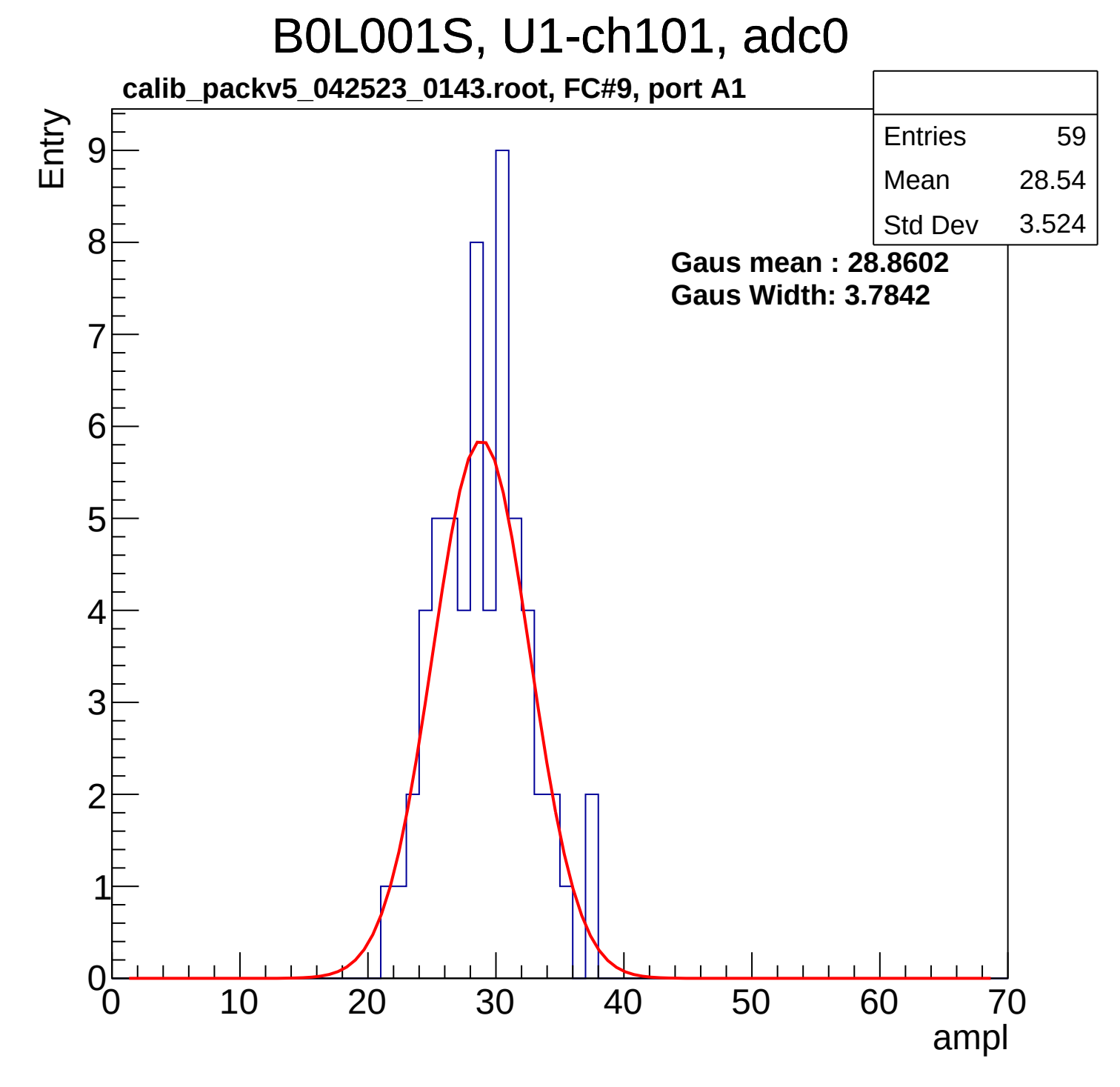
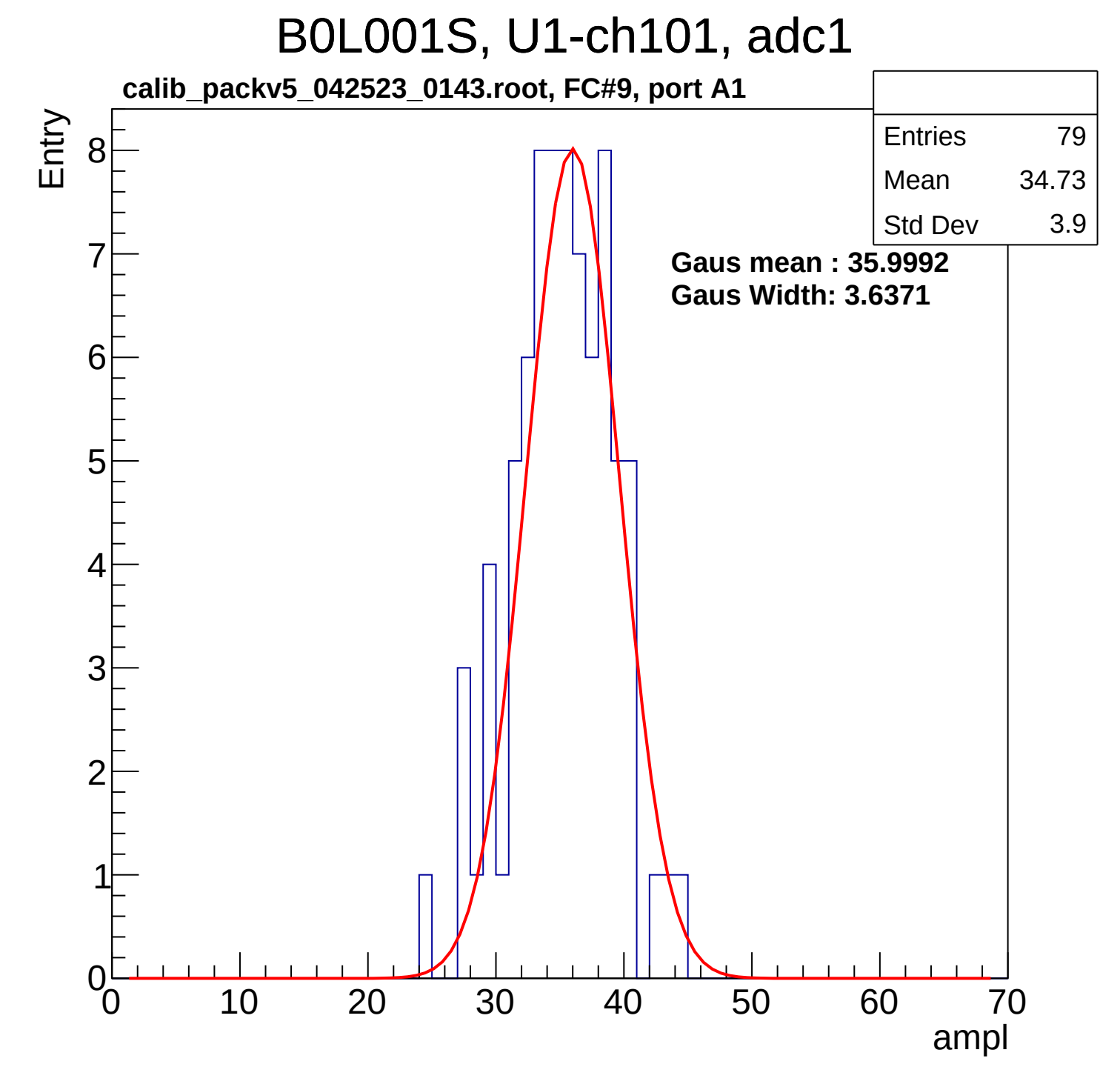
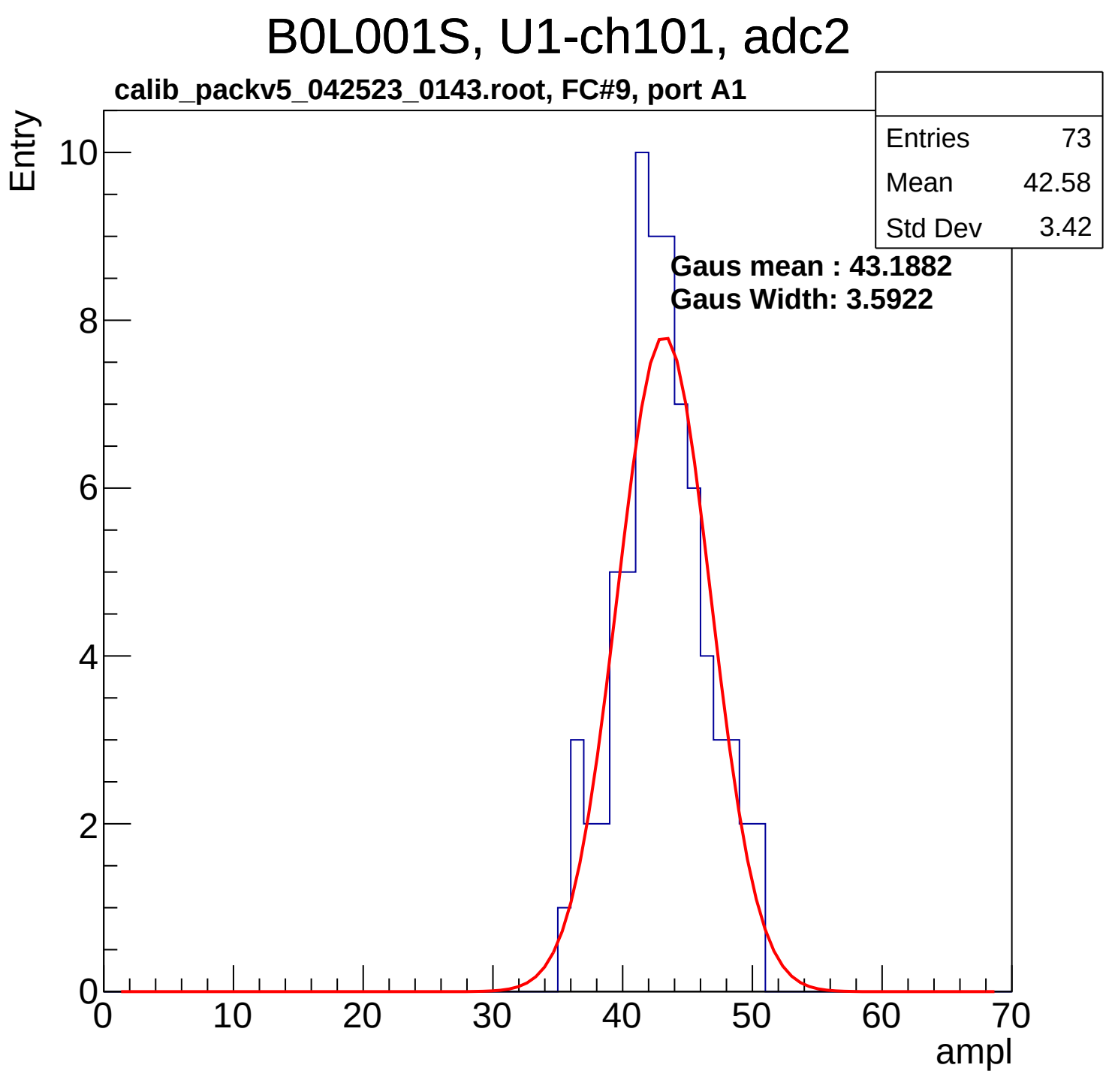
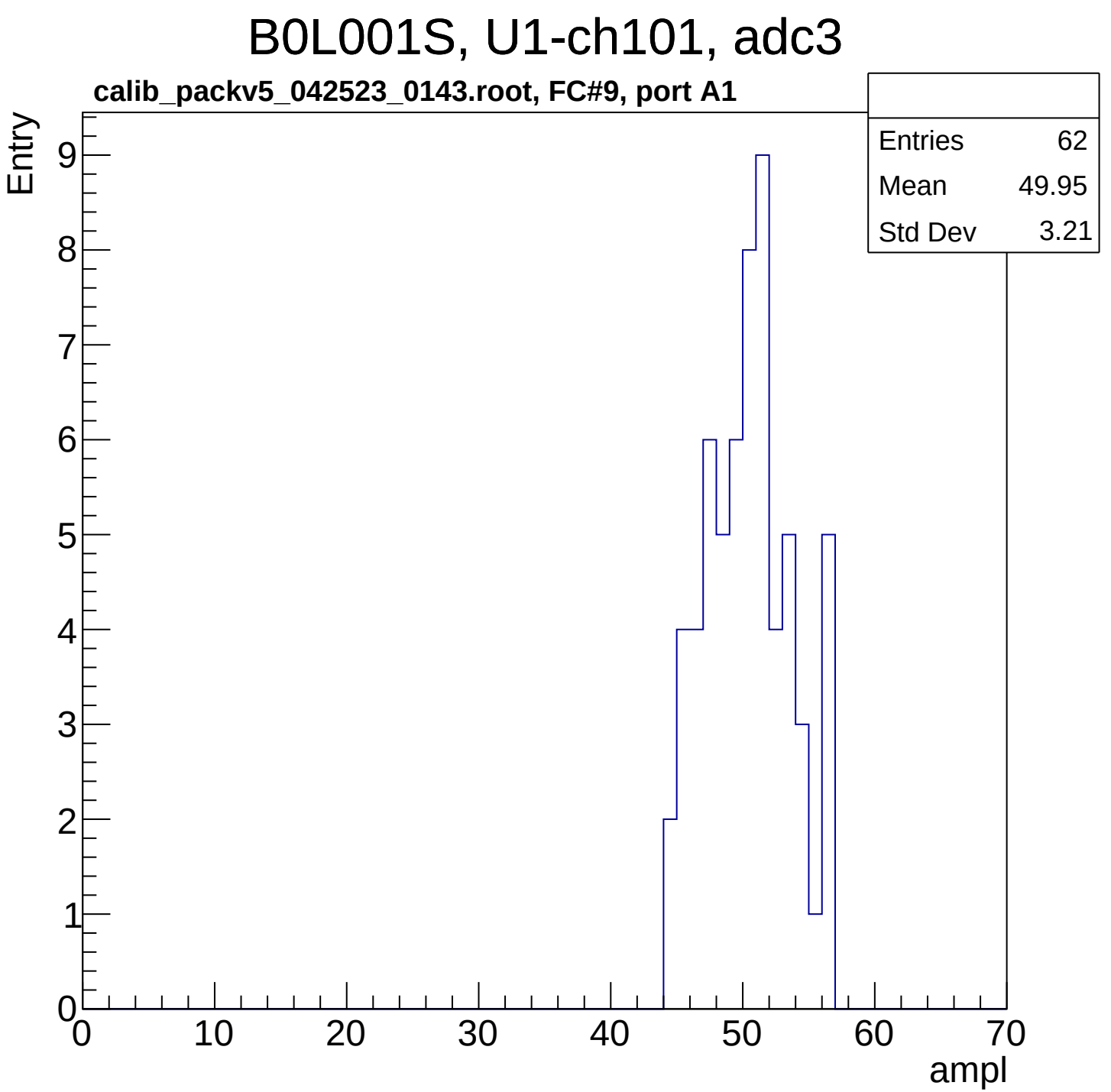

B0L001S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

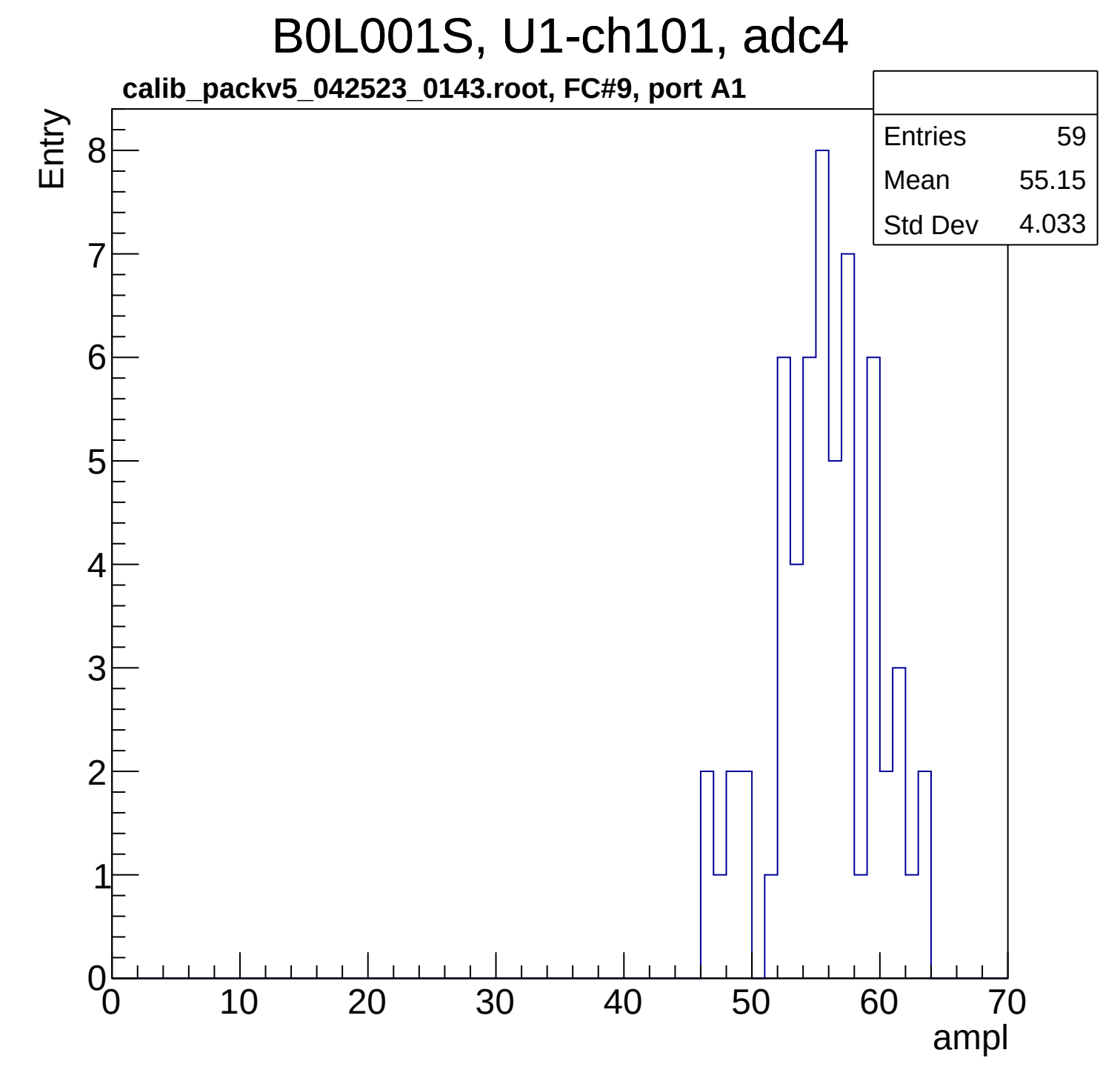

B0L001S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

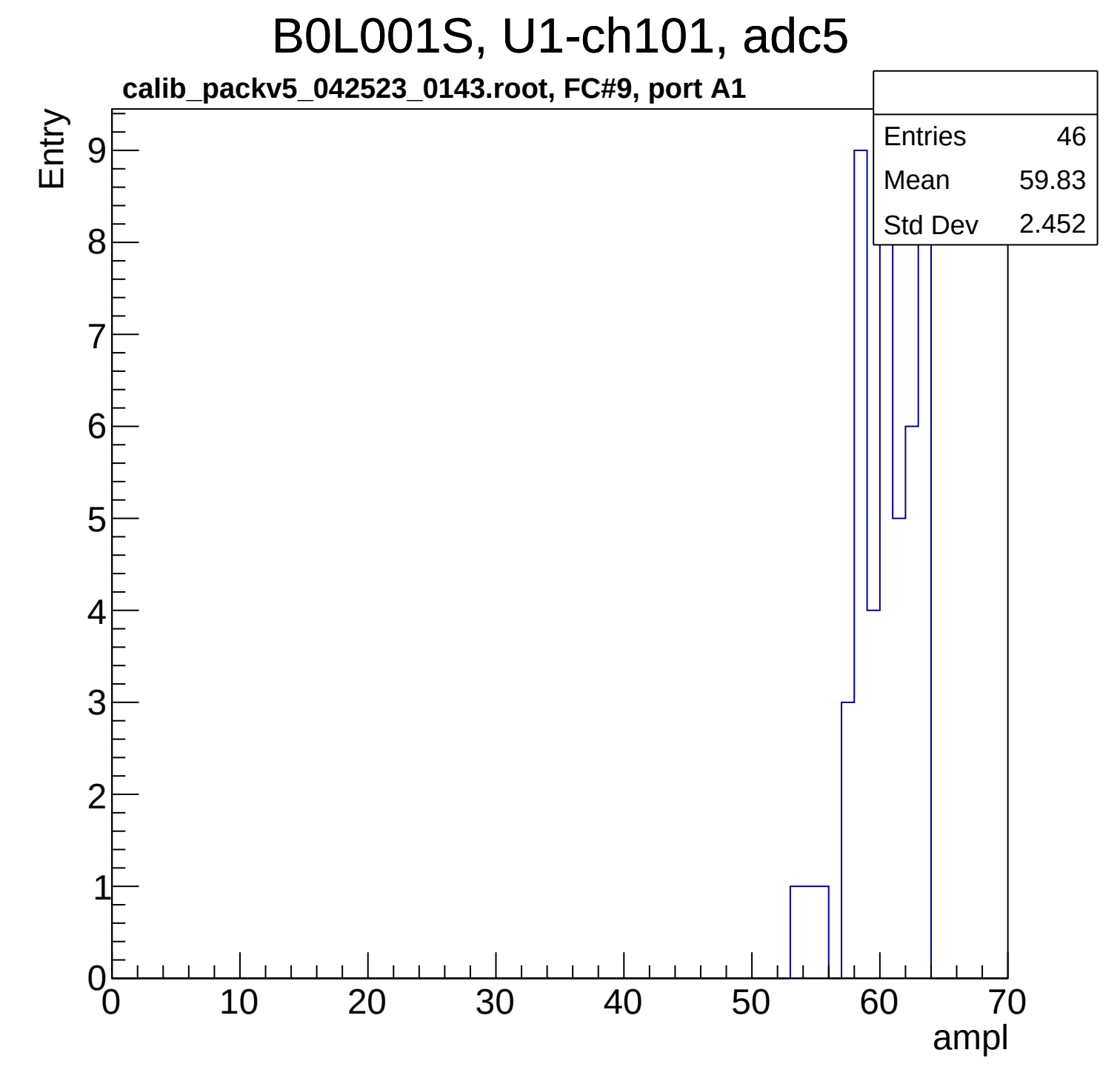

B0L001S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 28.54
Std Dev 3.524
Gaus mean : 28.8602
Gaus Width: 3.7842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

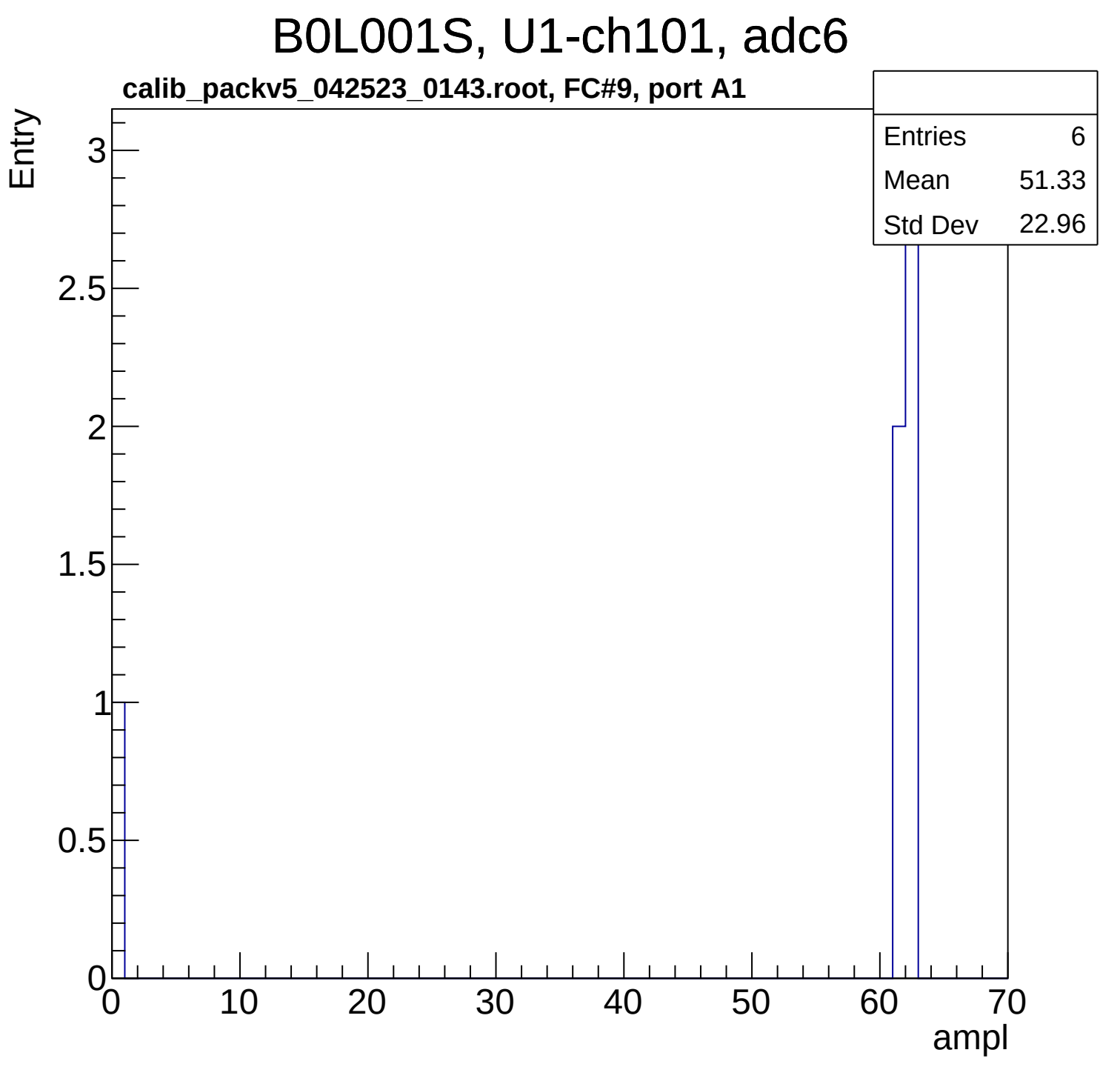

B0L001S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 34.73
Std Dev 3.9
Gaus mean : 35.9992
Gaus Width: 3.6371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

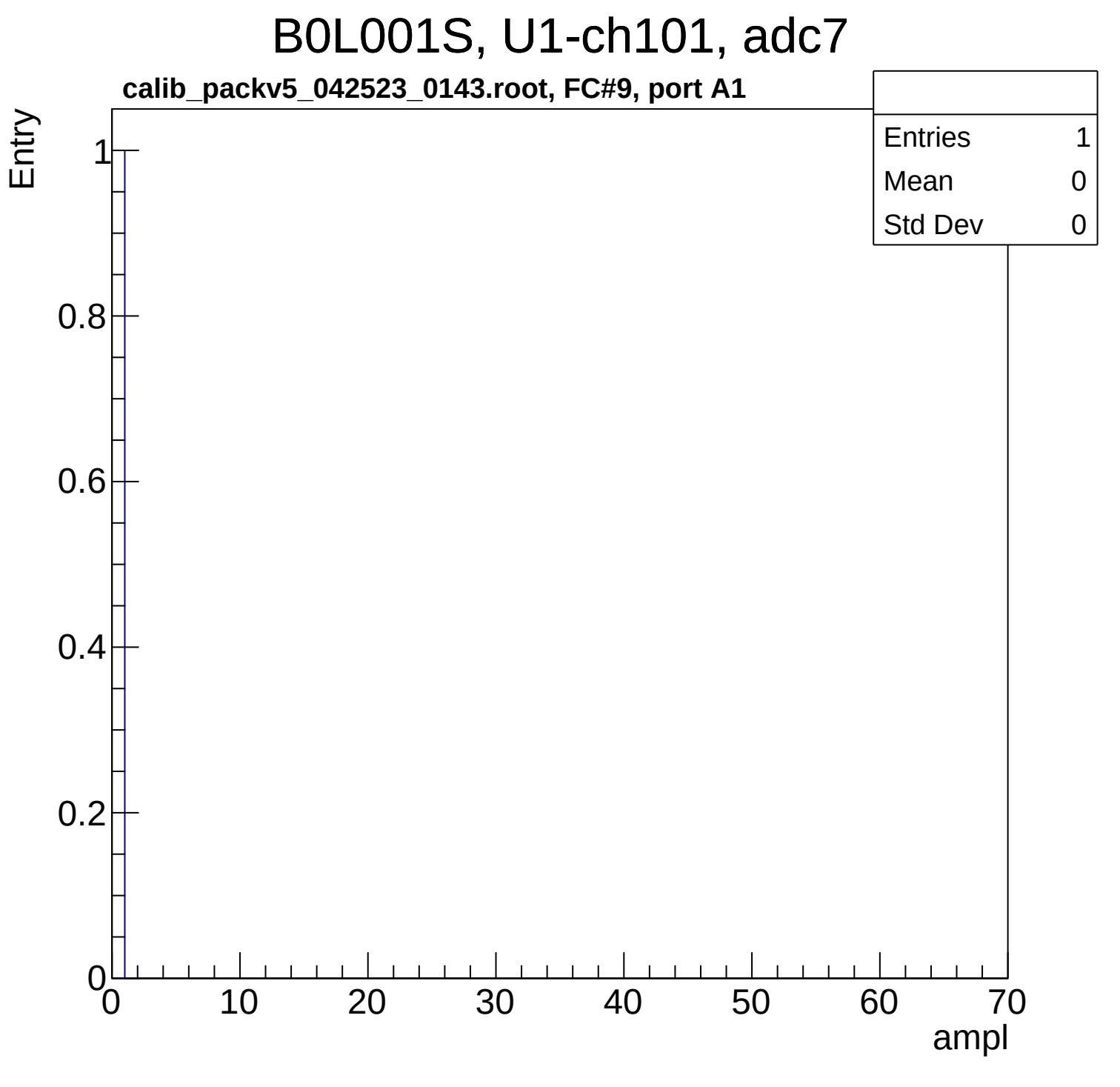

B0L001S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 42.58
Std Dev 3.42
Gaus mean : 43.1882
Gaus Width: 3.5922
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

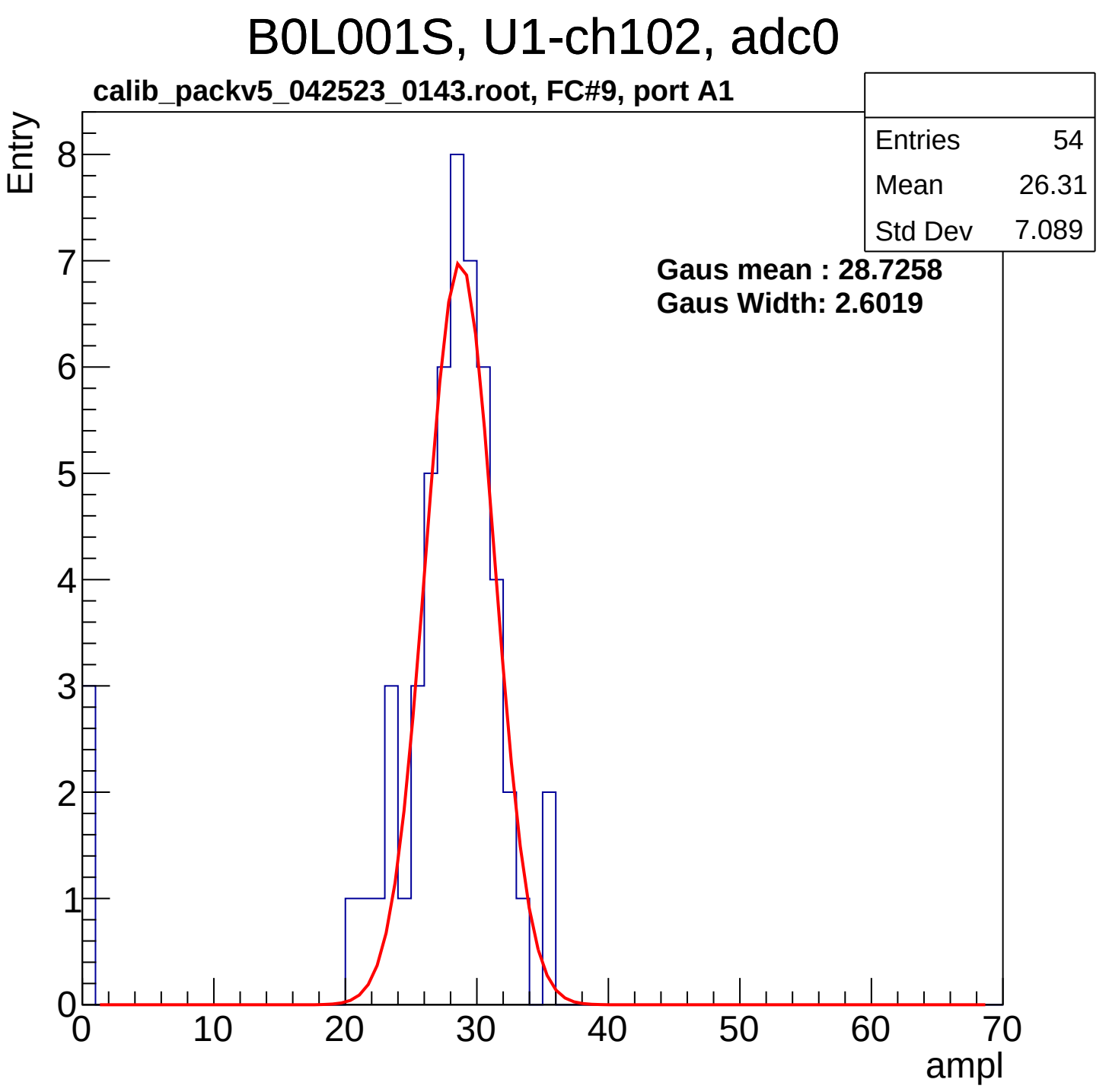

B0L001S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 49.95
Std Dev 3.21
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

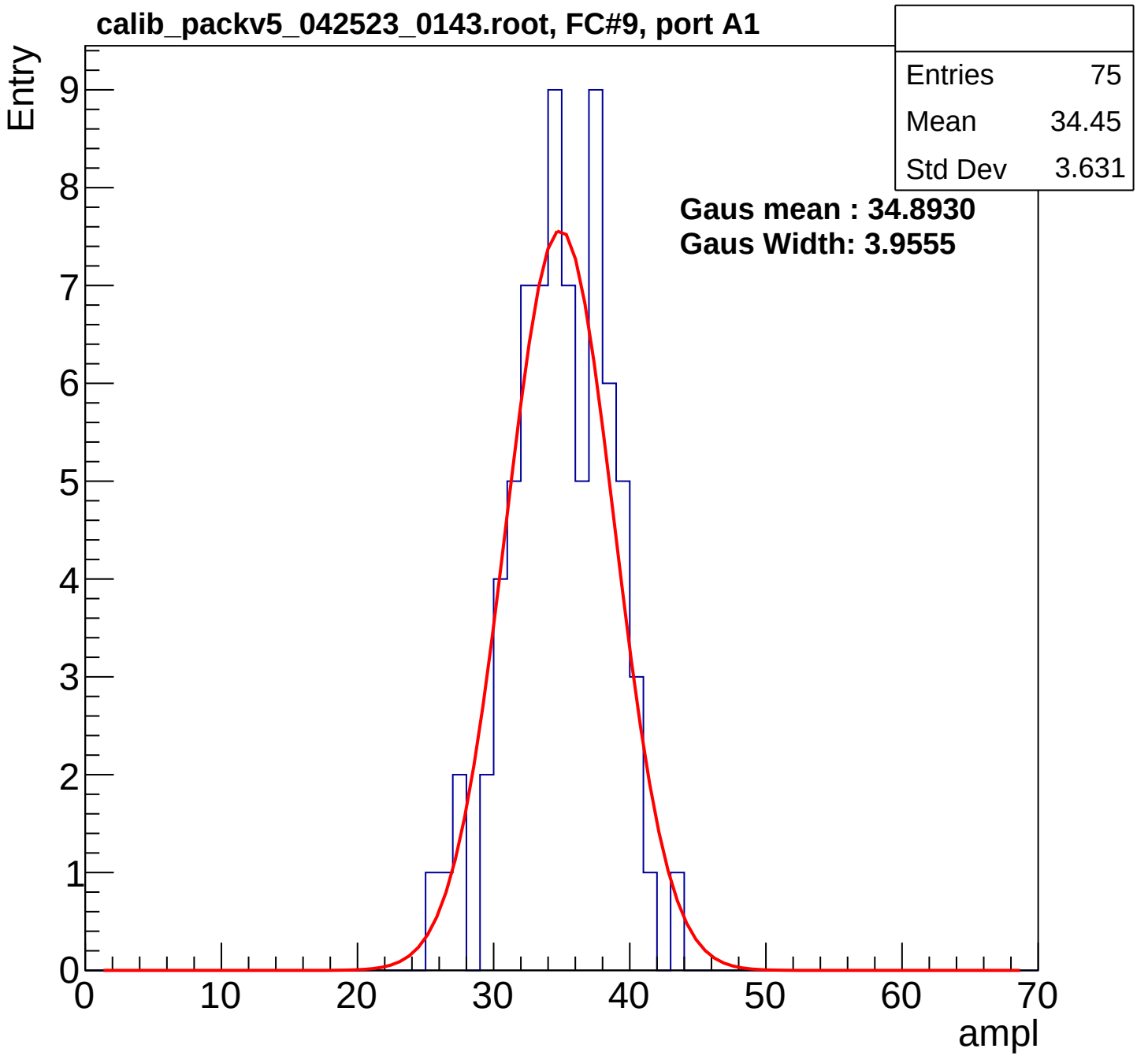

B0L001S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.15
Std Dev 4.033
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

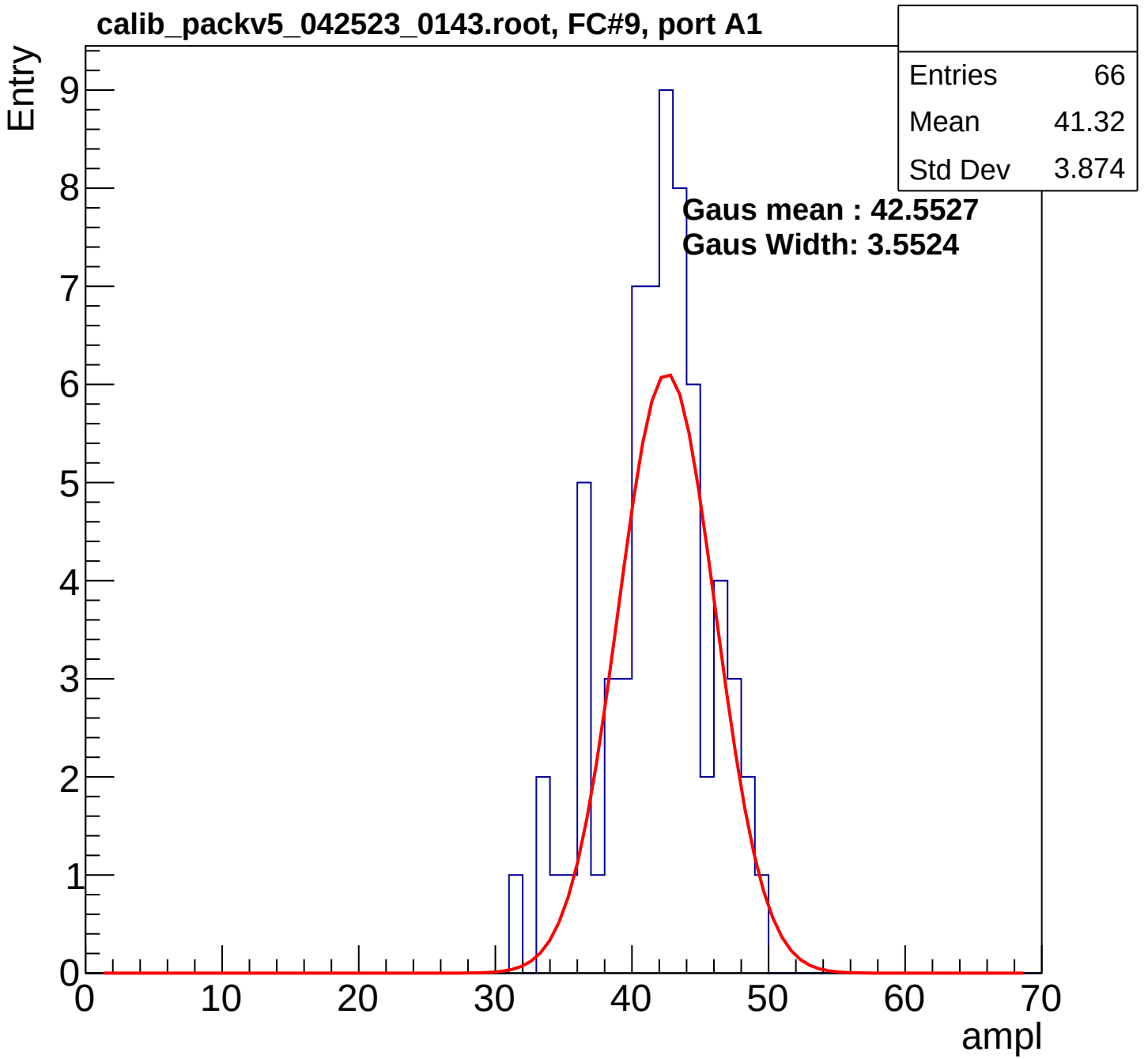

B0L001S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 59.83
Std Dev 2.452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 51.33
Std Dev 22.96
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 54
Mean 26.31
Std Dev 7.089
Gaus mean : 28.7258
Gaus Width: 2.6019
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 34.45
Std Dev 3.631
Gaus mean : 34.8930
Gaus Width: 3.9555
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 41.32
Std Dev 3.874
Gaus mean : 42.5527
Gaus Width: 3.5524
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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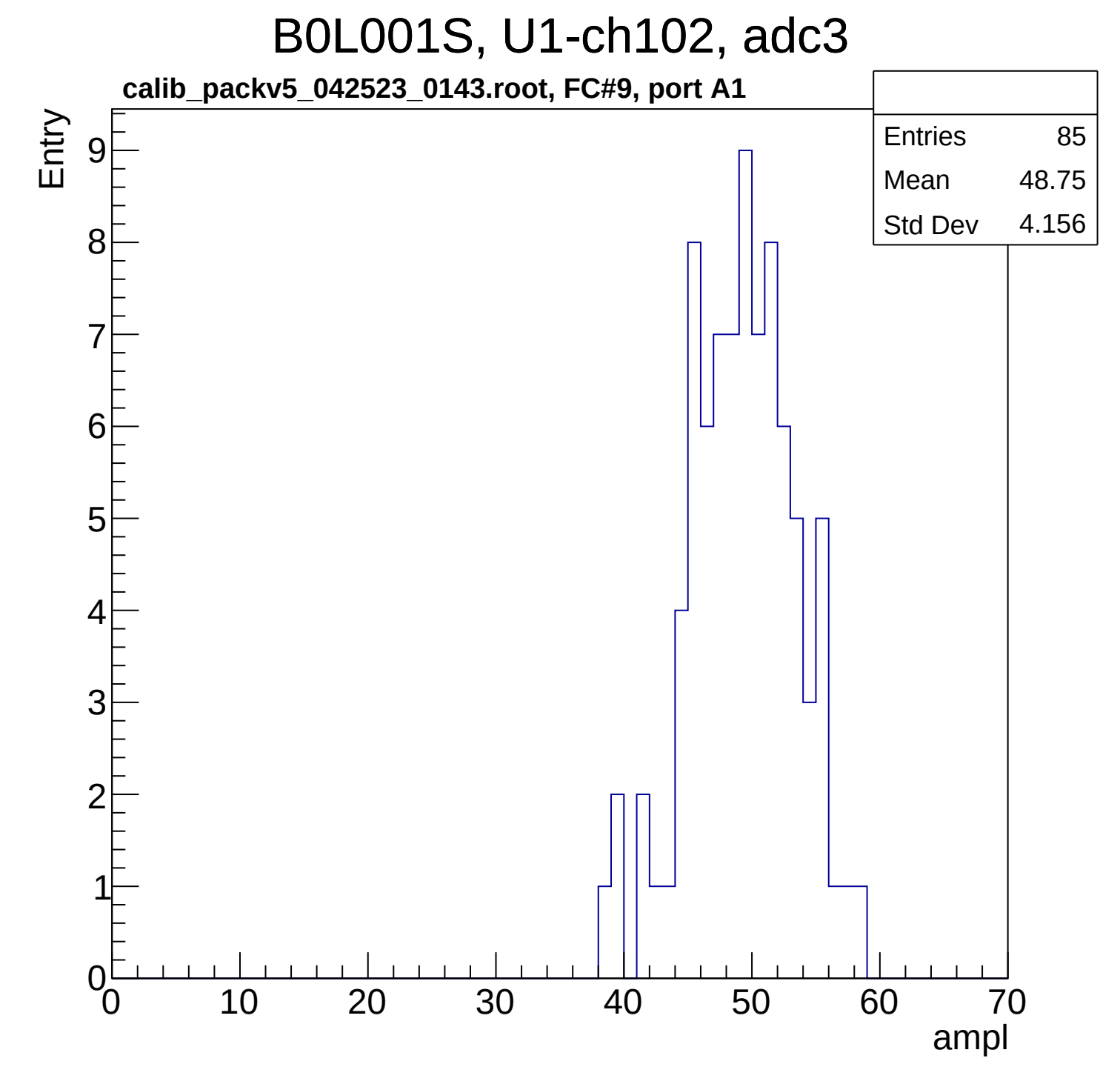
B0L001S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 48.75
Std Dev 4.156
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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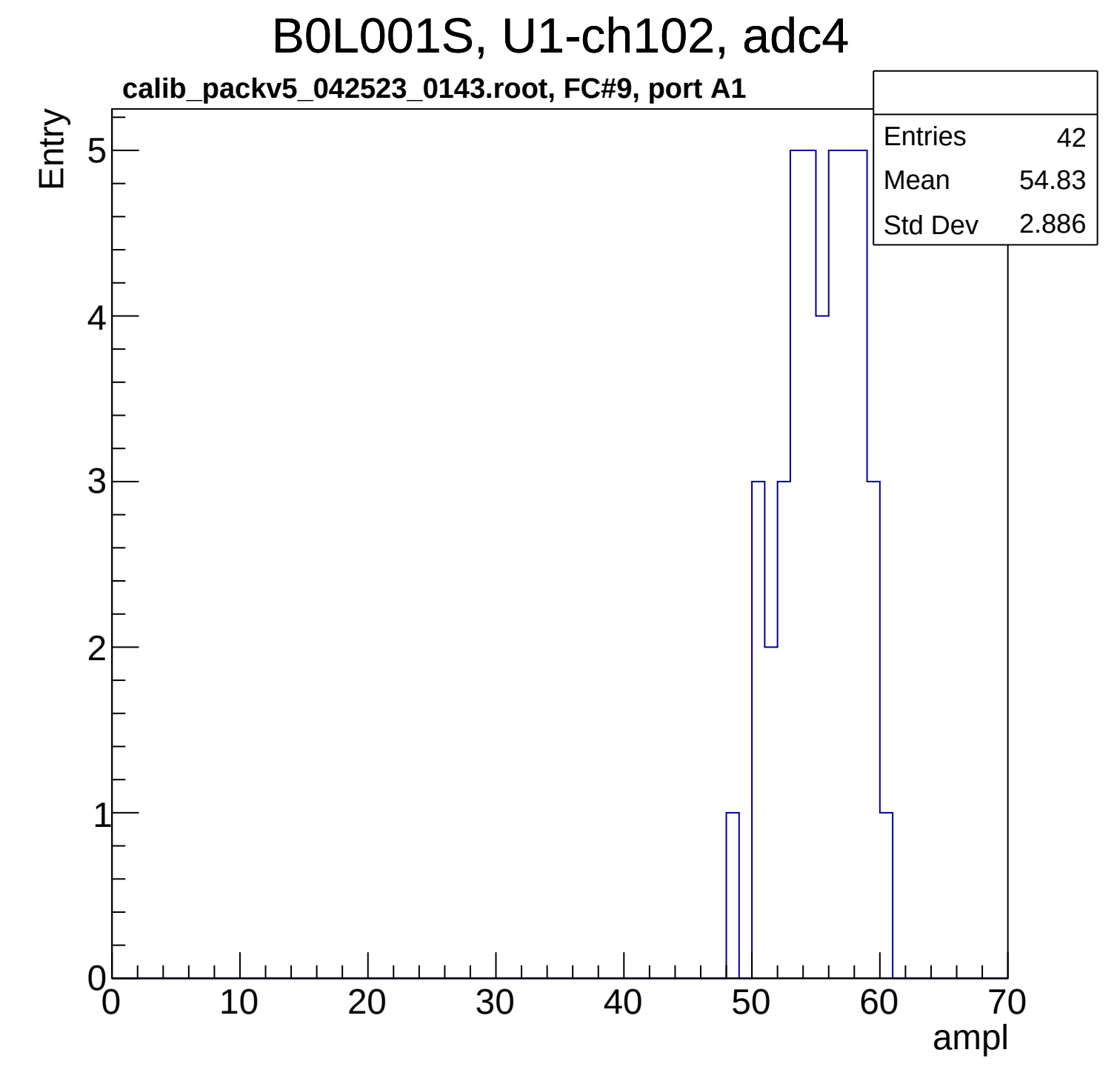
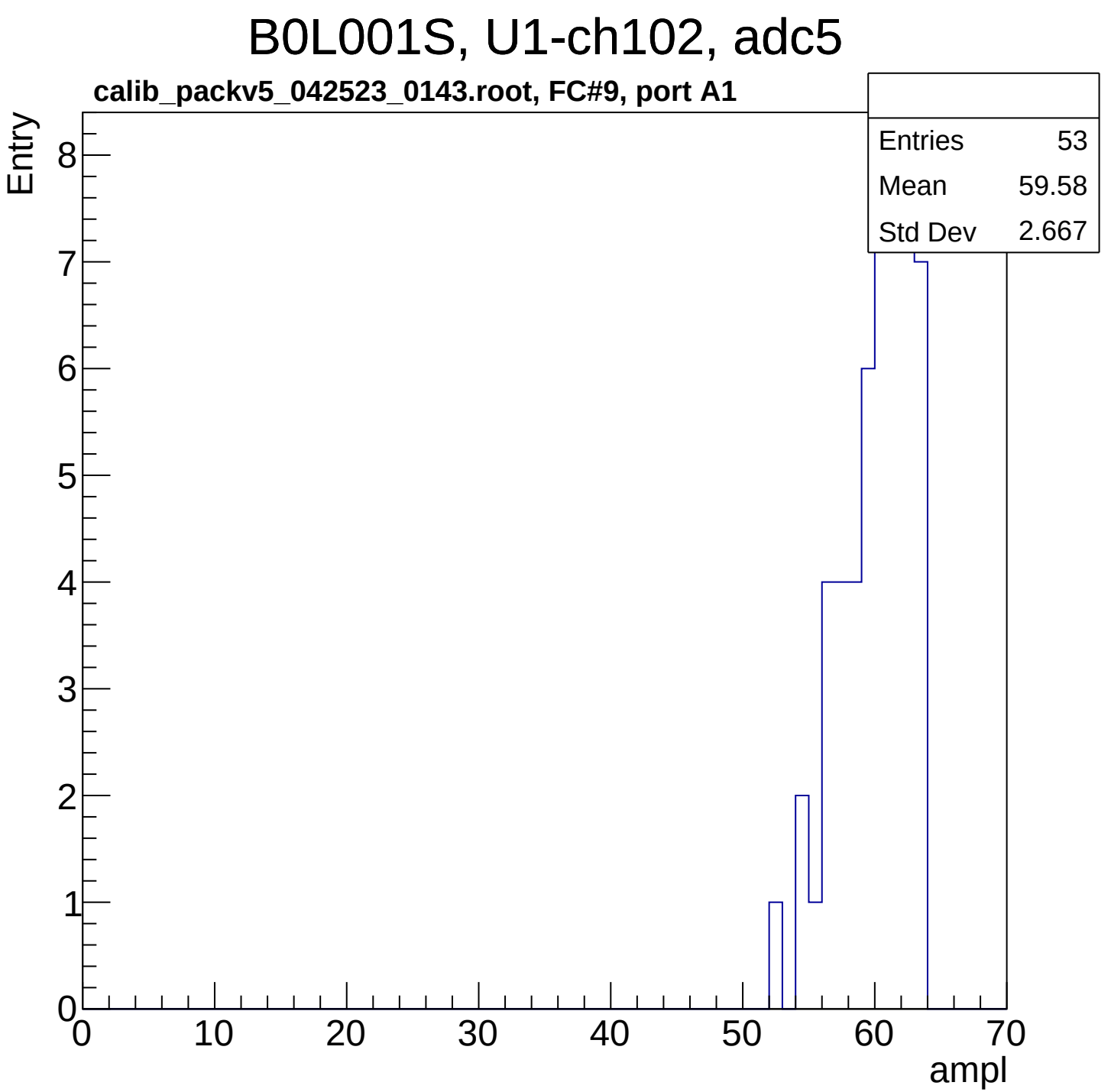

B0L001S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 54.83
Std Dev 2.886
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B0L001S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 59.58
Std Dev 2.667
Entry
ampl
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1
2
3
4
5
6
7
8
0
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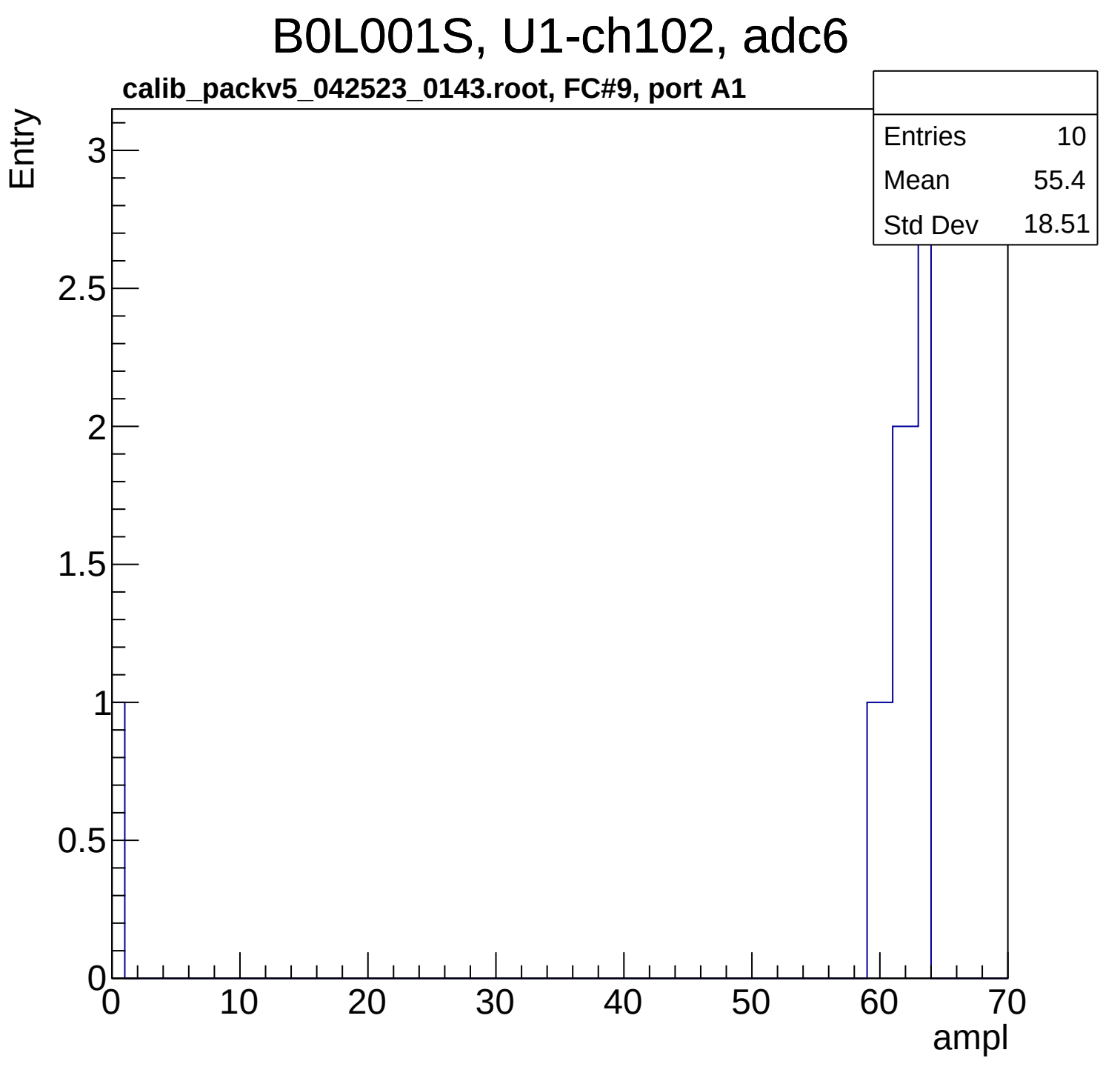
20
30
40
50
60
70


B0L001S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 10
Mean 55.4
Std Dev 18.51
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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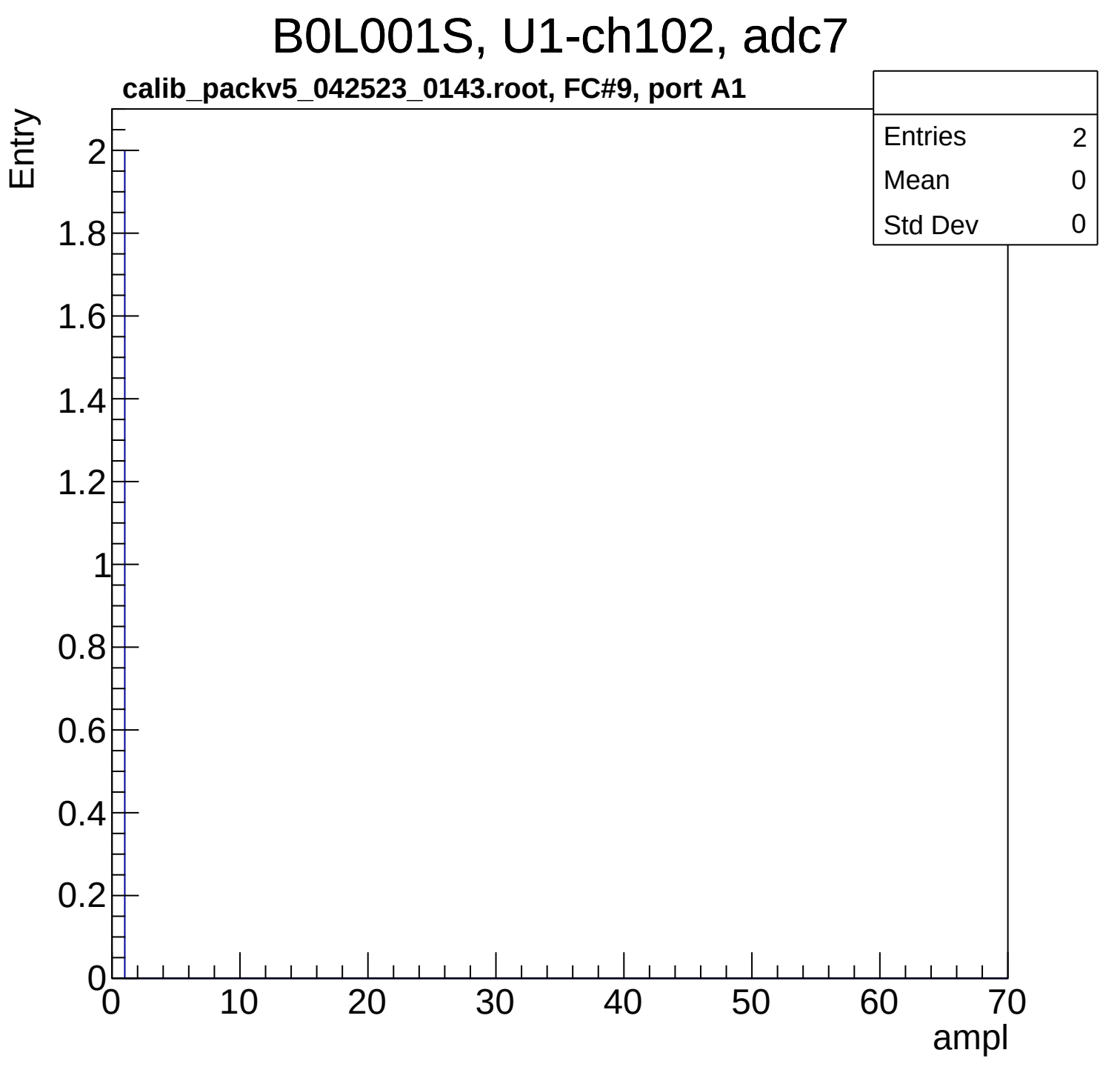
50
60
70


B0L001S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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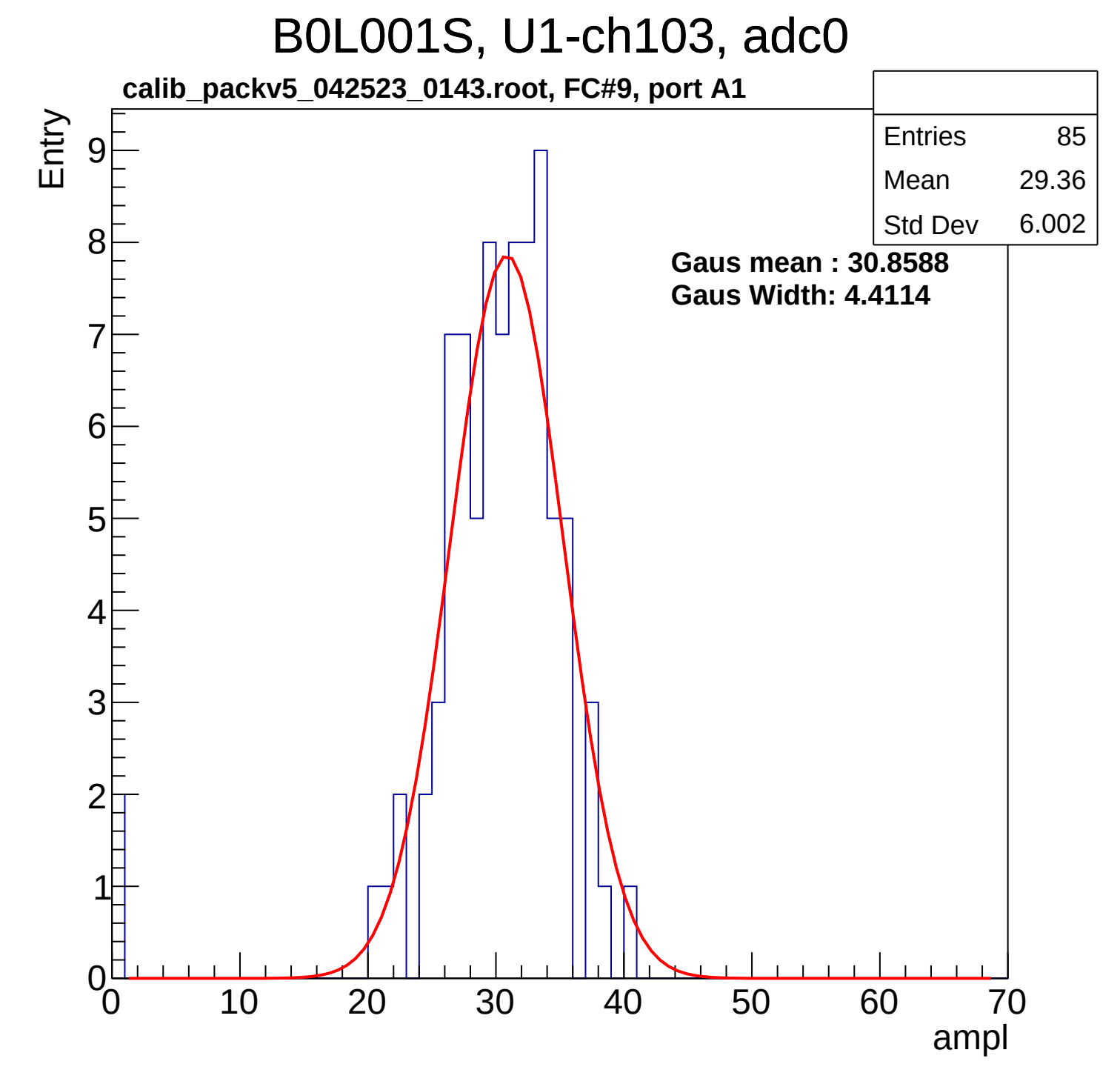
B0L001S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 29.36
Std Dev 6.002
Gaus mean : 30.8588
Gaus Width: 4.4114
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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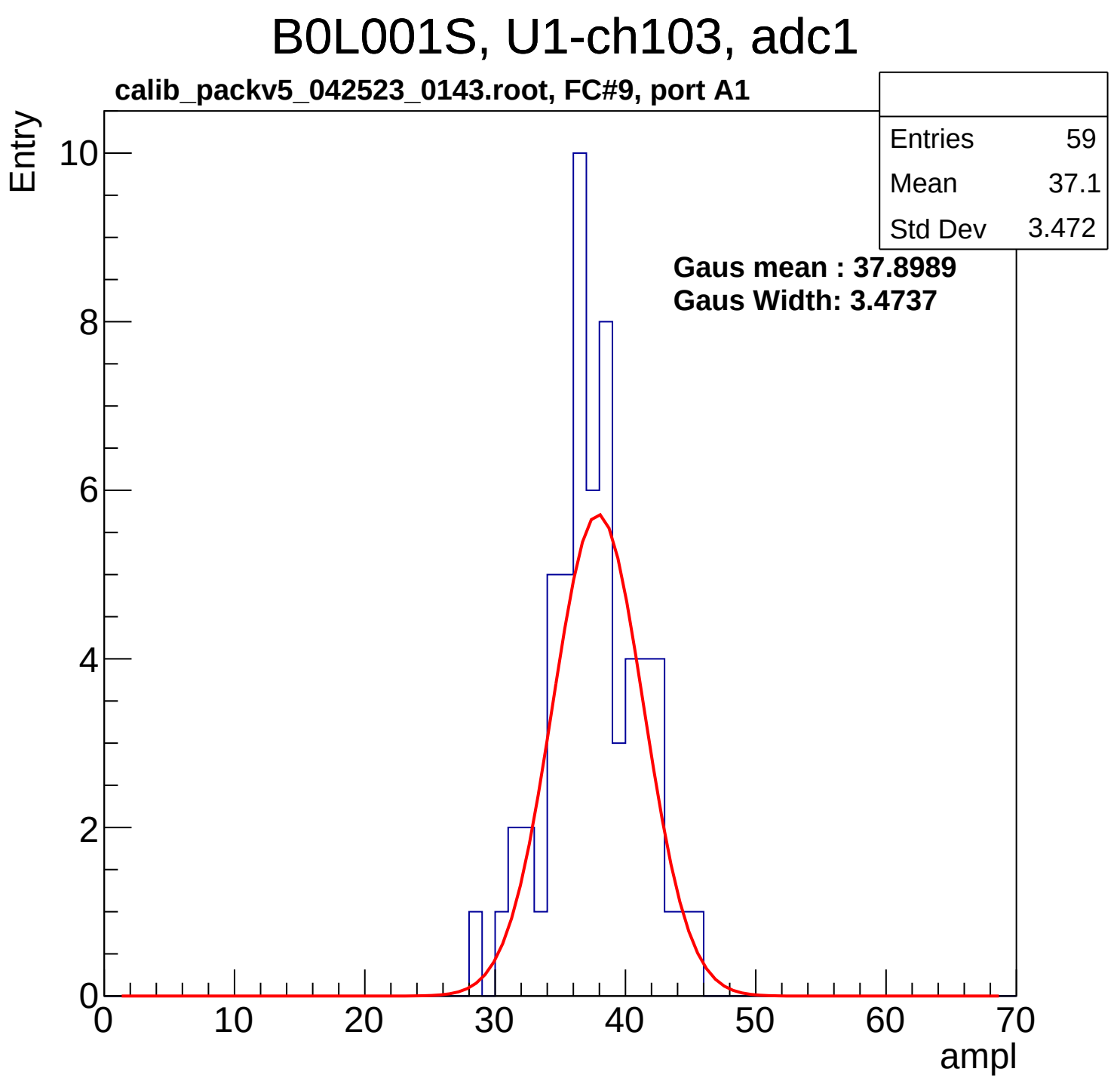
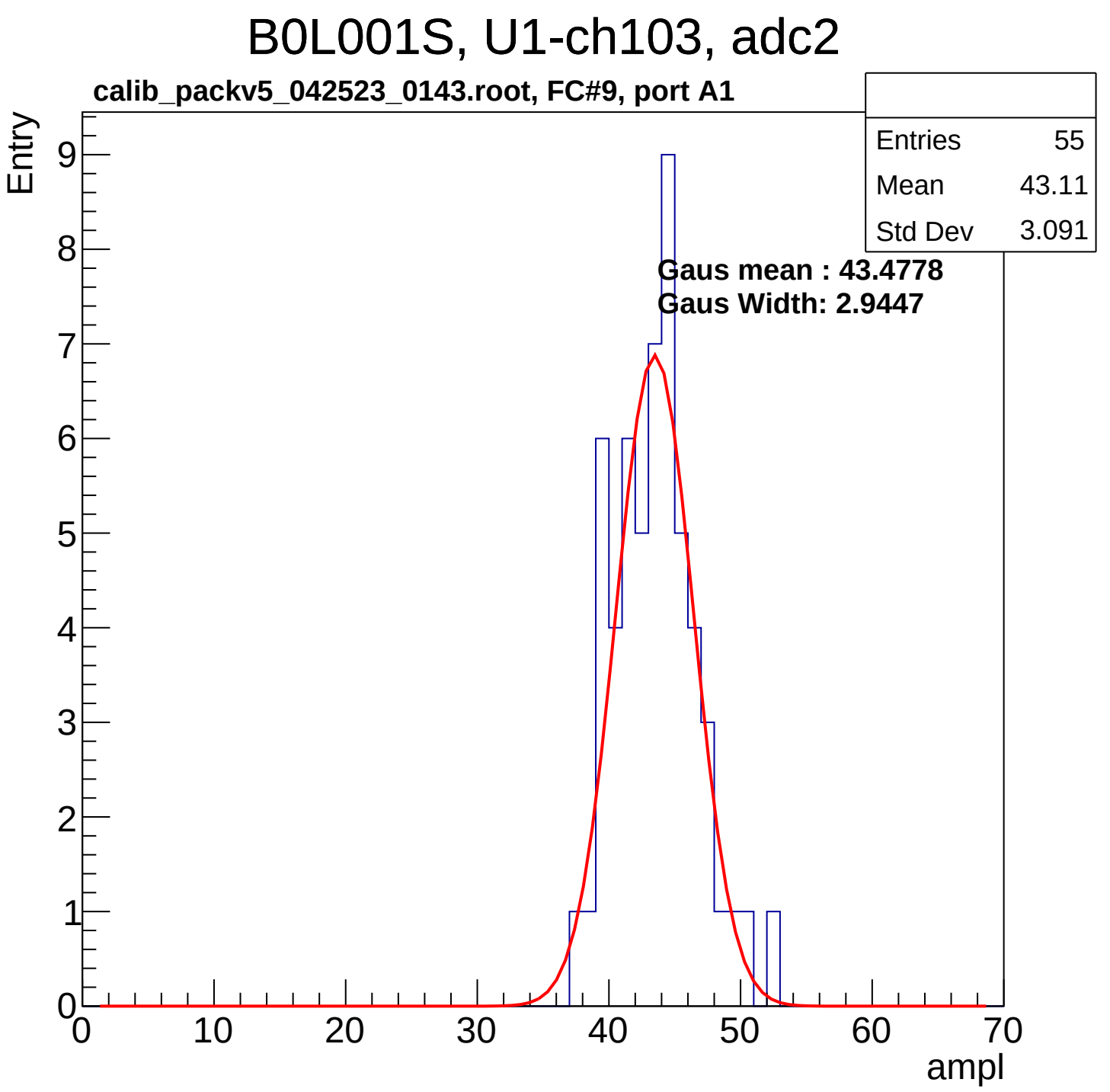
40
50
60
70


B0L001S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.1
Std Dev 3.472
Gaus mean : 37.8989
Gaus Width: 3.4737
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 43.11
Std Dev 3.091
Gaus mean : 43.4778
Gaus Width: 2.9447
Entry
ampl
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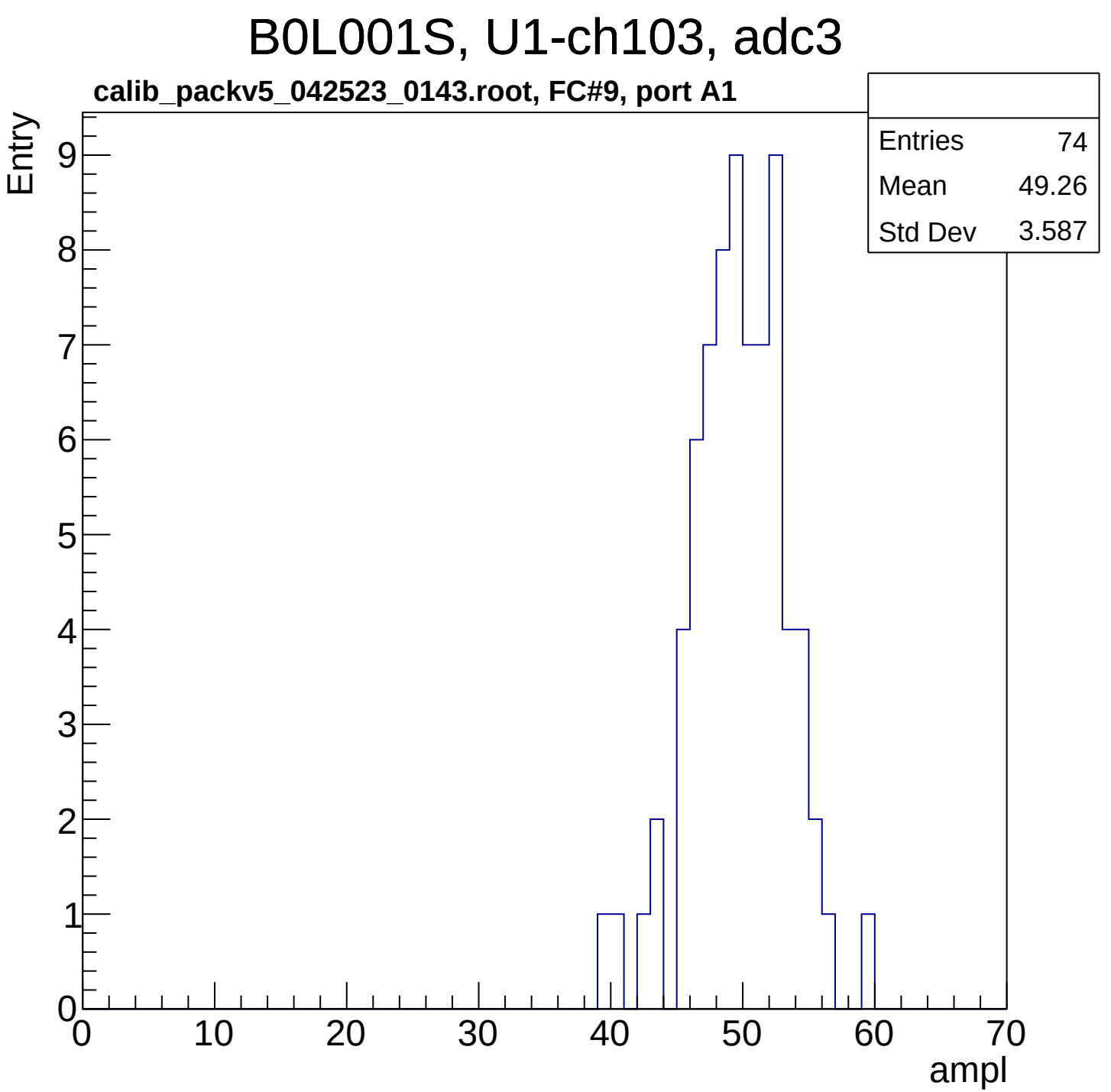
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 49.26
Std Dev 3.587
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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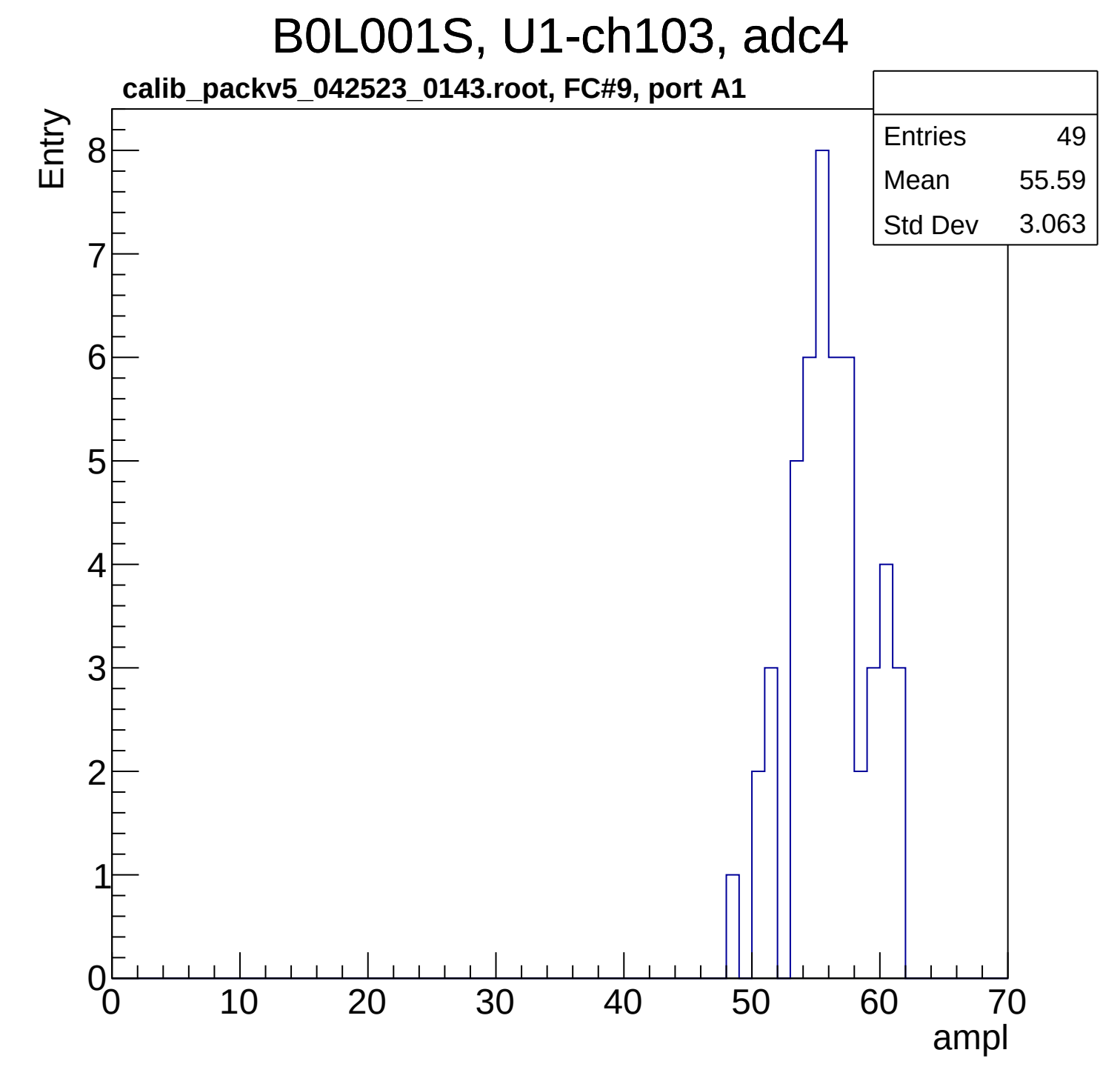
B0L001S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 55.59
Std Dev 3.063
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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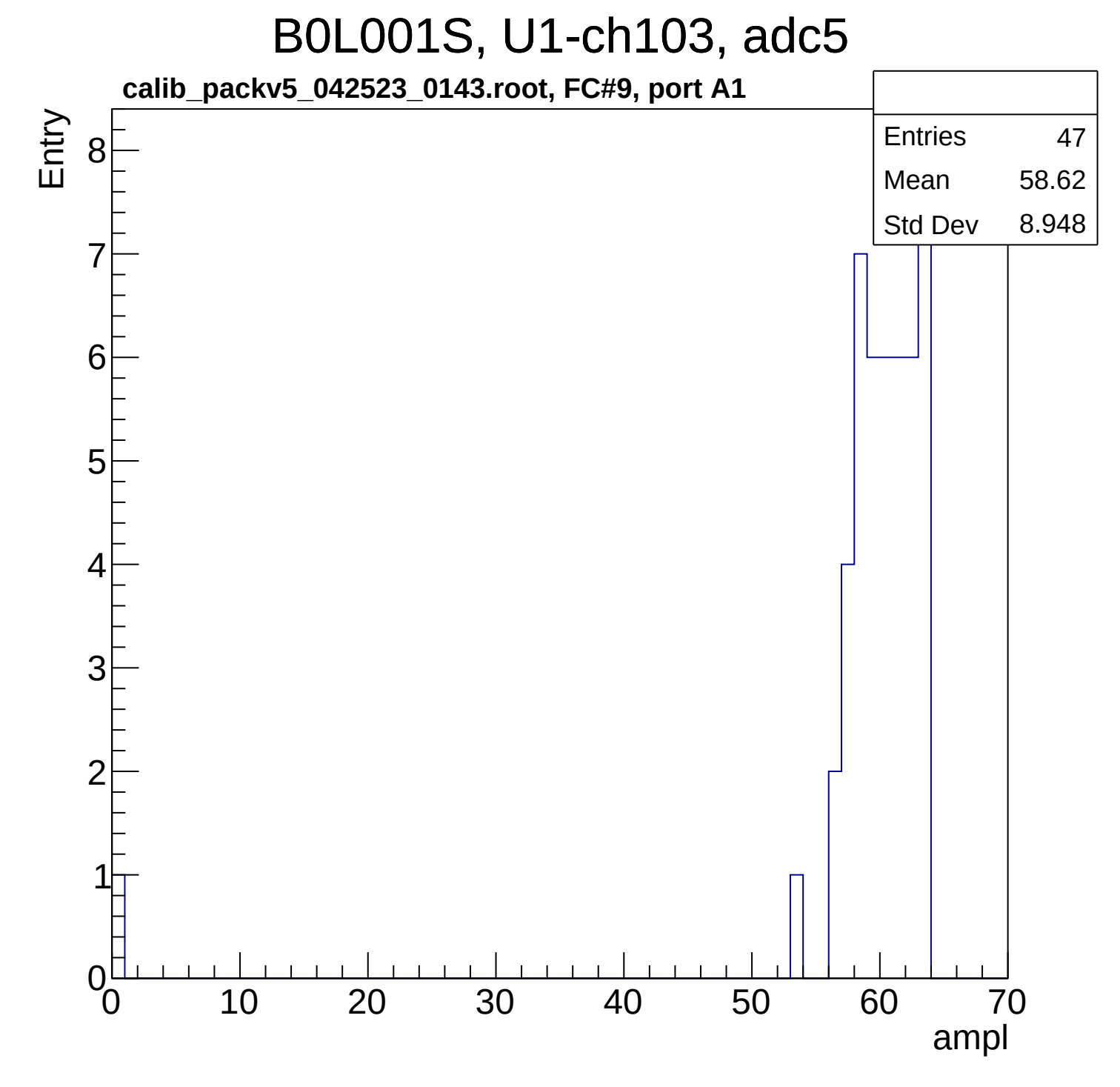
B0L001S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 58.62
Std Dev 8.948
Entry
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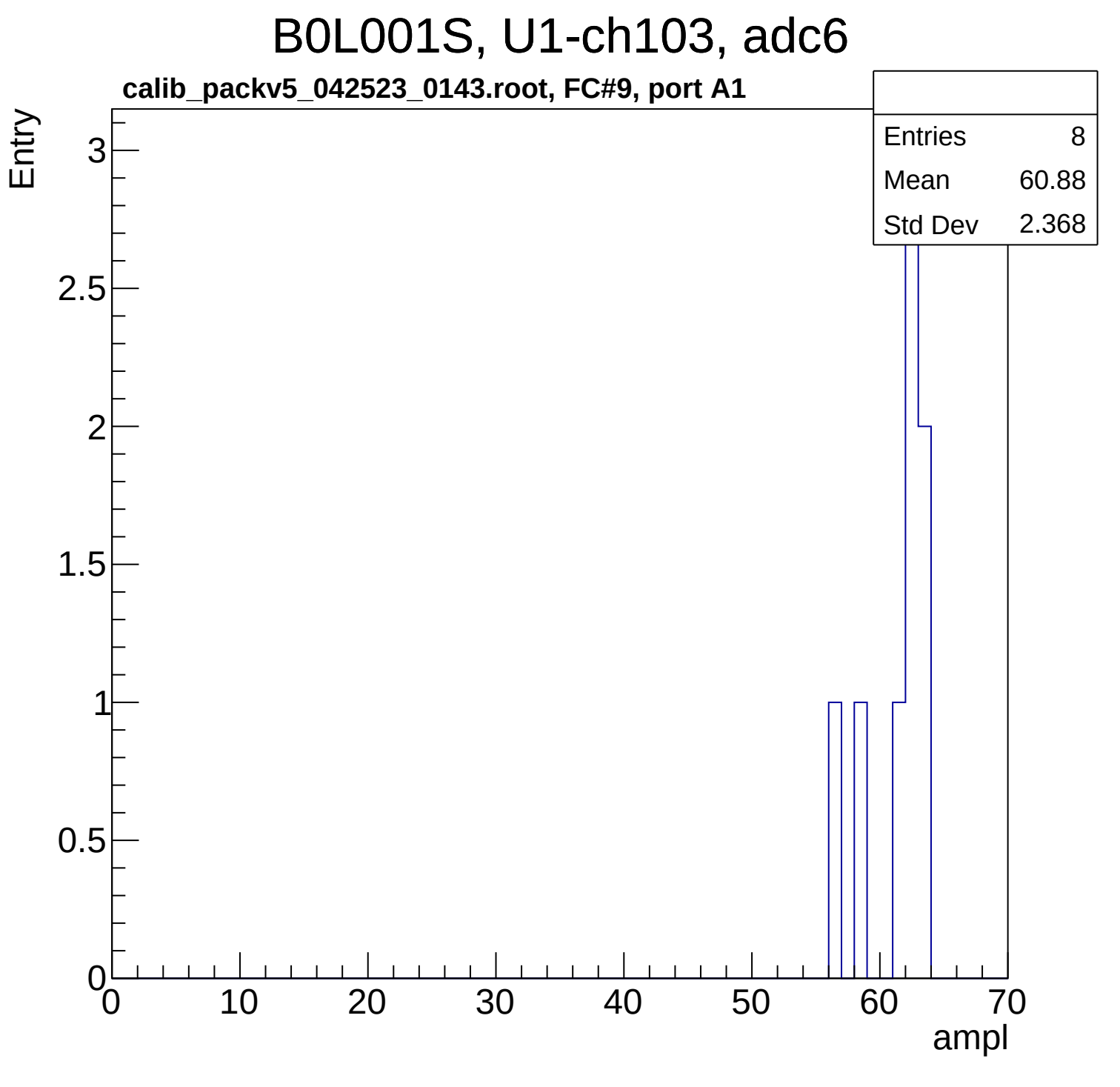
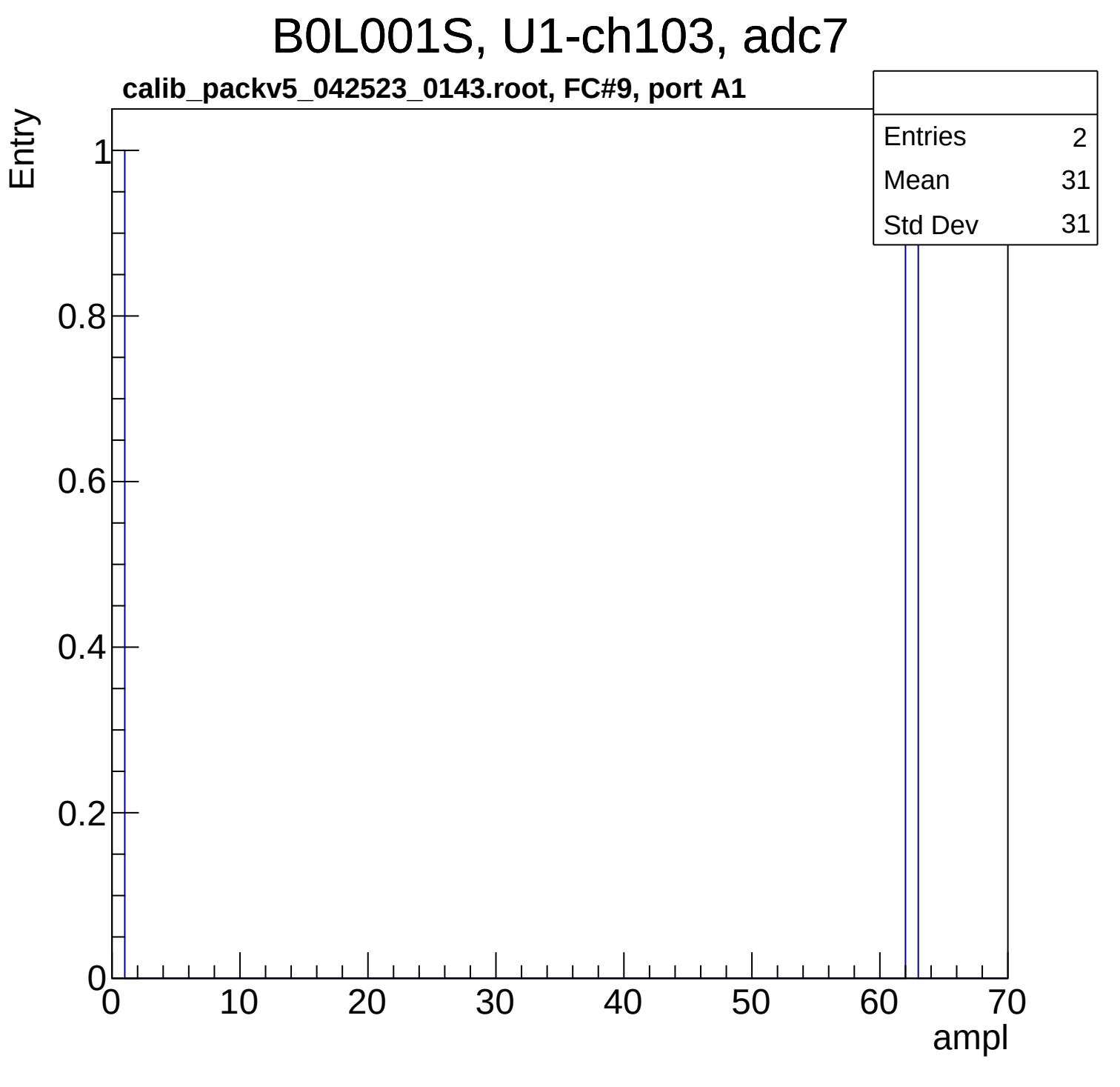
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 60.88
Std Dev 2.368
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B0L001S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
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0.6
0.4
0.2
0
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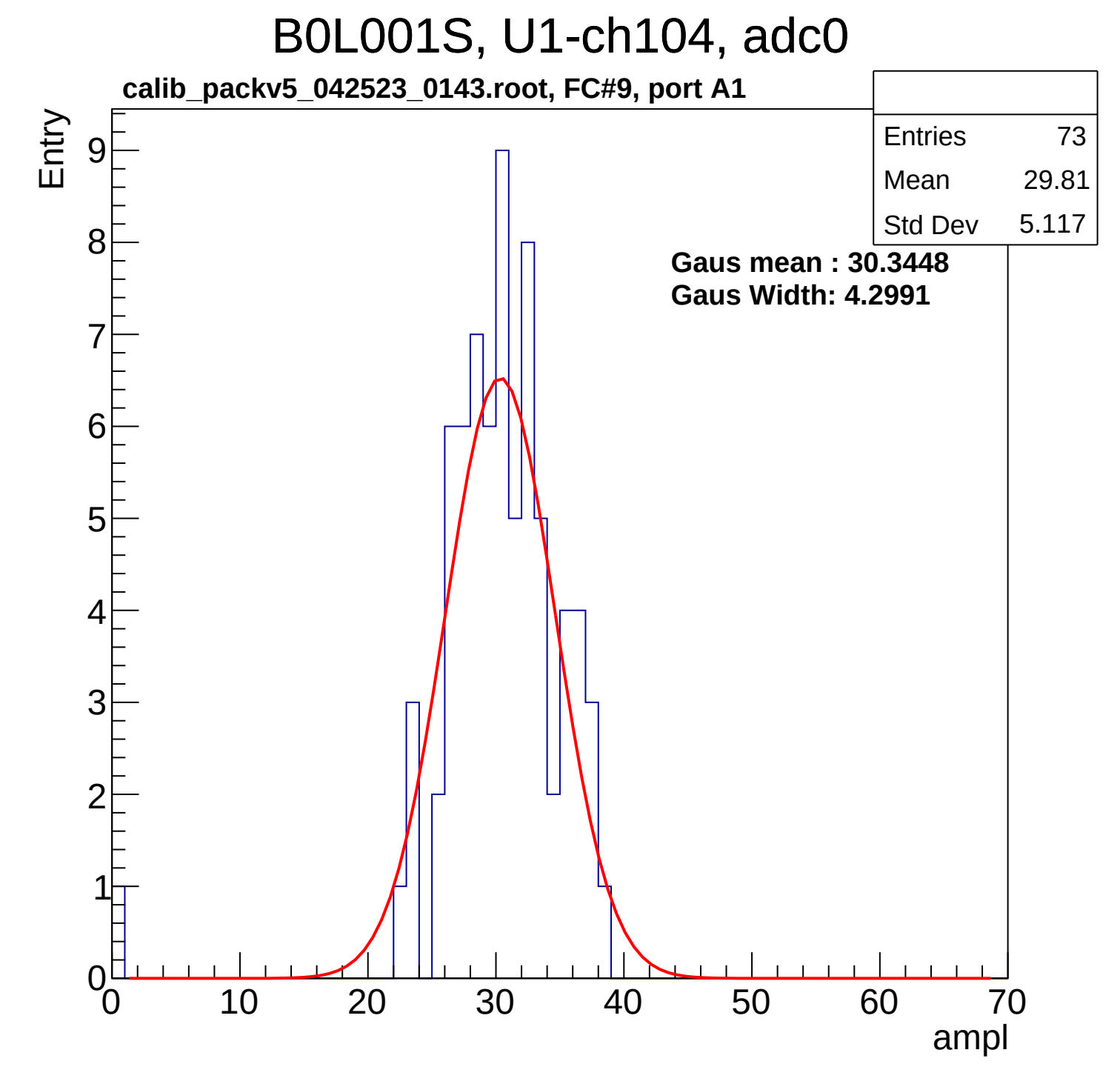
50
60
70
ampl


B0L001S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 29.81
Std Dev 5.117
Gaus mean : 30.3448
Gaus Width: 4.2991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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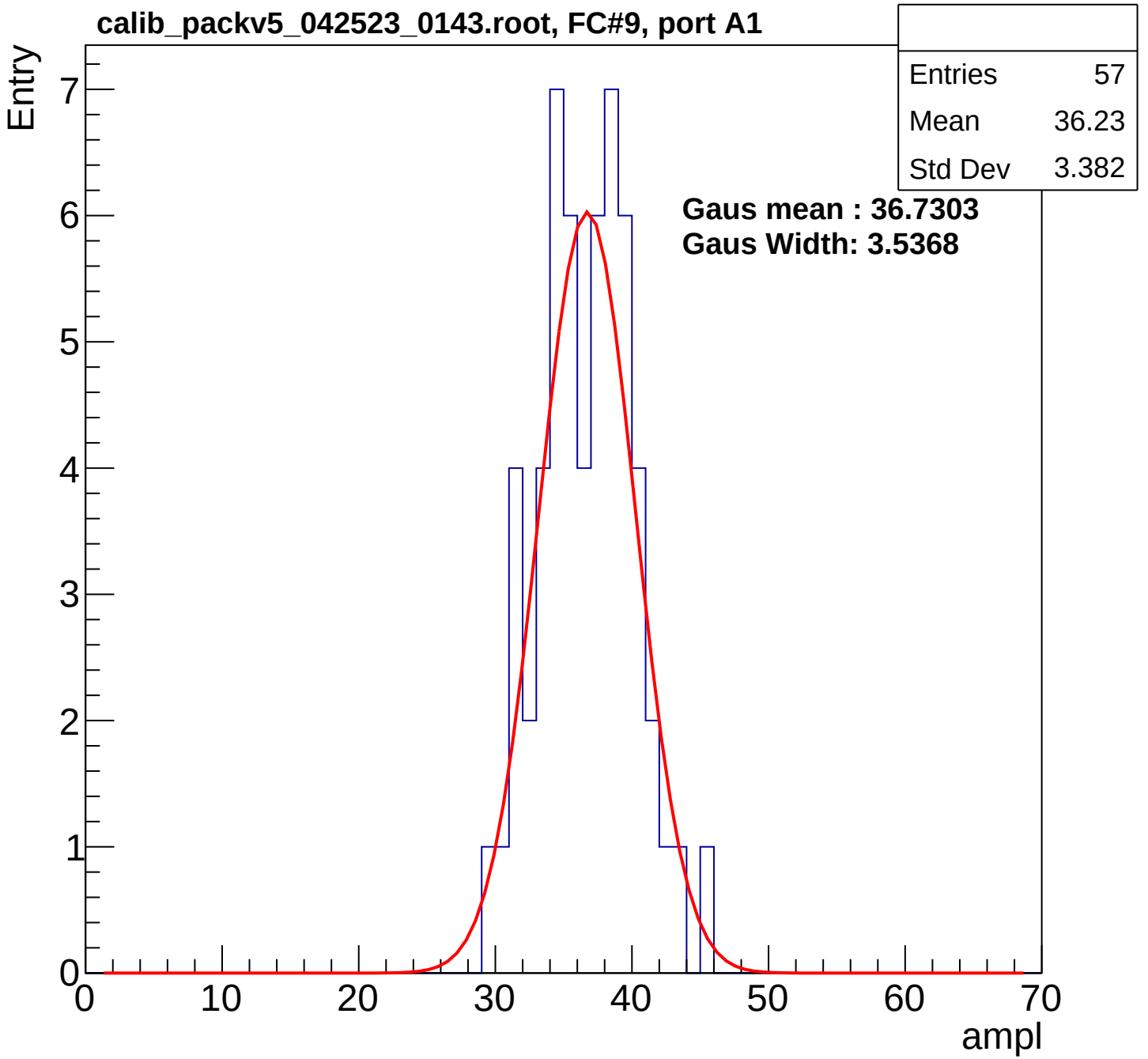
B0L001S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 36.23
Std Dev 3.382
Gaus mean : 36.7303
Gaus Width: 3.5368
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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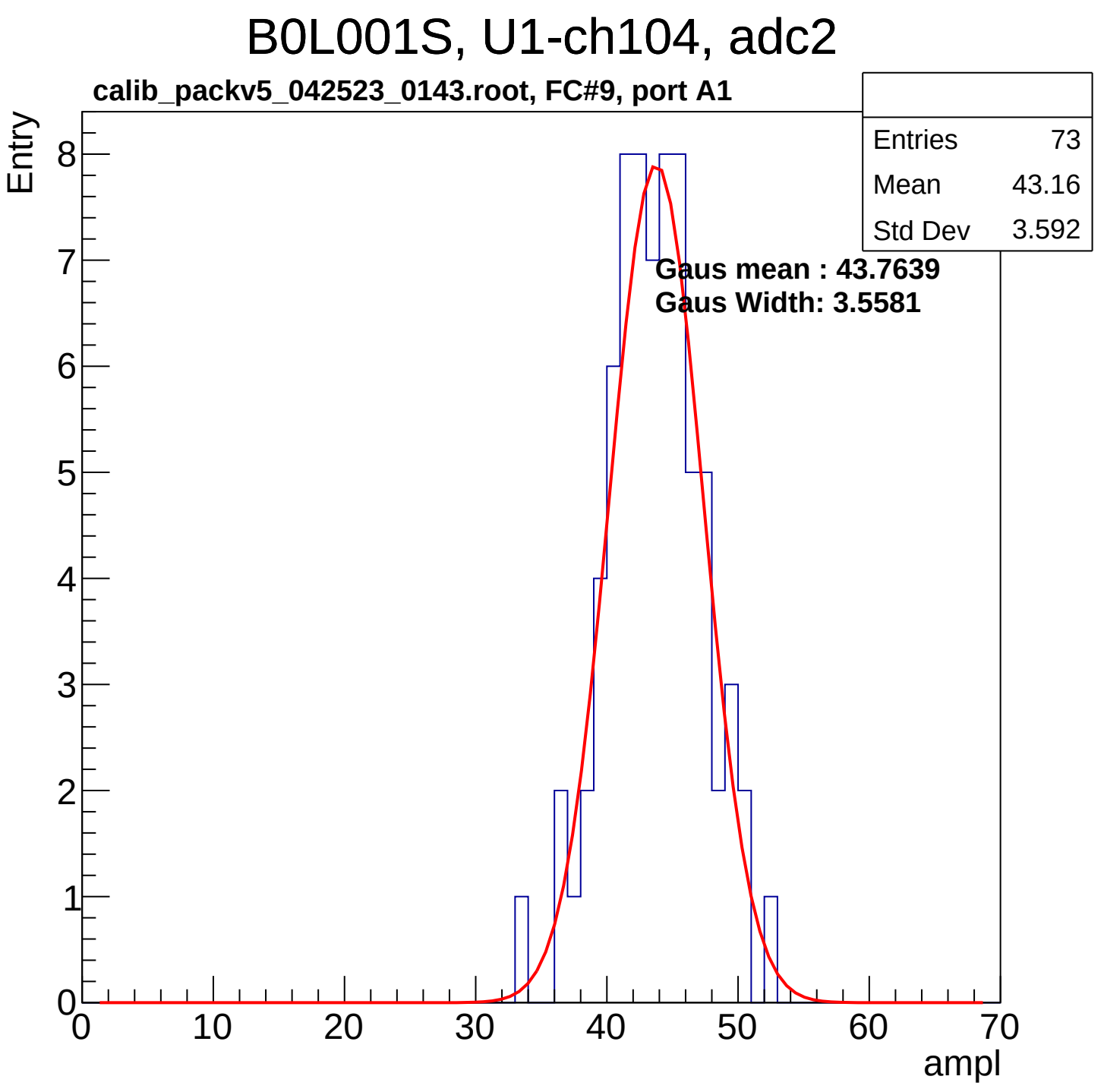
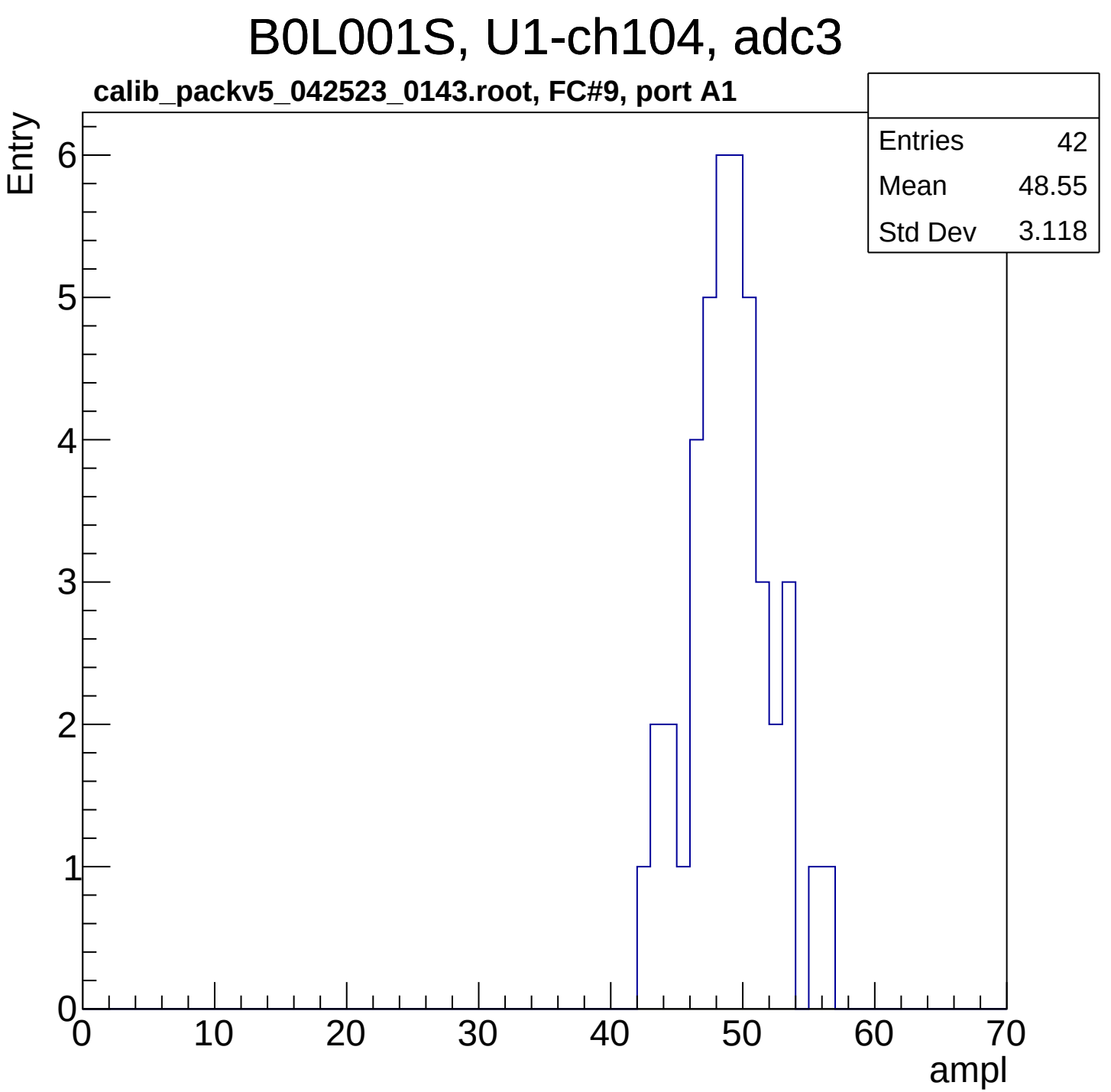
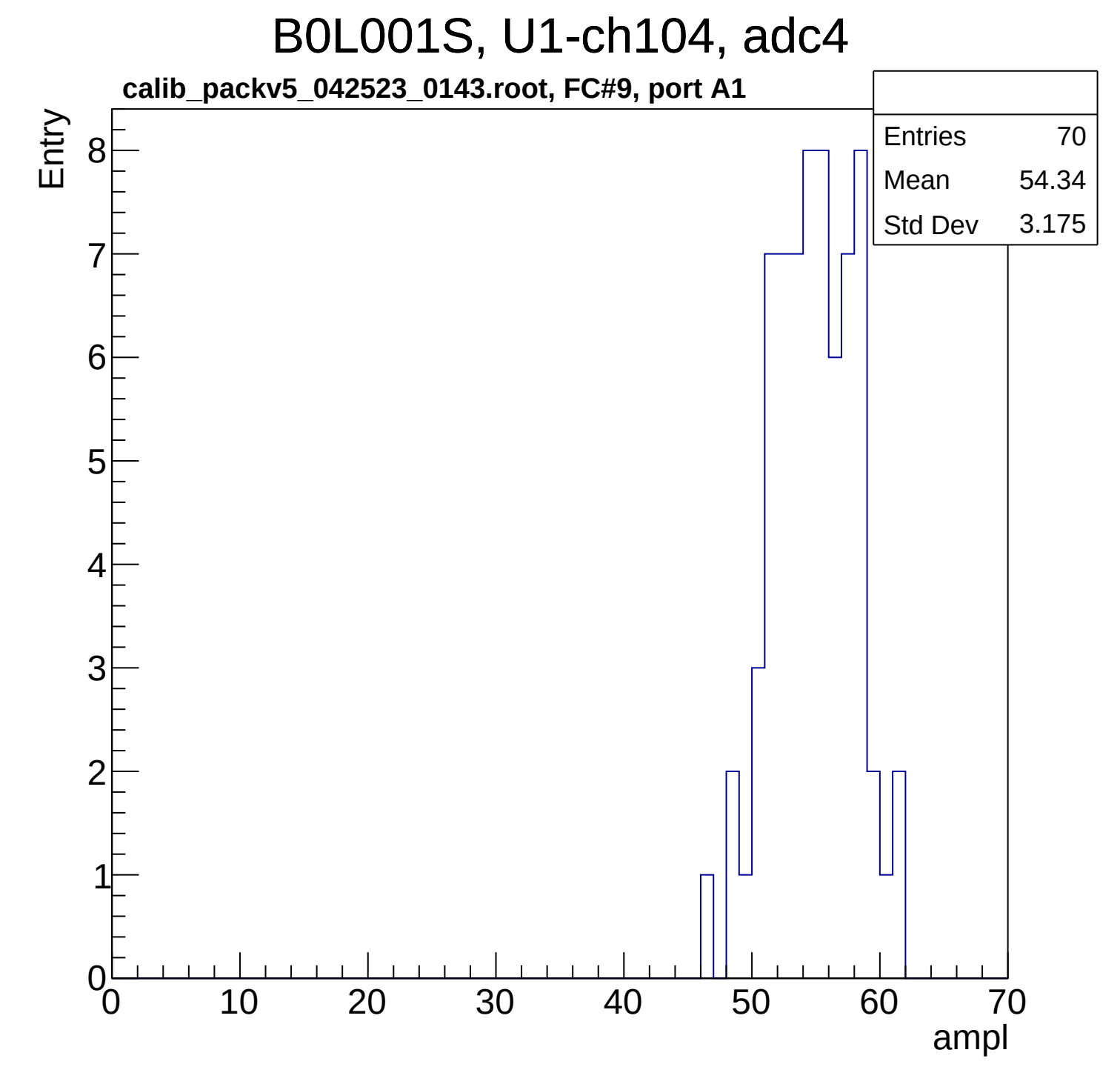

B0L001S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 43.16
Std Dev 3.592
Gaus mean : 43.7639
Gaus Width: 3.5581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 42
Mean 48.55
Std Dev 3.118
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 54.34
Std Dev 3.175
Entry
ampl
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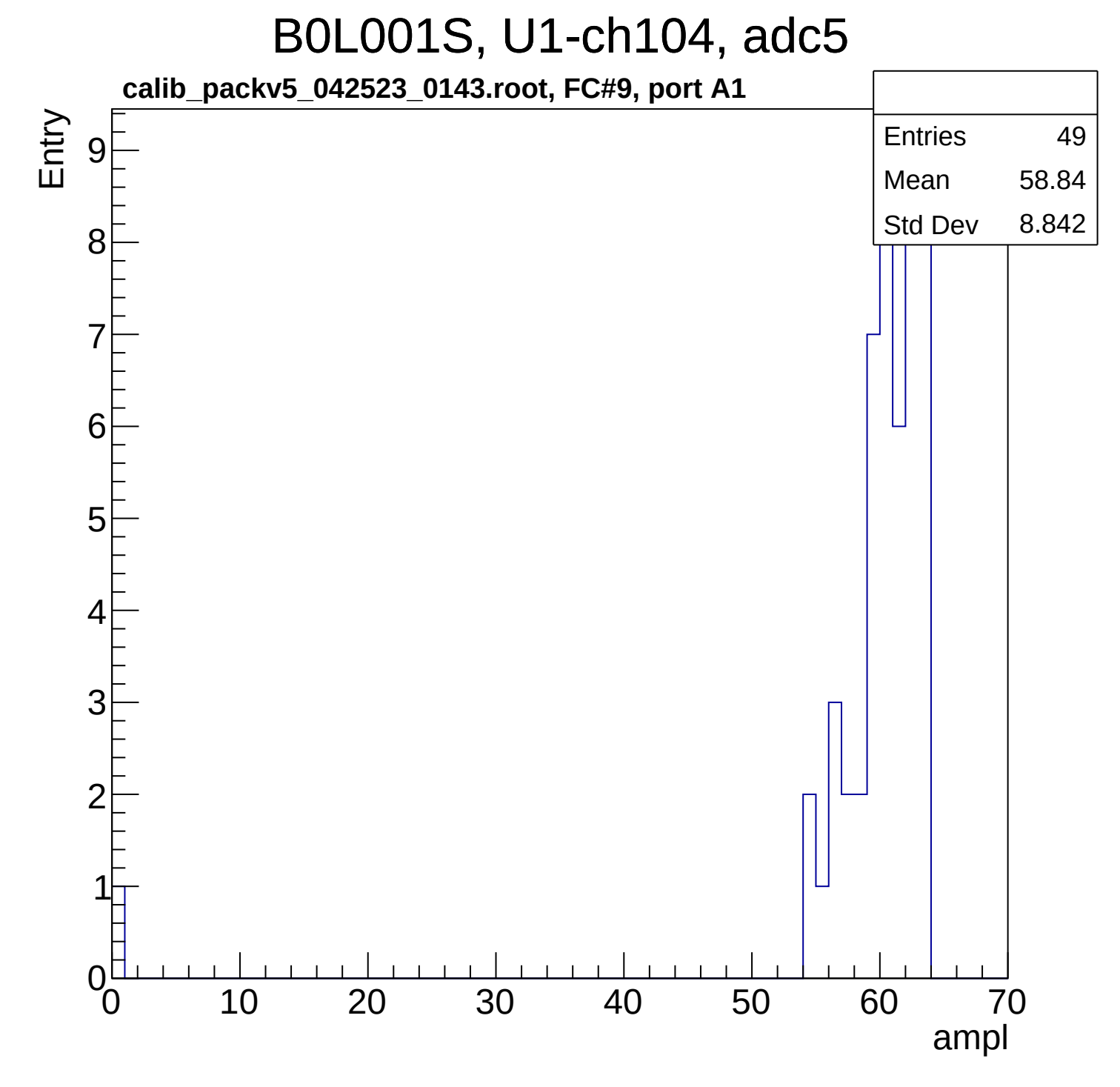
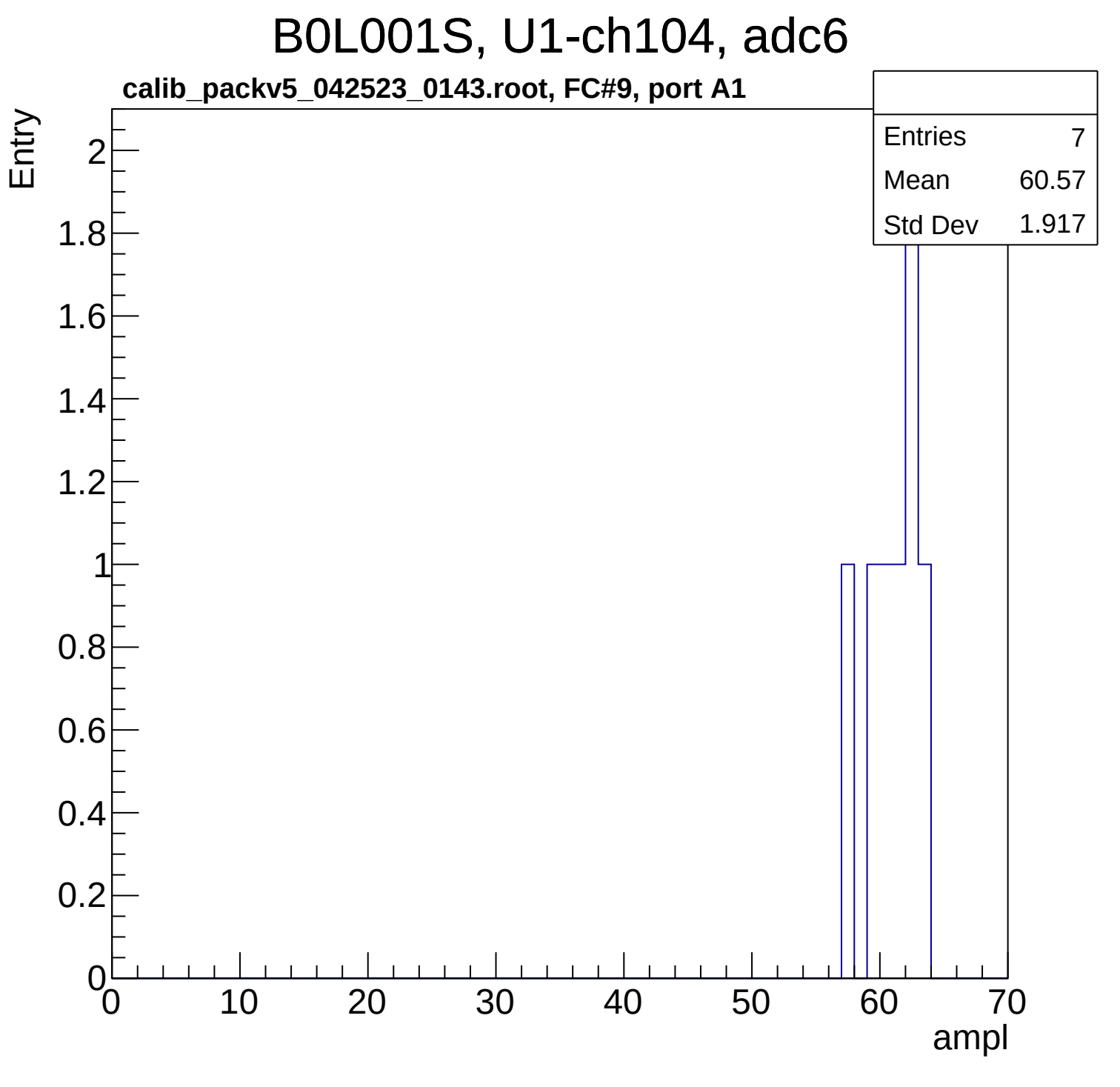
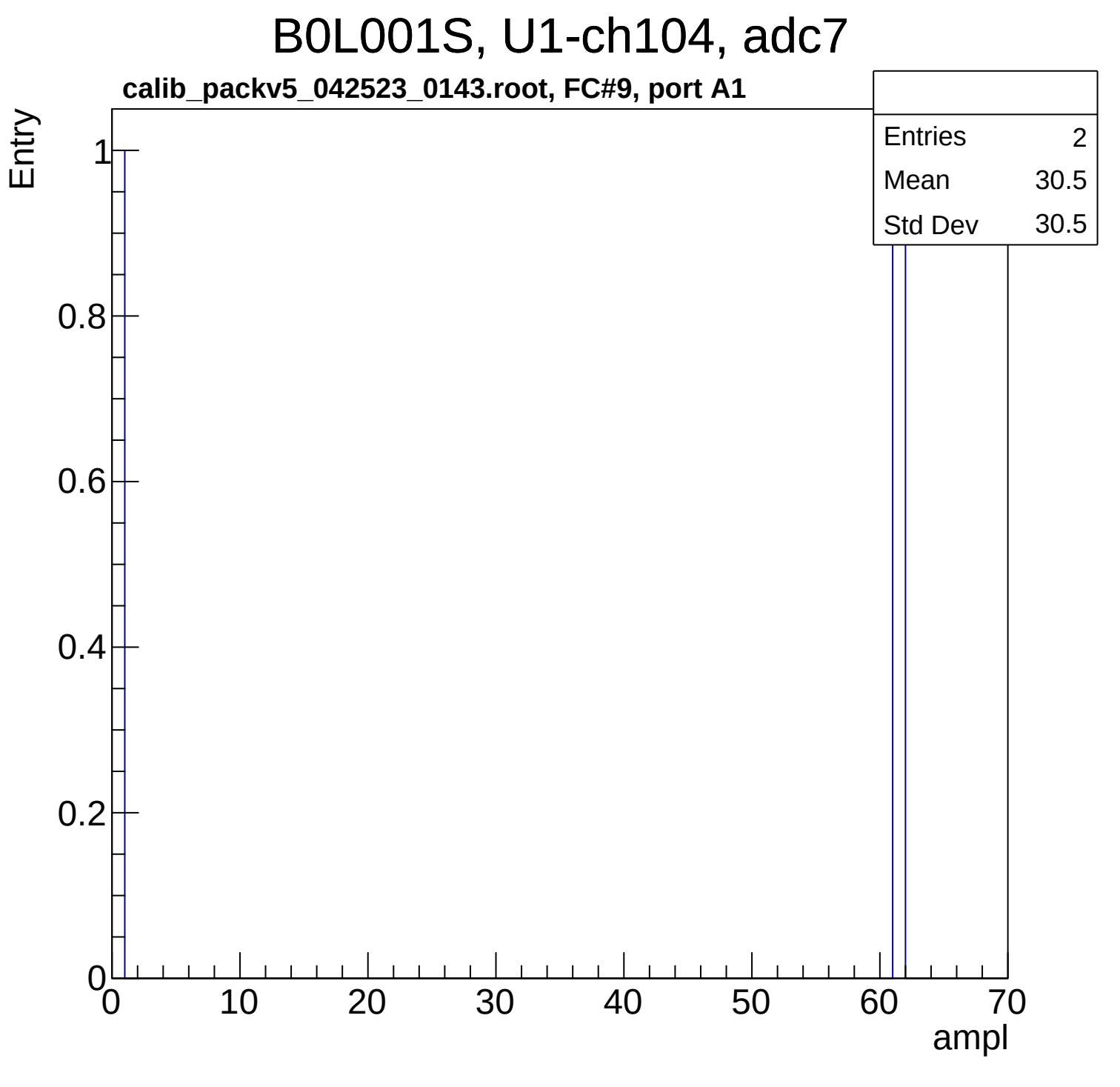
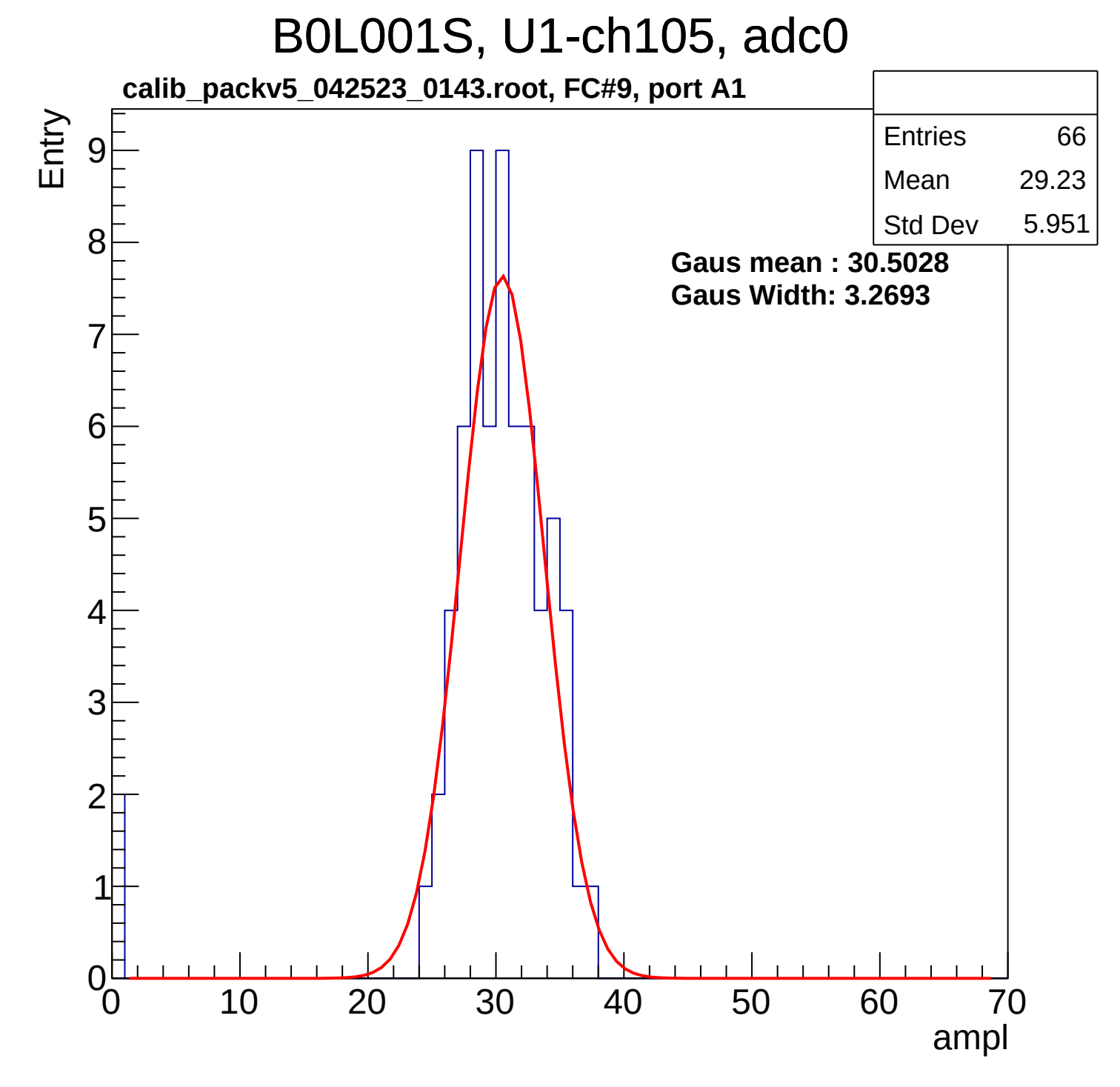
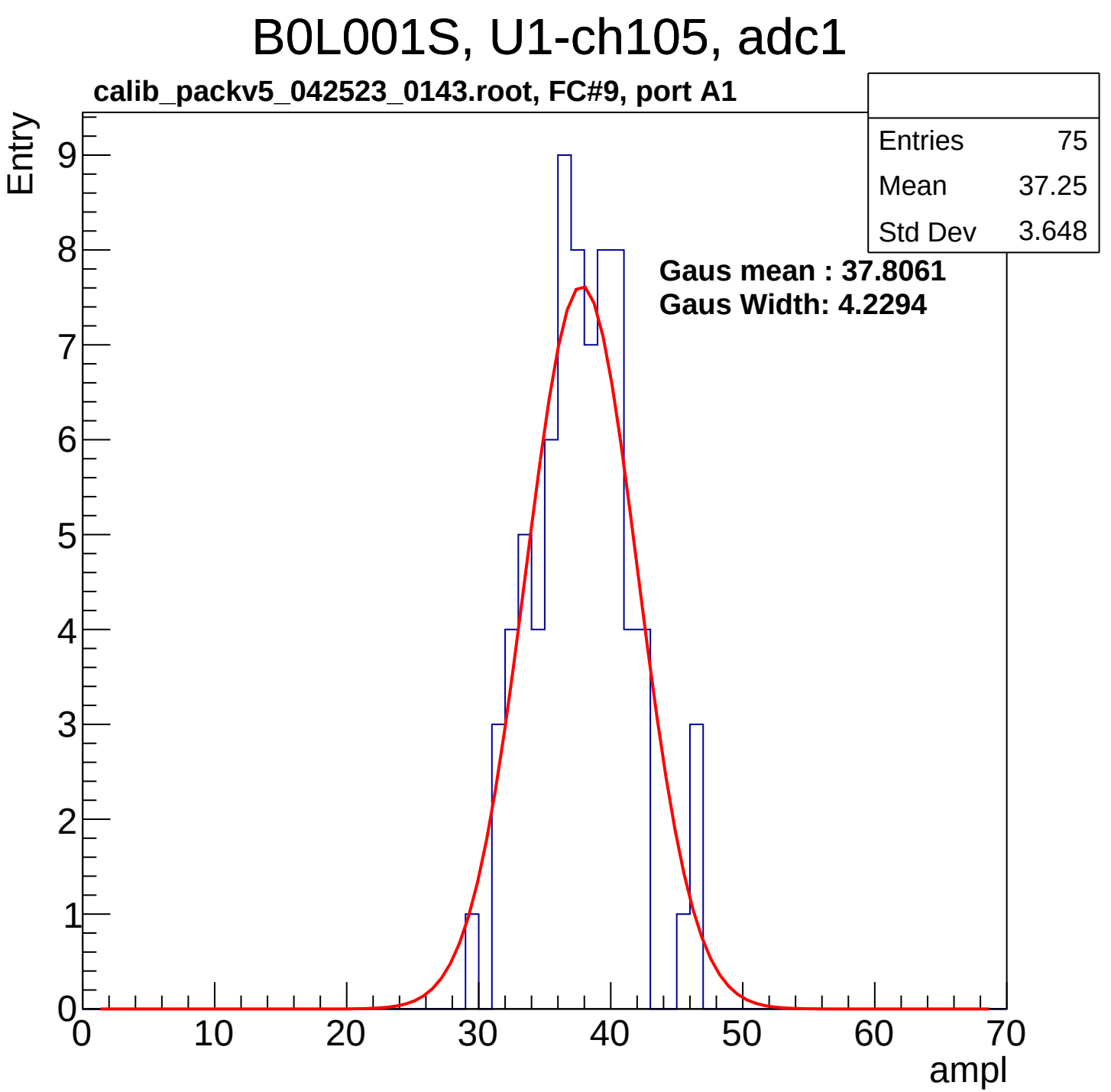
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

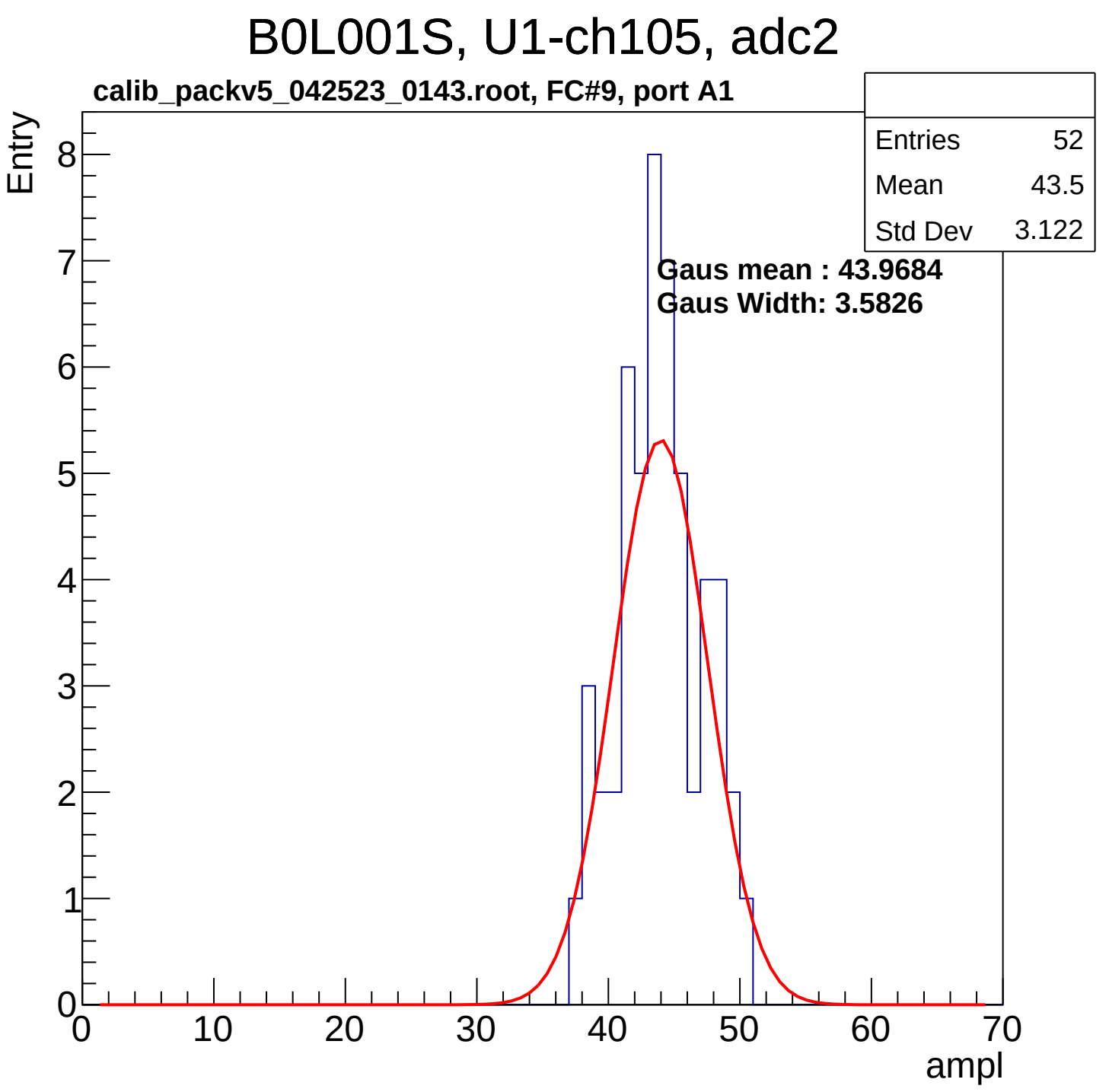

B0L001S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.84
Std Dev 8.842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 7
Mean 60.57
Std Dev 1.917
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.23
Std Dev 5.951
Gaus mean : 30.5028
Gaus Width: 3.2693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 37.25
Std Dev 3.648
Gaus mean : 37.8061
Gaus Width: 4.2294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 43.5
Std Dev 3.122
Gaus mean : 43.9684
Gaus Width: 3.5826
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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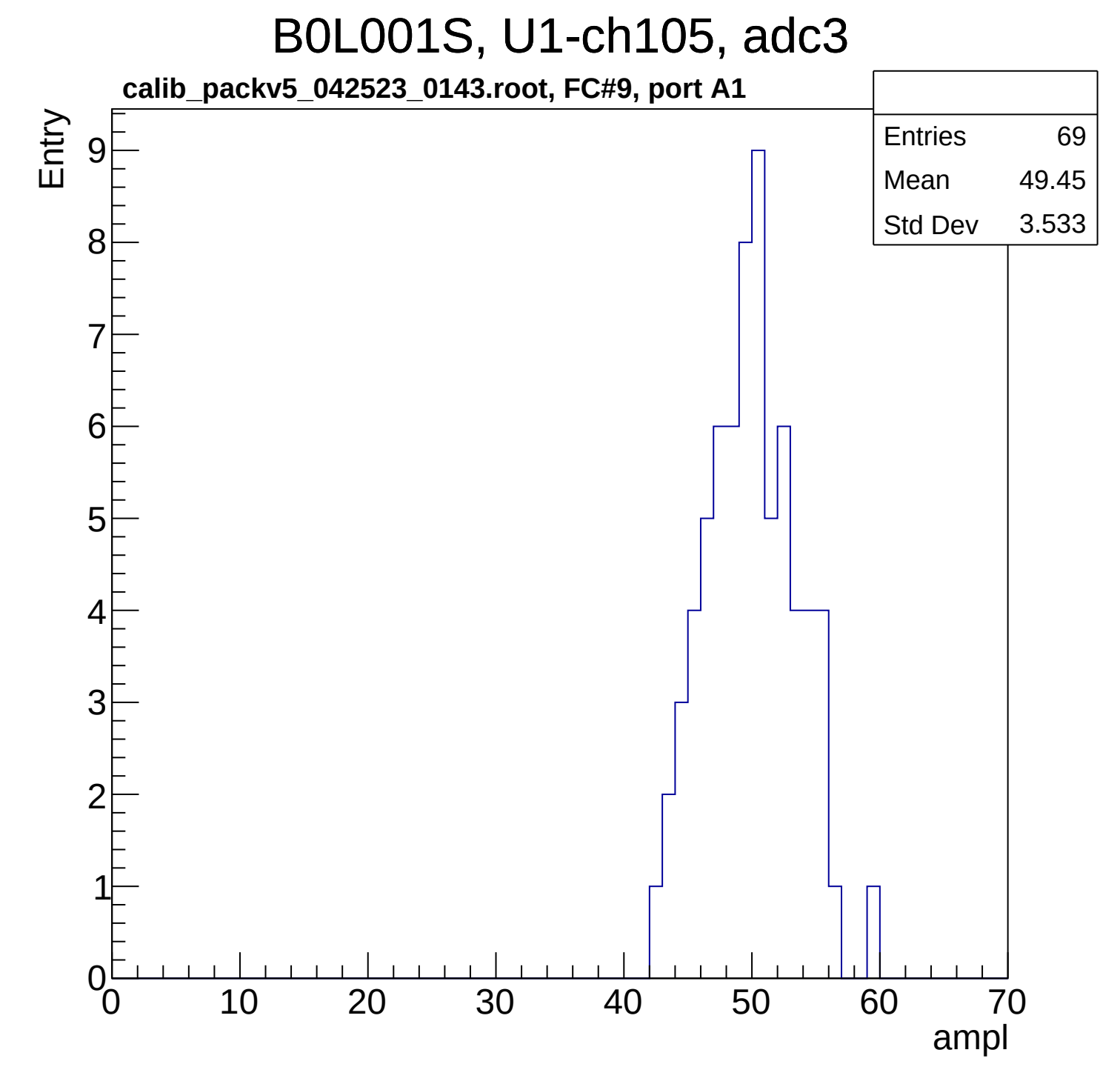
B0L001S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 49.45
Std Dev 3.533
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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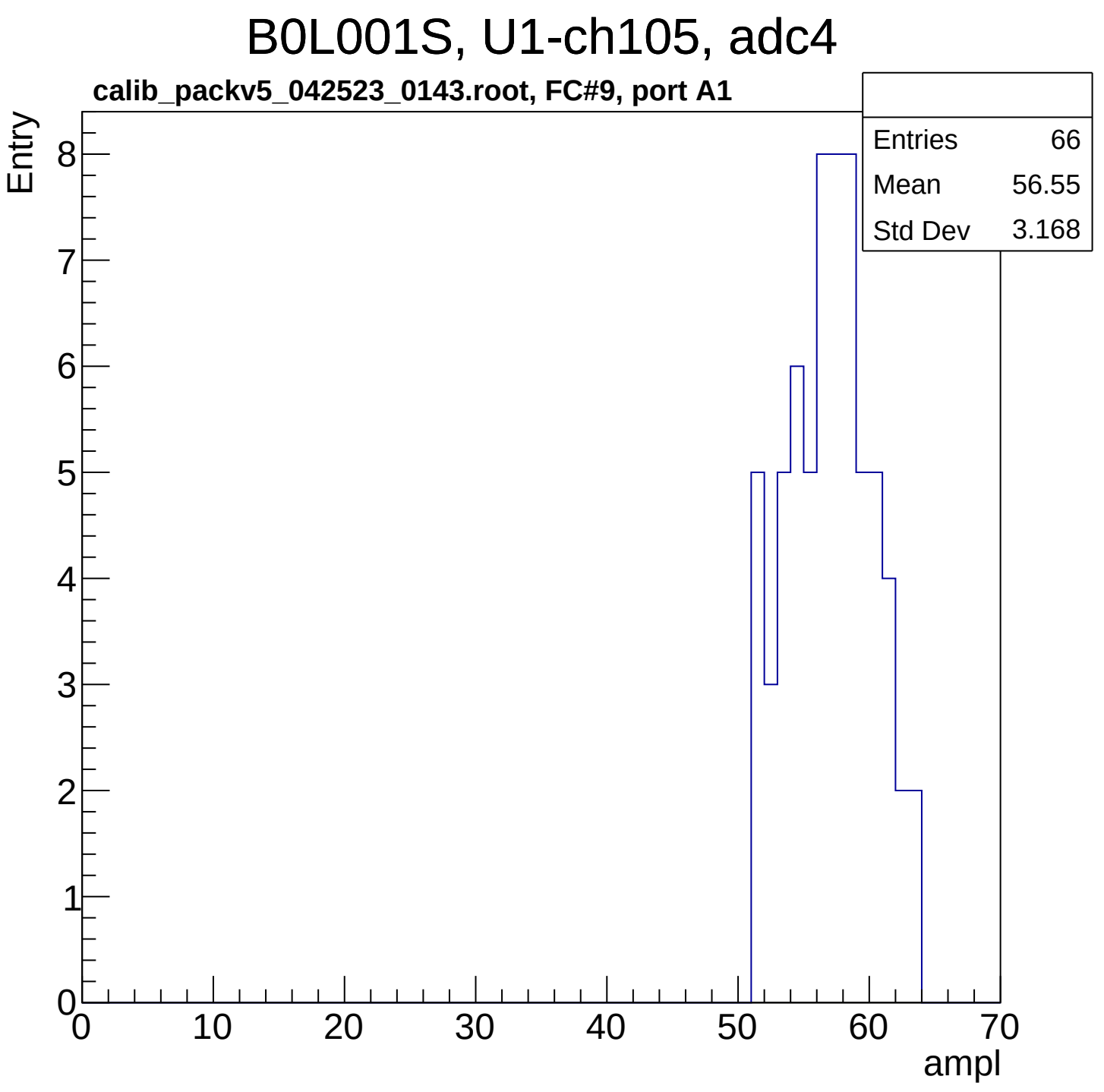
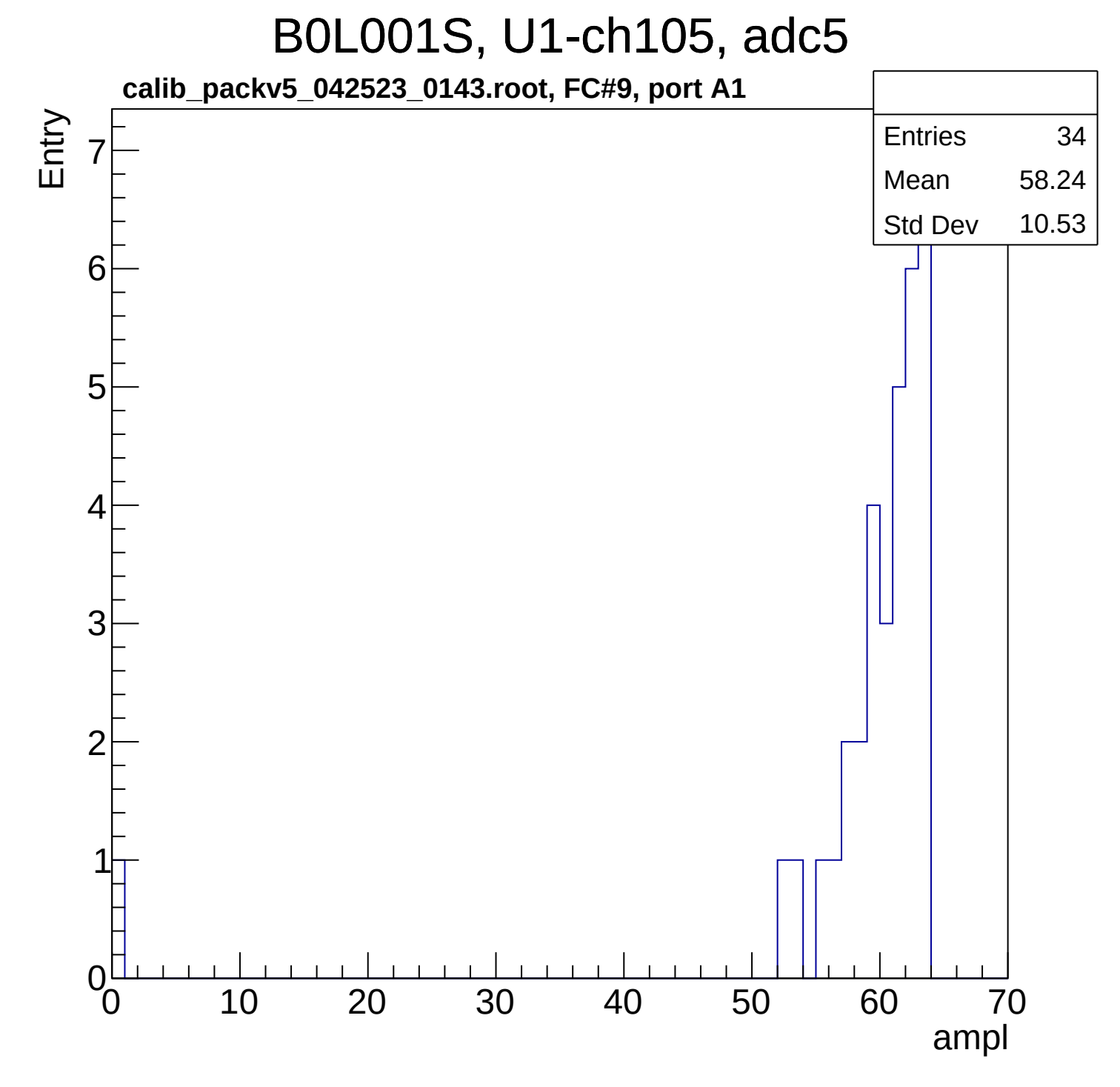
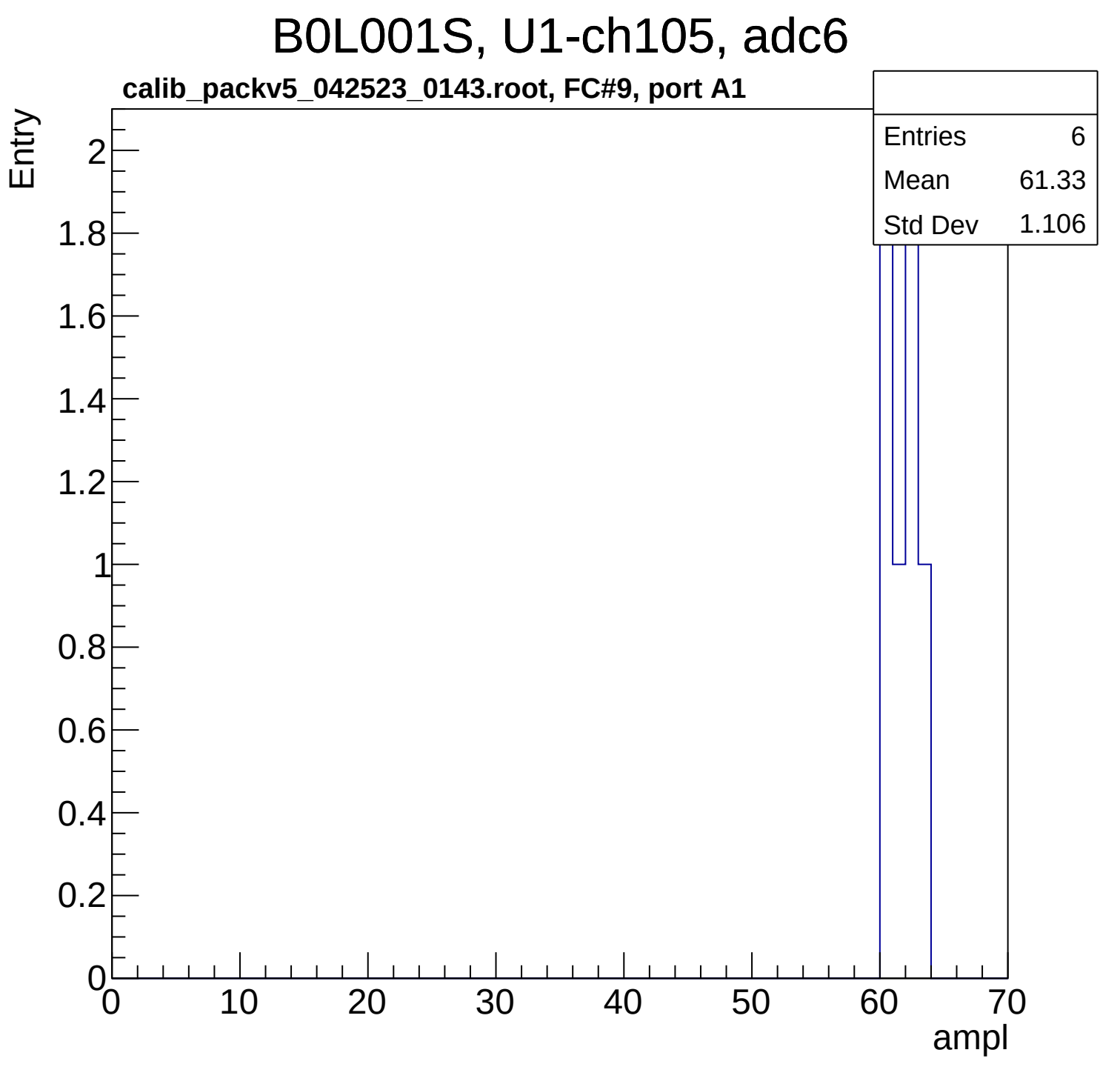
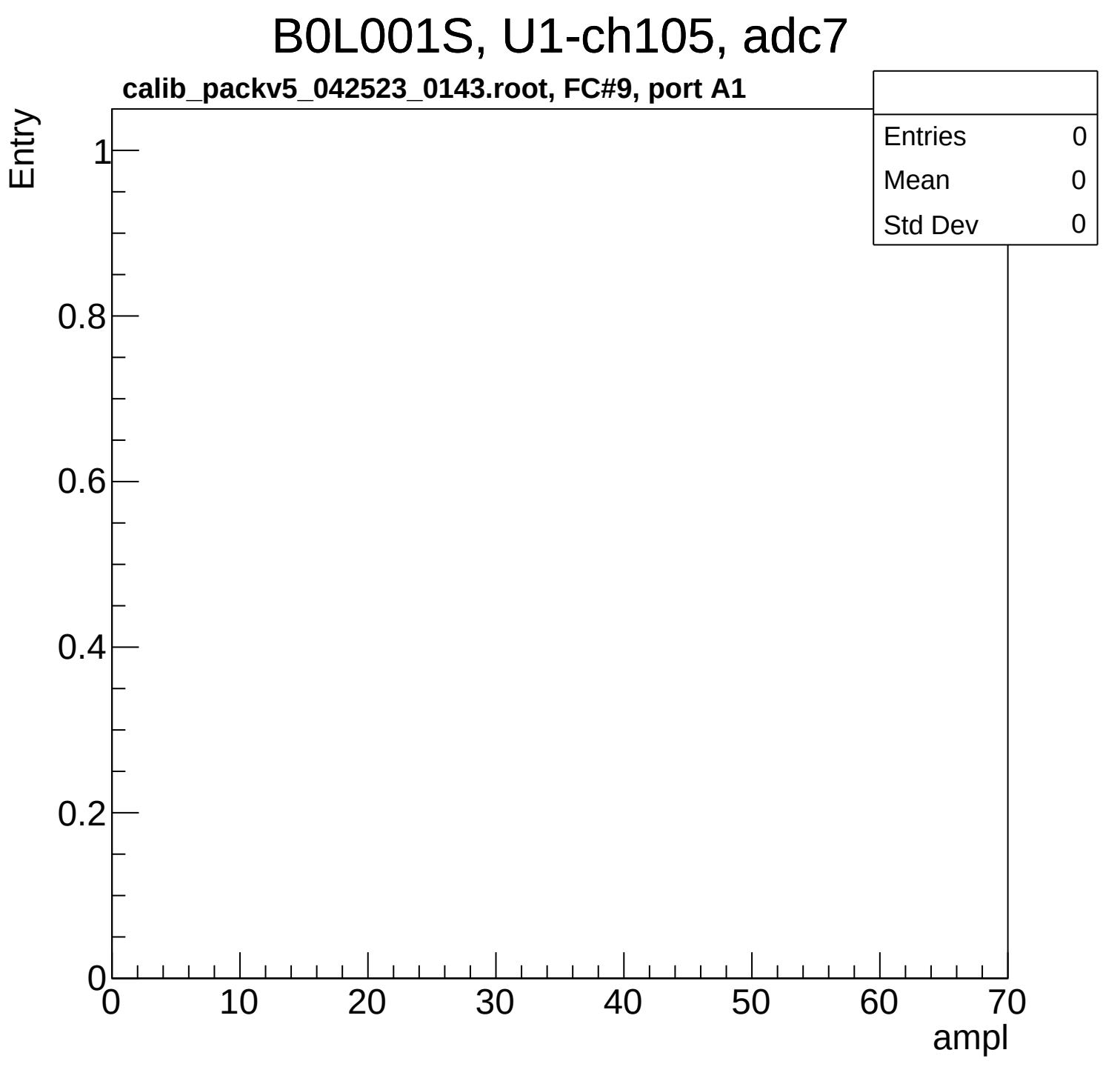
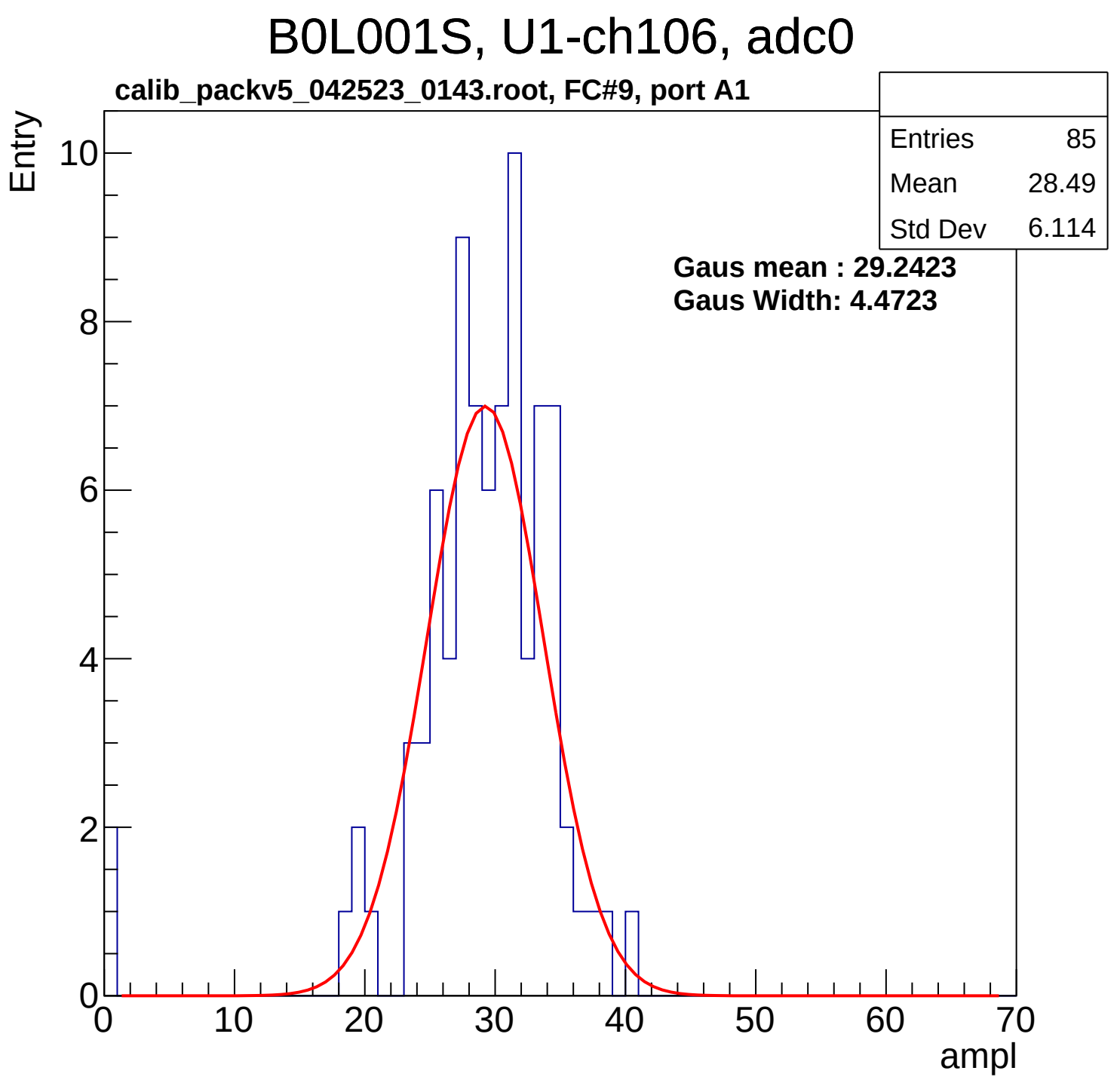
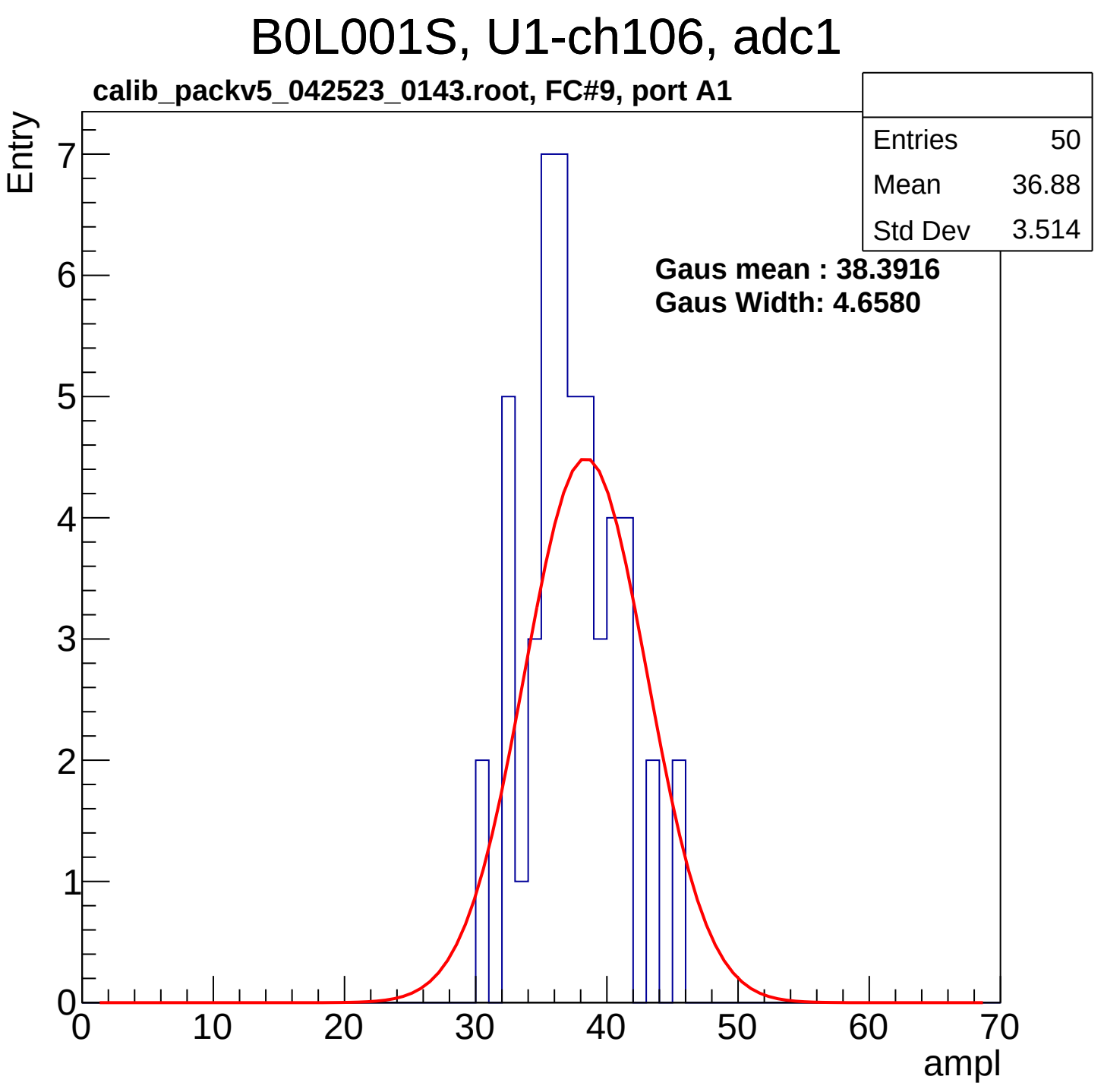

B0L001S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.55
Std Dev 3.168
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 34
Mean 58.24
Std Dev 10.53
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 61.33
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 85
Mean 28.49
Std Dev 6.114
Gaus mean : 29.2423
Gaus Width: 4.4723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 36.88
Std Dev 3.514
Gaus mean : 38.3916
Gaus Width: 4.6580
Entry
ampl
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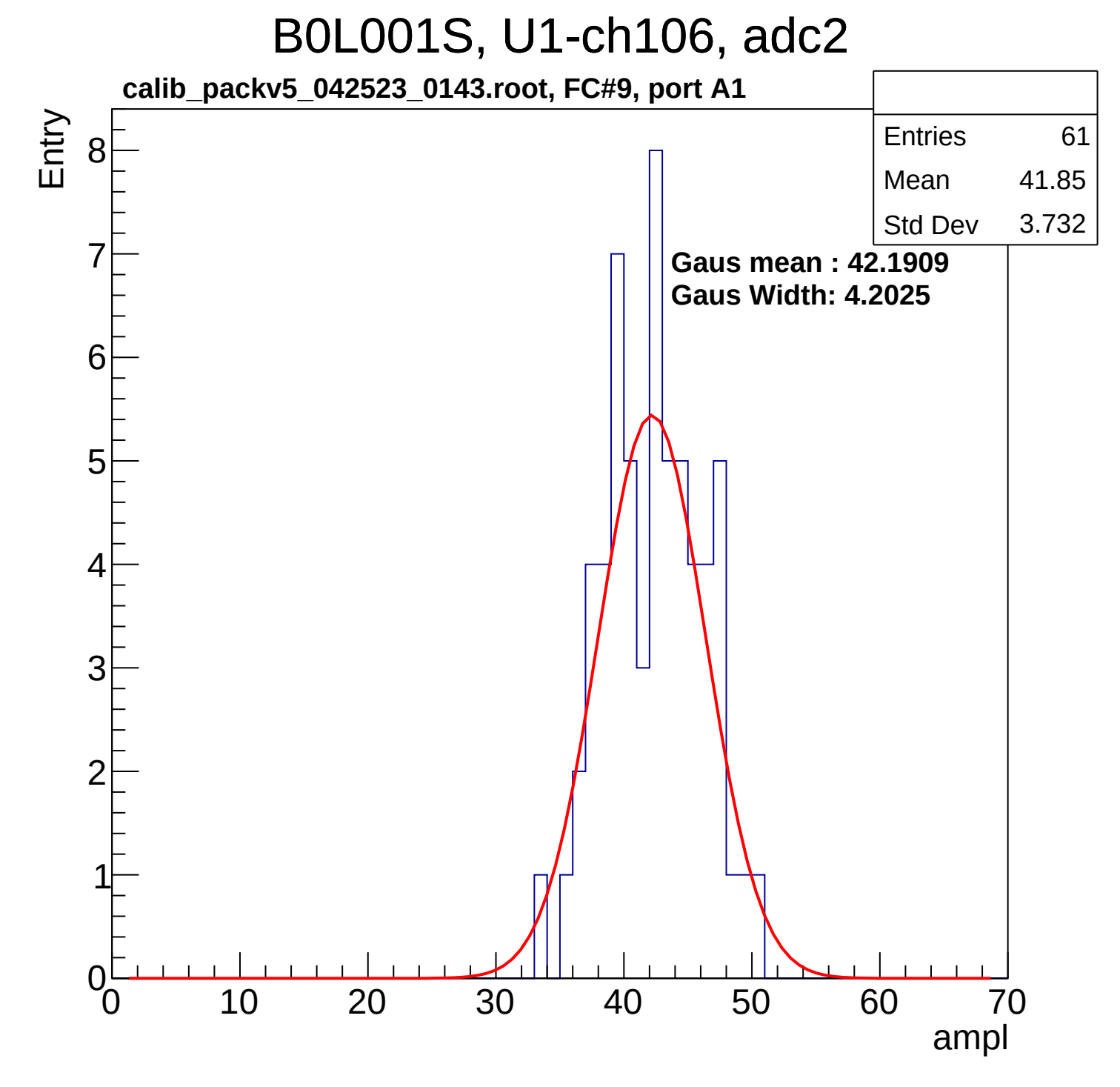
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 41.85
Std Dev 3.732
Gaus mean : 42.1909
Gaus Width: 4.2025
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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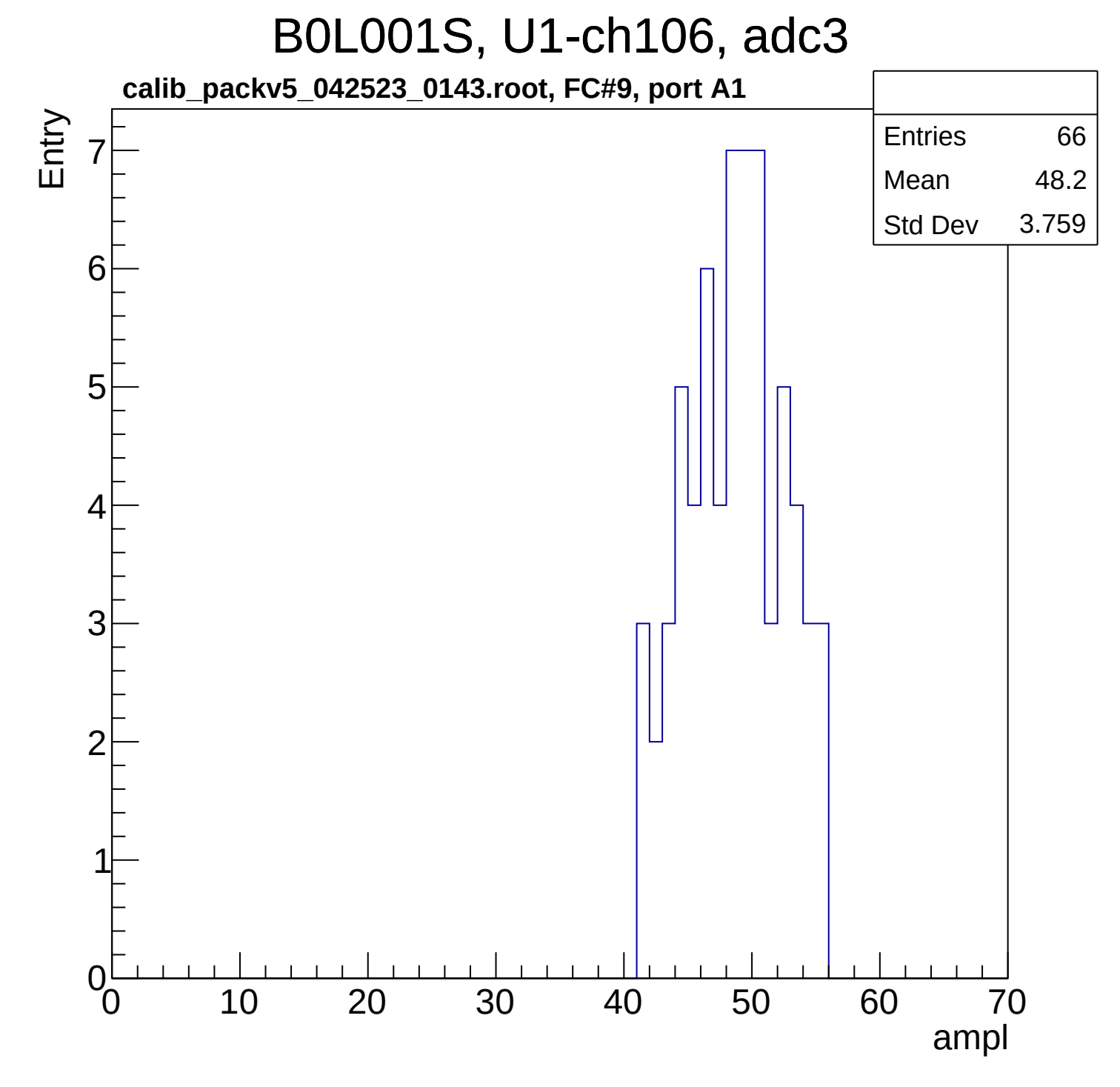
B0L001S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 48.2
Std Dev 3.759
Entry
ampl
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1
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3
4
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0
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60
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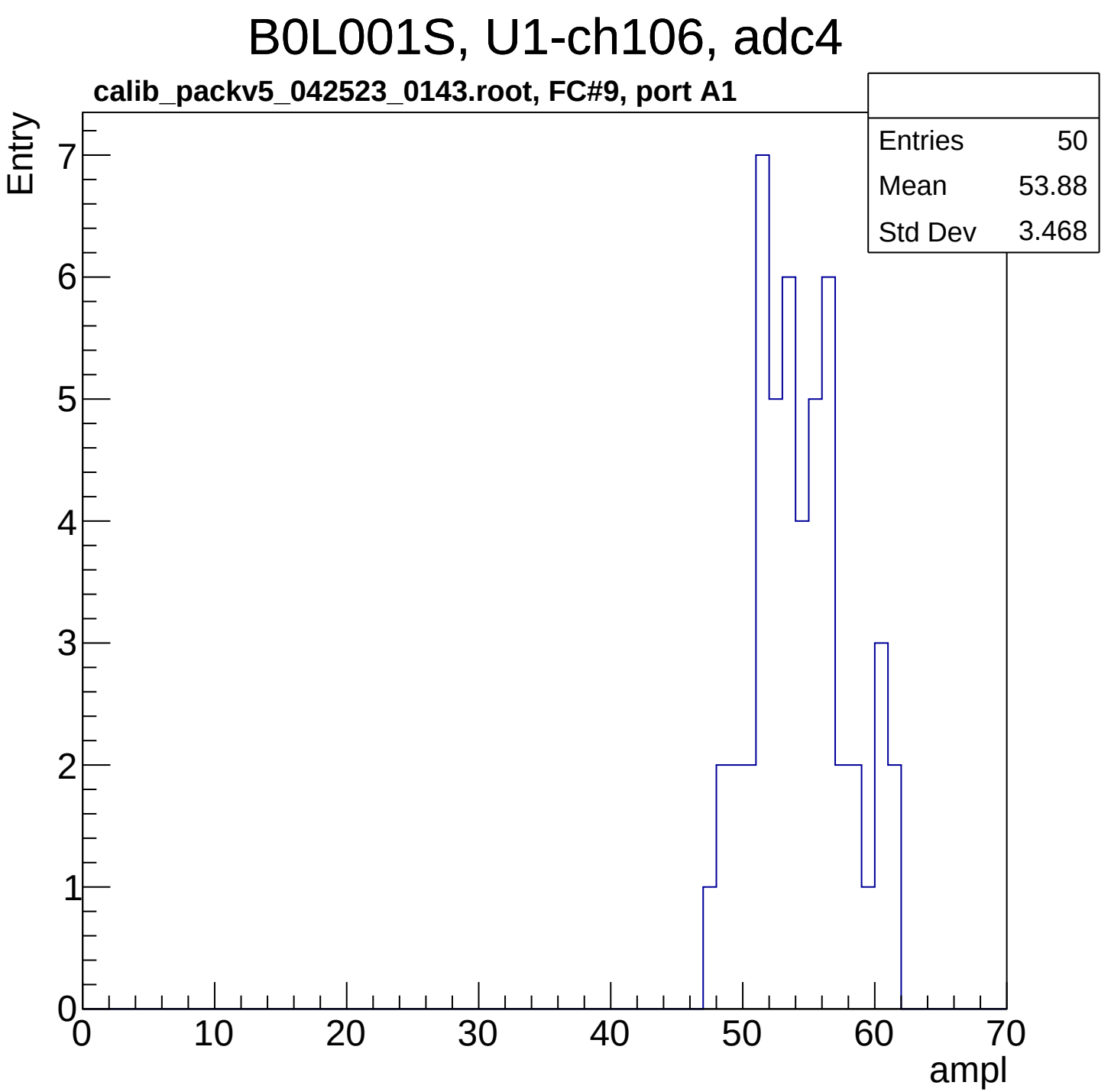
B0L001S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 53.88
Std Dev 3.468
Entry
ampl
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1
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4
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0
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60
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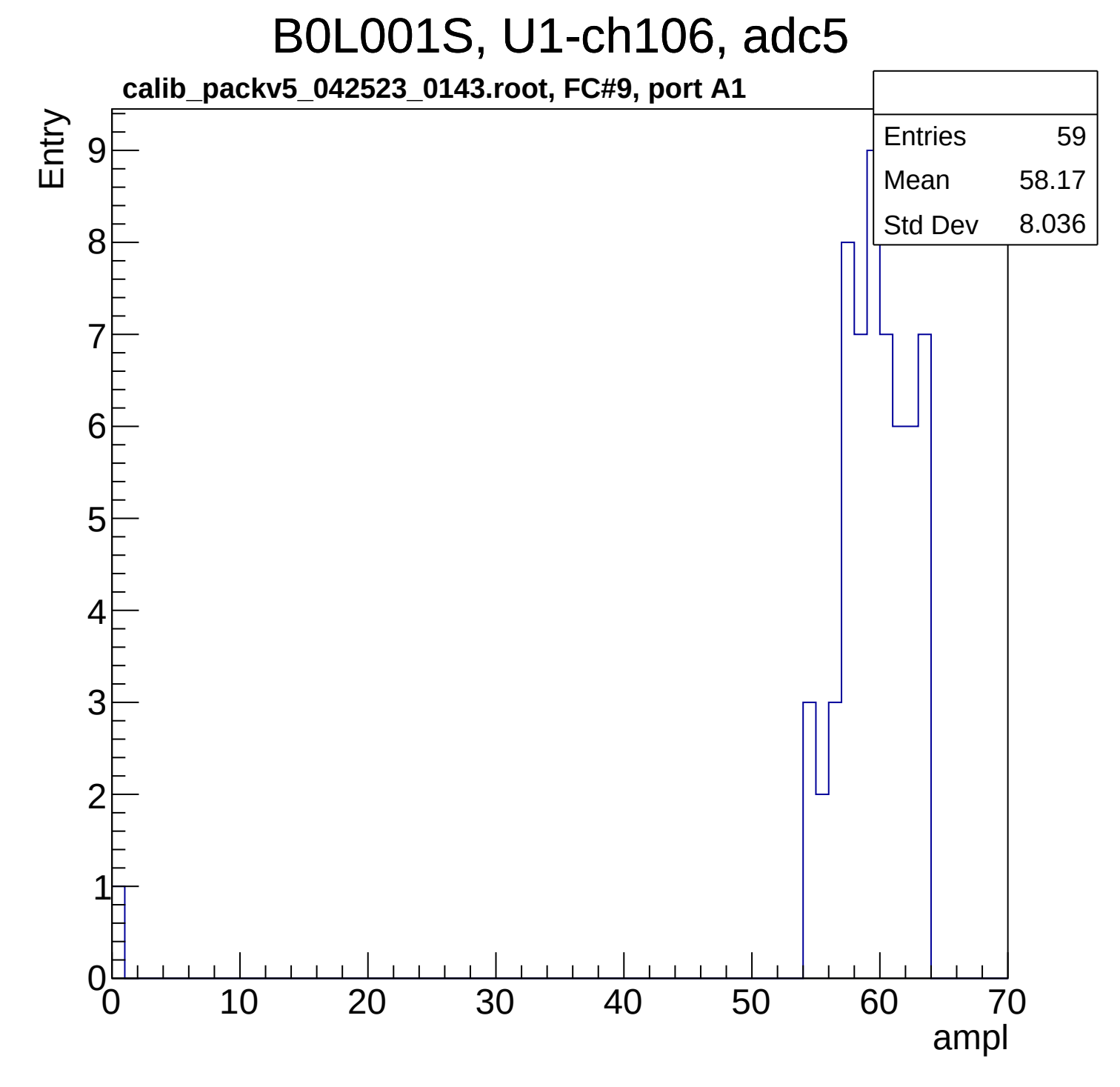
B0L001S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 58.17
Std Dev 8.036
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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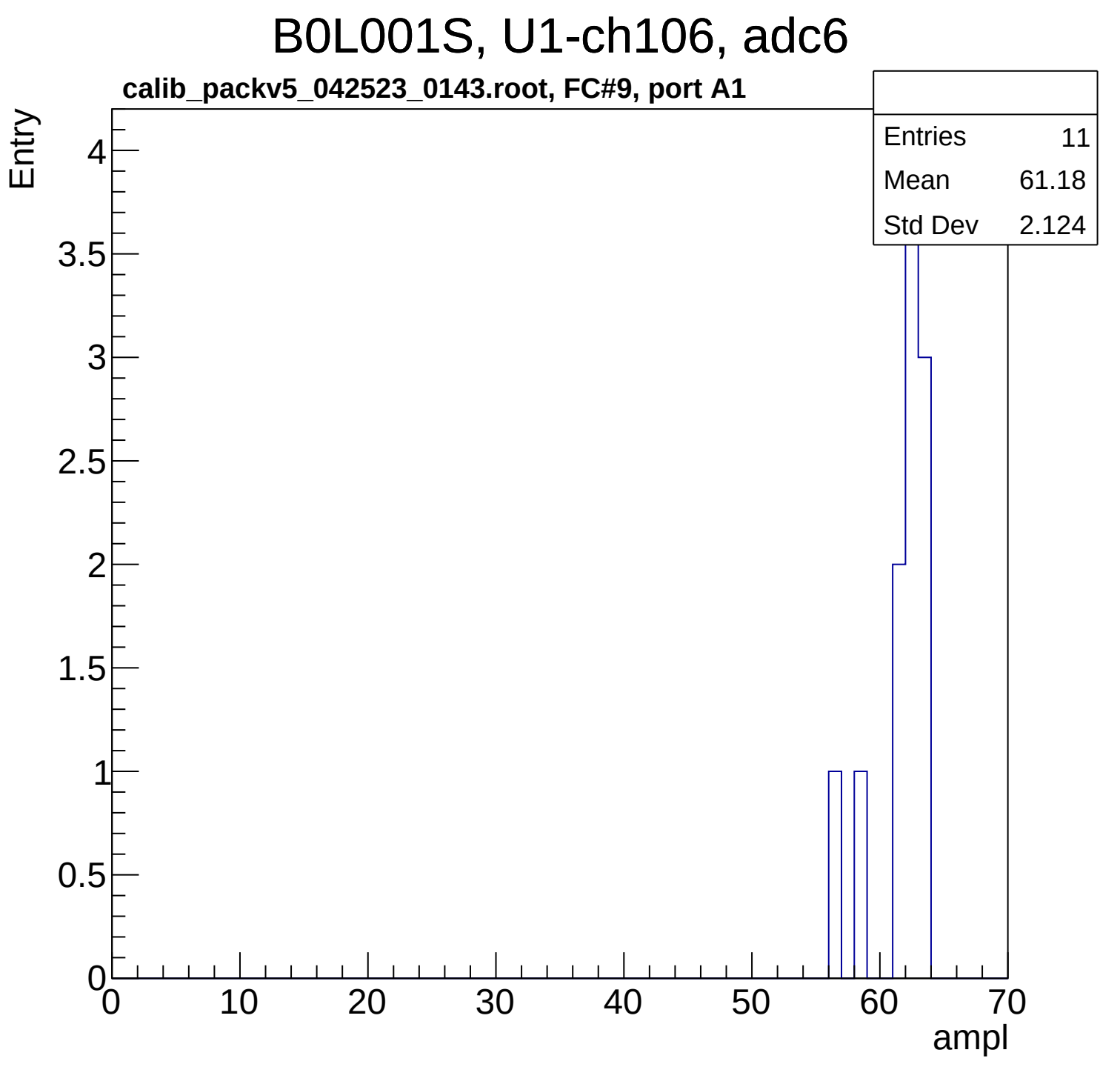
40
50
60
70


B0L001S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.18
Std Dev 2.124
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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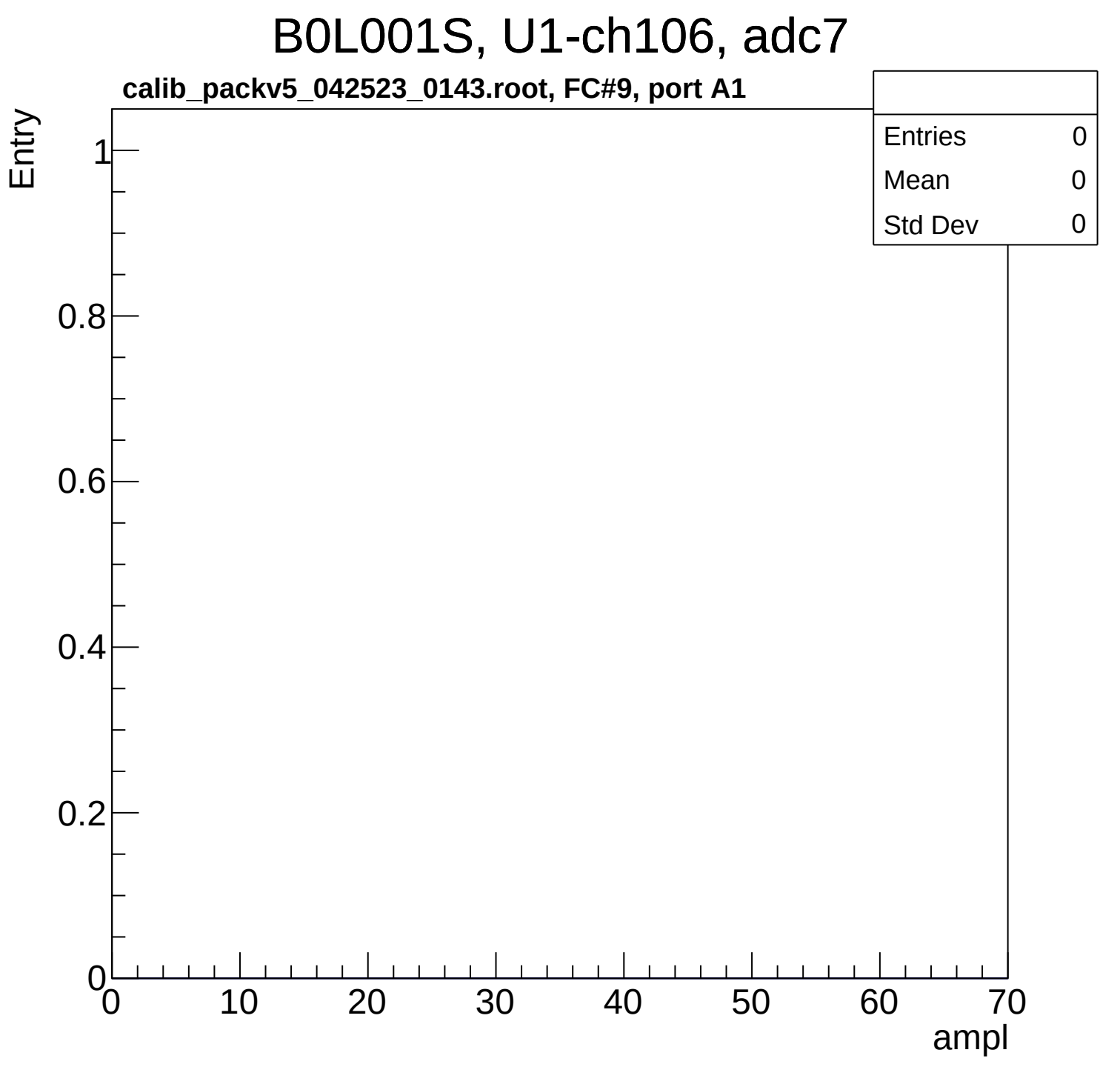
40
50
60
70


B0L001S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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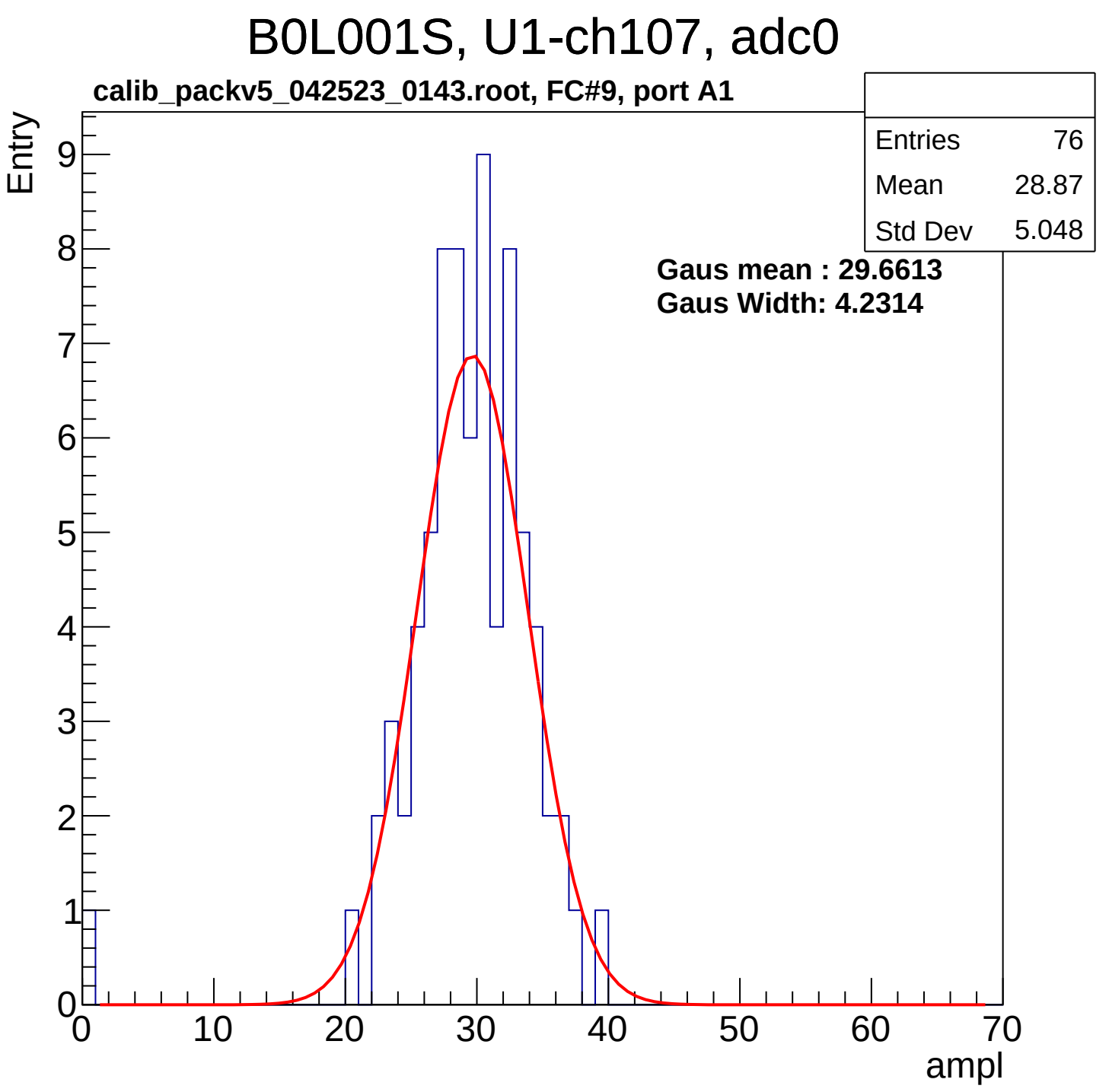
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 28.87
Std Dev 5.048
Gaus mean : 29.6613
Gaus Width: 4.2314
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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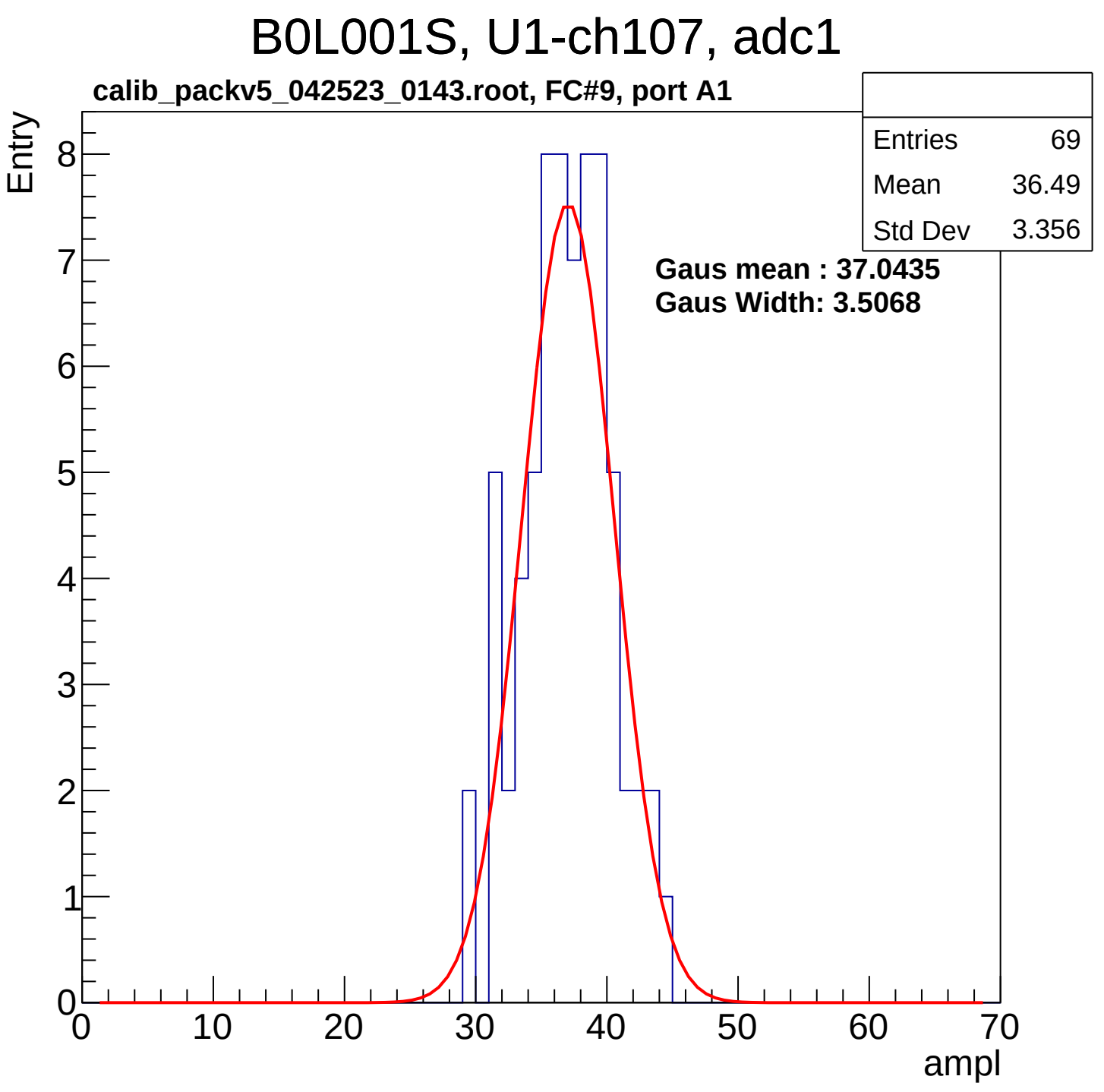
B0L001S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 69
Mean 36.49
Std Dev 3.356
Gaus mean : 37.0435
Gaus Width: 3.5068
Entry
ampl
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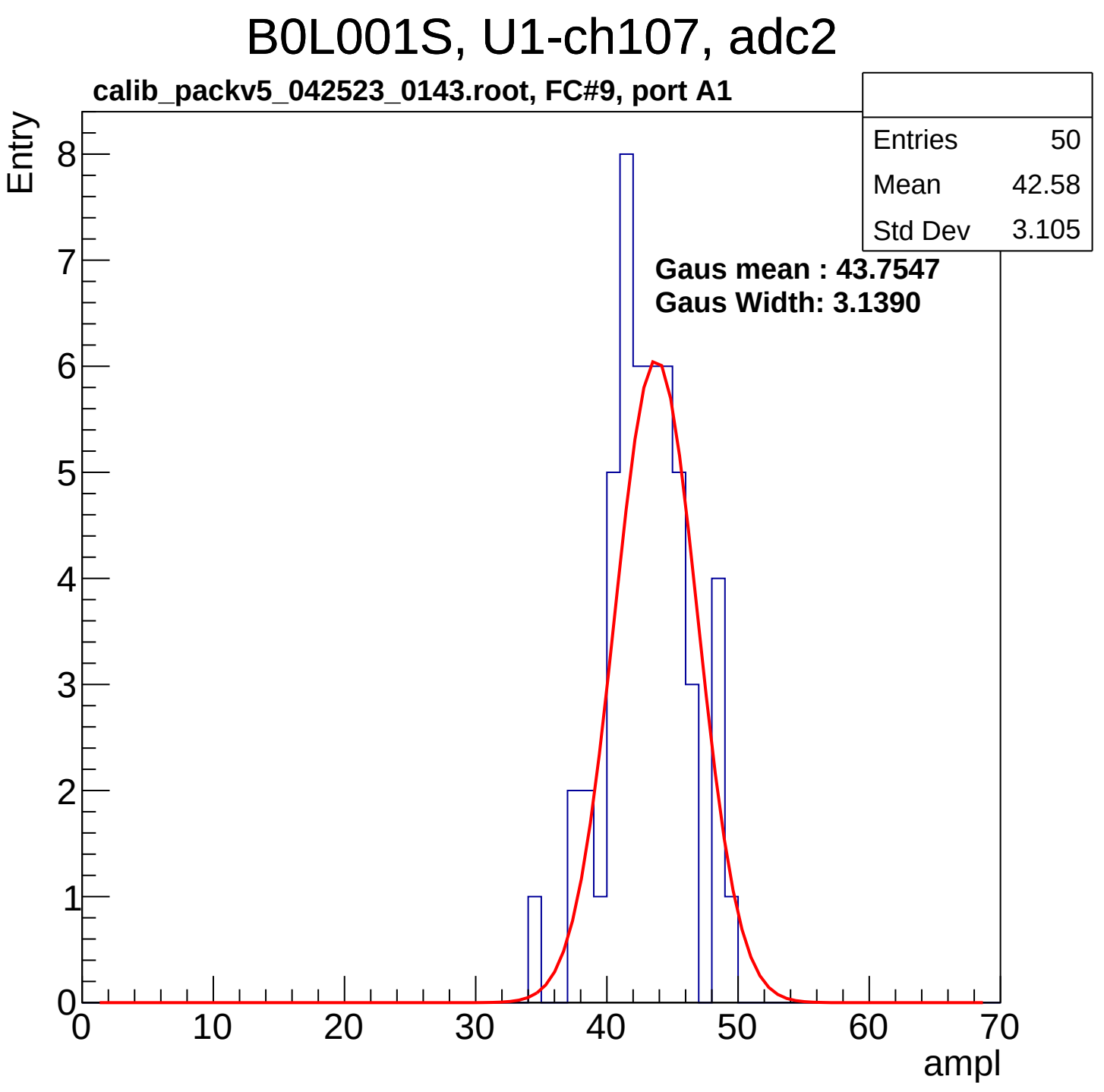
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 42.58
Std Dev 3.105
Gaus mean : 43.7547
Gaus Width: 3.1390
Entry
ampl
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1
2
3
4
5
6
7
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0
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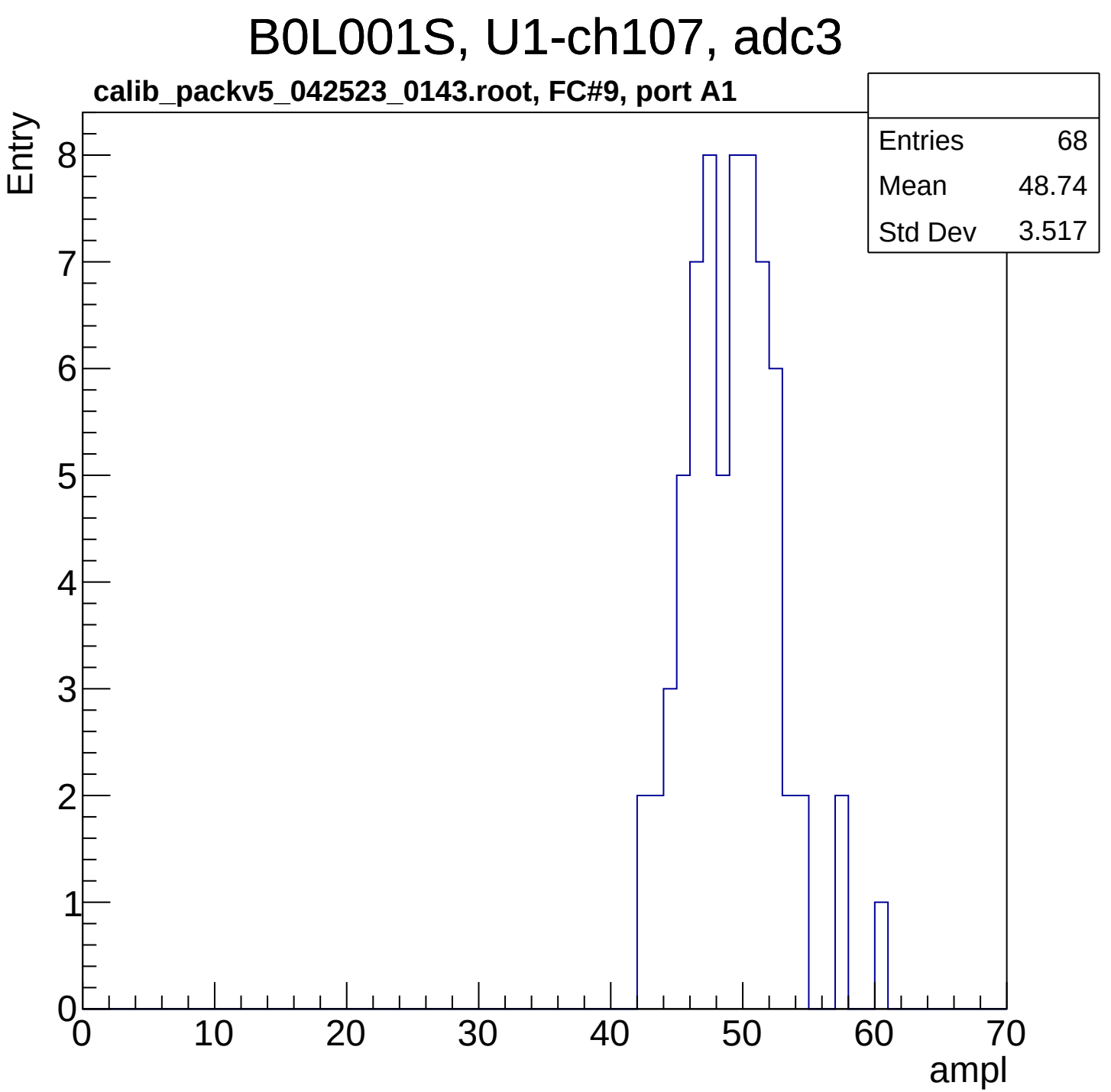
B0L001S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 68
Mean 48.74
Std Dev 3.517
Entry
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3
4
5
6
7
8
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10
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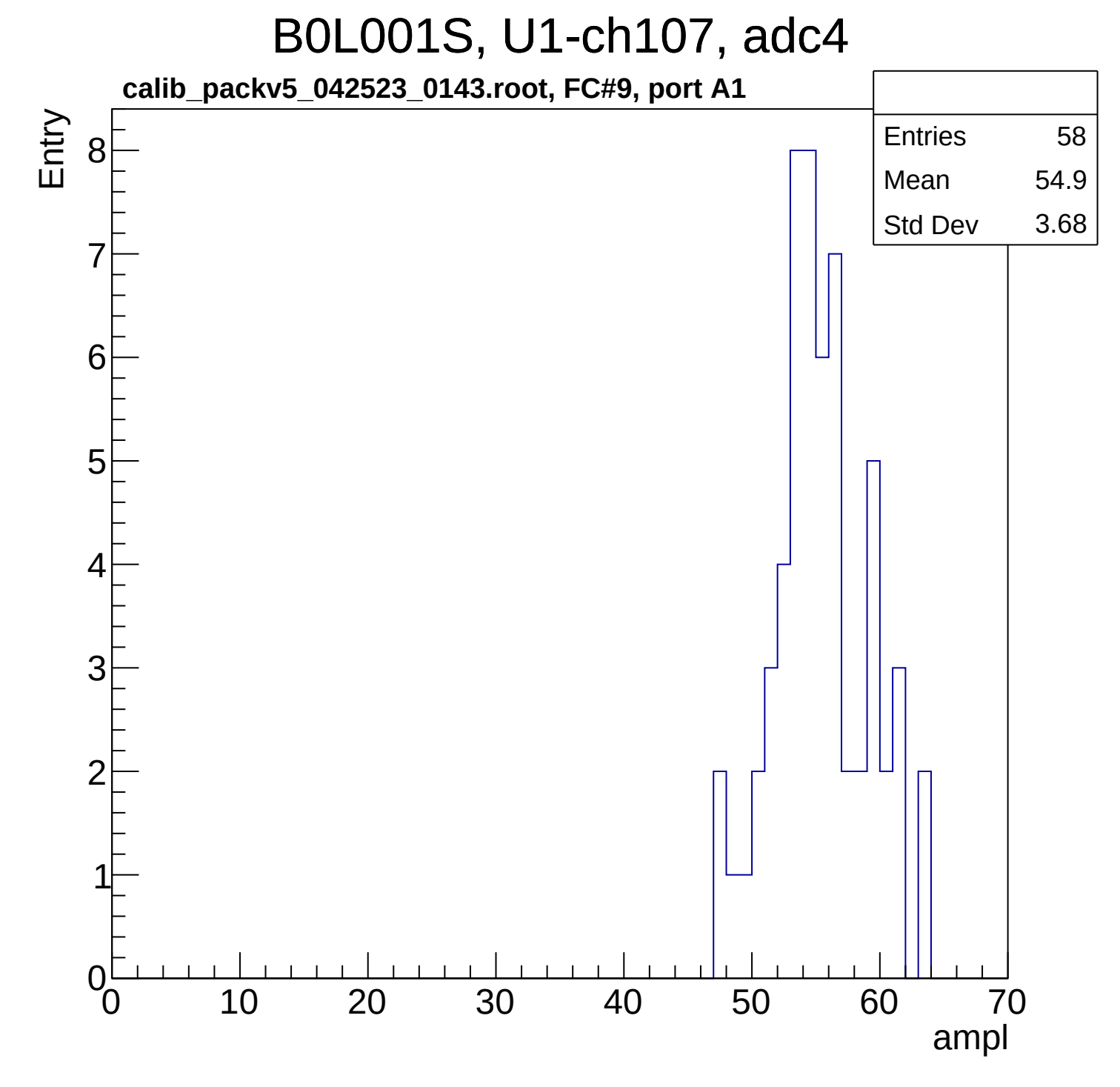
70
ampl


B0L001S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 54.9
Std Dev 3.68
Entry
ampl
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60
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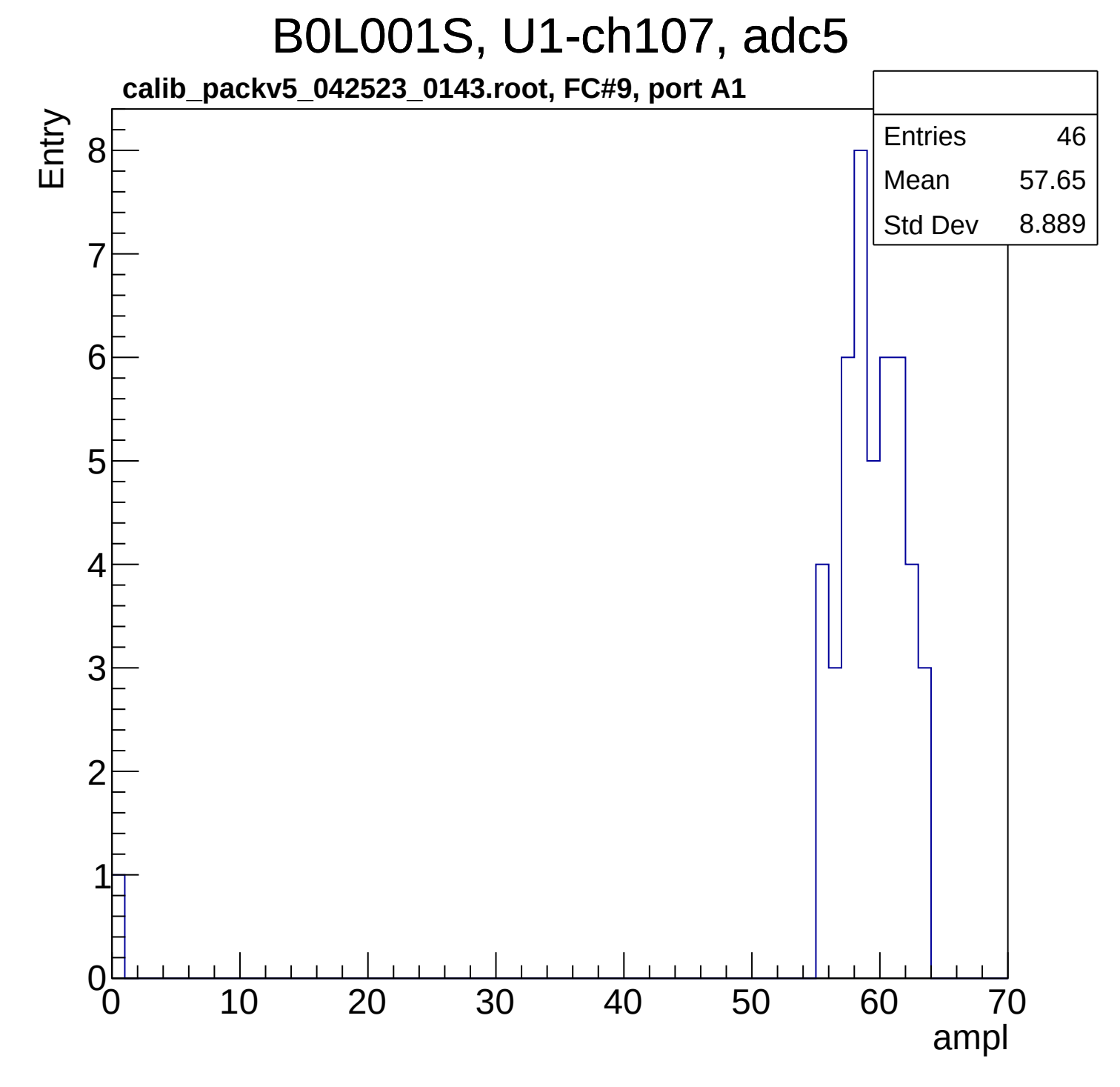
B0L001S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 46
Mean 57.65
Std Dev 8.889
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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60
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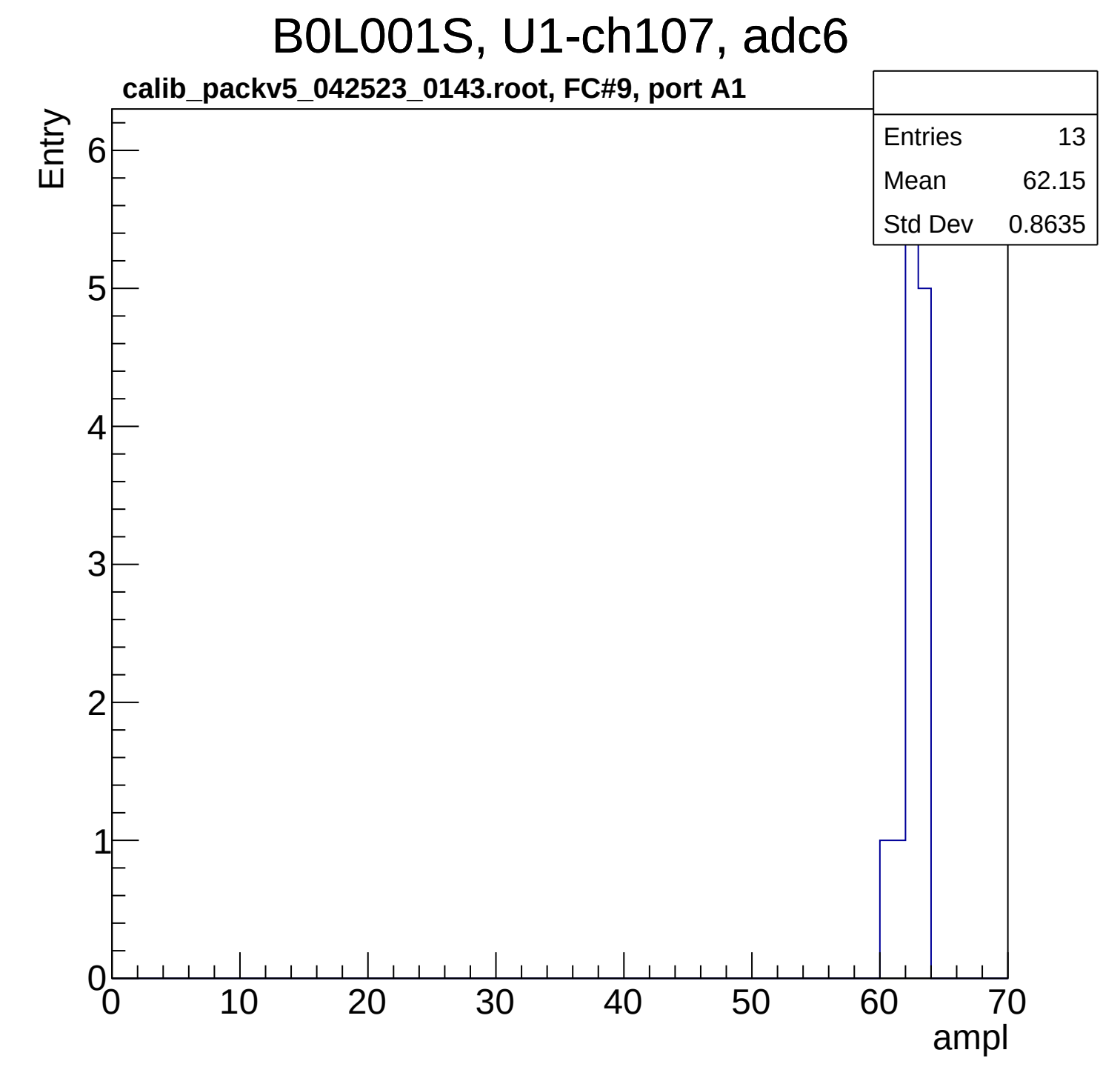
B0L001S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 13
Mean 62.15
Std Dev 0.8635
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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60
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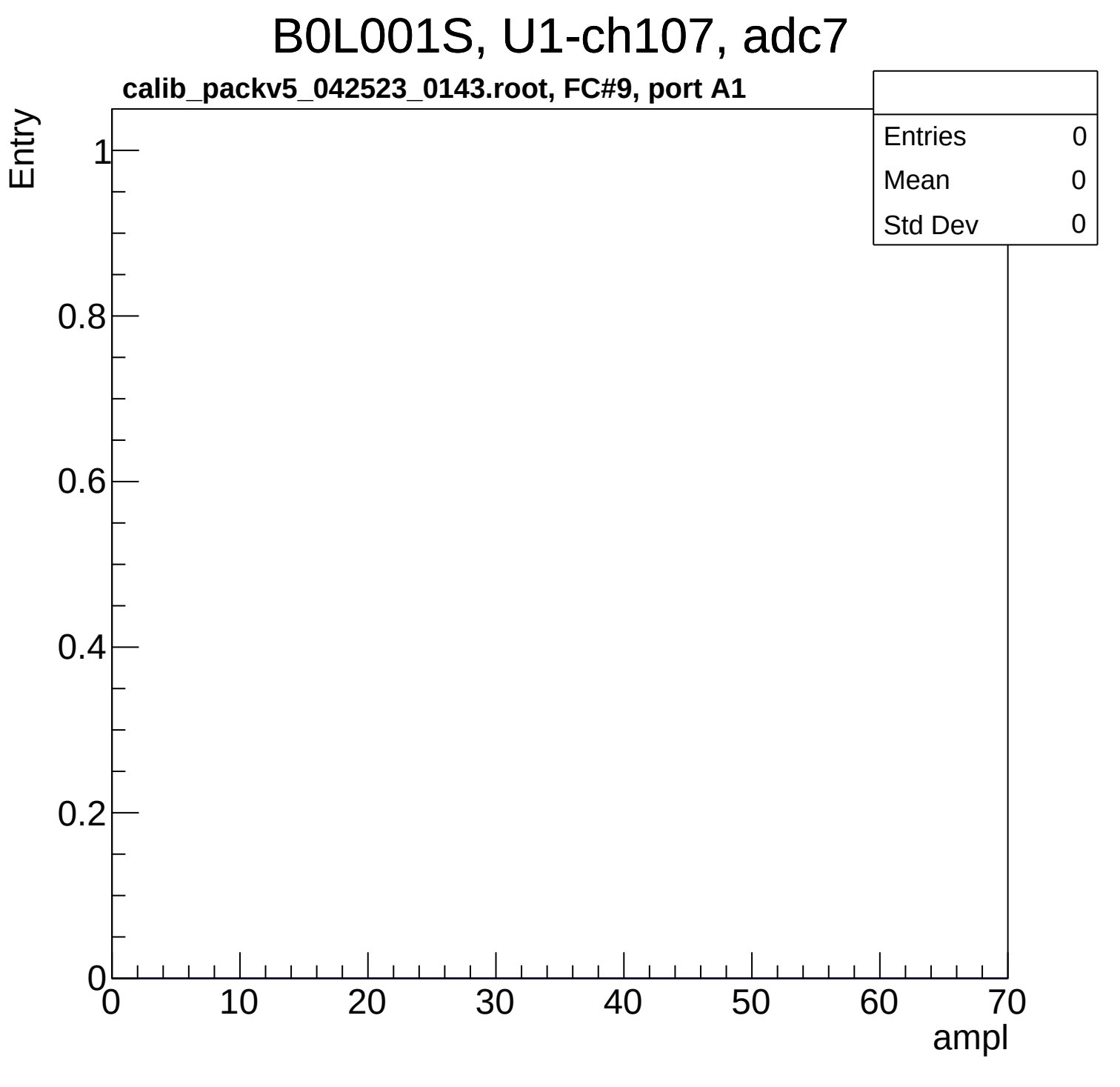
B0L001S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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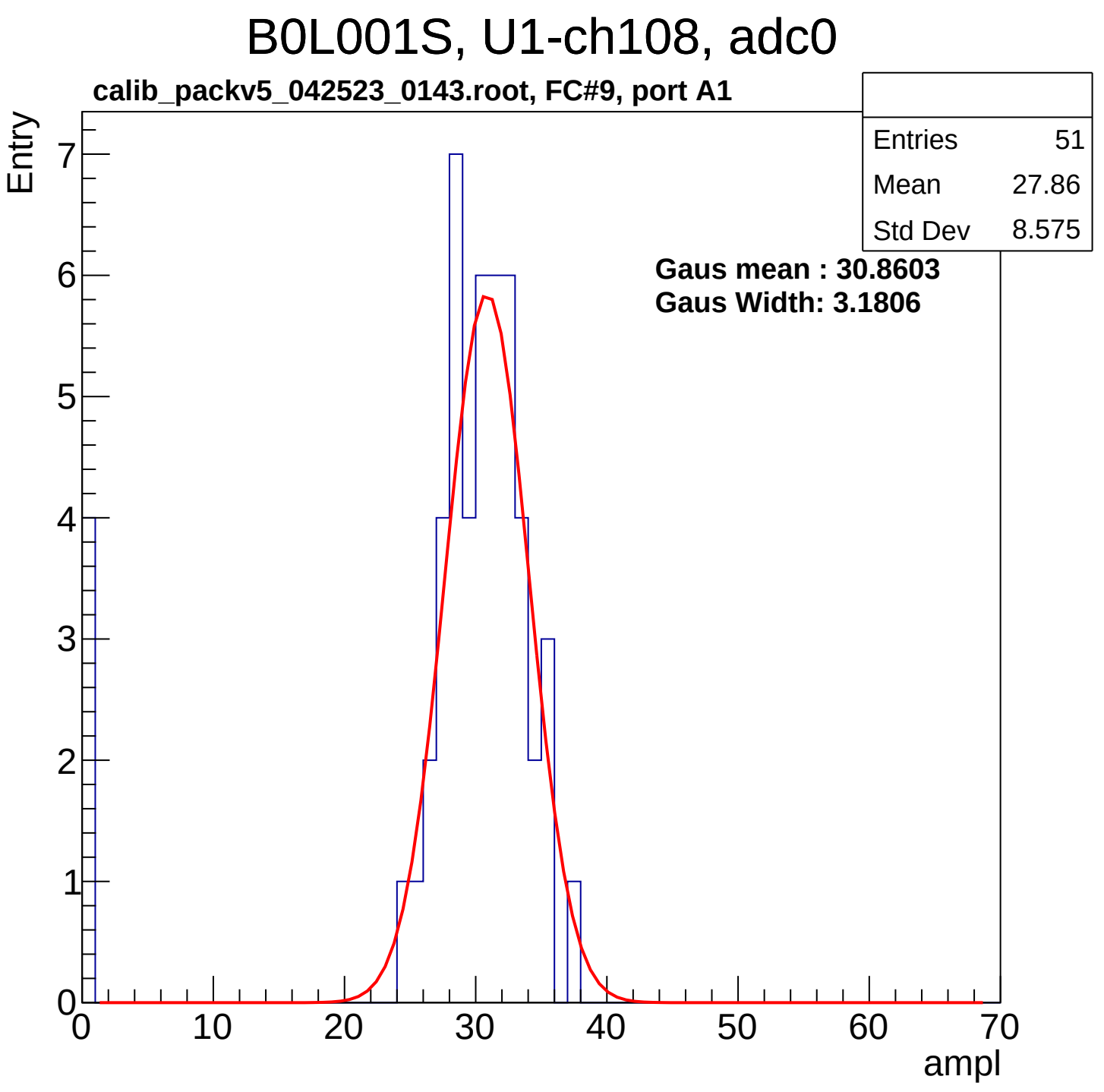
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 27.86
Std Dev 8.575
Gaus mean : 30.8603
Gaus Width: 3.1806
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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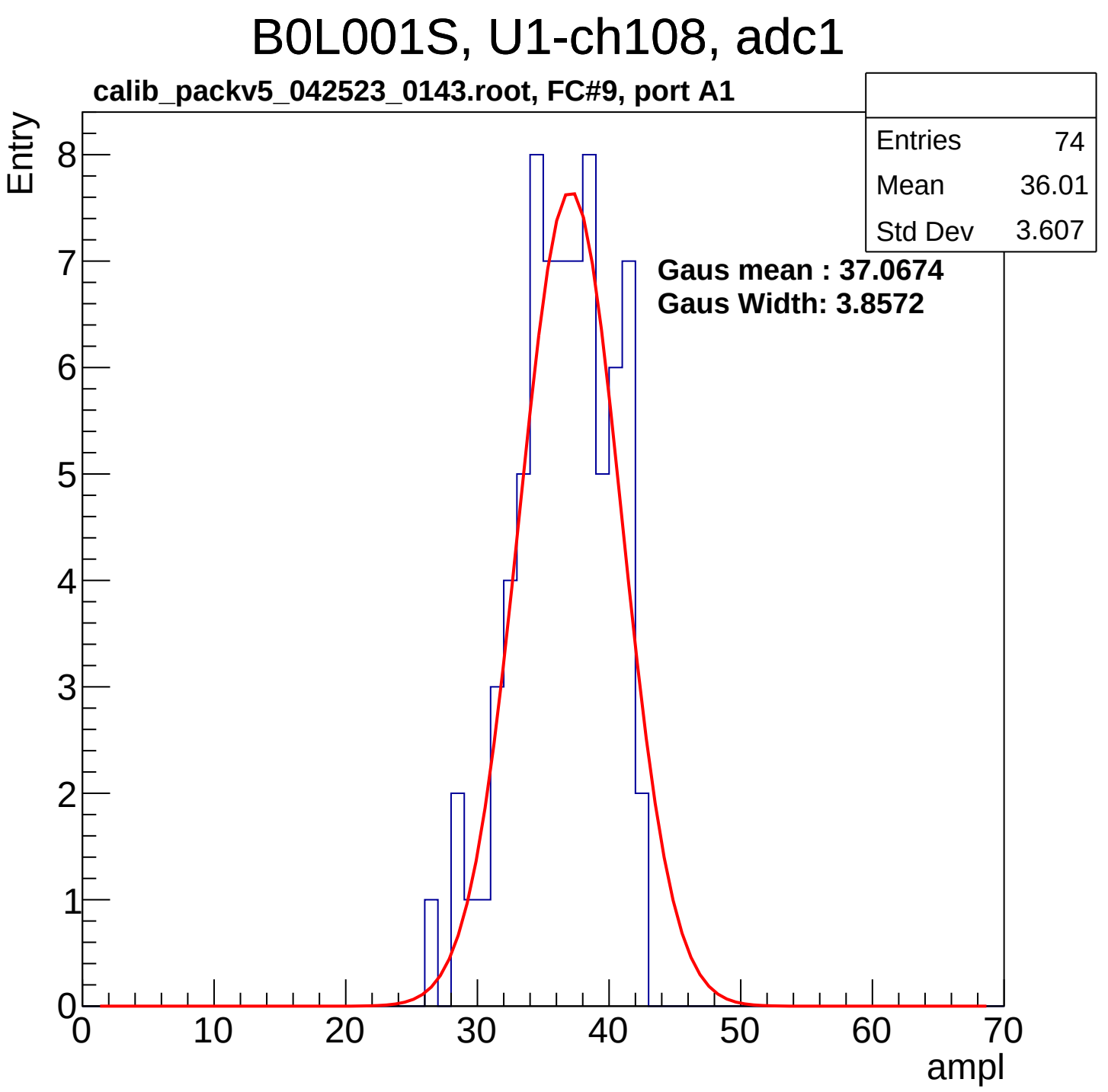
B0L001S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 36.01
Std Dev 3.607
Gaus mean : 37.0674
Gaus Width: 3.8572
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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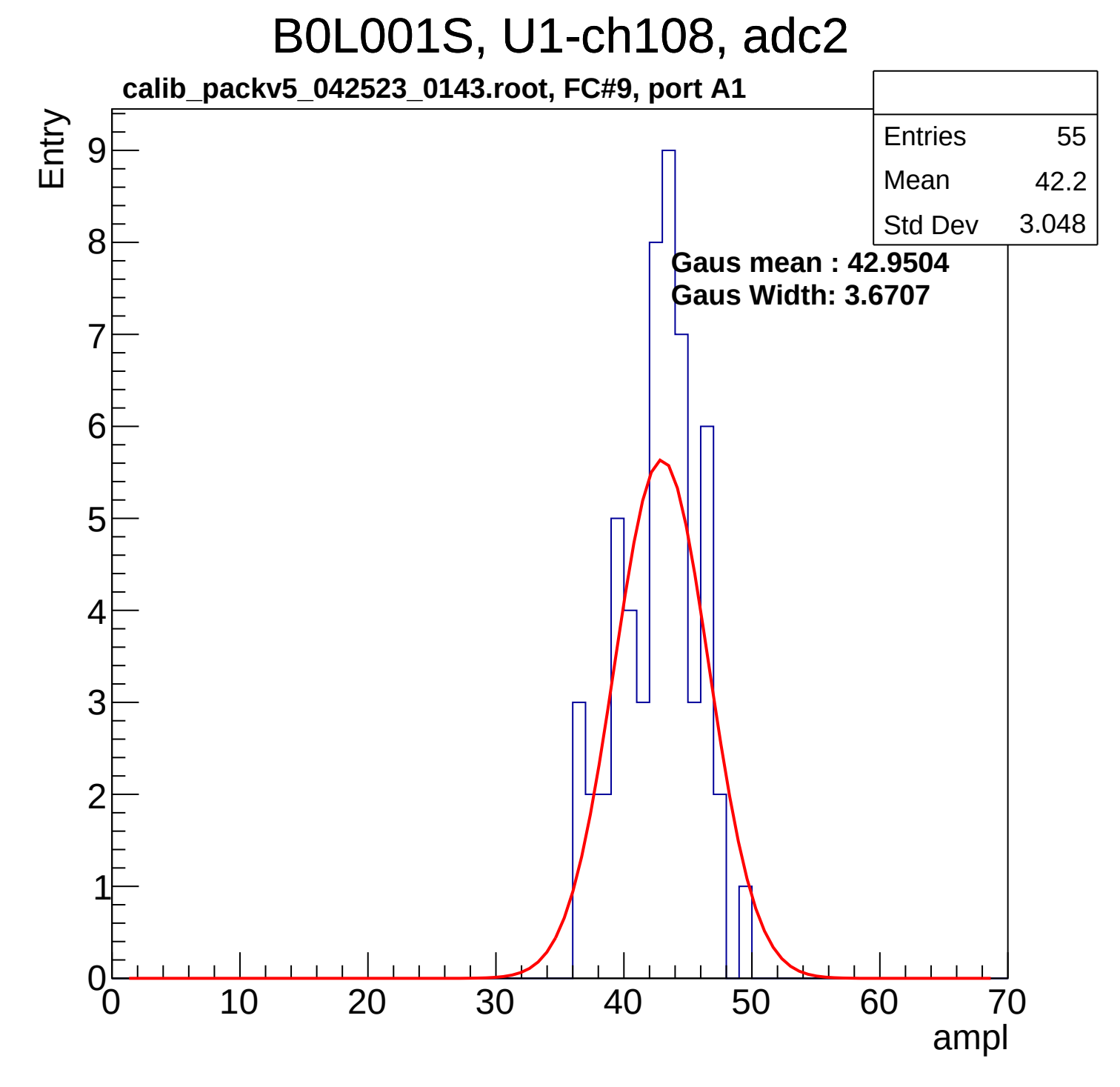
B0L001S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.2
Std Dev 3.048
Gaus mean : 42.9504
Gaus Width: 3.6707
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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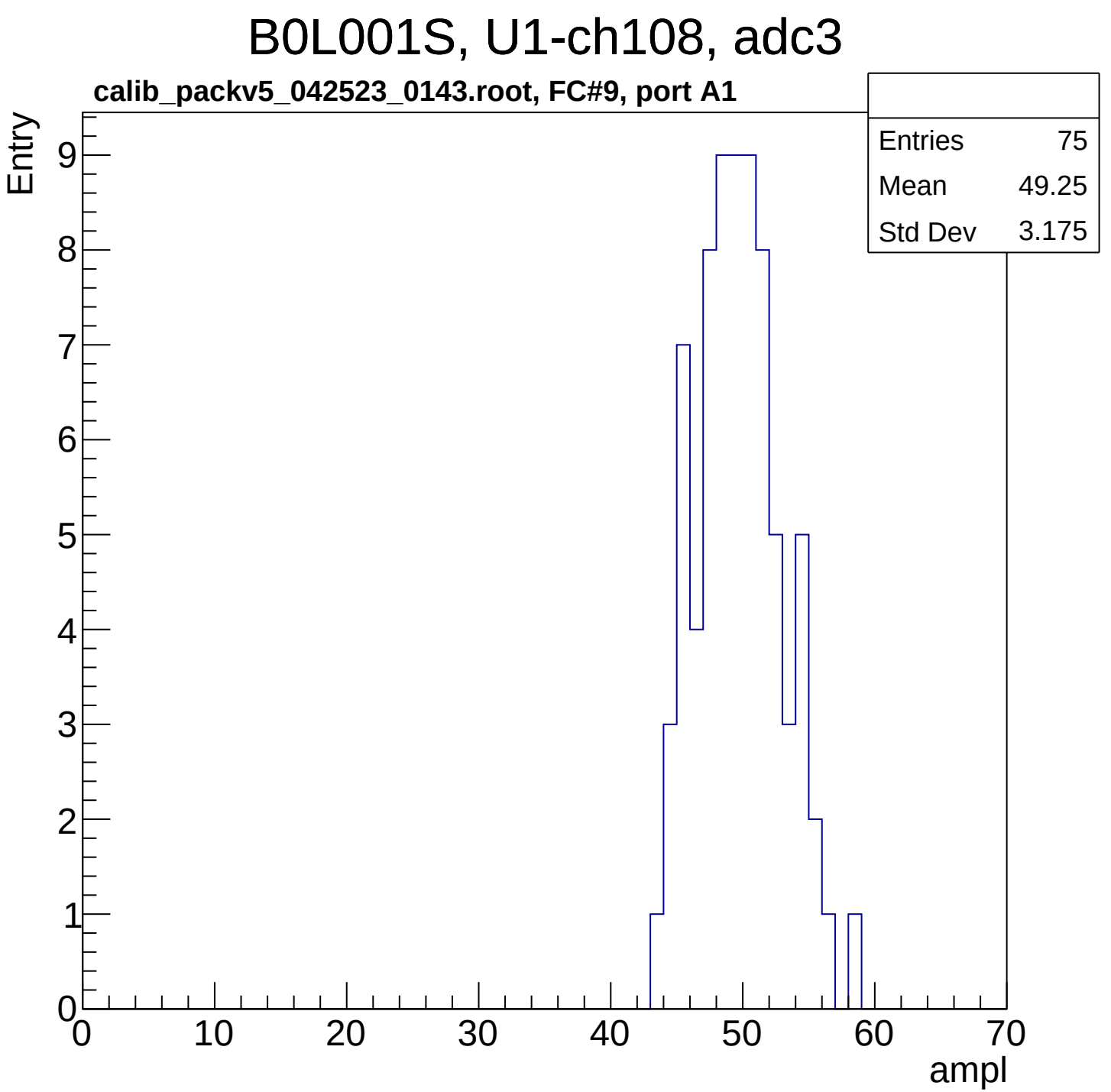
B0L001S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 49.25
Std Dev 3.175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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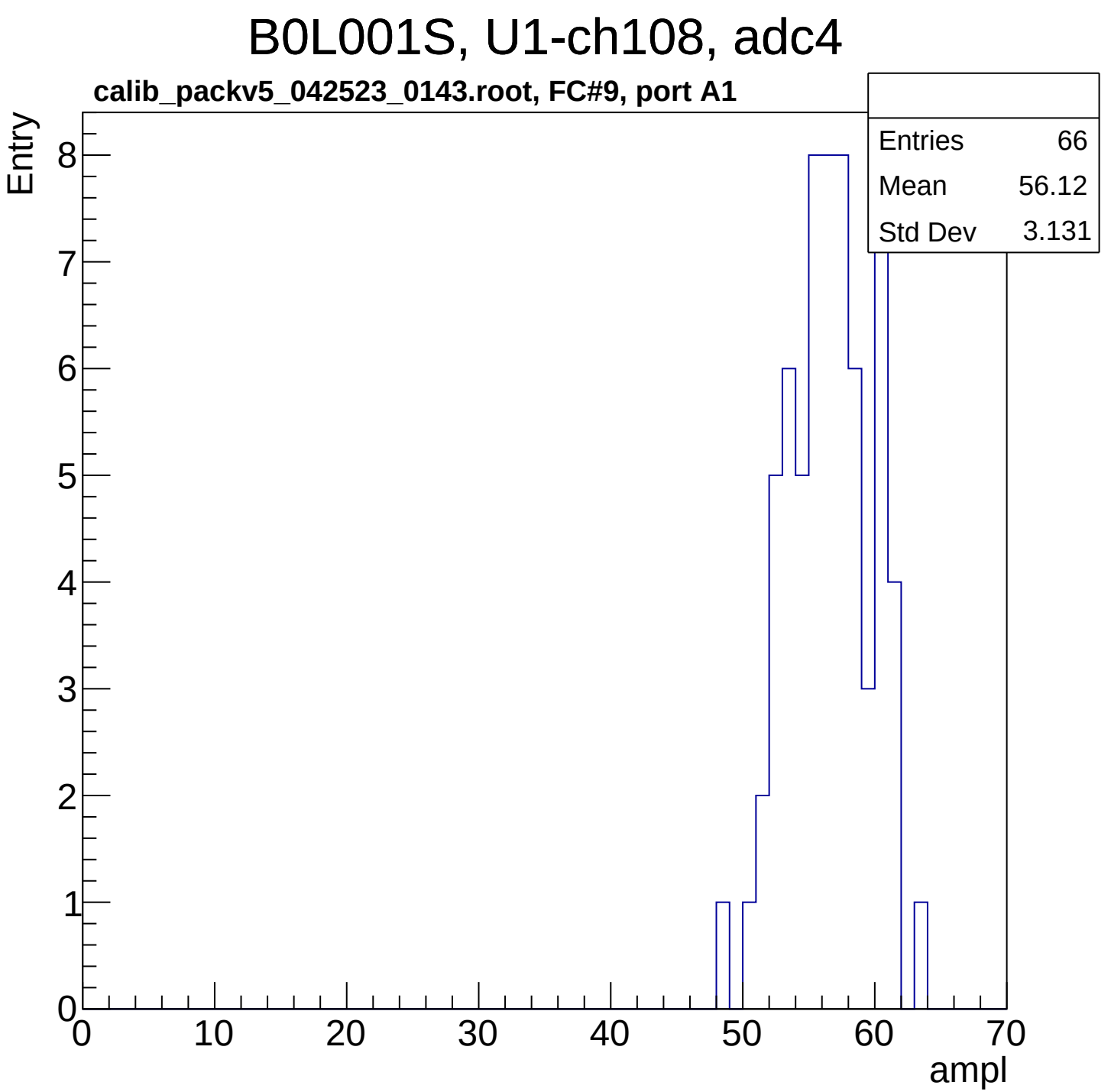
B0L001S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 56.12
Std Dev 3.131
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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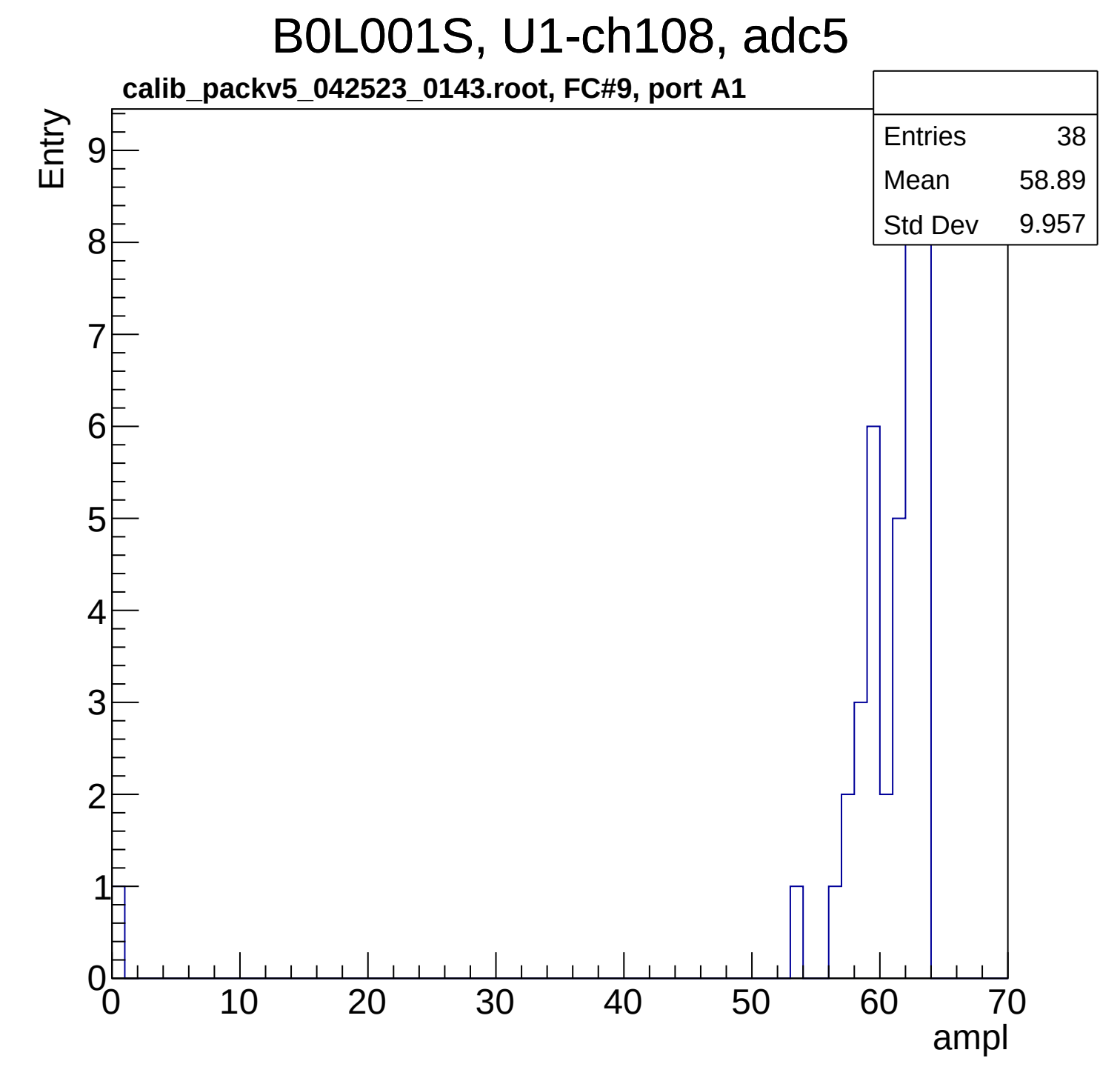
B0L001S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 58.89
Std Dev 9.957
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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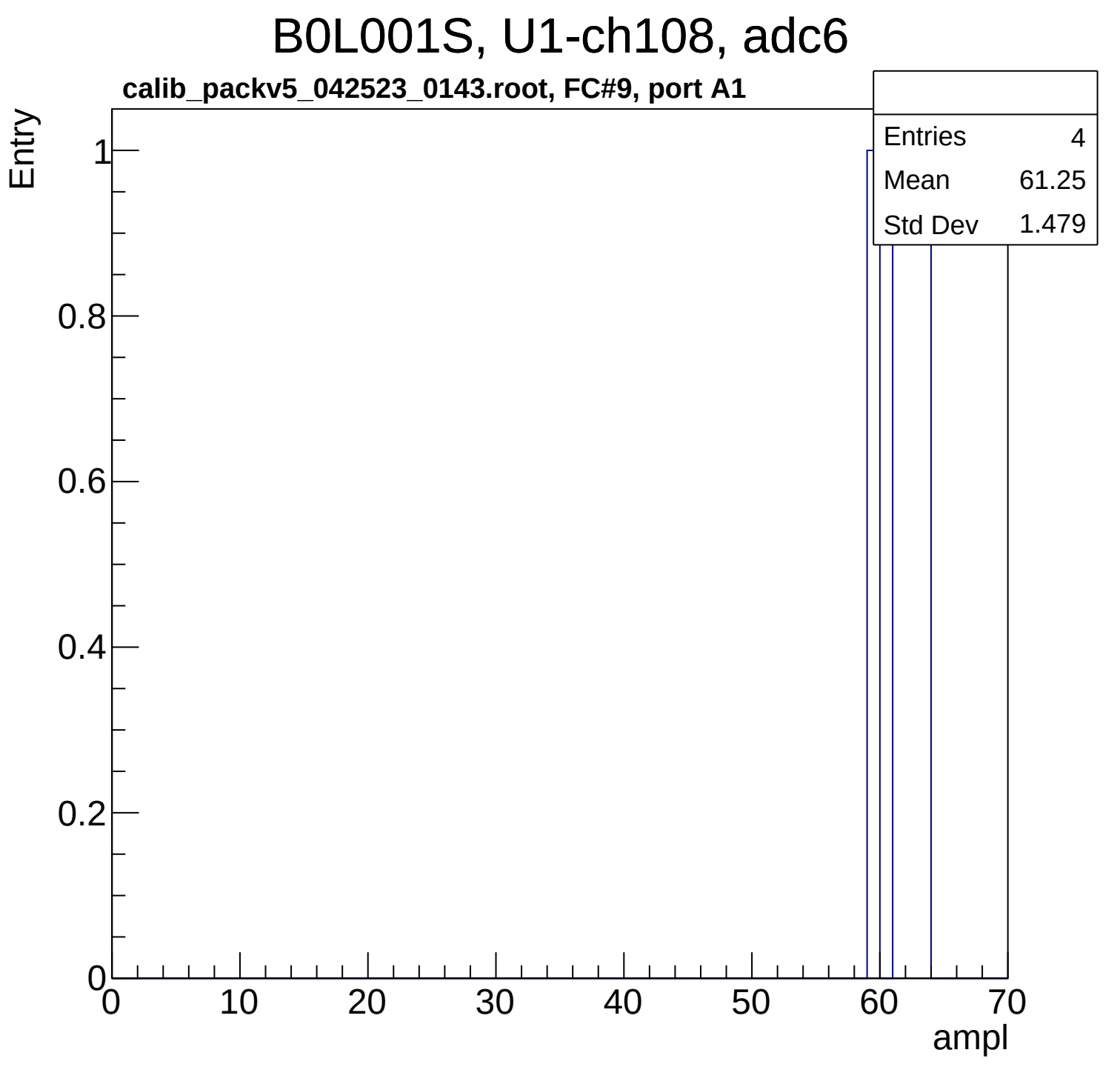
B0L001S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
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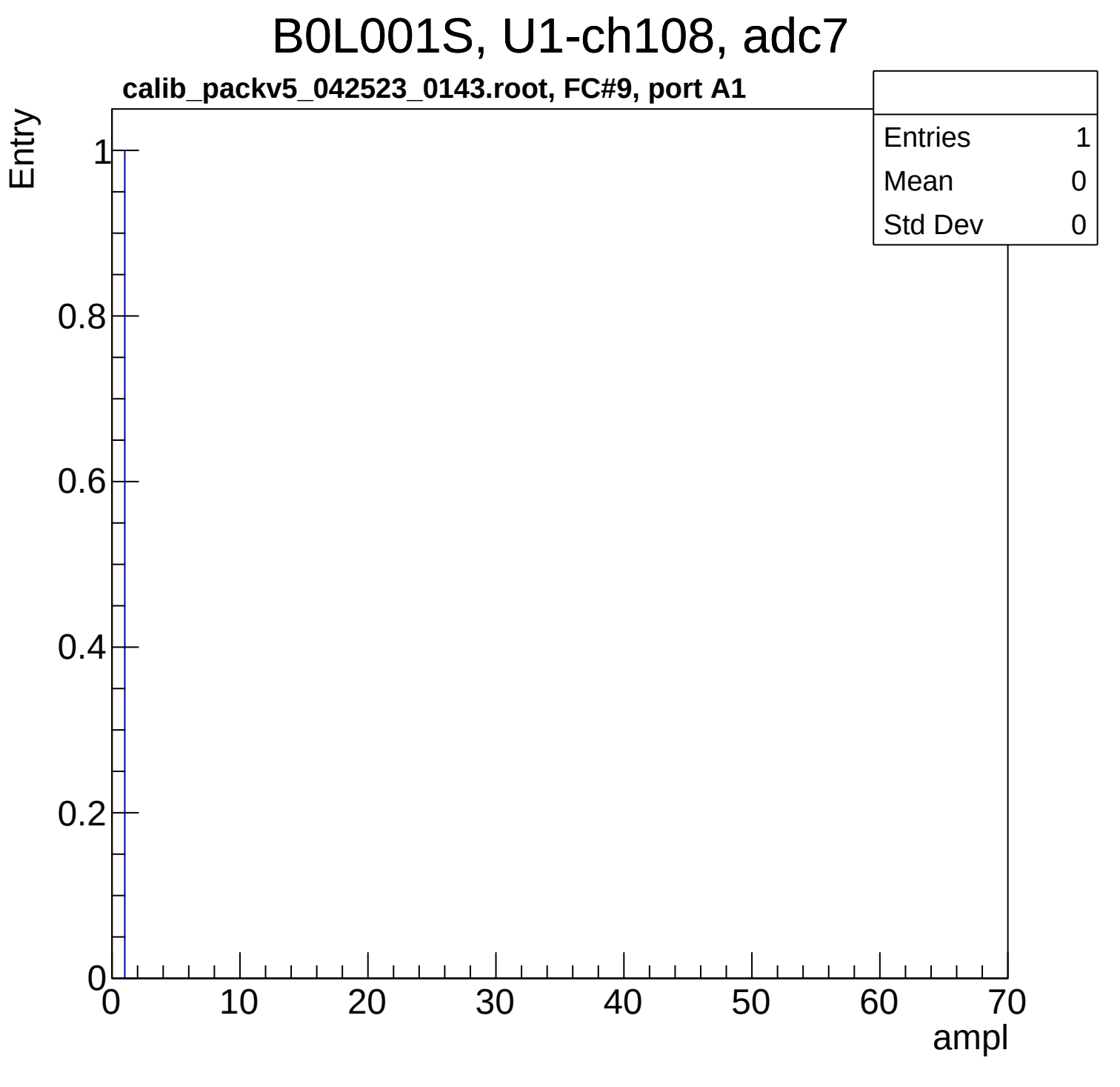
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B0L001S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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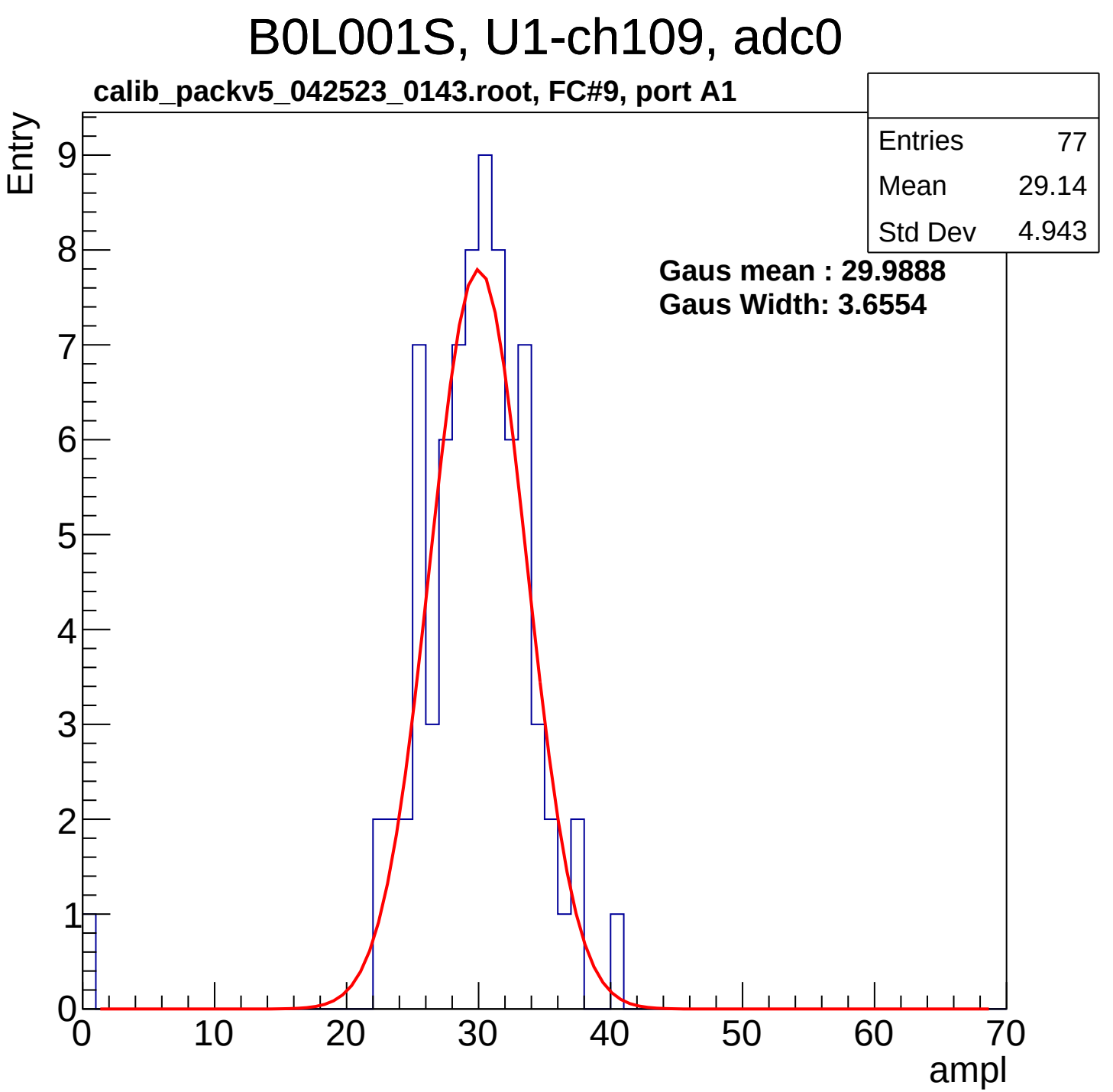
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 29.14
Std Dev 4.943
Gaus mean : 29.9888
Gaus Width: 3.6554
Entry
ampl
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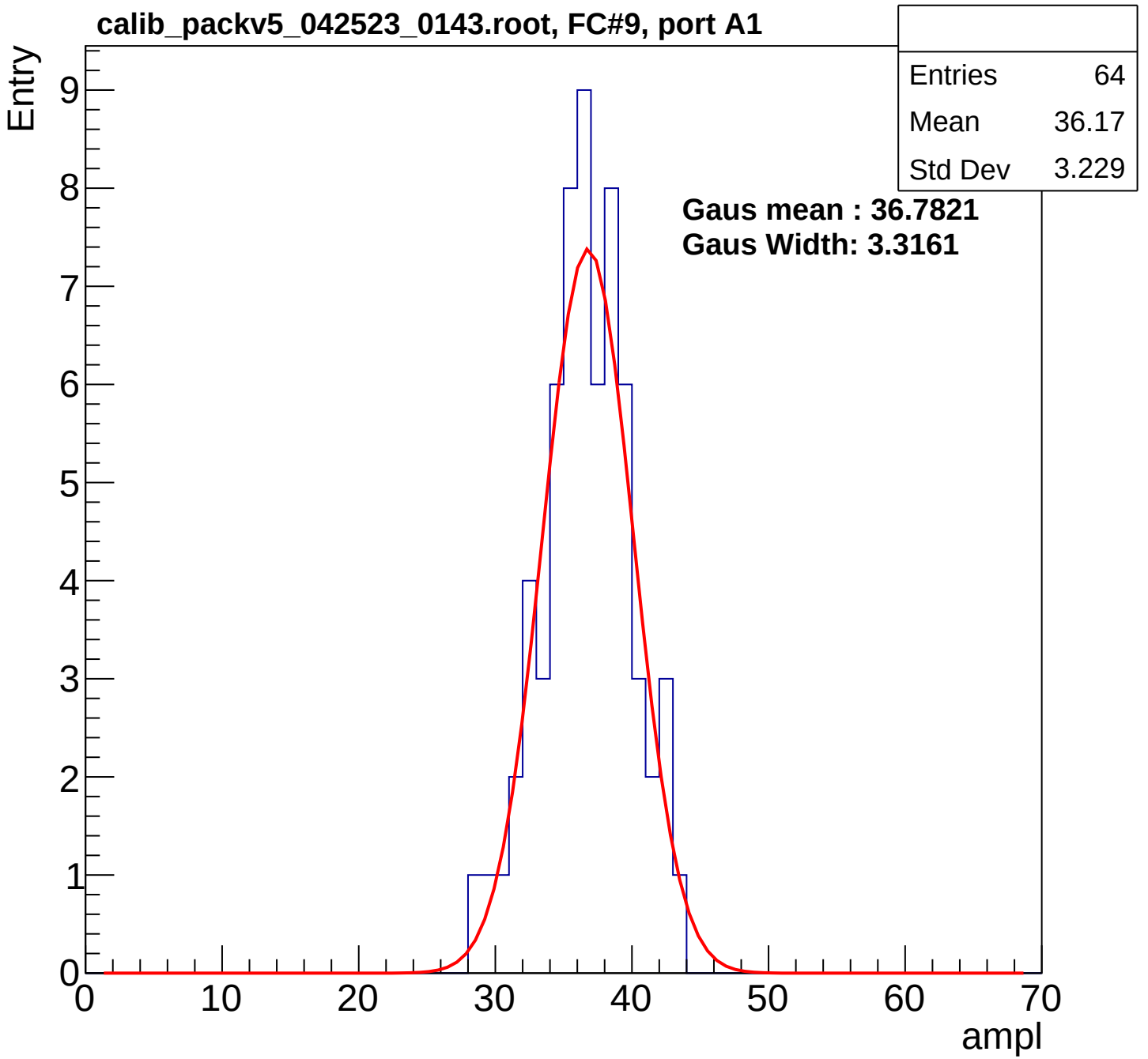
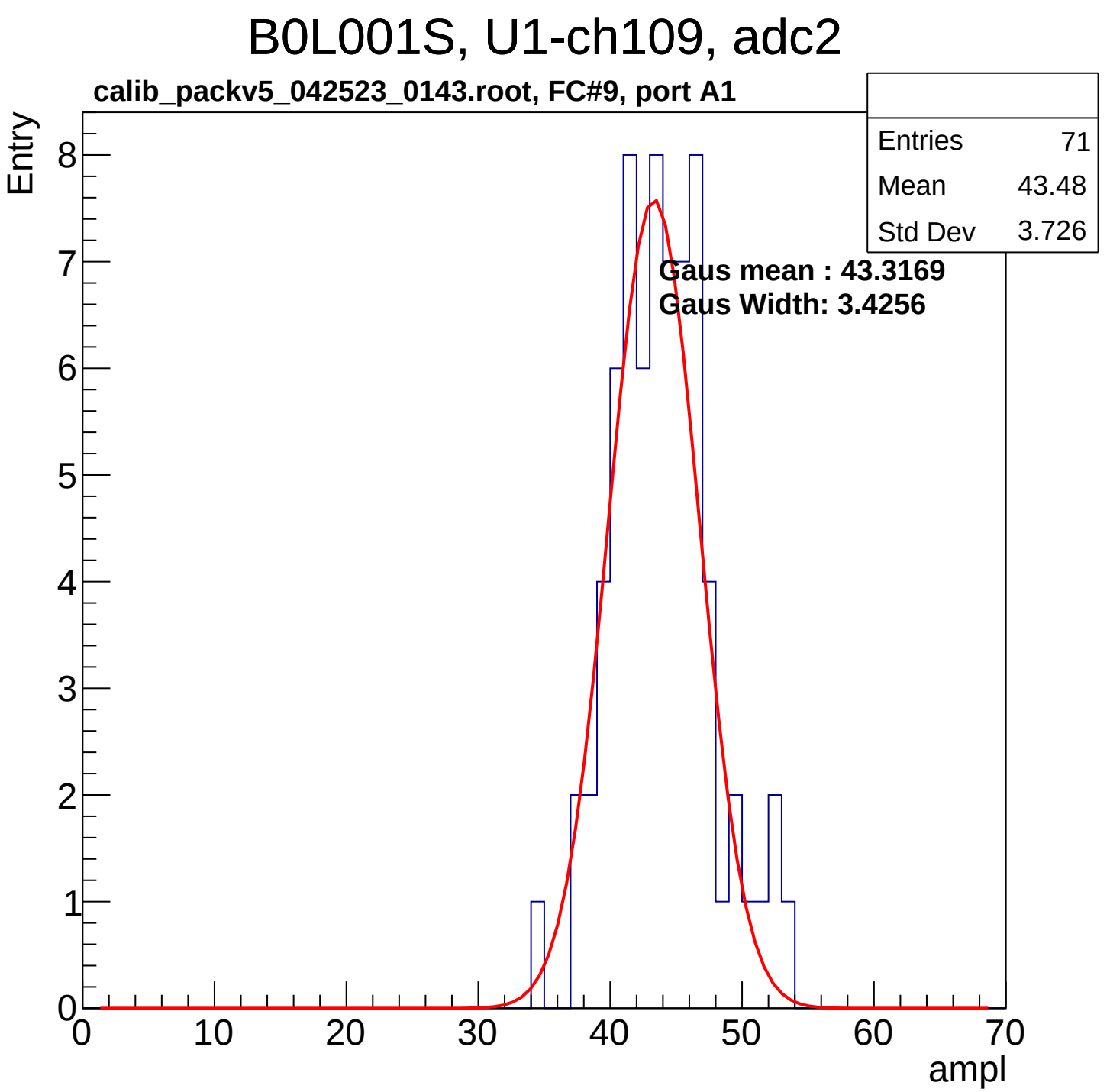
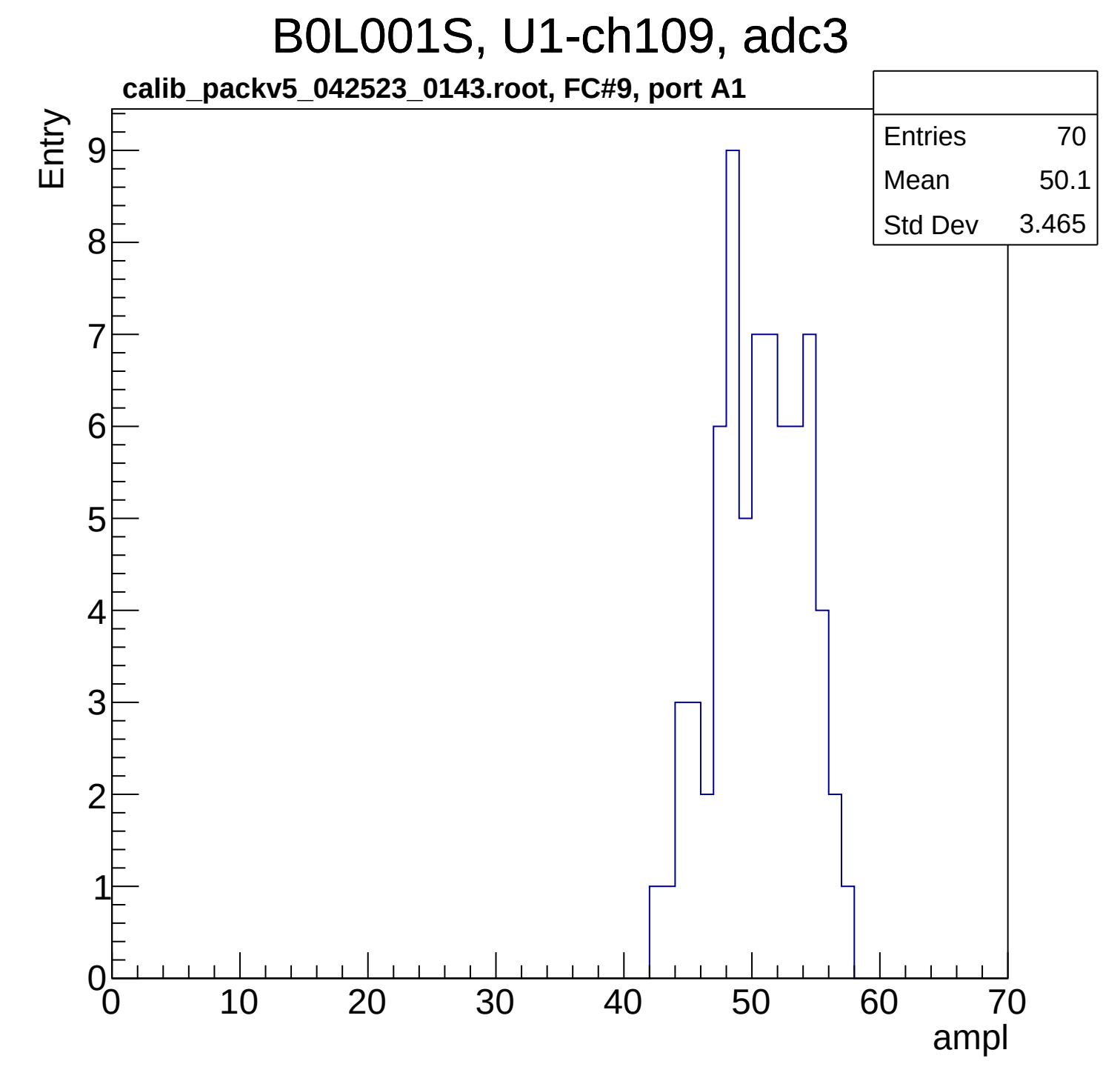
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 36.17
Std Dev 3.229
Gaus mean : 36.7821
Gaus Width: 3.3161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.48
Std Dev 3.726
Gaus mean : 43.3169
Gaus Width: 3.4256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 50.1
Std Dev 3.465
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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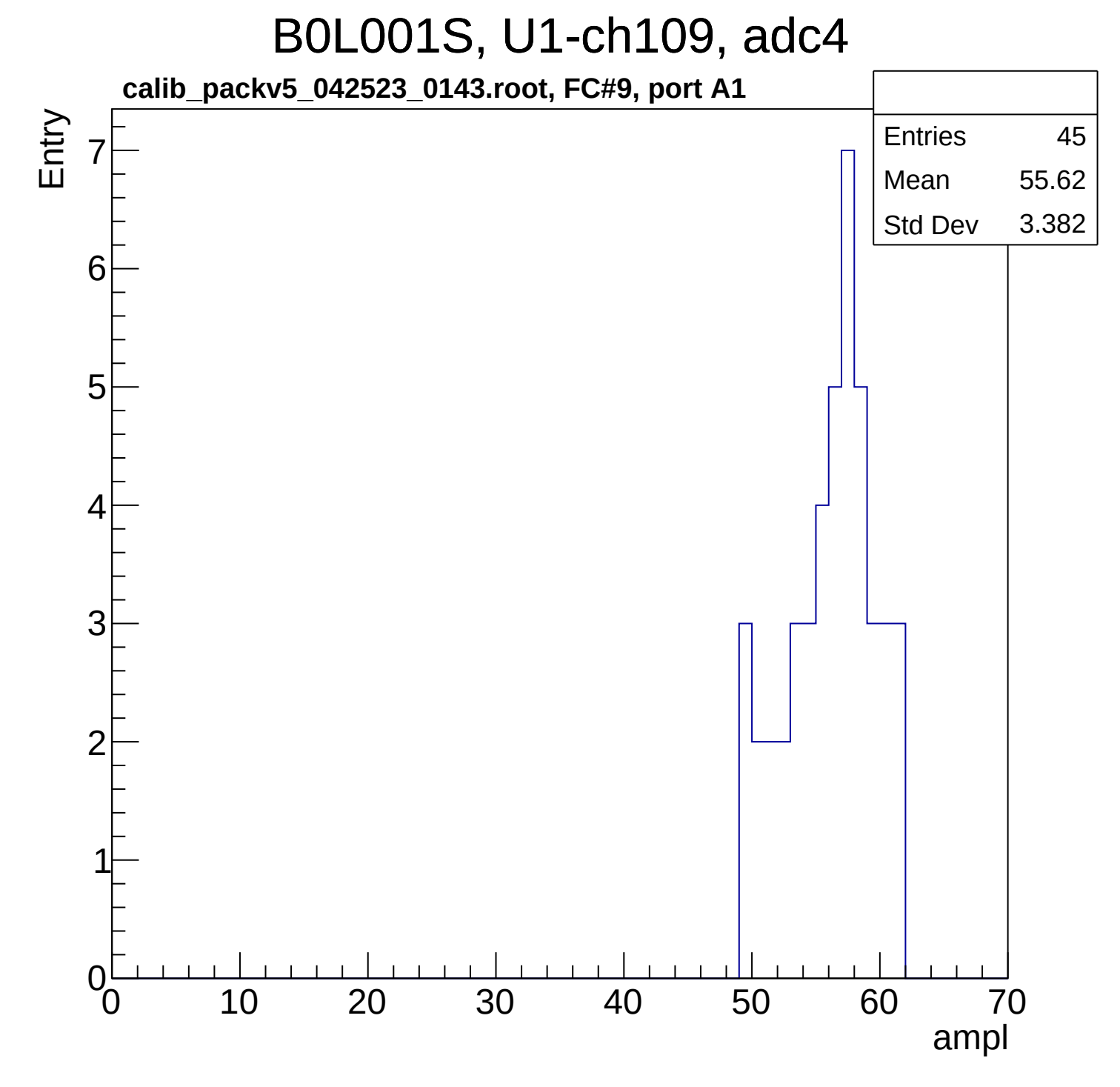
B0L001S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 45
Mean 55.62
Std Dev 3.382
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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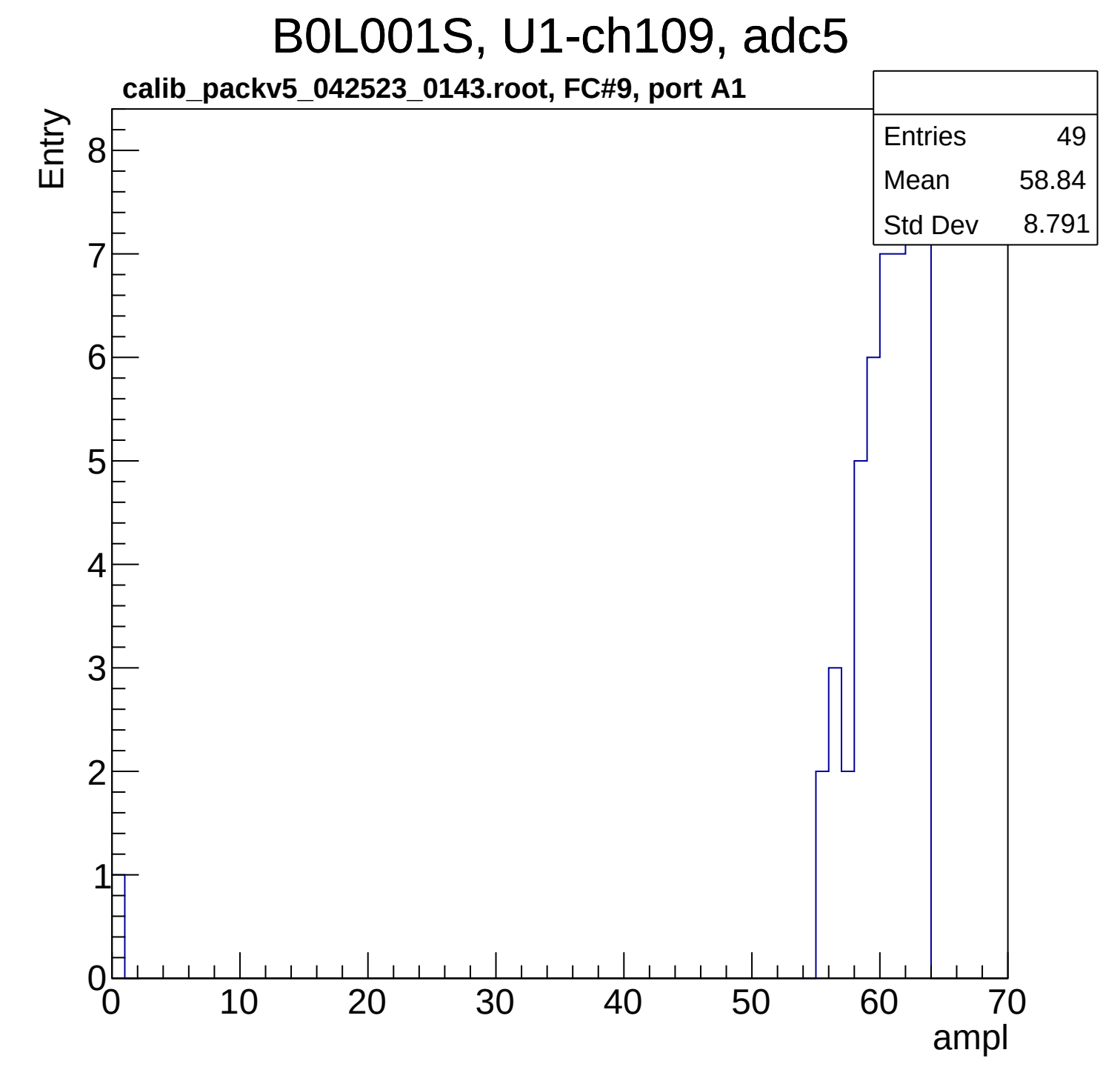
B0L001S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 58.84
Std Dev 8.791
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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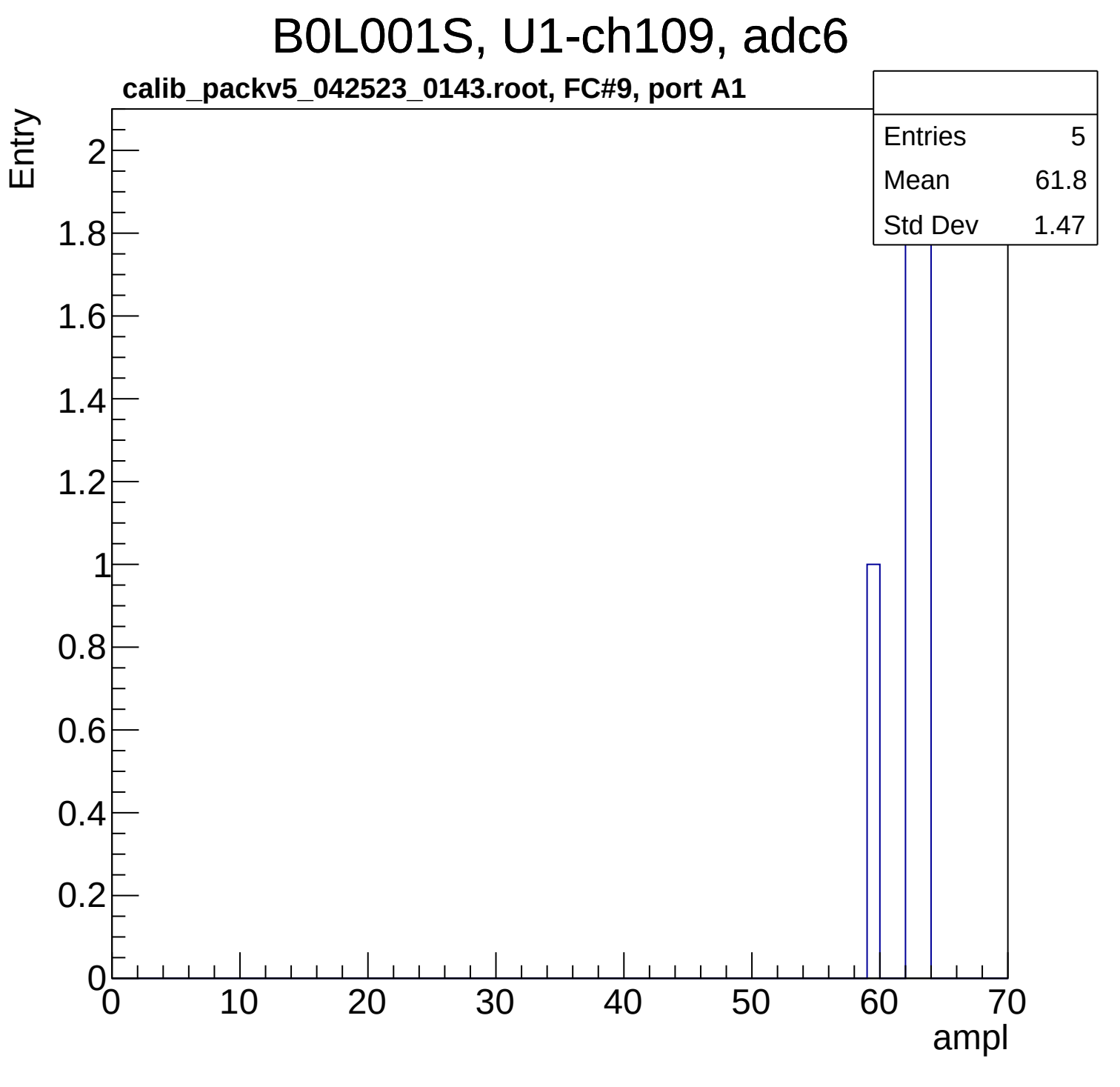
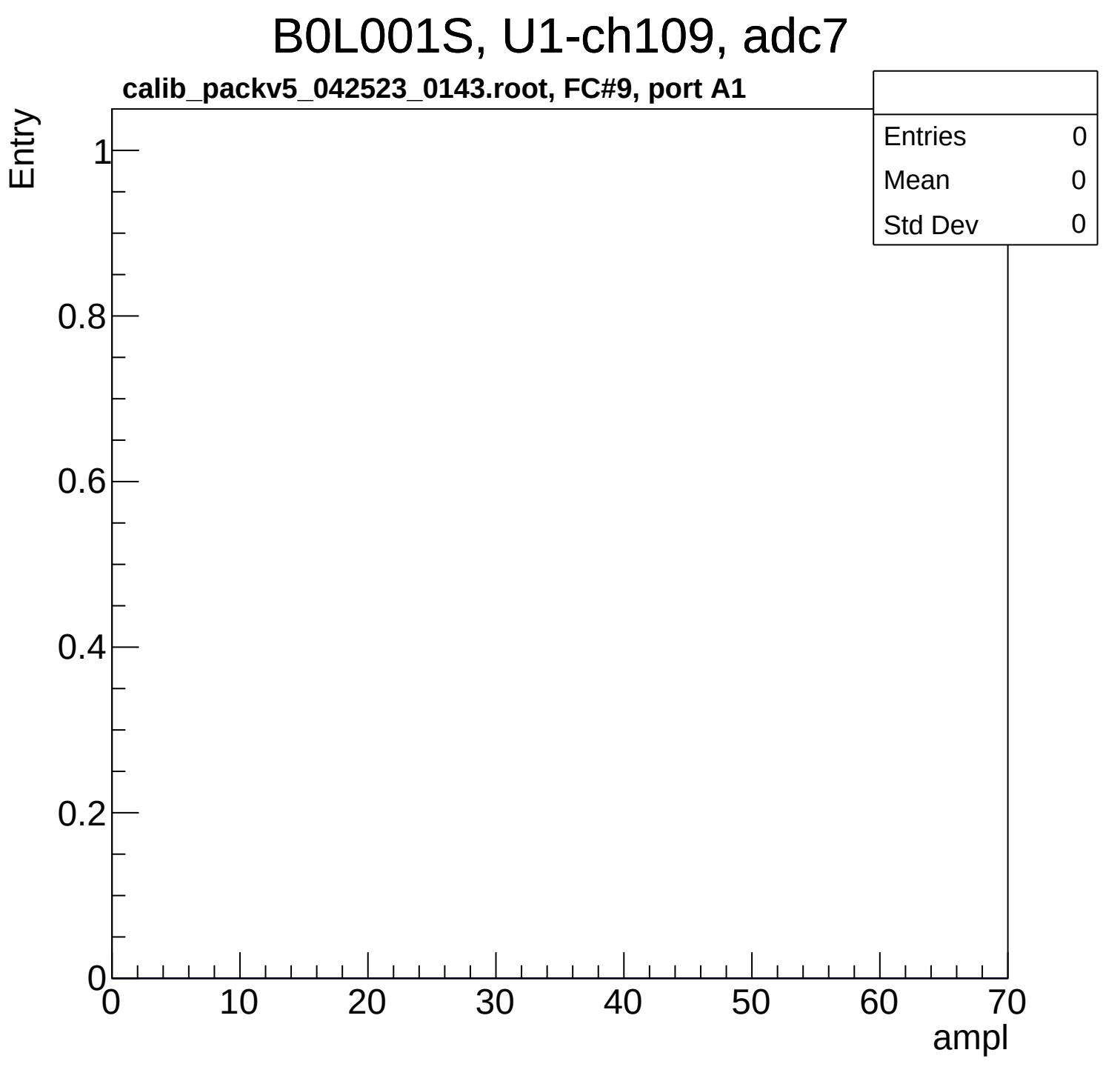

B0L001S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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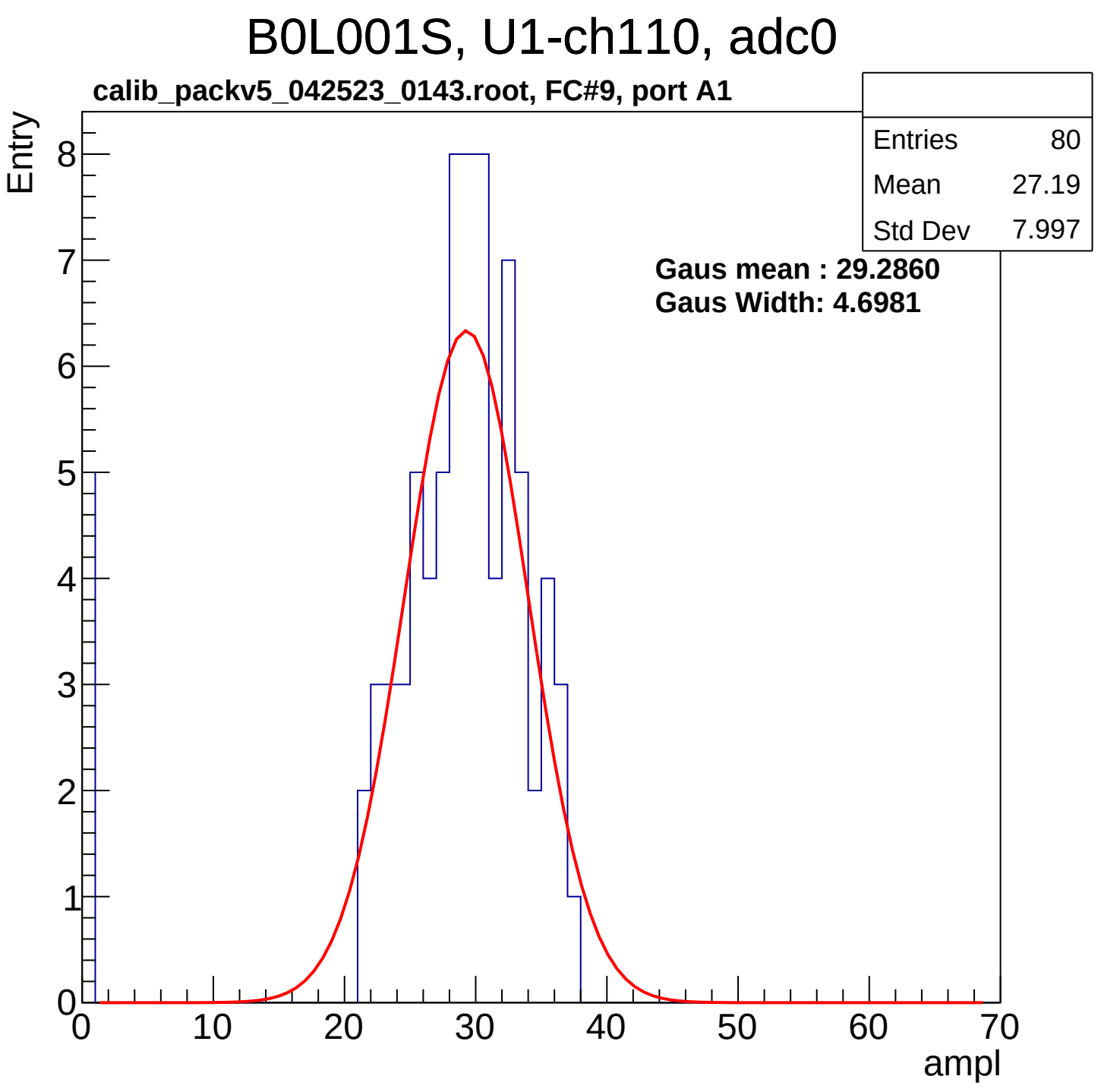
50
60
70
ampl


B0L001S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 80
Mean 27.19
Std Dev 7.997
Gaus mean : 29.2860
Gaus Width: 4.6981
Entry
ampl
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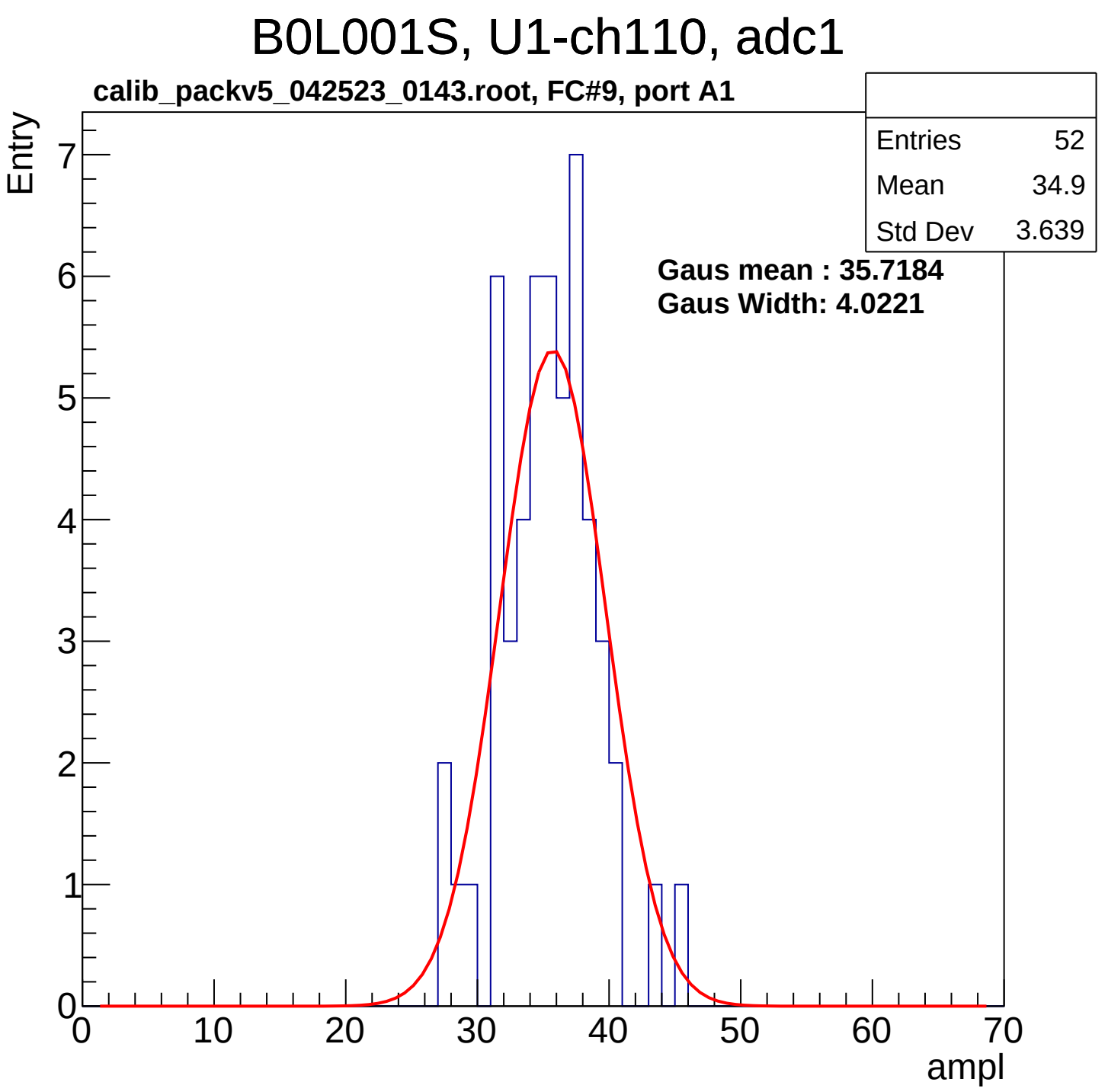
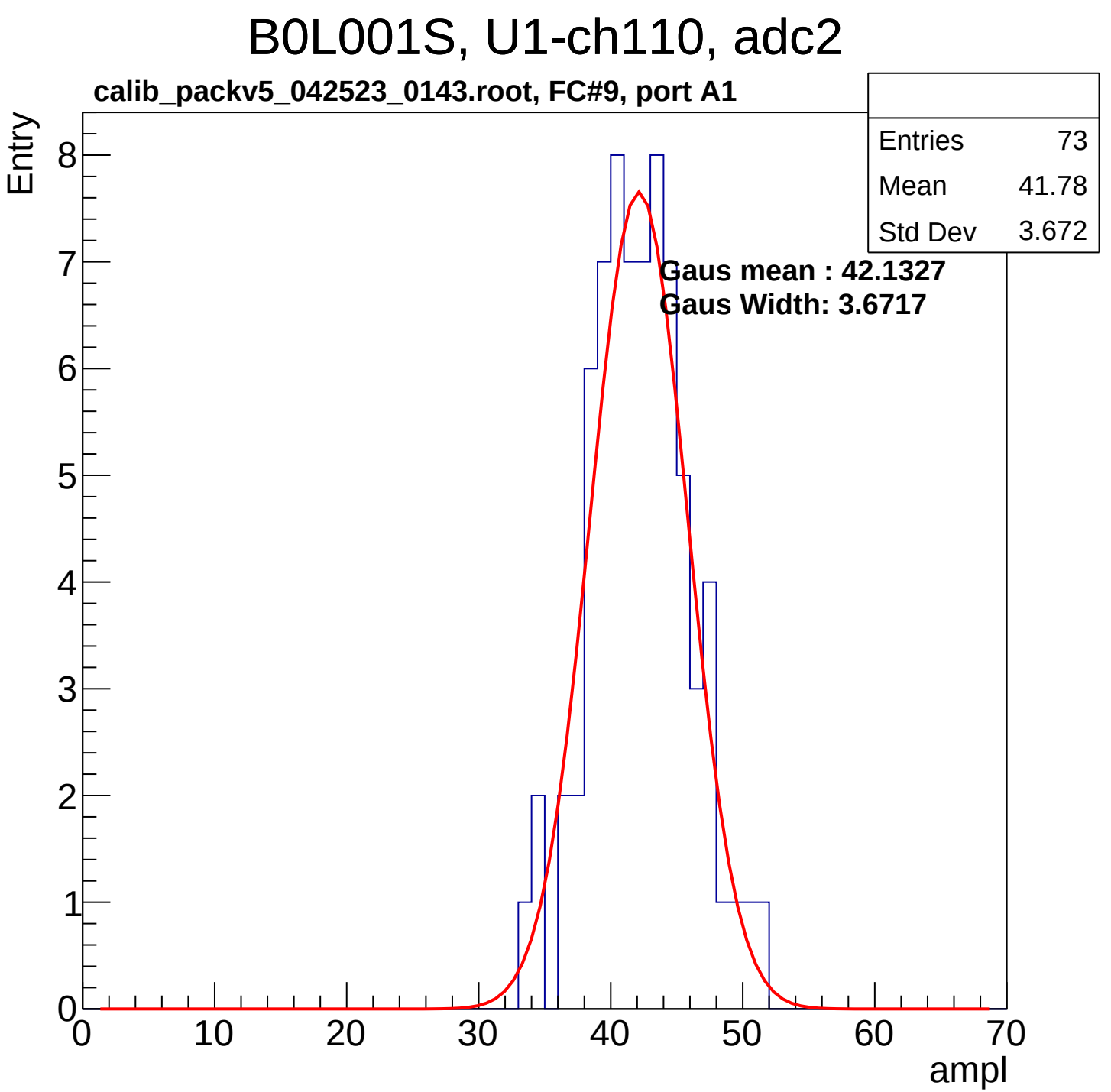
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 34.9
Std Dev 3.639
Gaus mean : 35.7184
Gaus Width: 4.0221
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 73
Mean 41.78
Std Dev 3.672
Gaus mean : 42.1327
Gaus Width: 3.6717
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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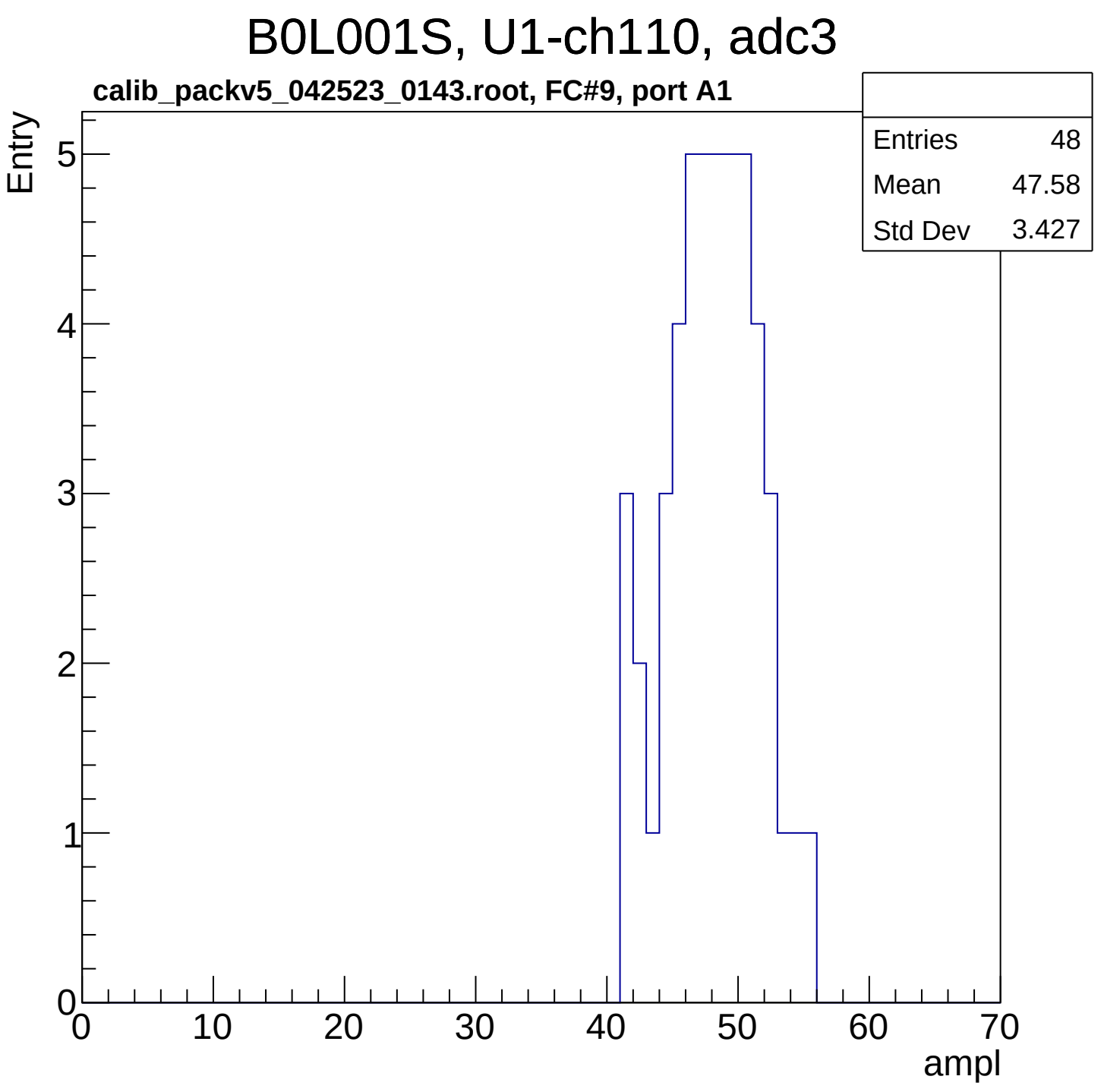
B0L001S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 48
Mean 47.58
Std Dev 3.427
Entry
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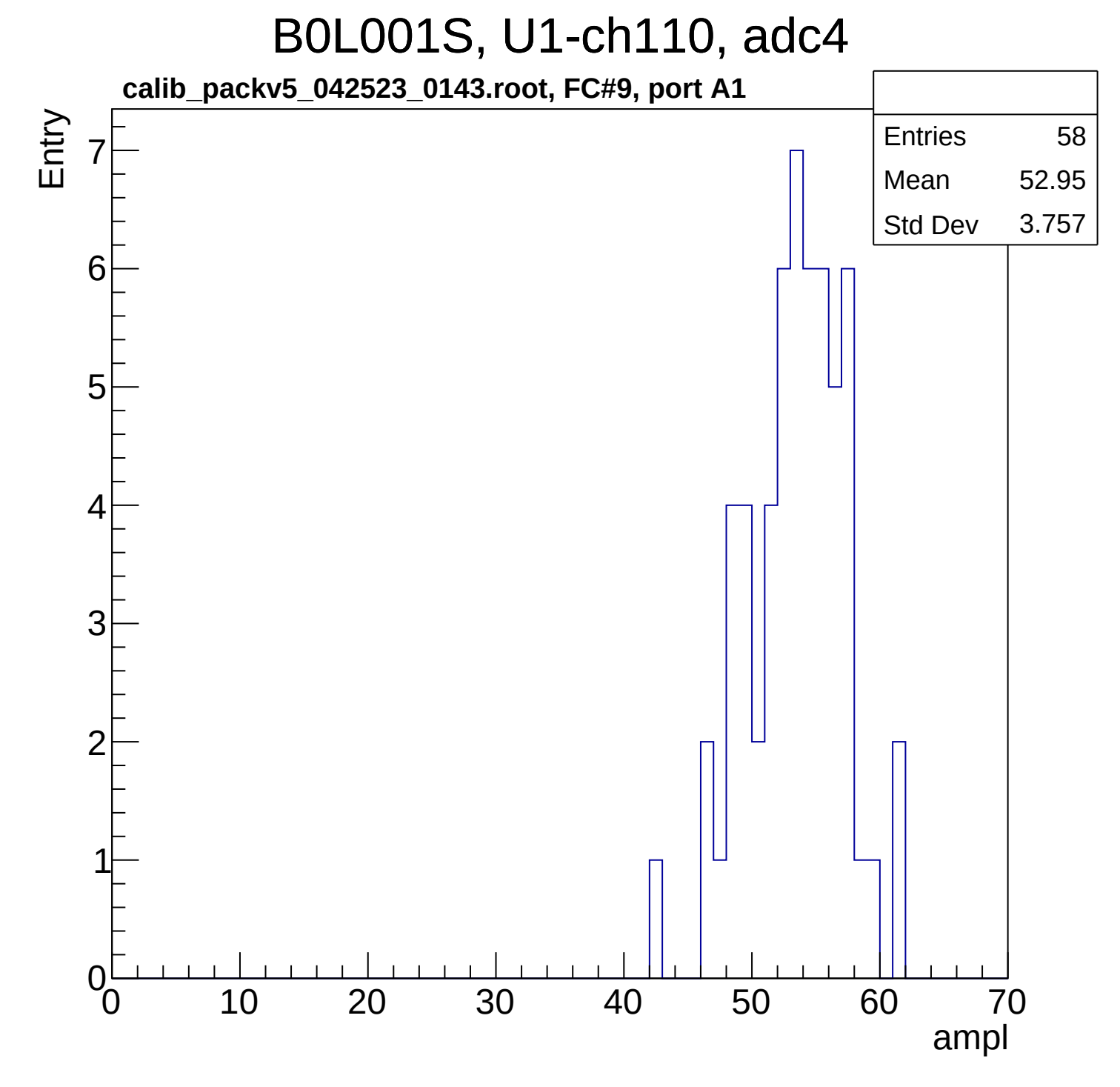
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 52.95
Std Dev 3.757
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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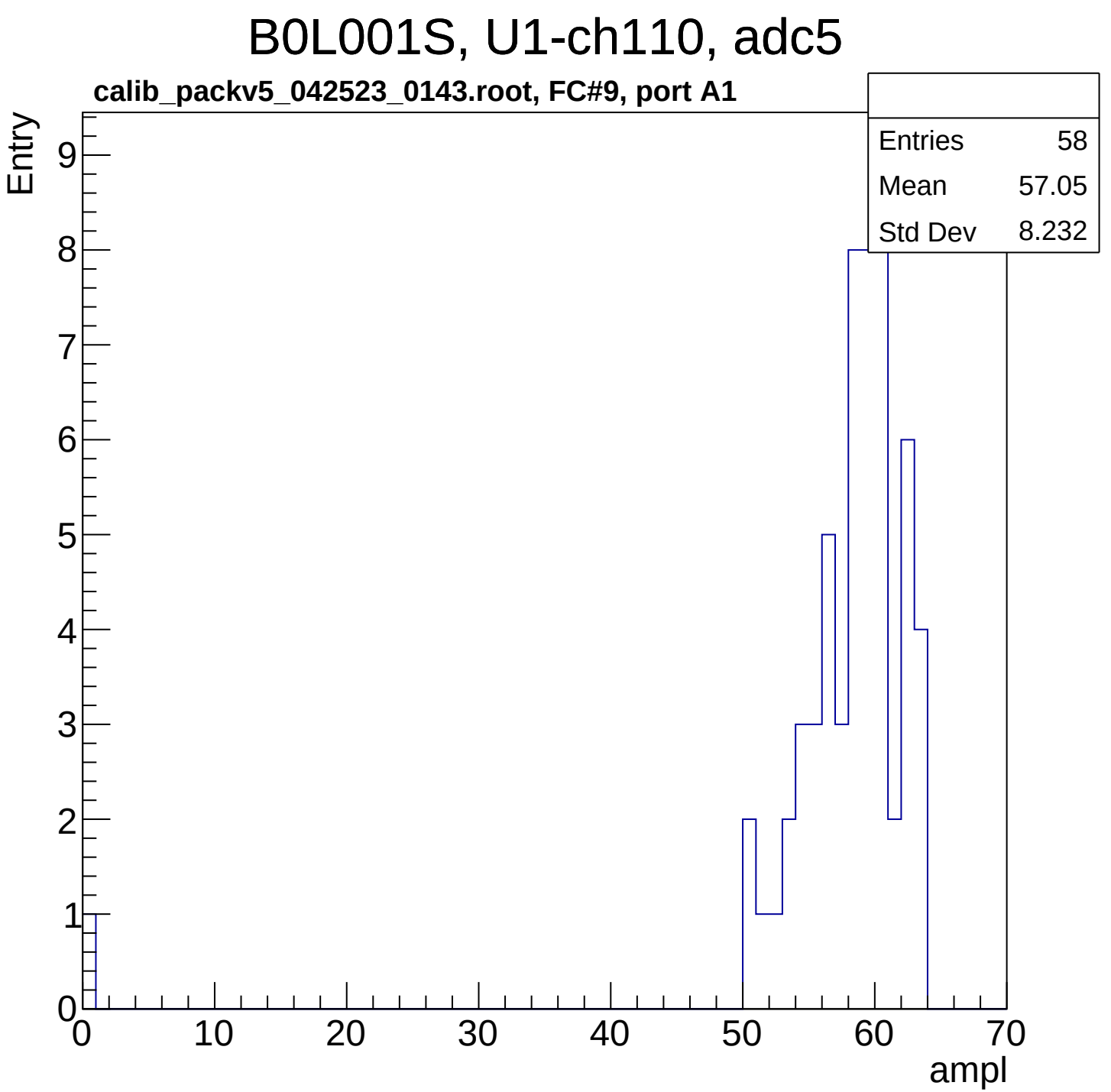
40
50
60
70


B0L001S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 57.05
Std Dev 8.232
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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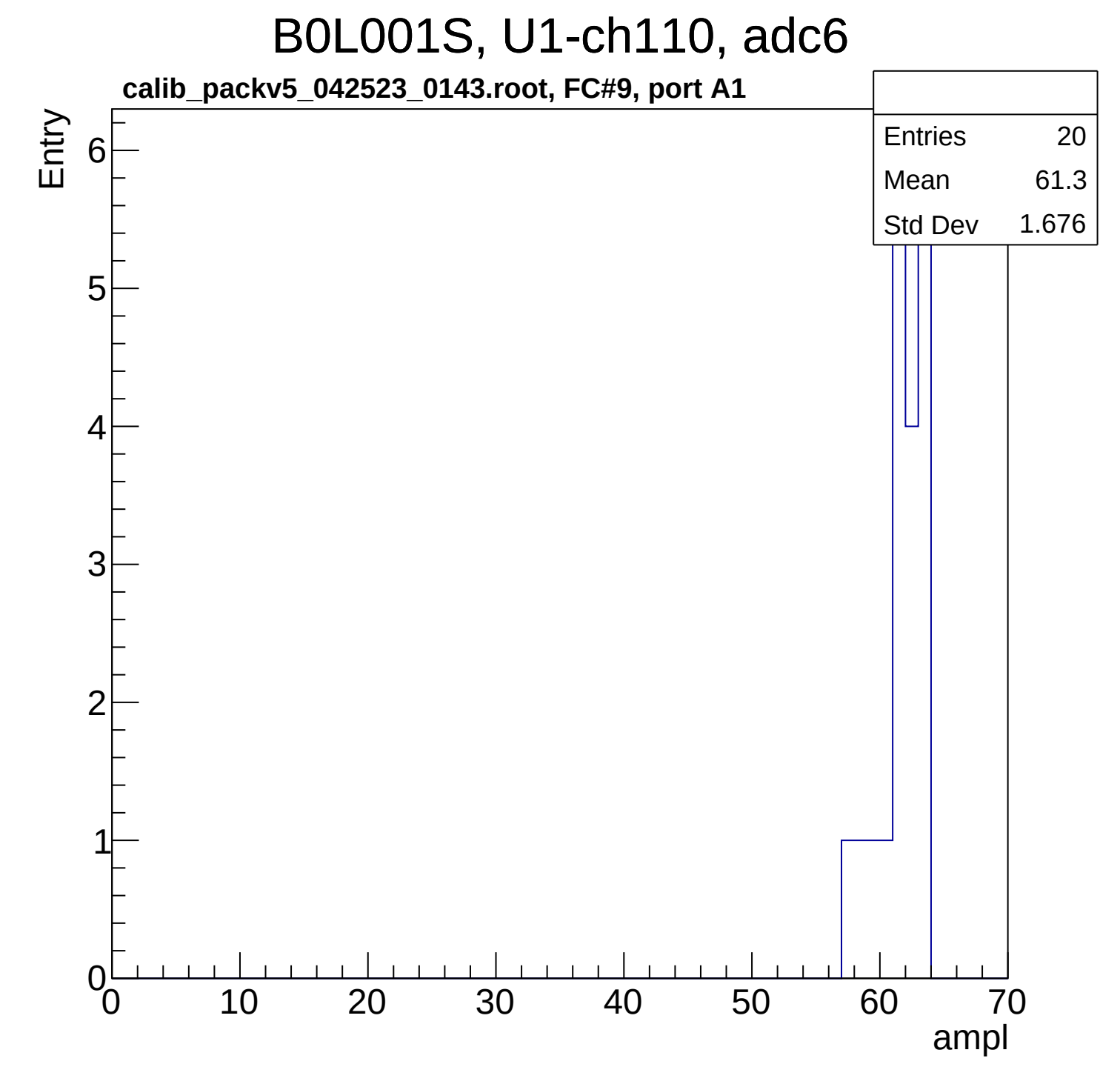
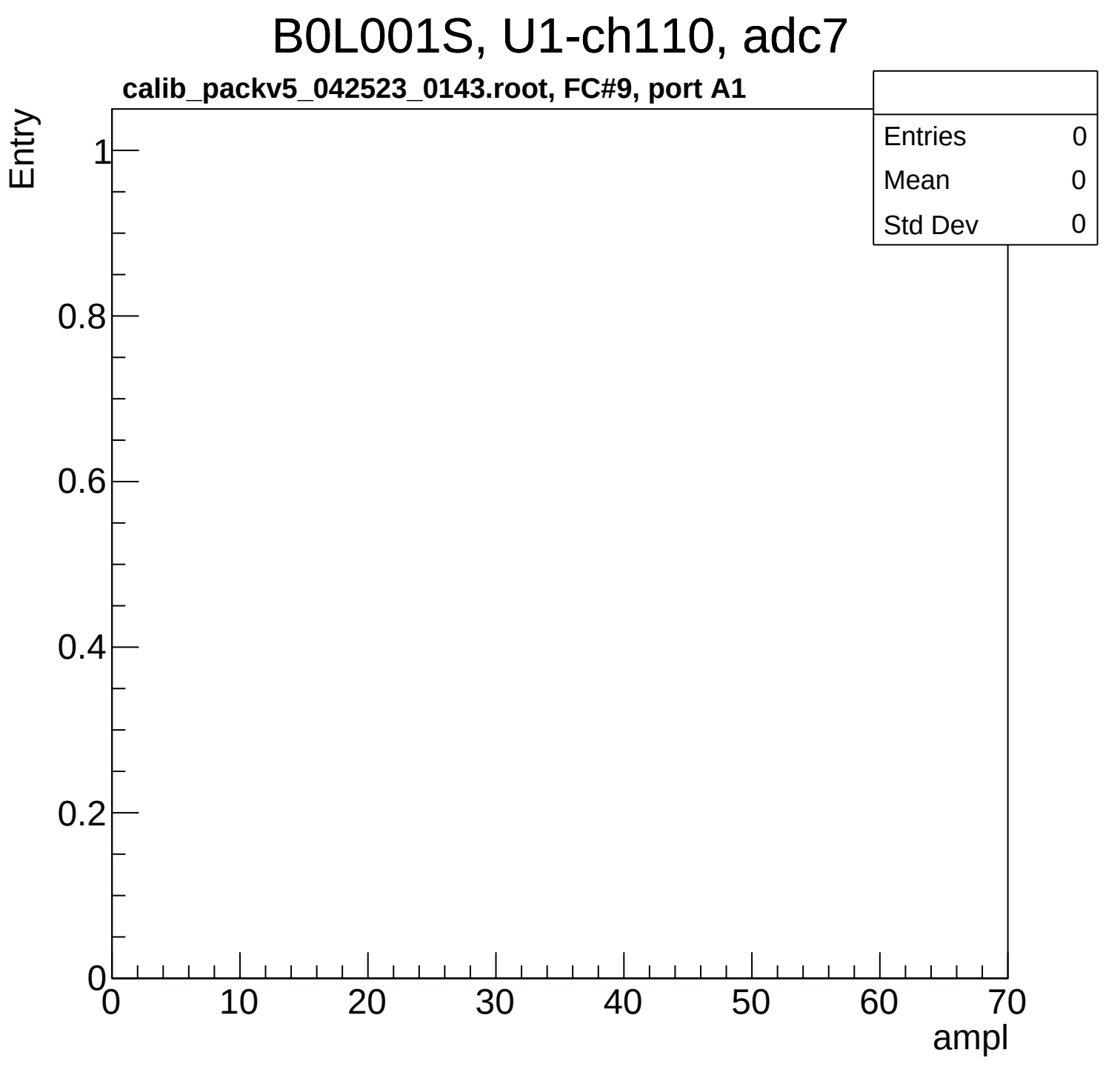

B0L001S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 20
Mean 61.3
Std Dev 1.676
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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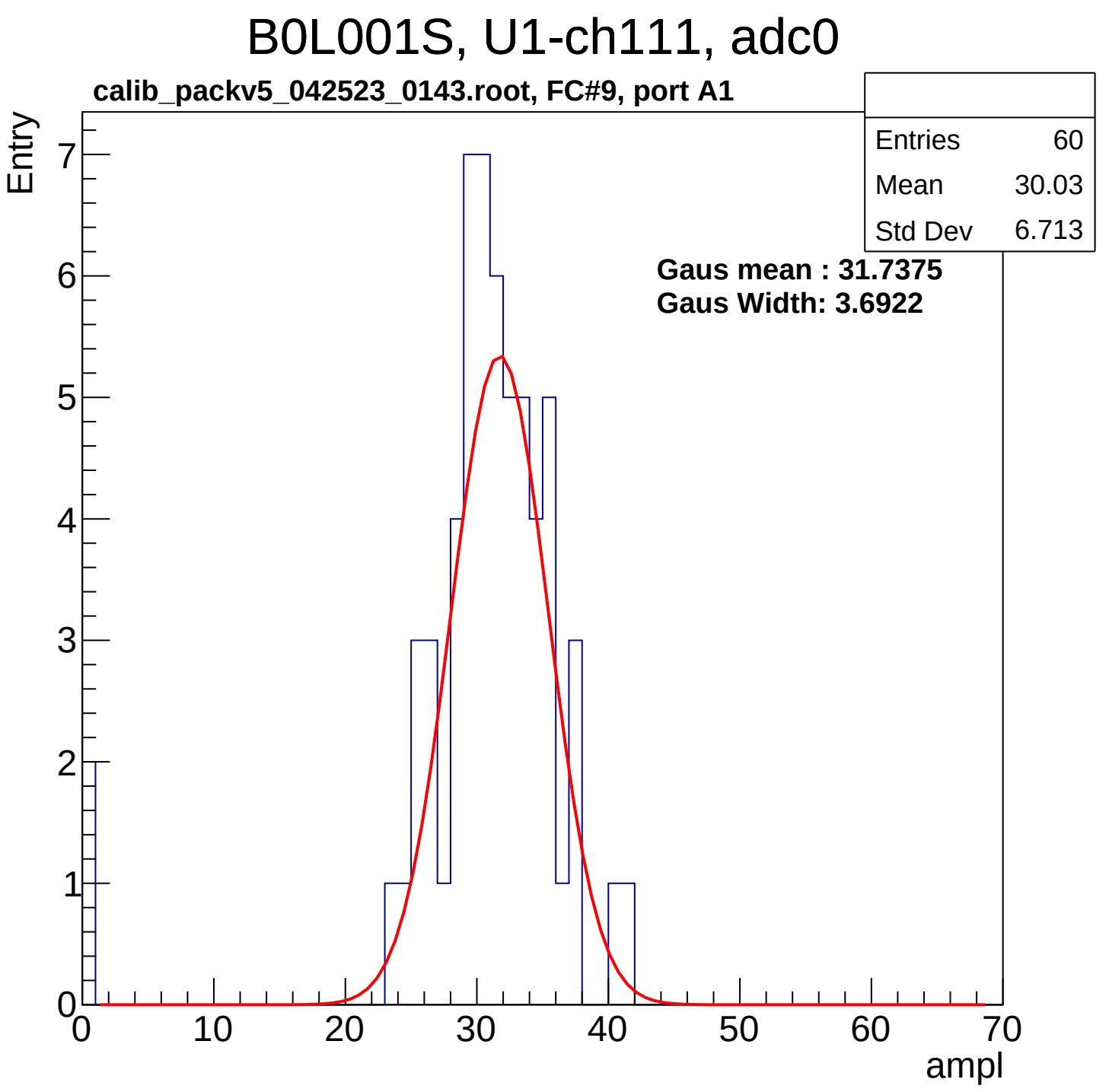
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 30.03
Std Dev 6.713
Gaus mean : 31.7375
Gaus Width: 3.6922
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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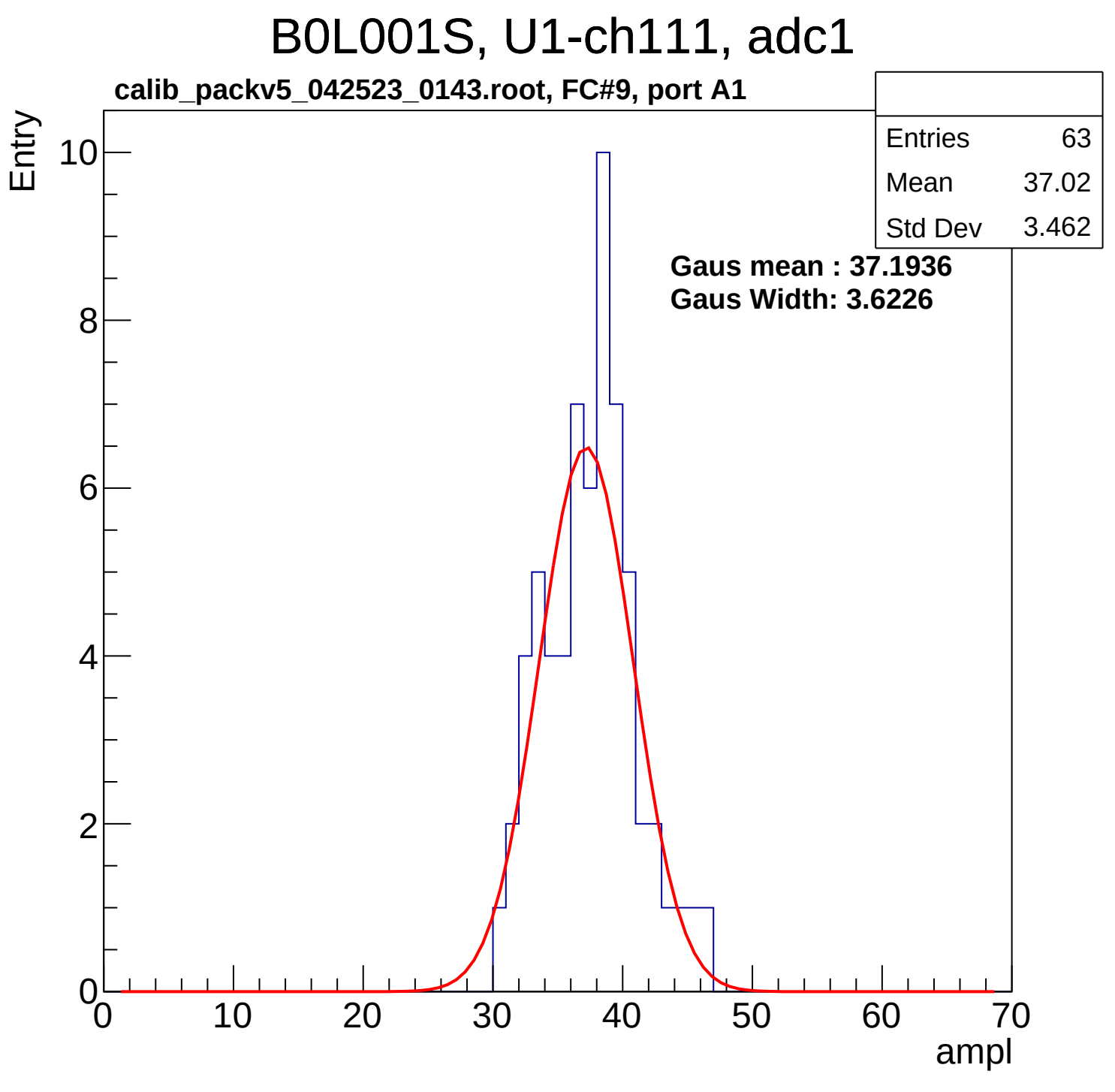
B0L001S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 37.02
Std Dev 3.462
Gaus mean : 37.1936
Gaus Width: 3.6226
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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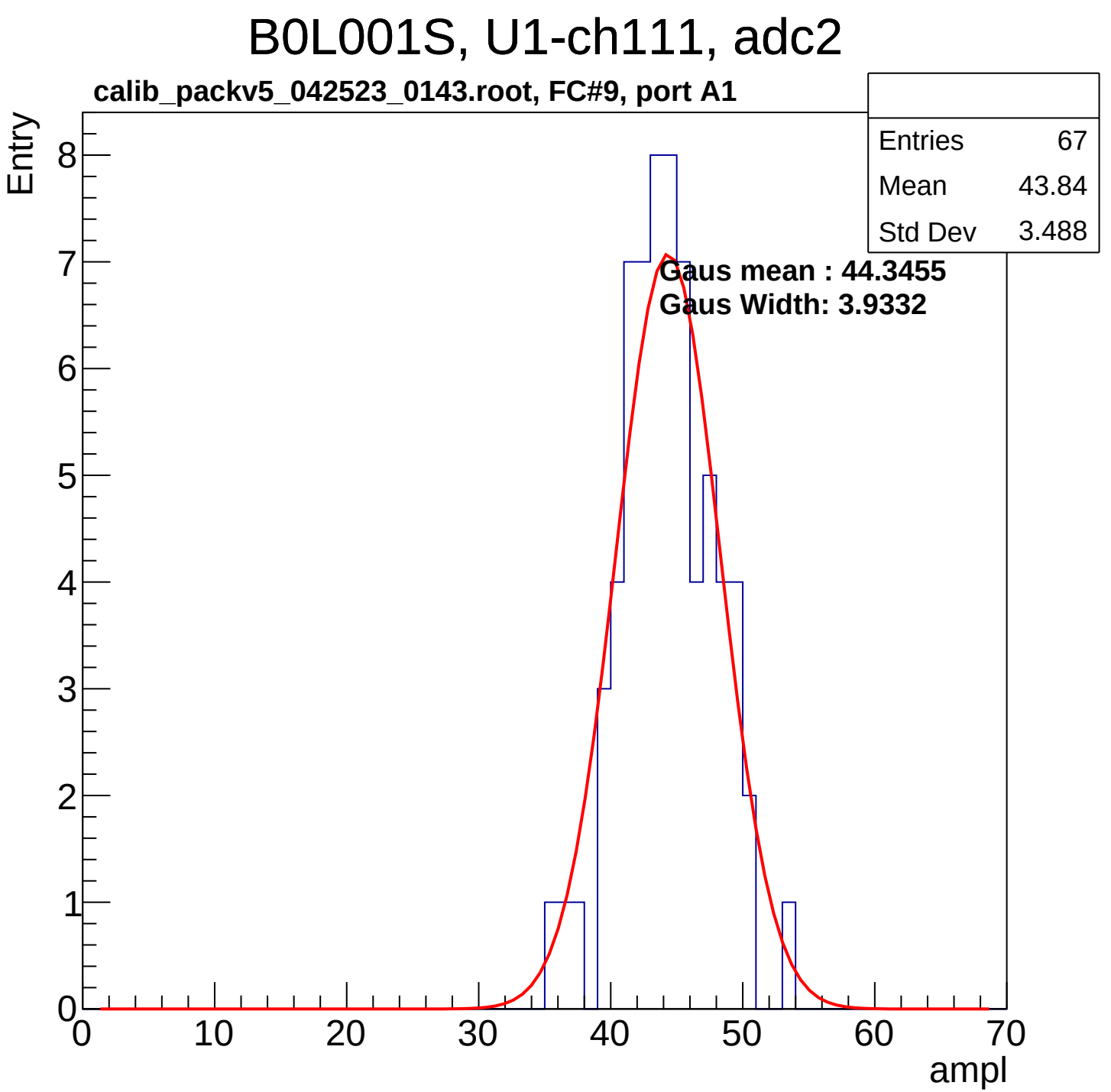
B0L001S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 67
Mean 43.84
Std Dev 3.488
Gaus mean : 44.3455
Gaus Width: 3.9332
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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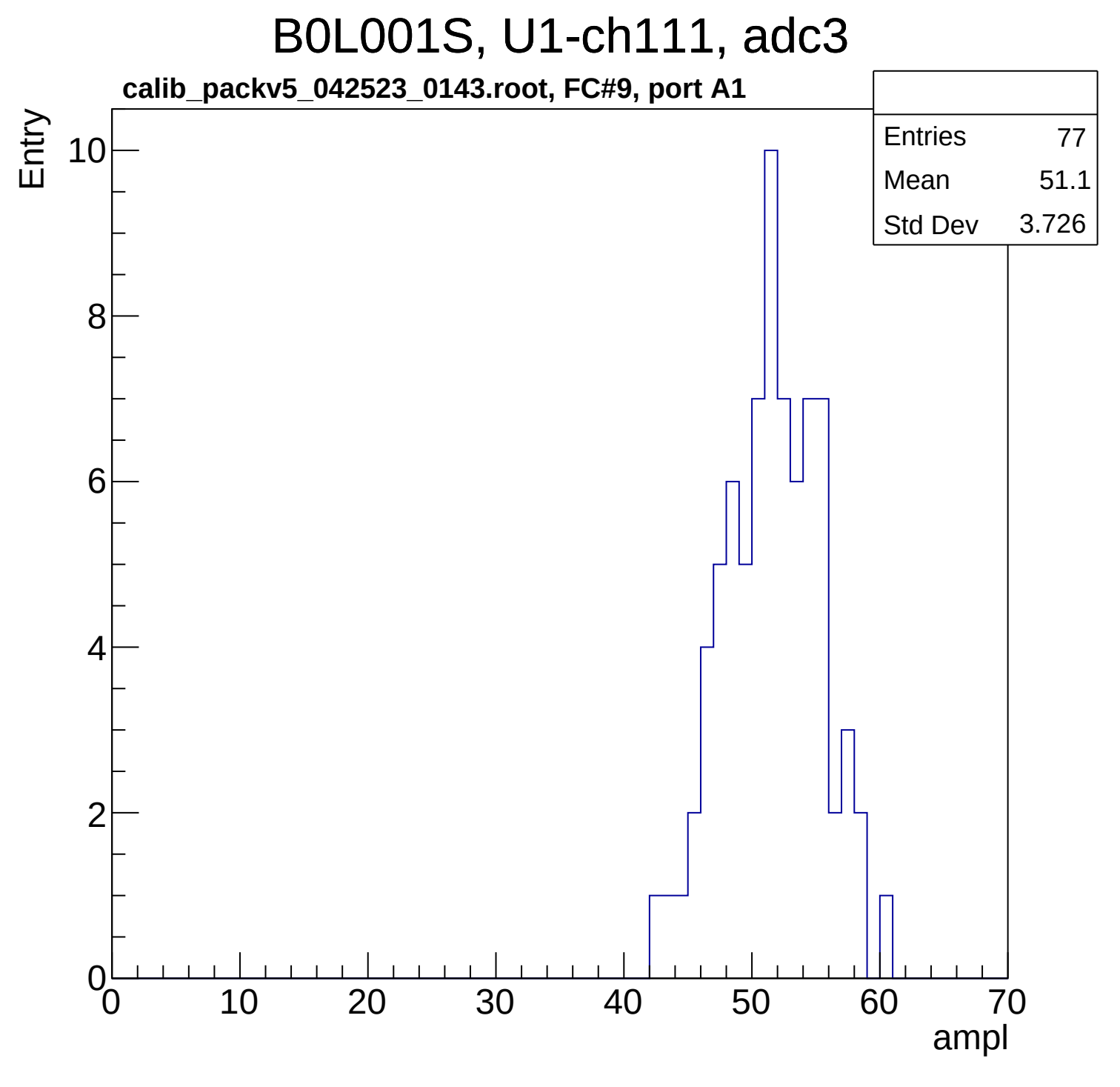
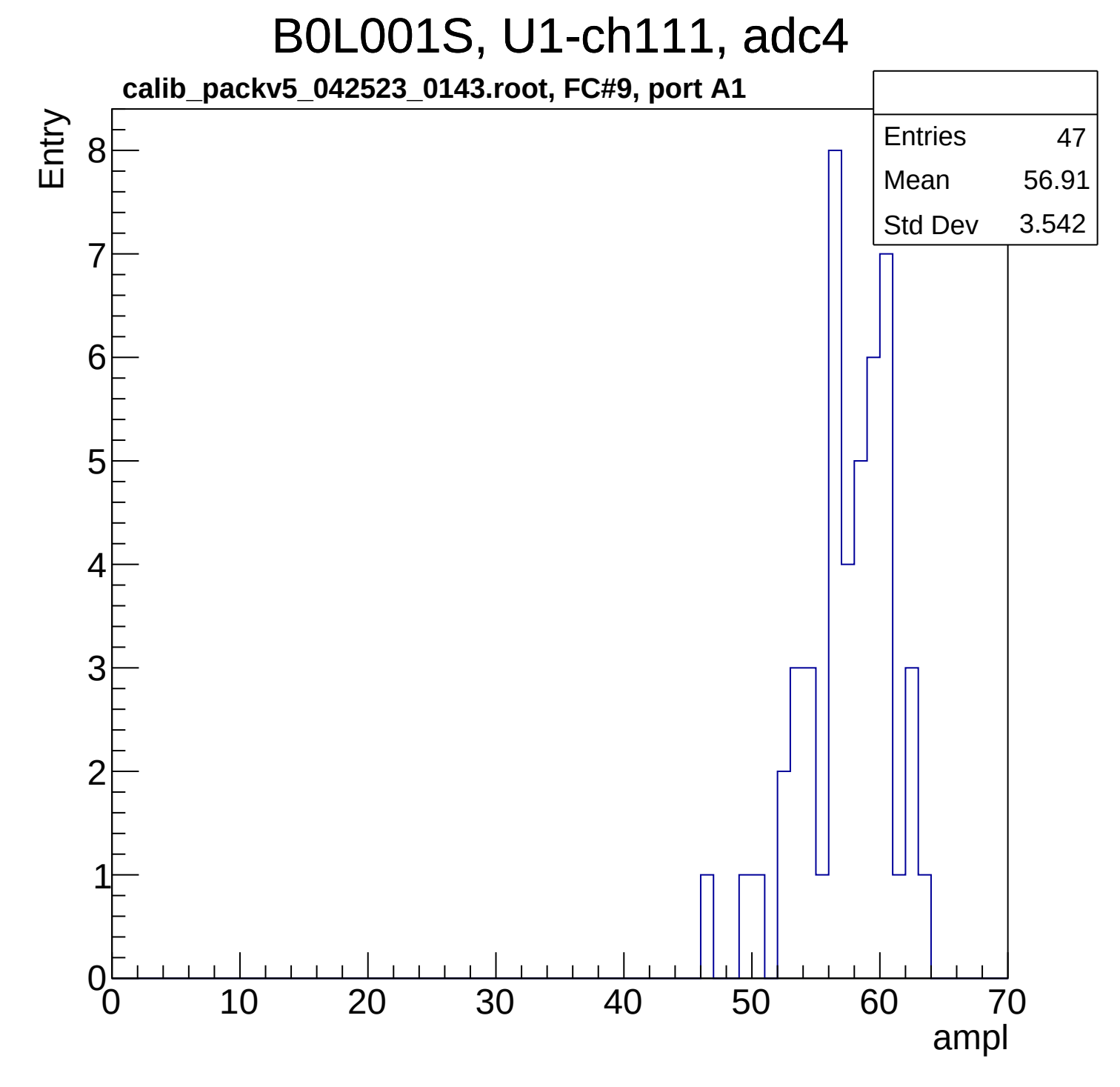

B0L001S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 51.1
Std Dev 3.726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 56.91
Std Dev 3.542
Entry
ampl
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1
2
3
4
5
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7
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0
10
20
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40
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60
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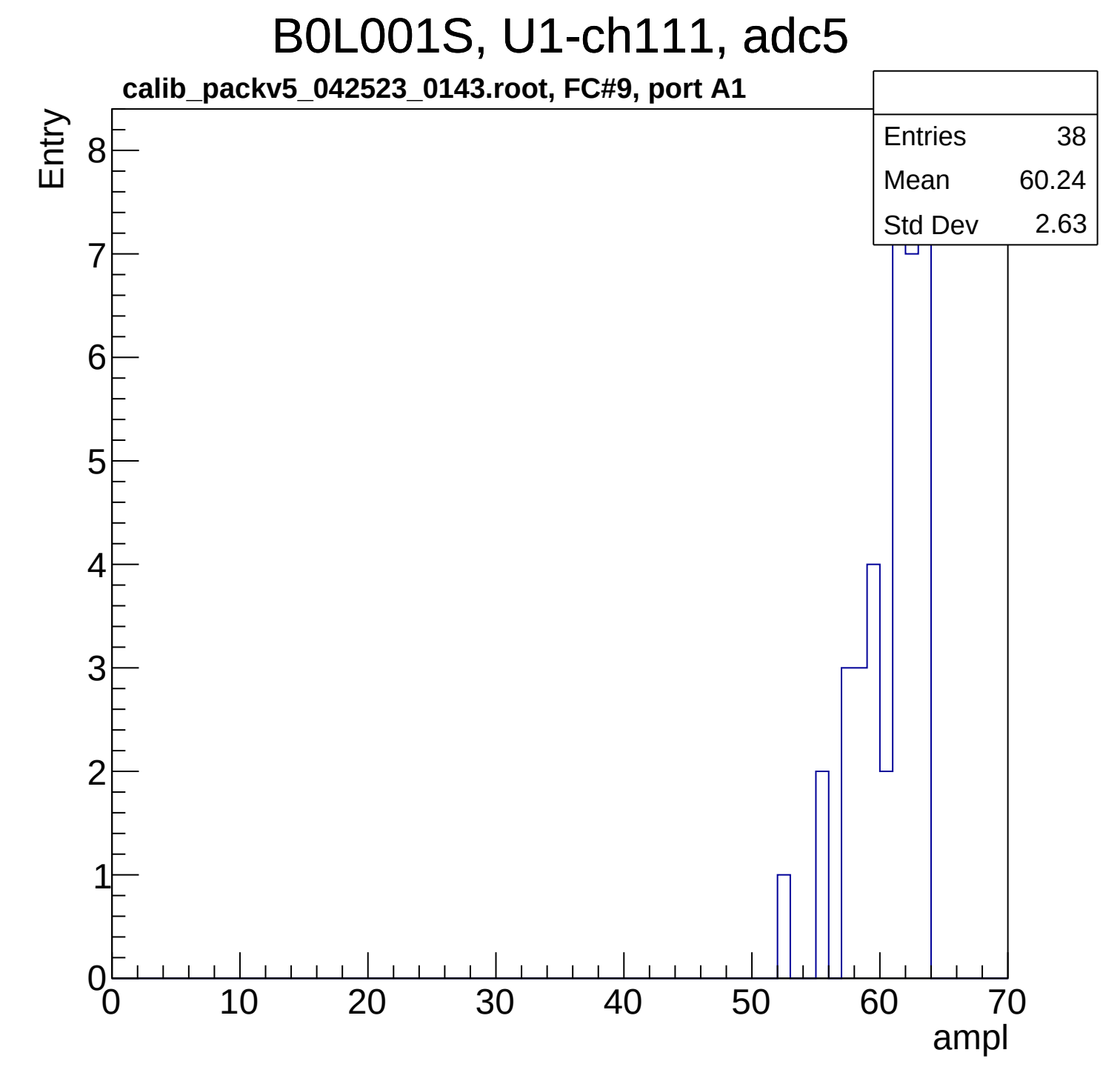
B0L001S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 38
Mean 60.24
Std Dev 2.63
Entry
ampl
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1
2
3
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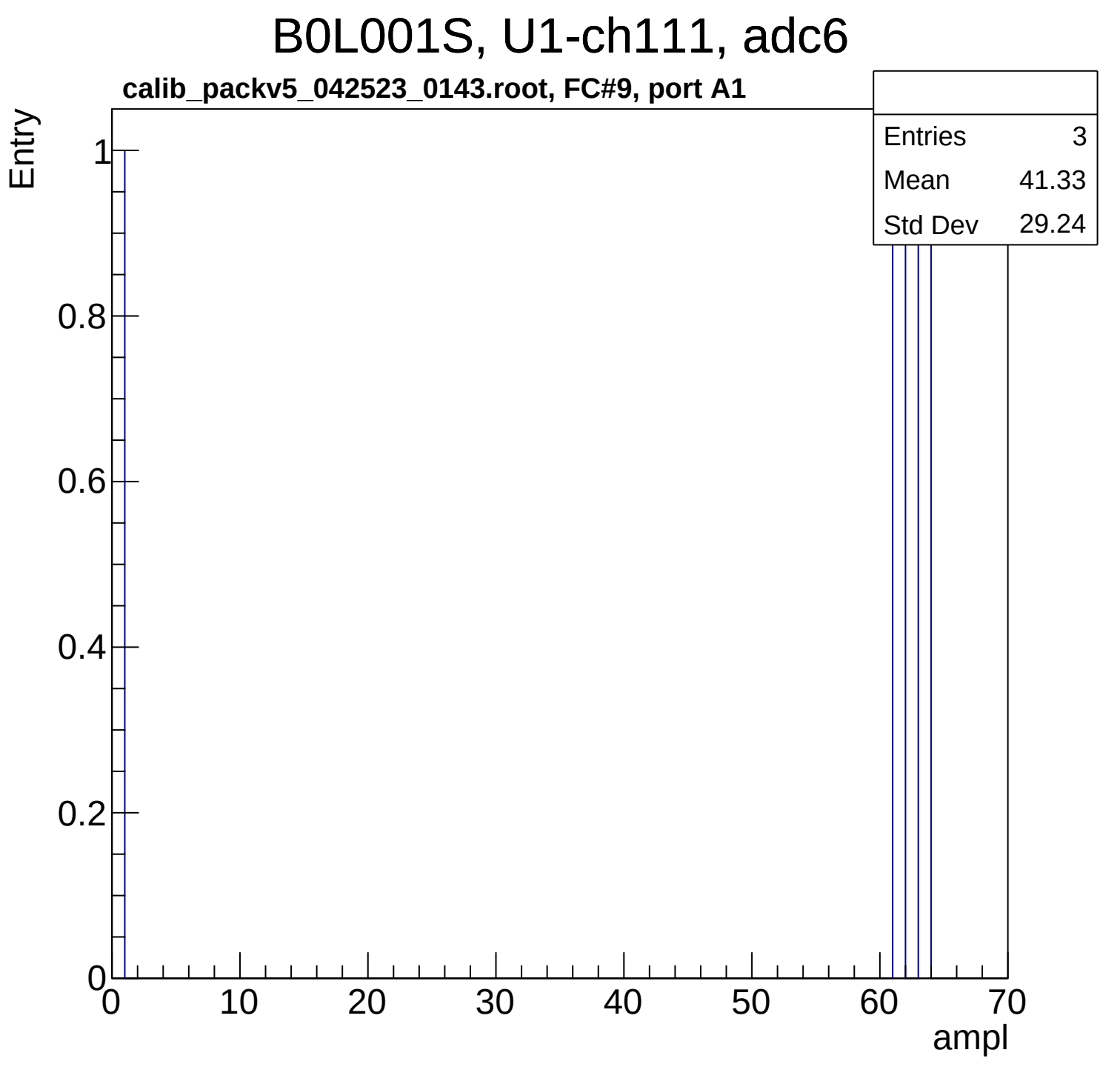
B0L001S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
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20
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40
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60
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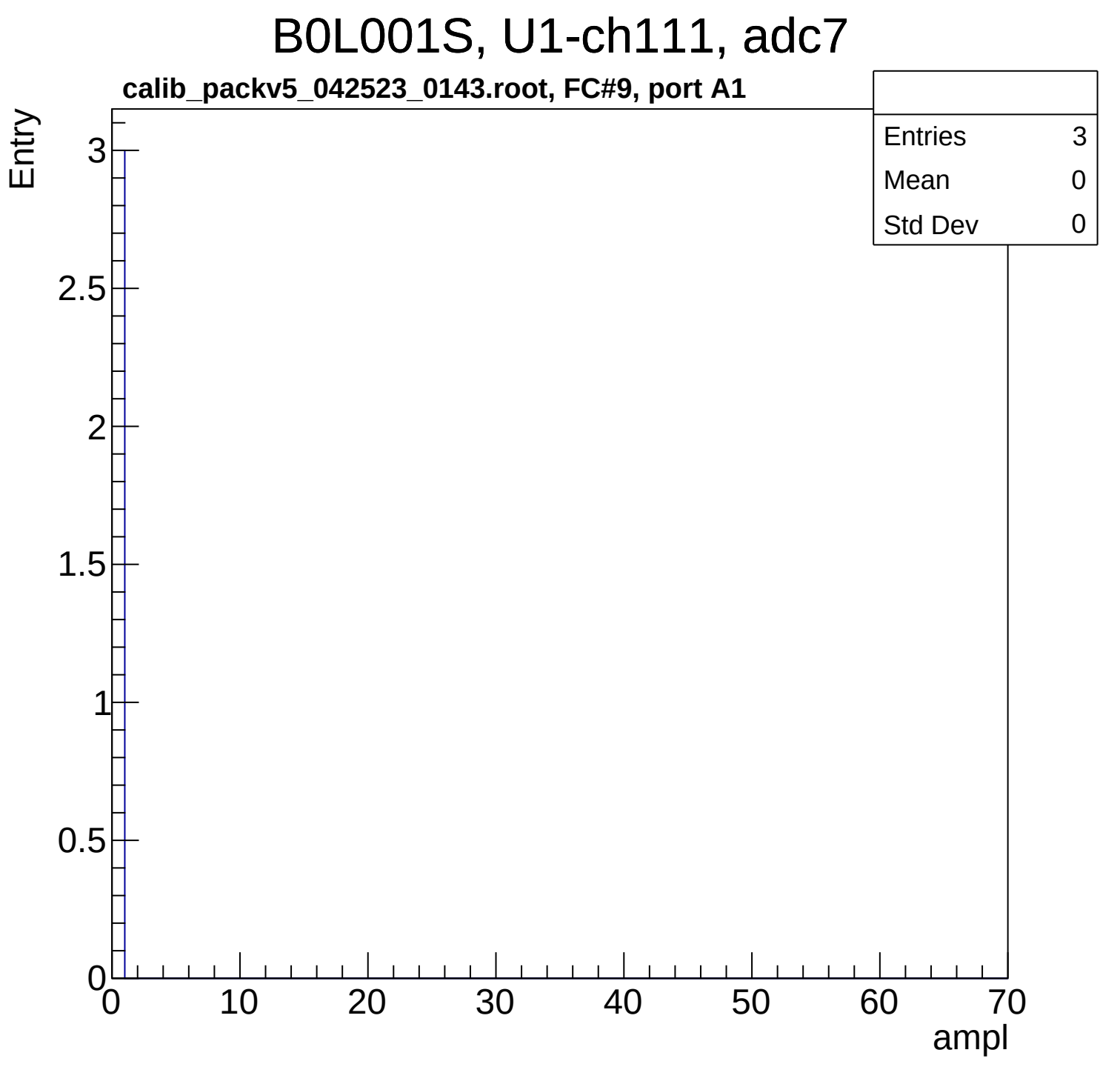
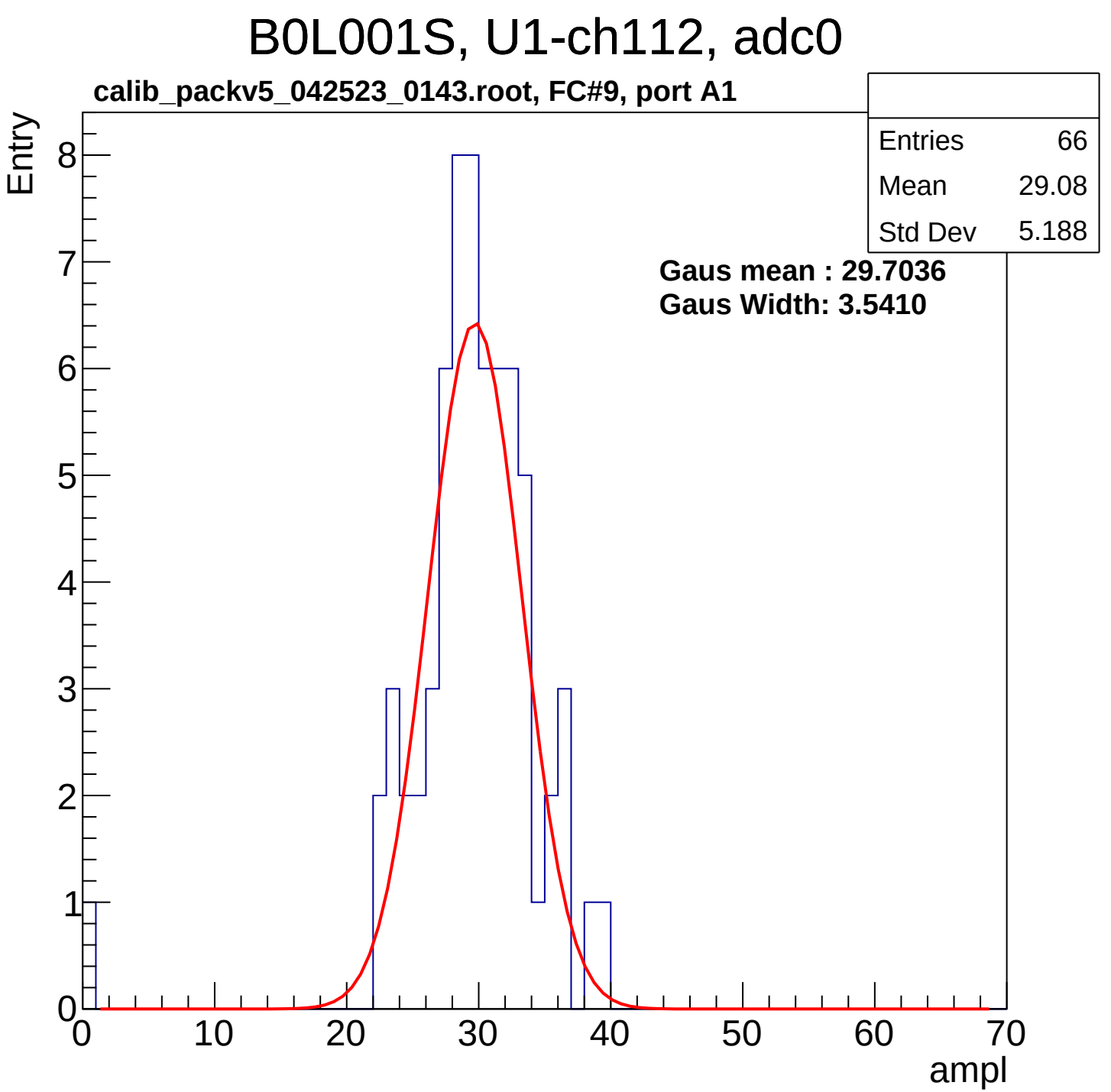

B0L001S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 29.08
Std Dev 5.188
Gaus mean : 29.7036
Gaus Width: 3.5410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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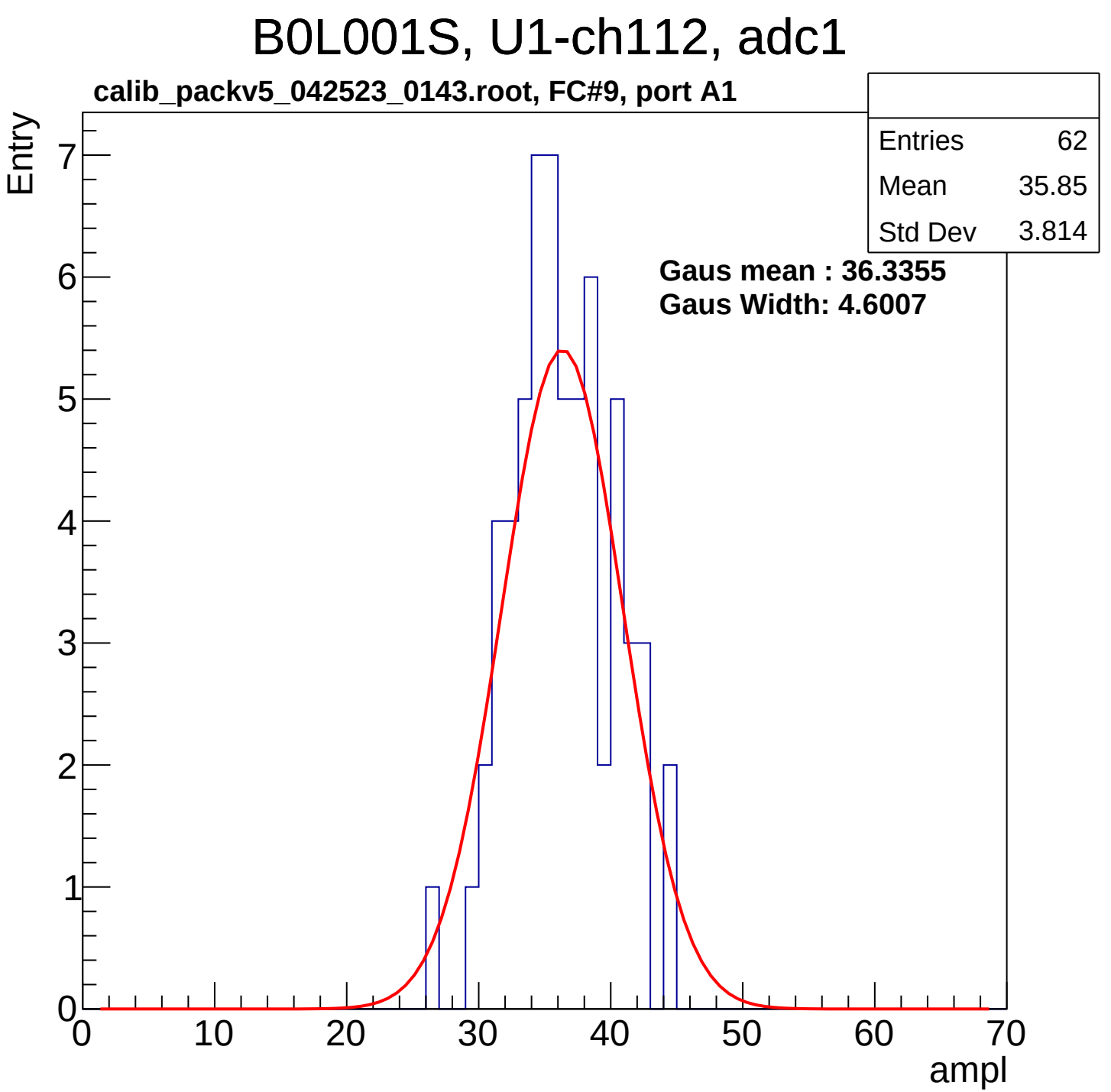
B0L001S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 35.85
Std Dev 3.814
Gaus mean : 36.3355
Gaus Width: 4.6007
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
50
60
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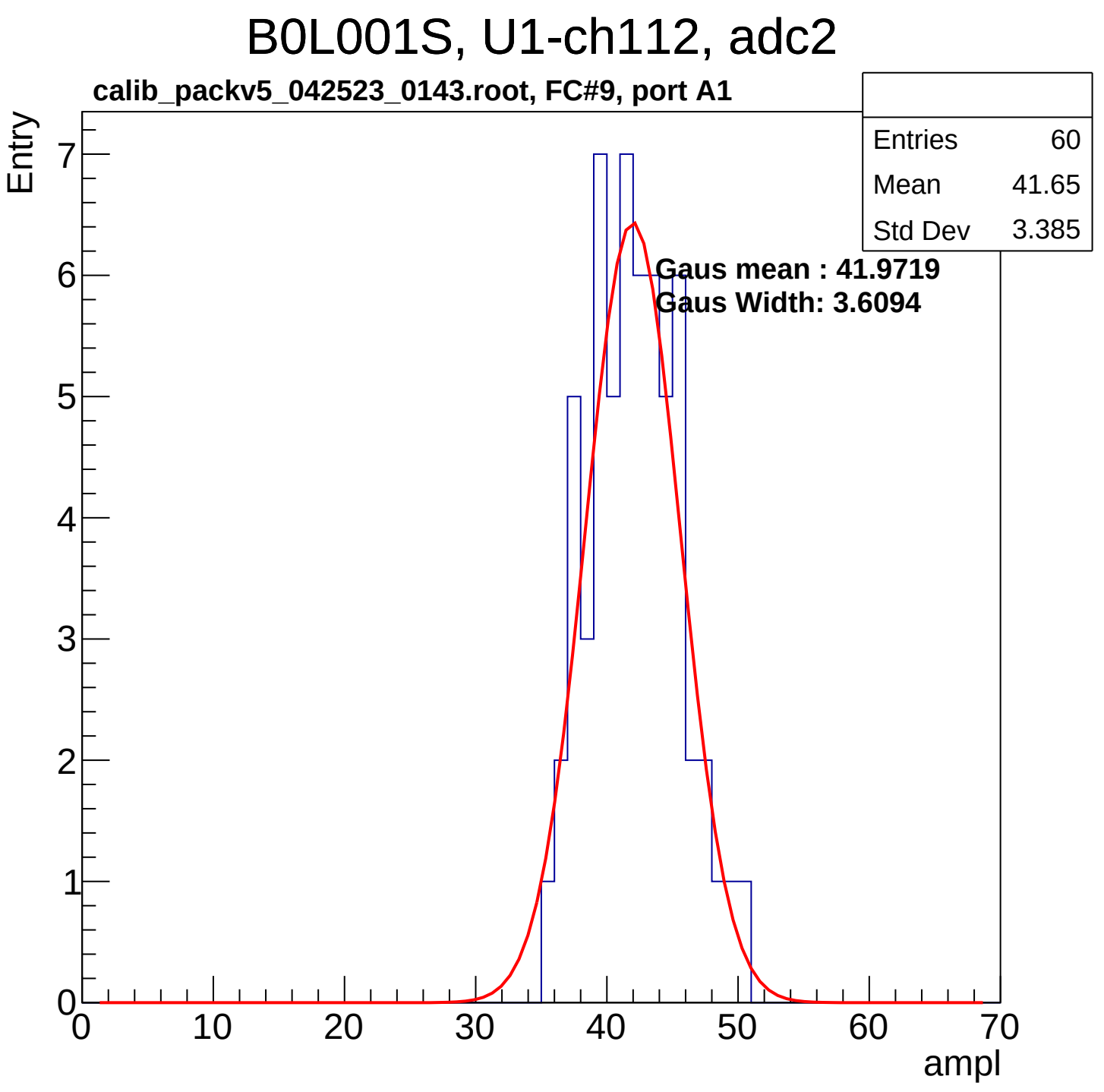
B0L001S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 41.65
Std Dev 3.385
Gaus mean : 41.9719
Gaus Width: 3.6094
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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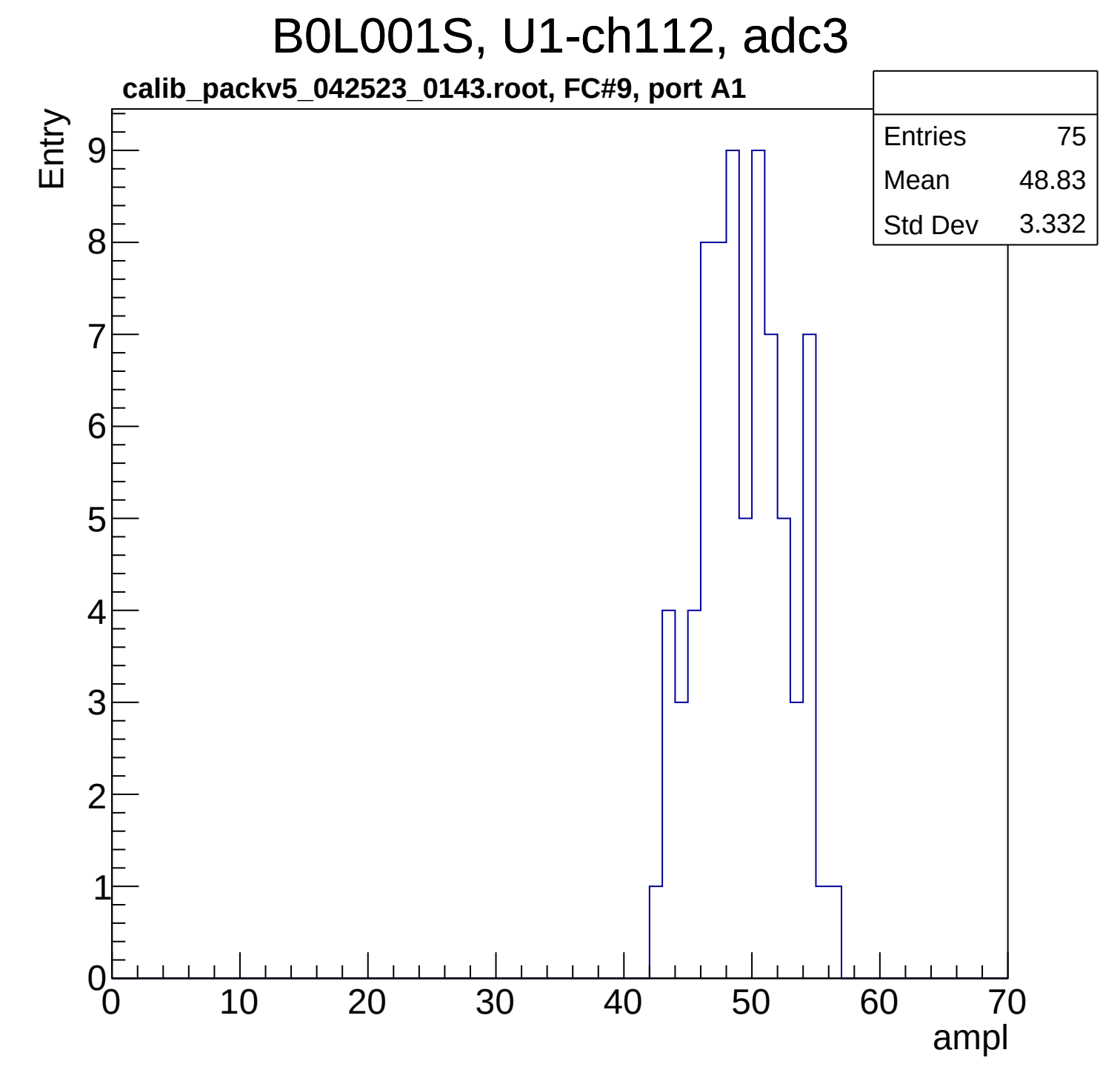
B0L001S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 48.83
Std Dev 3.332
Entry
ampl
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2
3
4
5
6
7
8
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0
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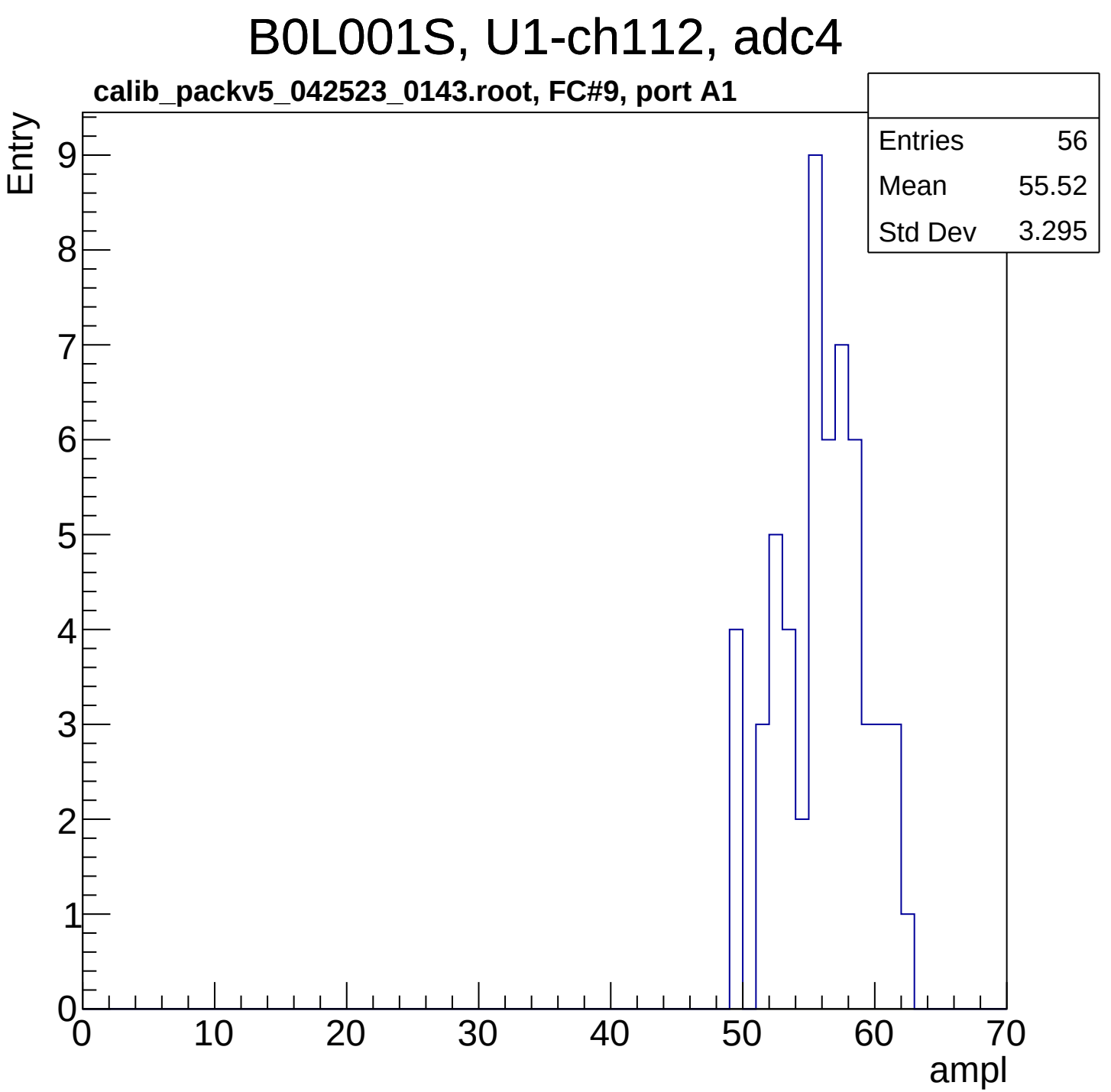
B0L001S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 55.52
Std Dev 3.295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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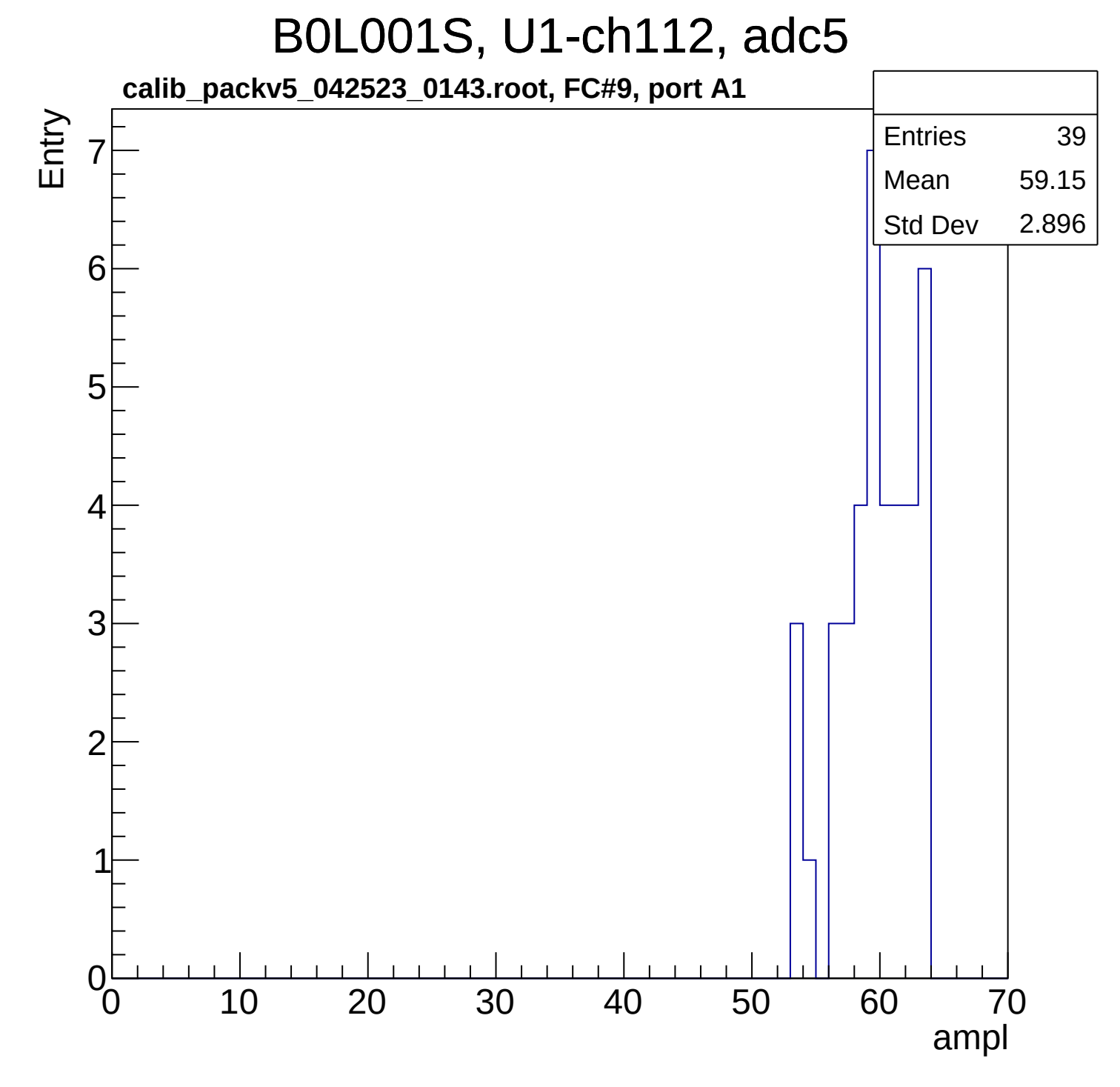
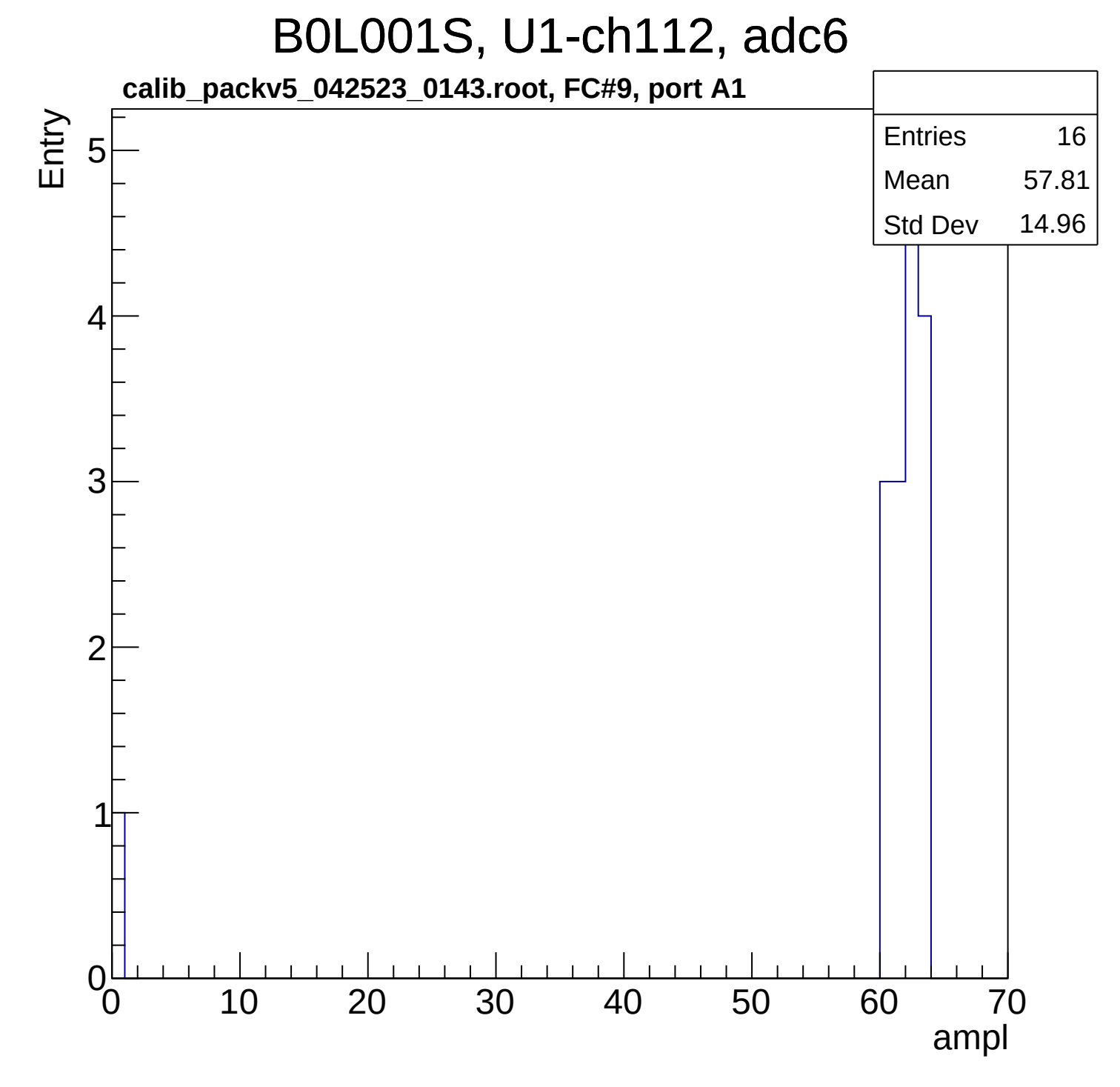
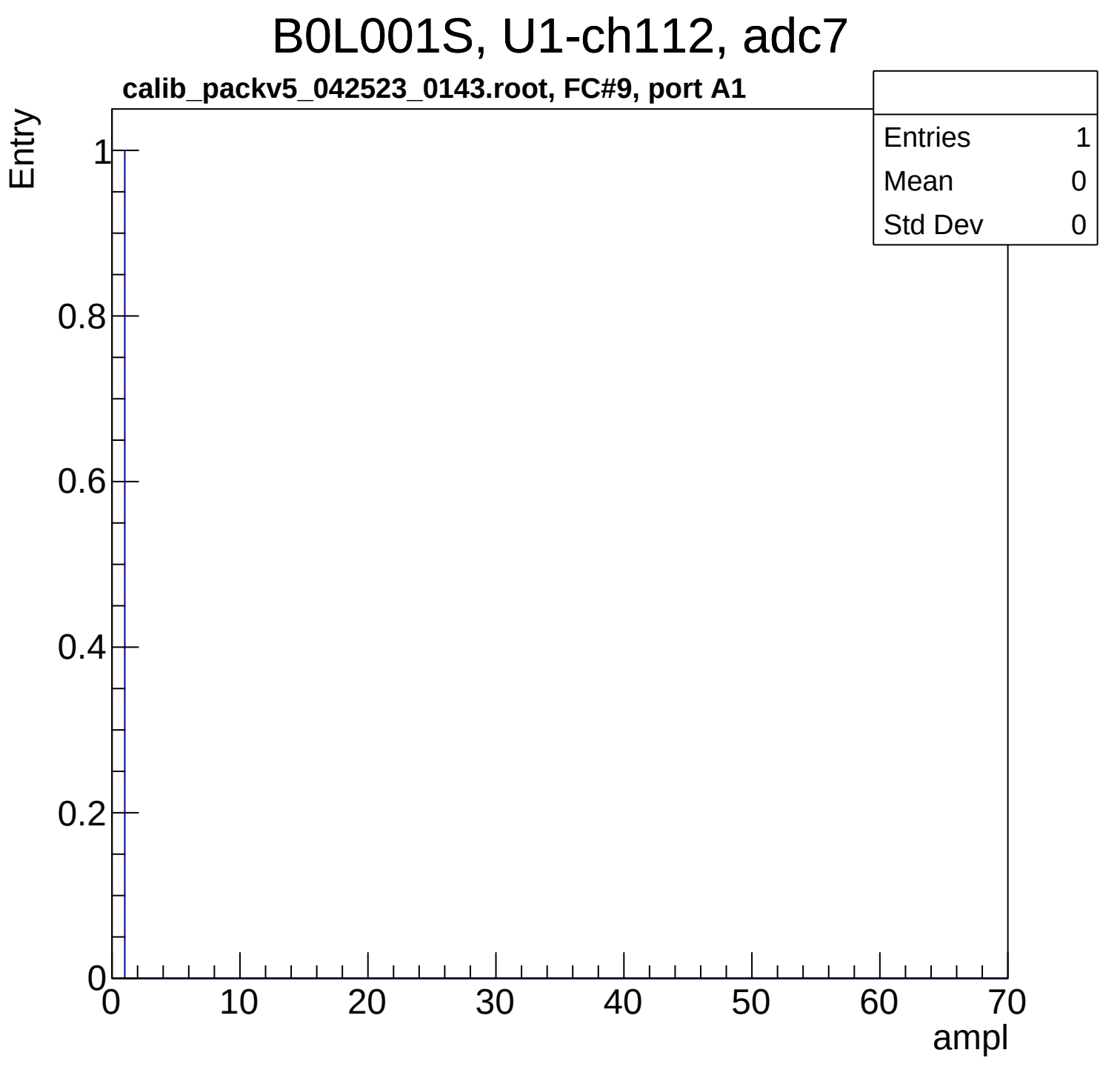
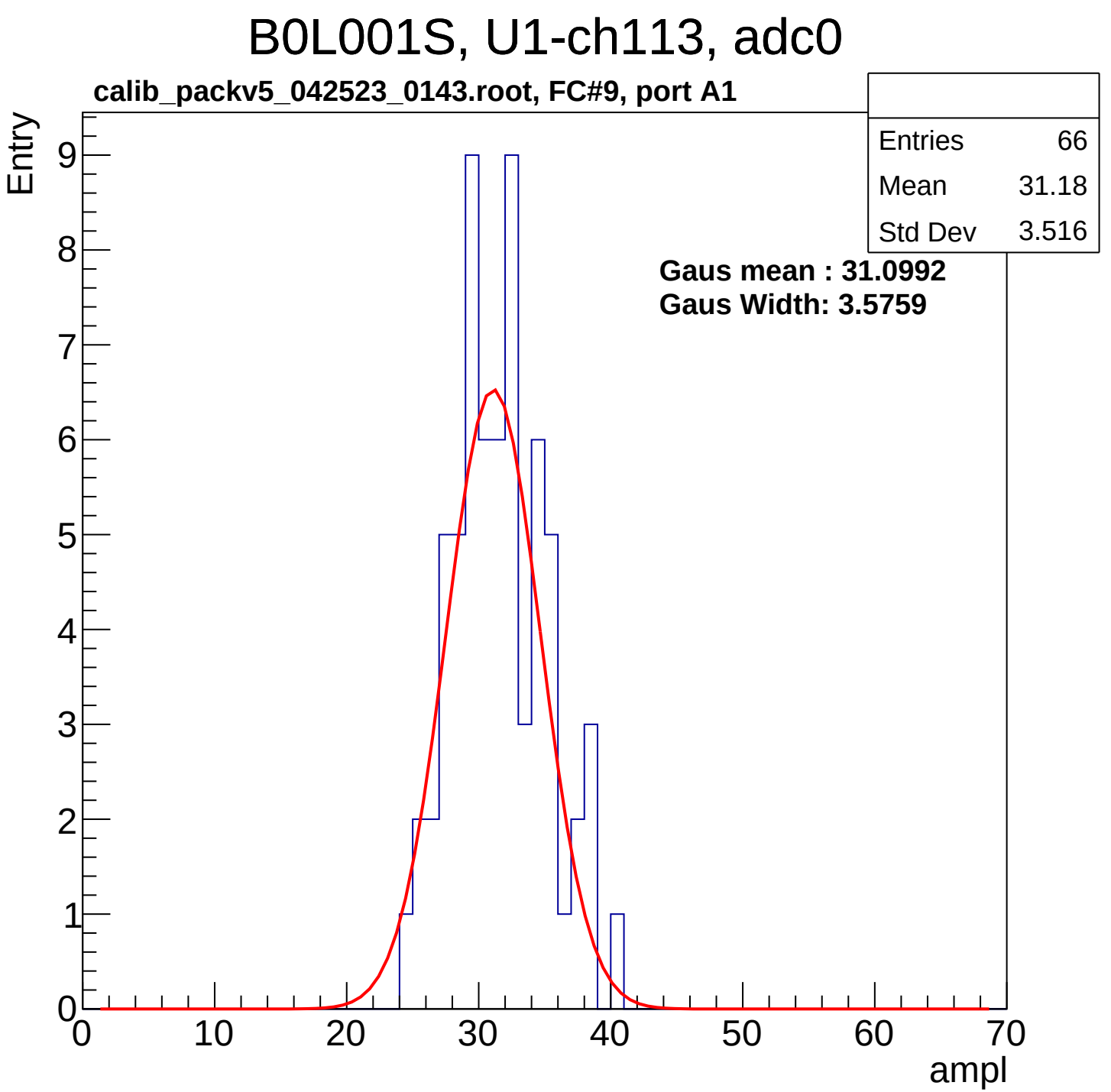
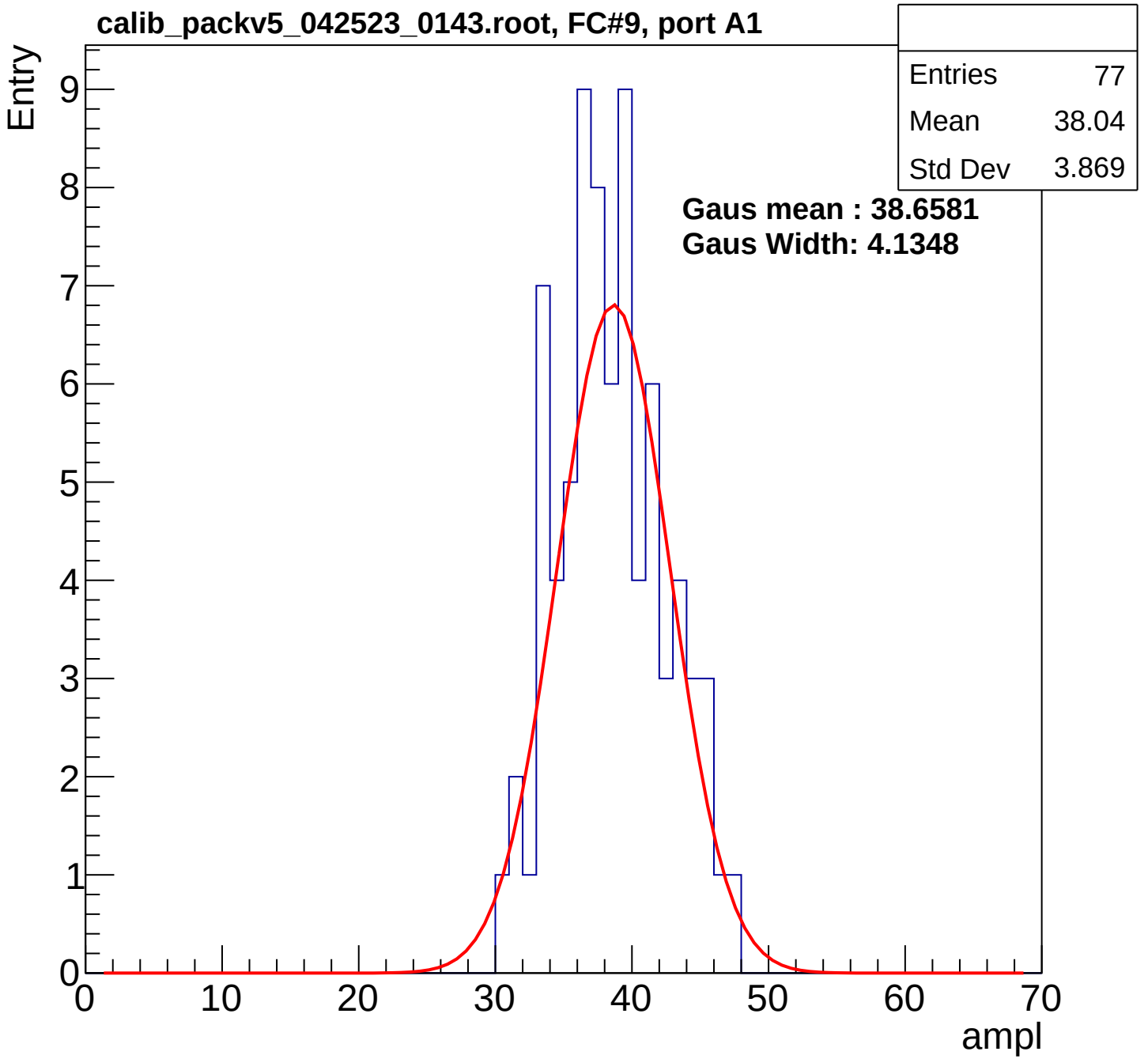

B0L001S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 59.15
Std Dev 2.896
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 57.81
Std Dev 14.96
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 31.18
Std Dev 3.516
Gaus mean : 31.0992
Gaus Width: 3.5759
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 77
Mean 38.04
Std Dev 3.869
Gaus mean : 38.6581
Gaus Width: 4.1348
Entry
ampl
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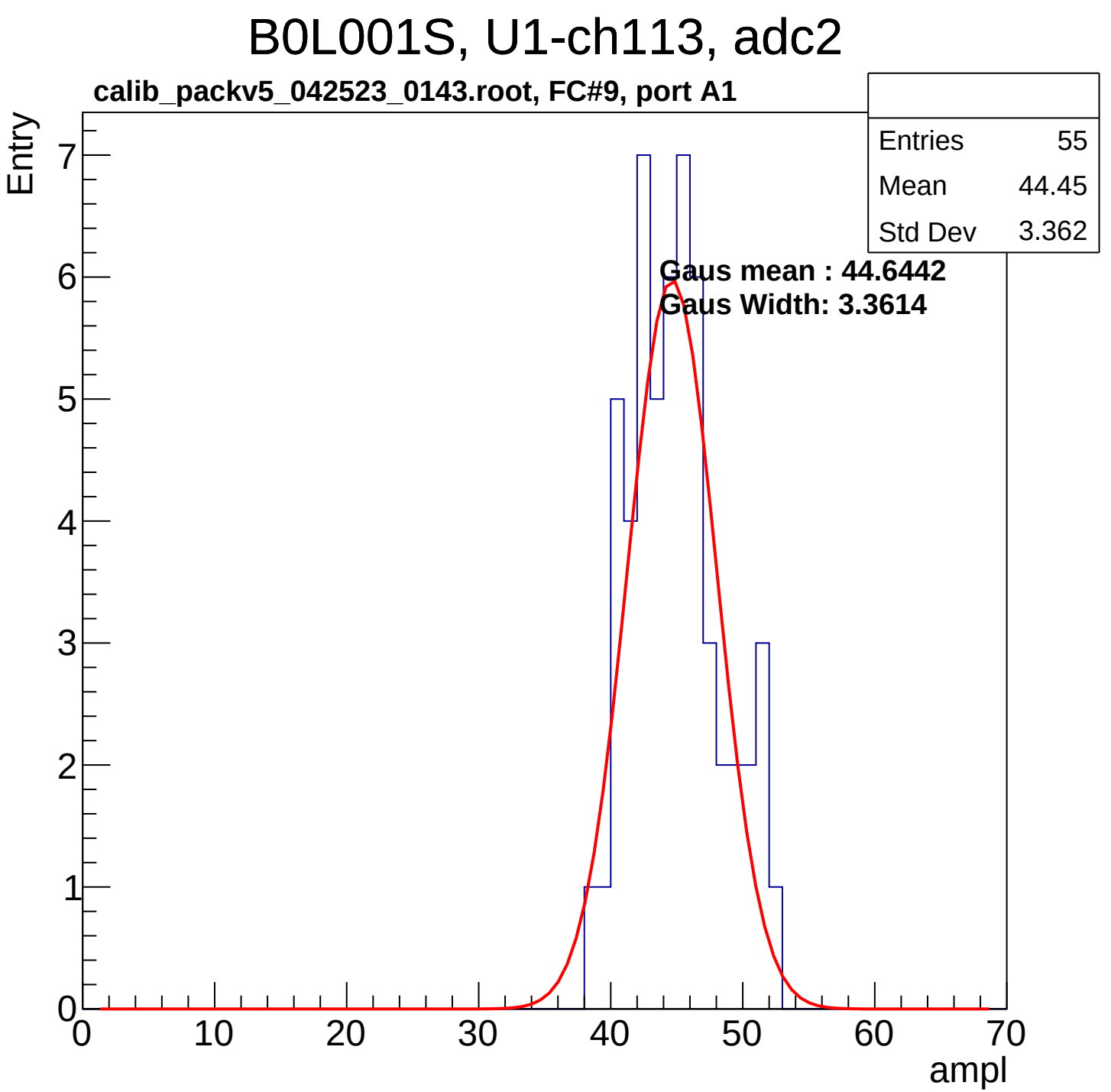
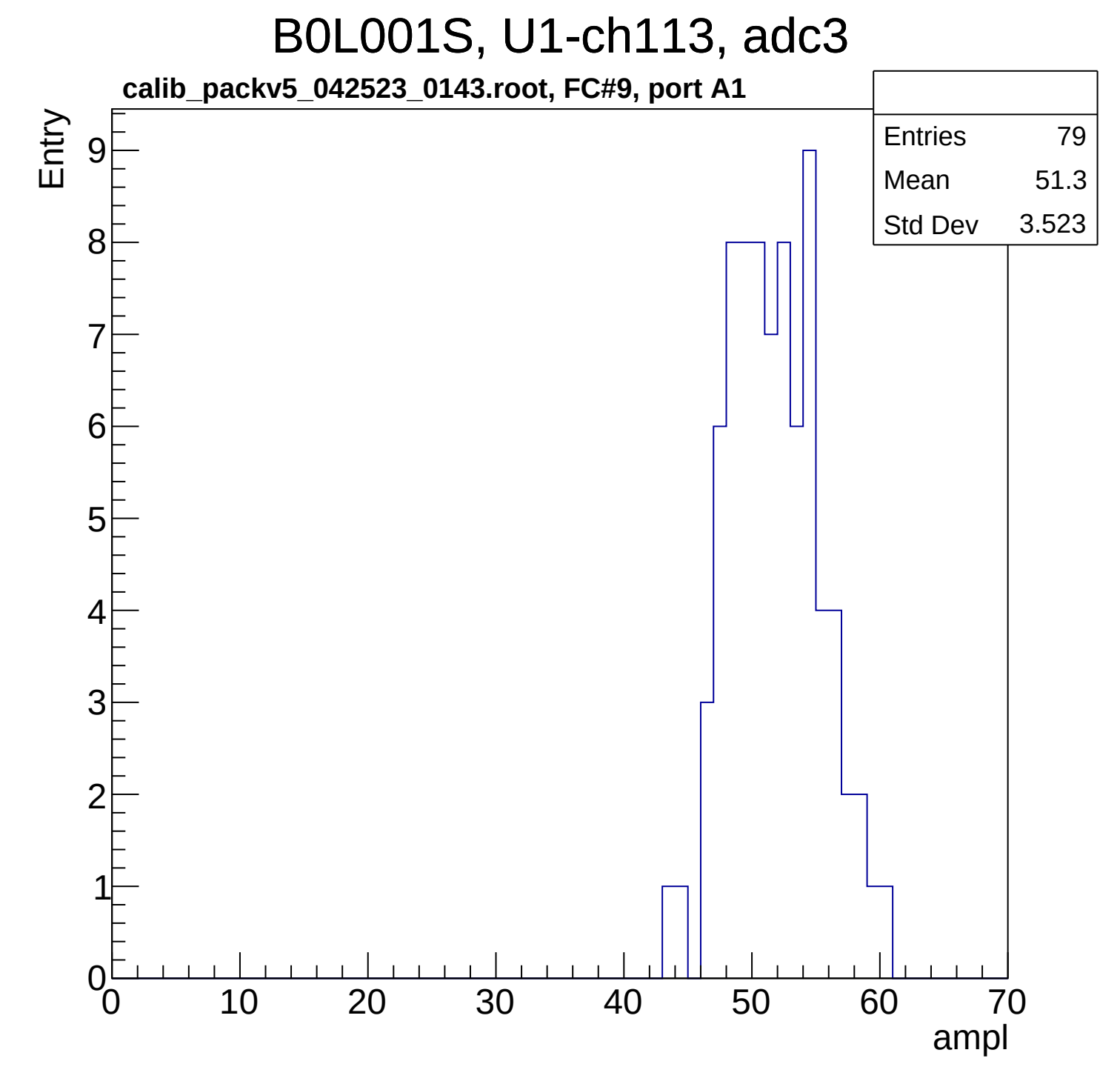
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 44.45
Std Dev 3.362
Gaus mean : 44.6442
Gaus Width: 3.3614
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 79
Mean 51.3
Std Dev 3.523
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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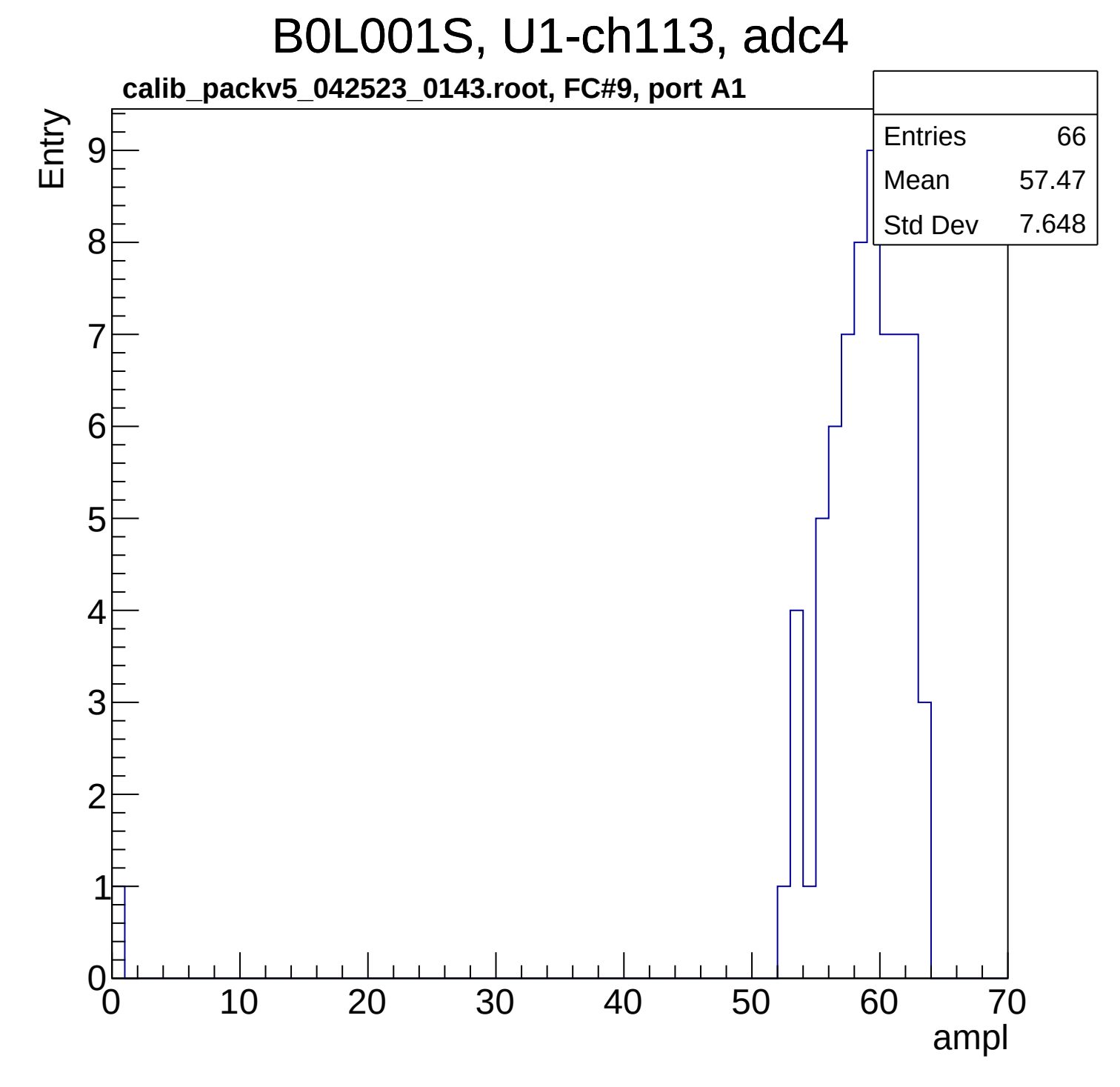
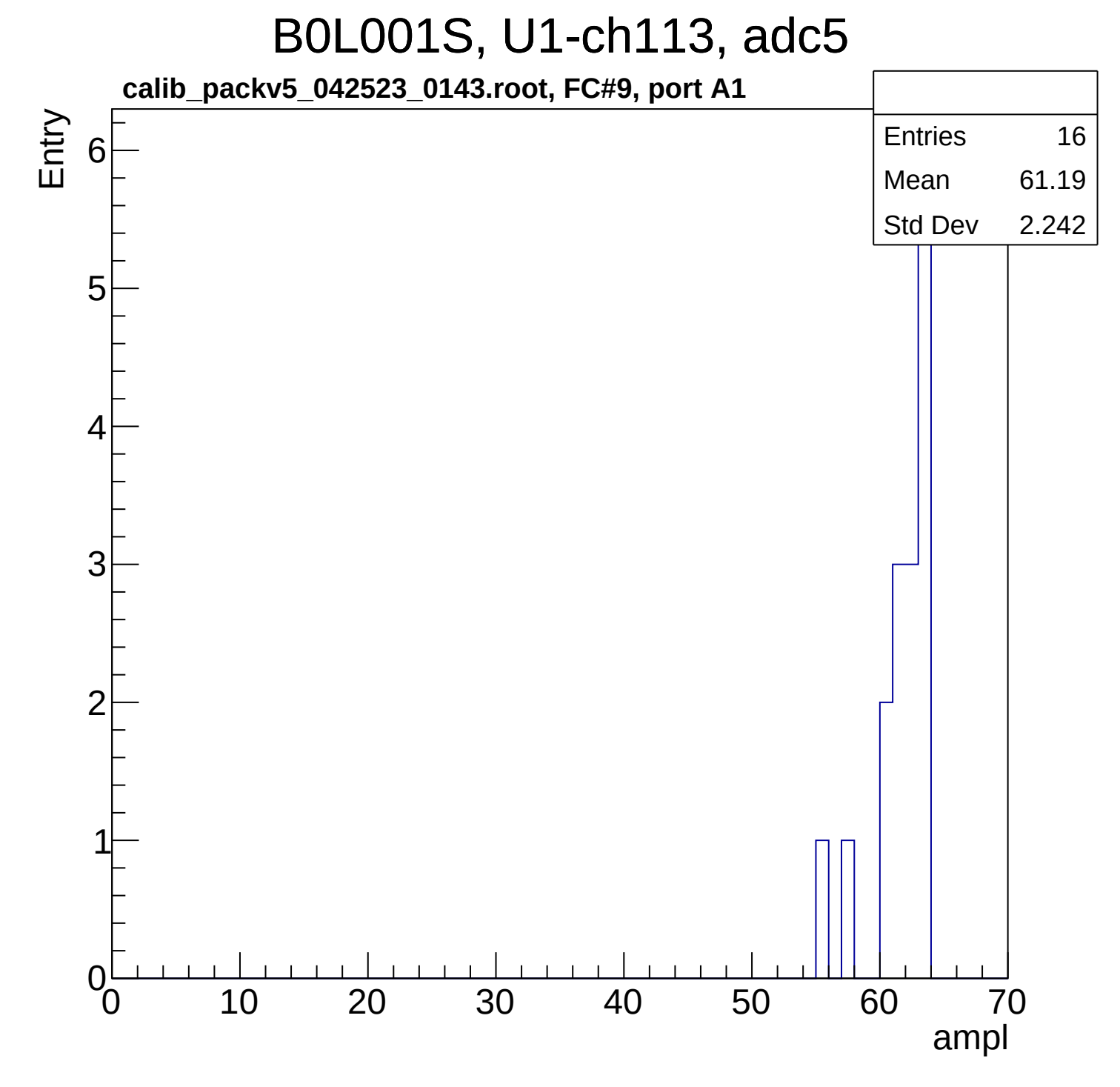
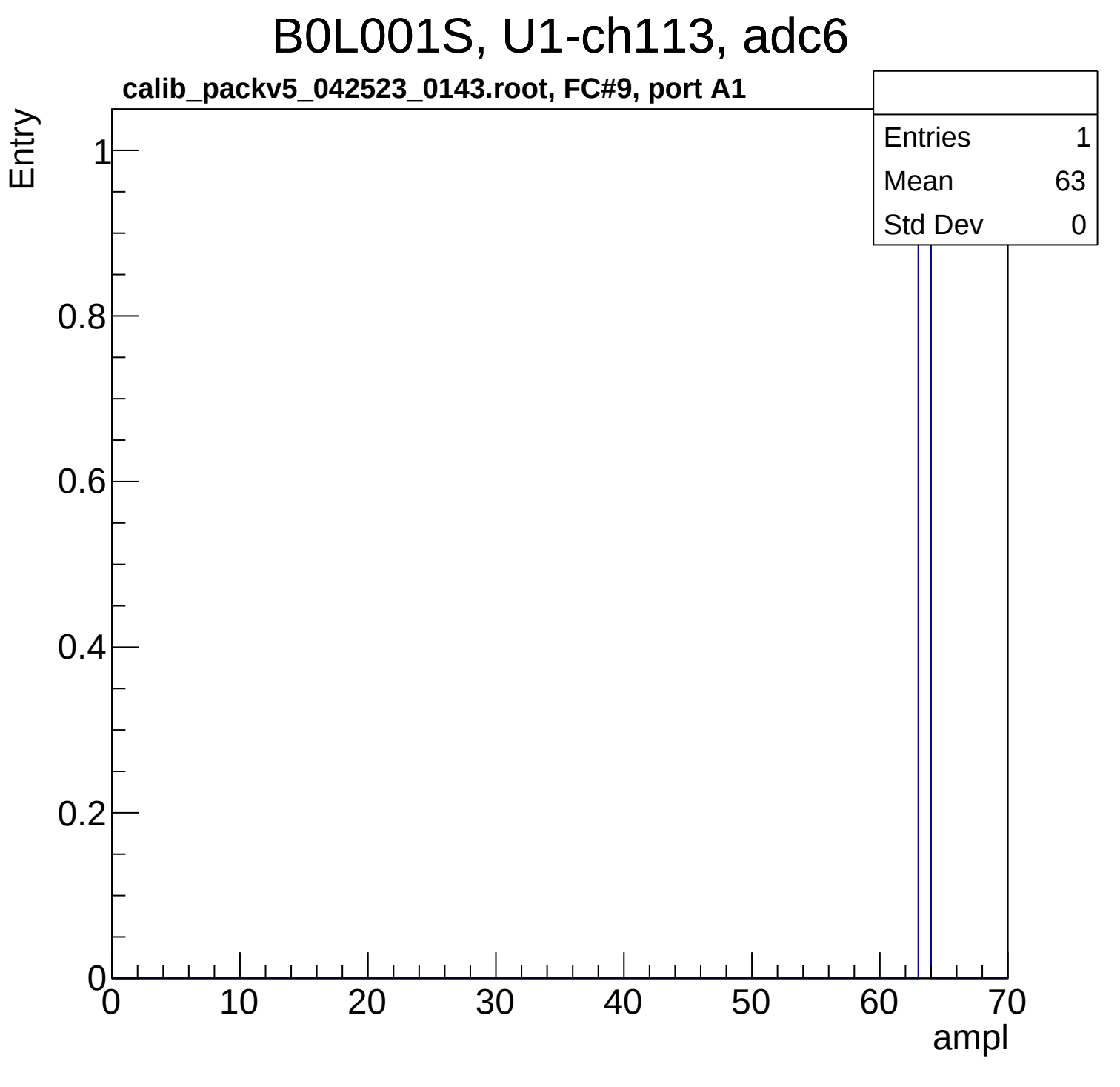
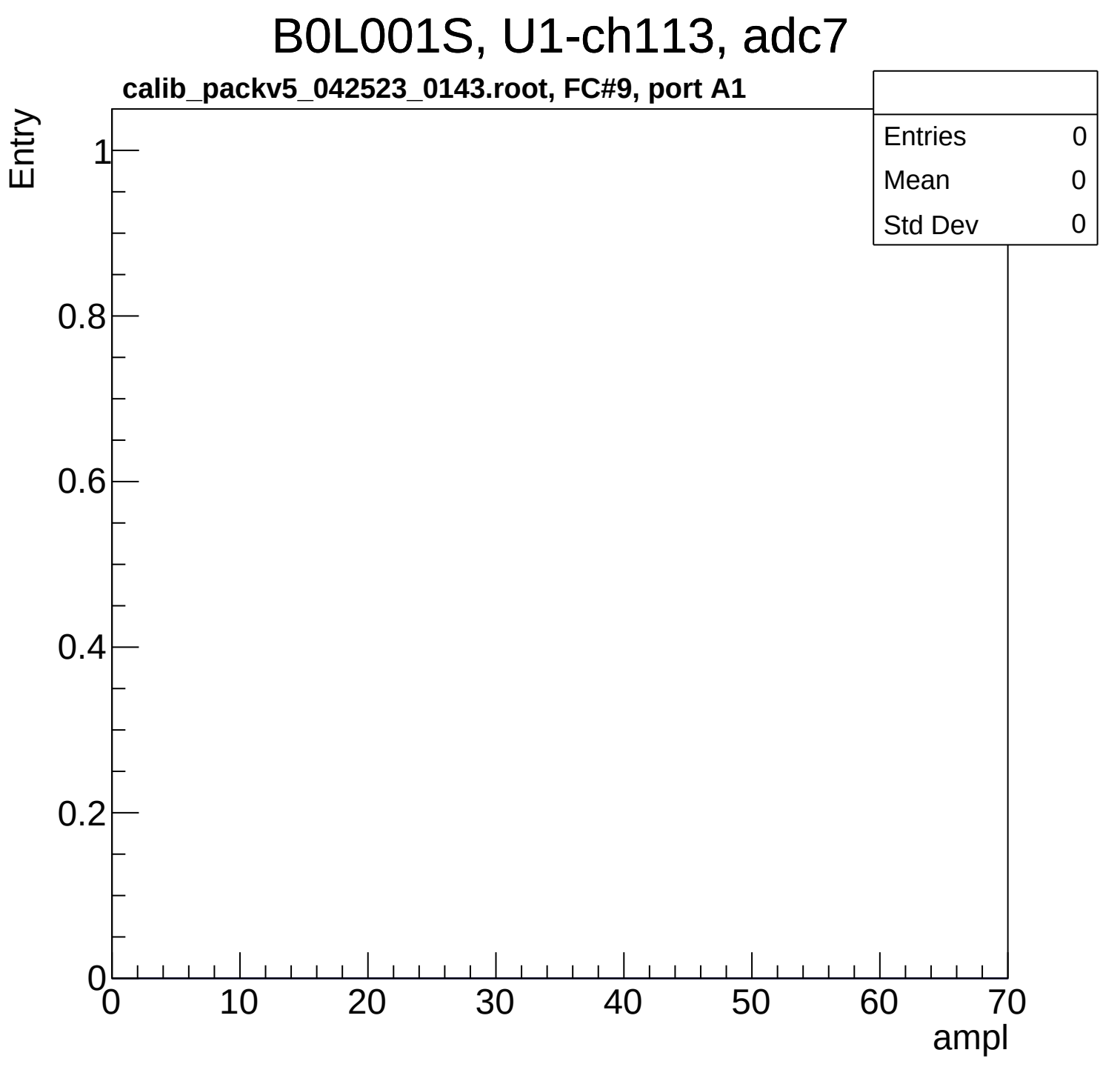

B0L001S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 57.47
Std Dev 7.648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 16
Mean 61.19
Std Dev 2.242
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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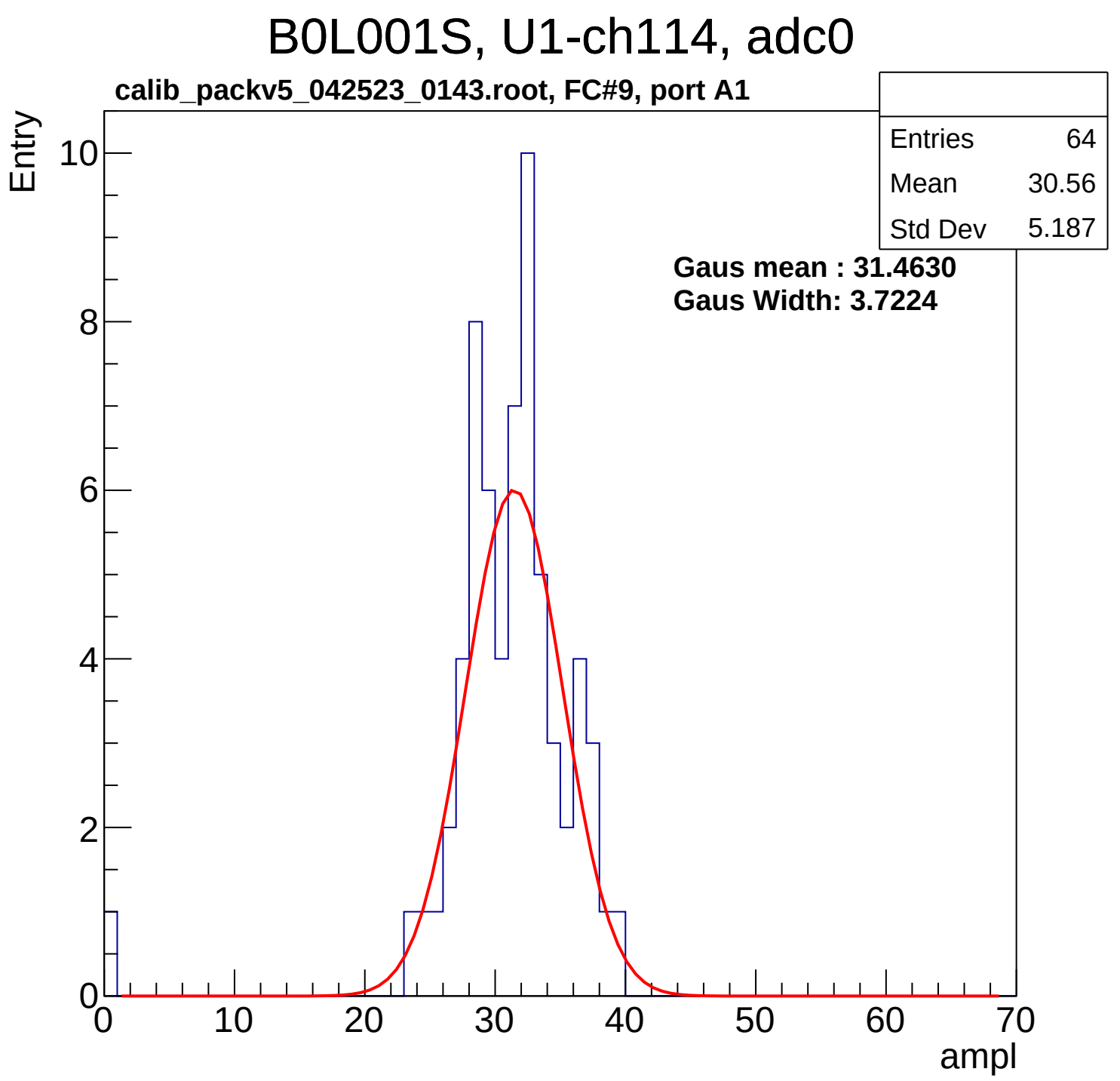
30
40
50
60
70
ampl


B0L001S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.56
Std Dev 5.187
Gaus mean : 31.4630
Gaus Width: 3.7224
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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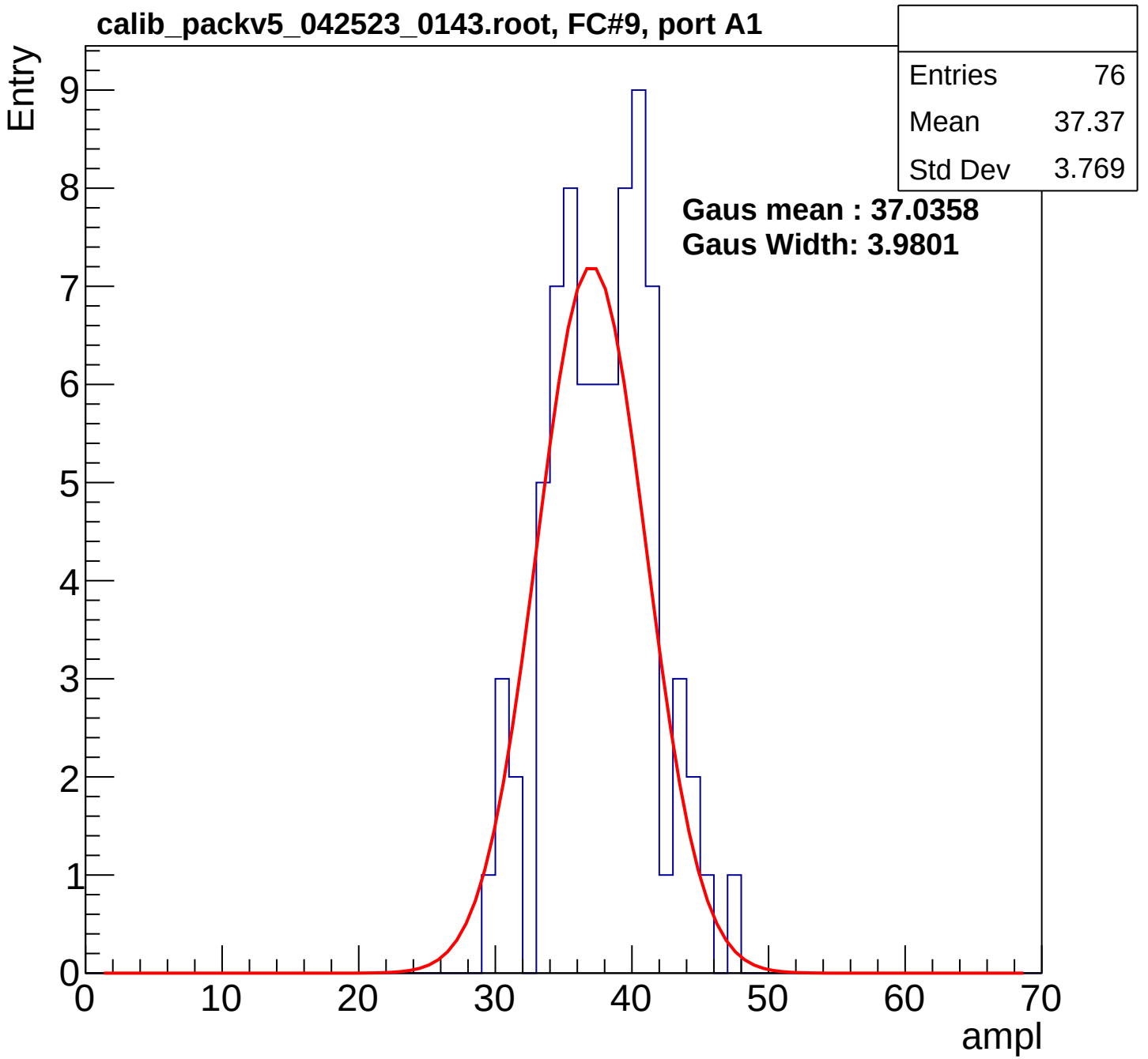
B0L001S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 76
Mean 37.37
Std Dev 3.769
Gaus mean : 37.0358
Gaus Width: 3.9801
Entry
ampl
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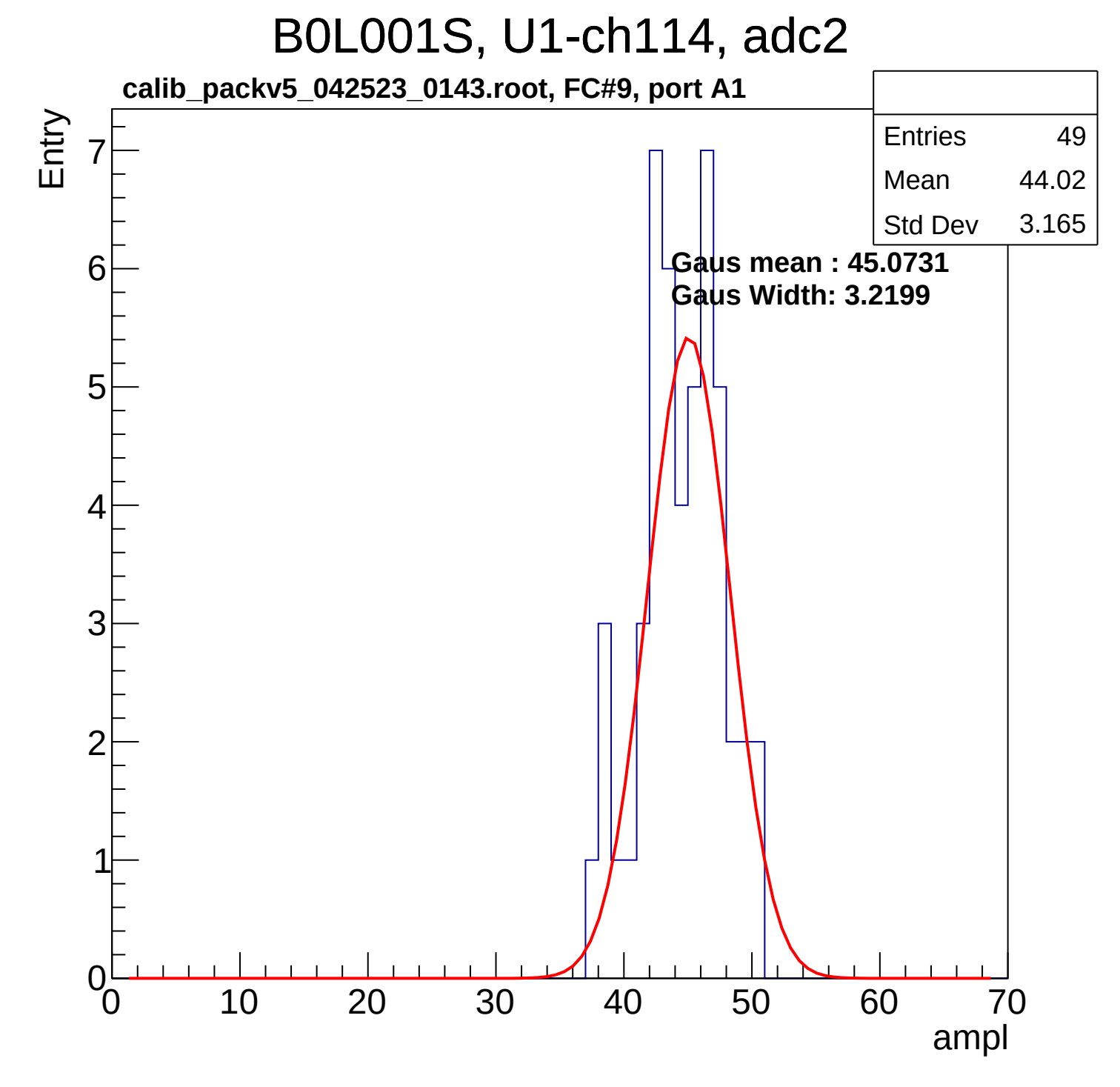
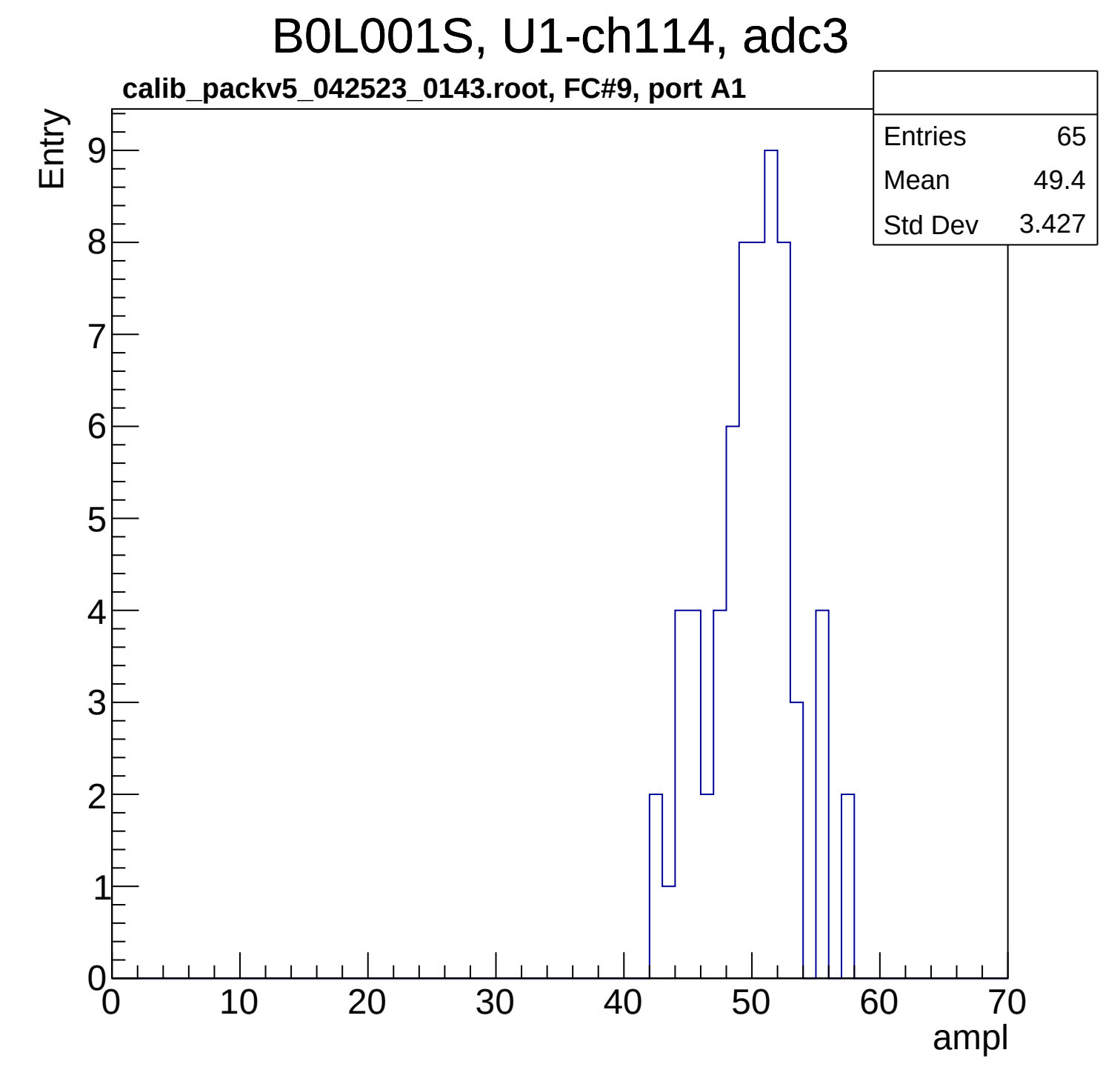
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 49
Mean 44.02
Std Dev 3.165
Gaus mean : 45.0731
Gaus Width: 3.2199
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 49.4
Std Dev 3.427
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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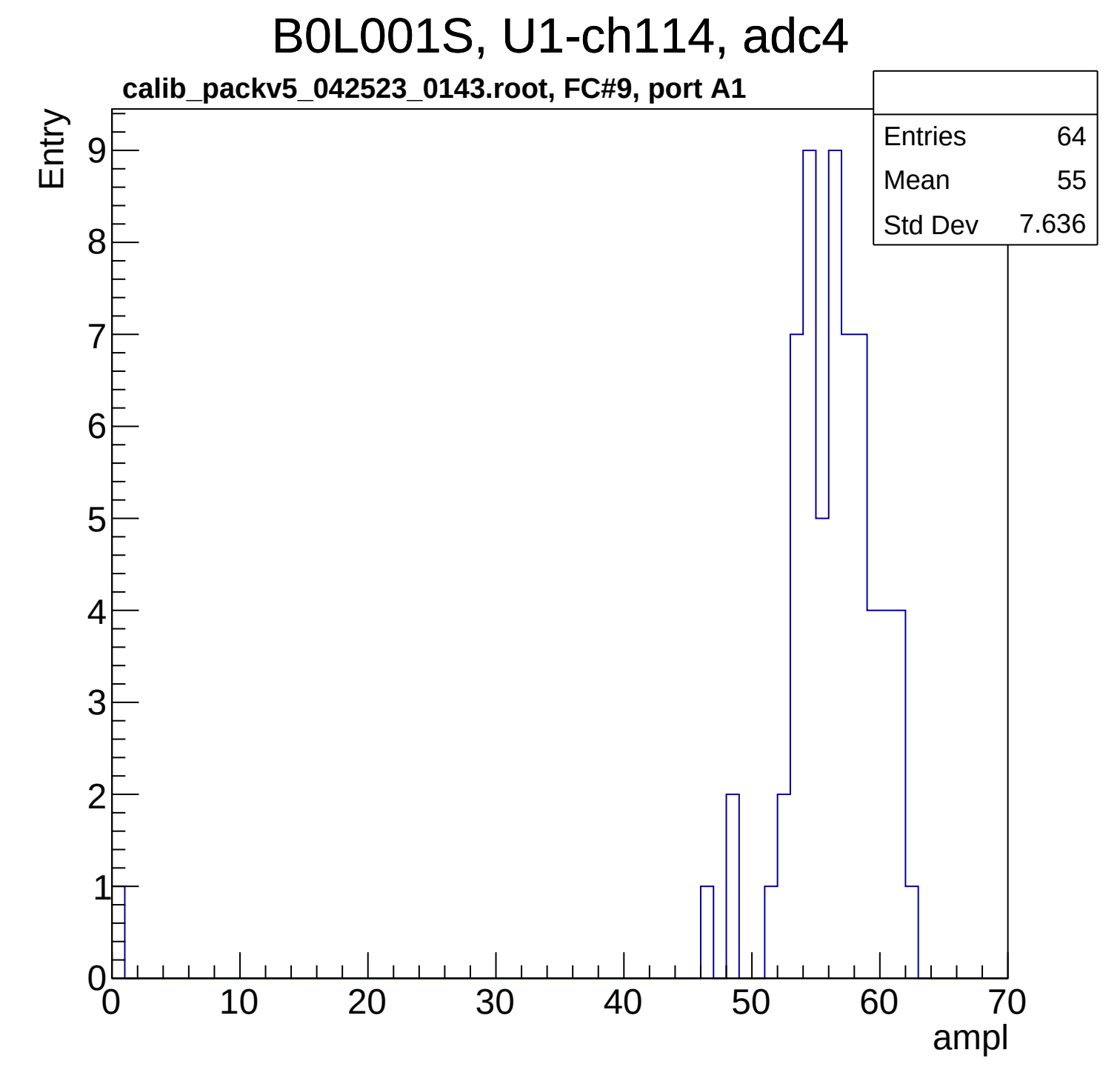
40
50
60
70


B0L001S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 55
Std Dev 7.636
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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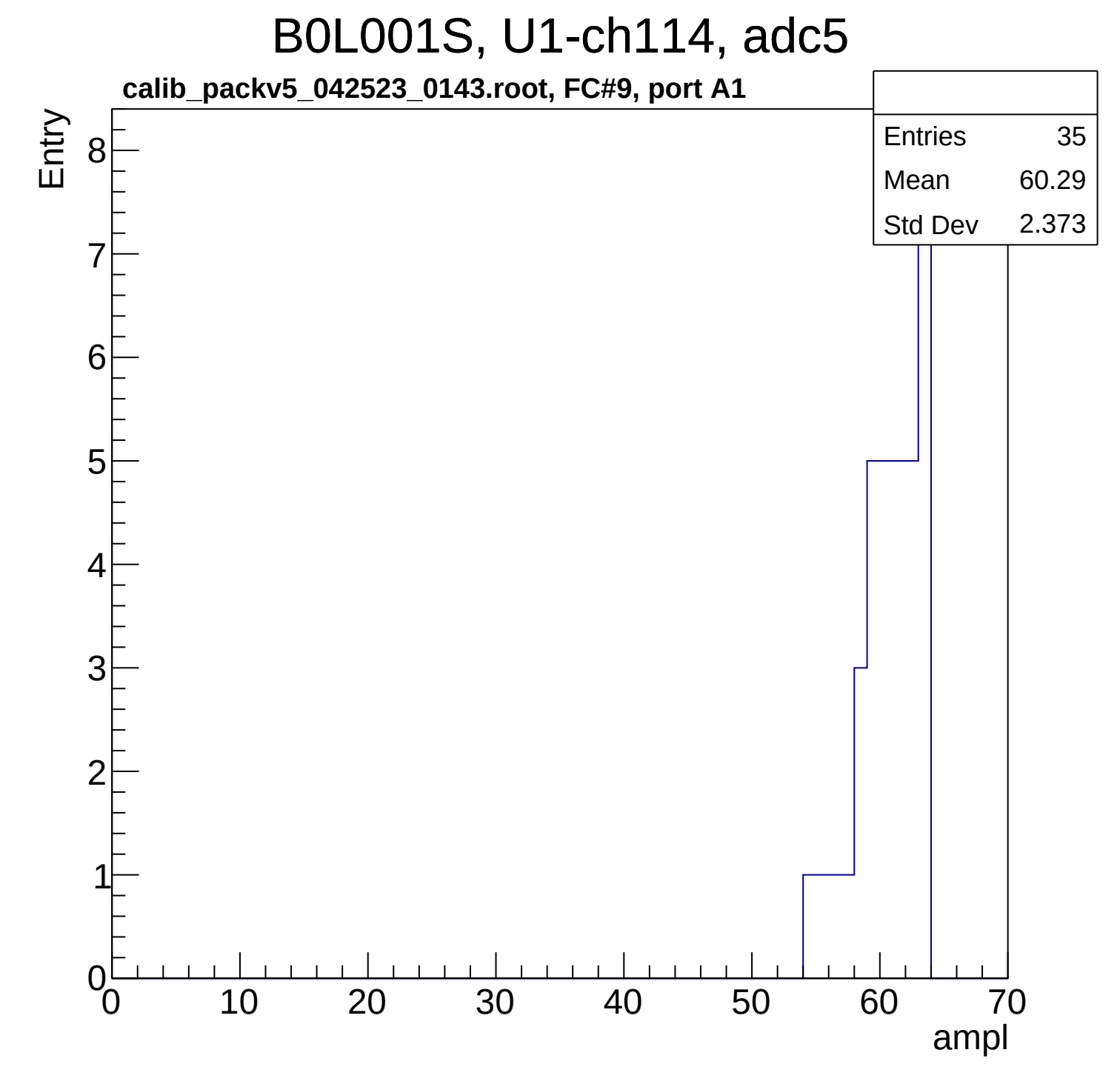
B0L001S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.29
Std Dev 2.373
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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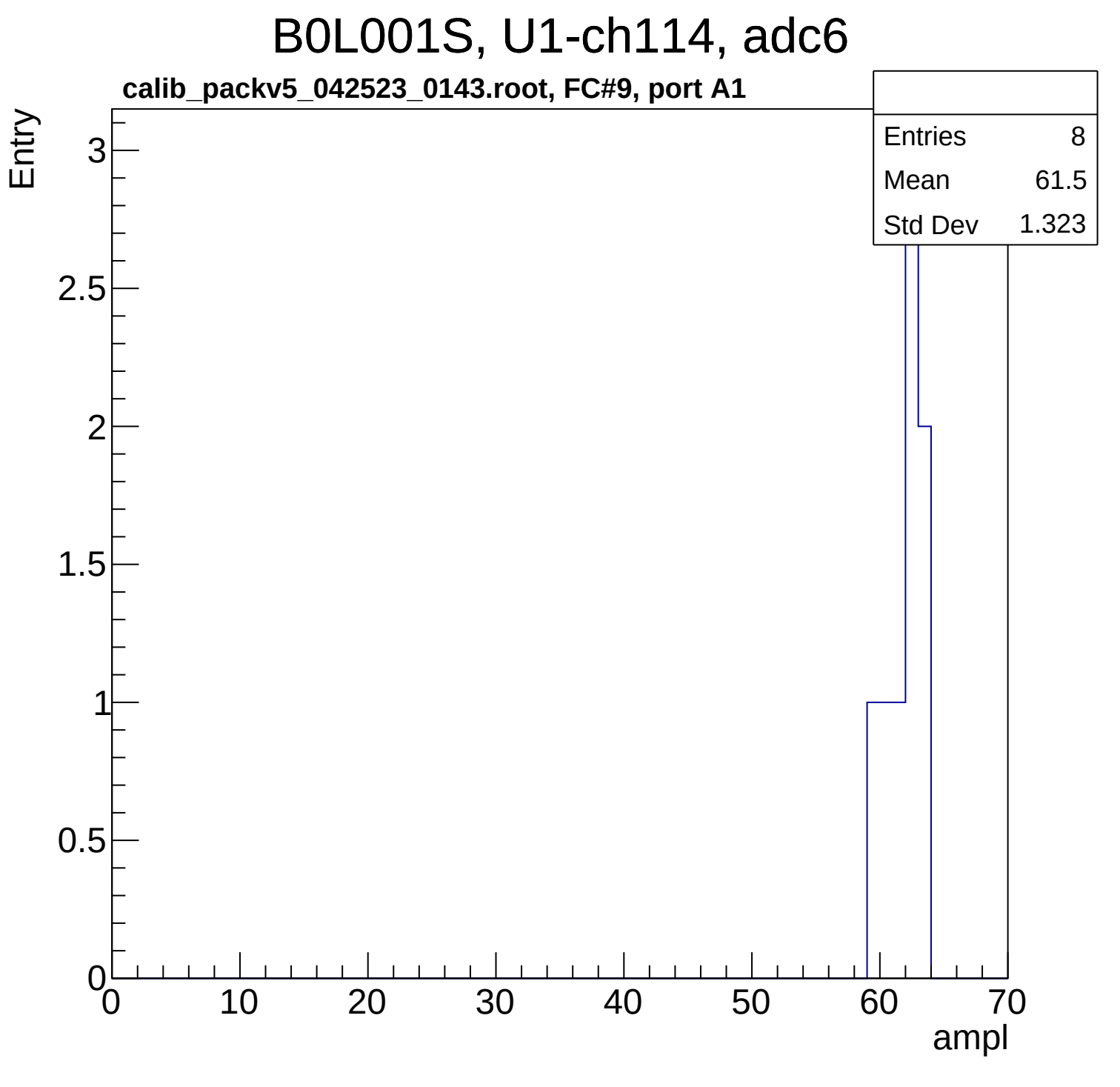
B0L001S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.5
Std Dev 1.323
Entry
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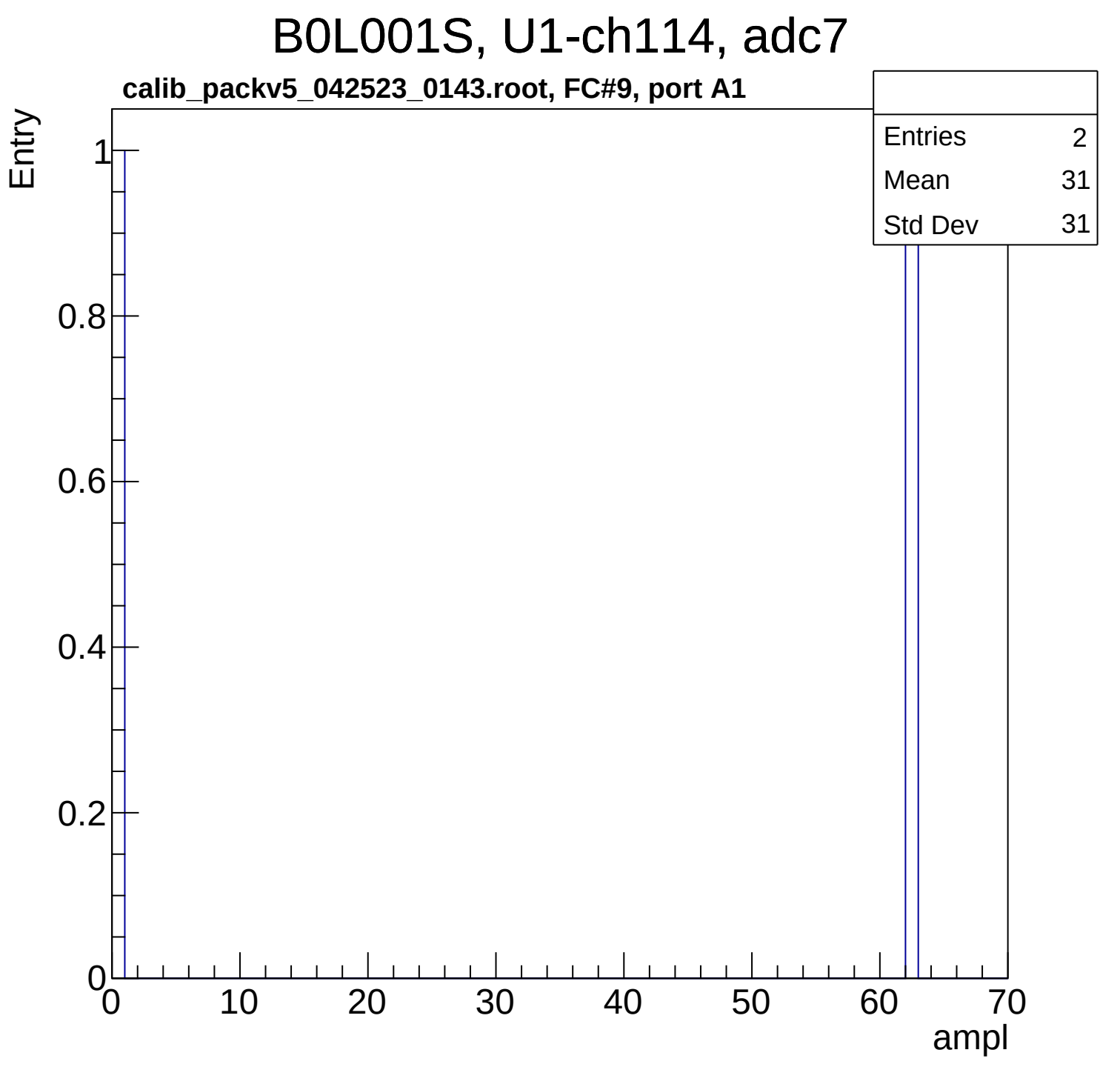
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
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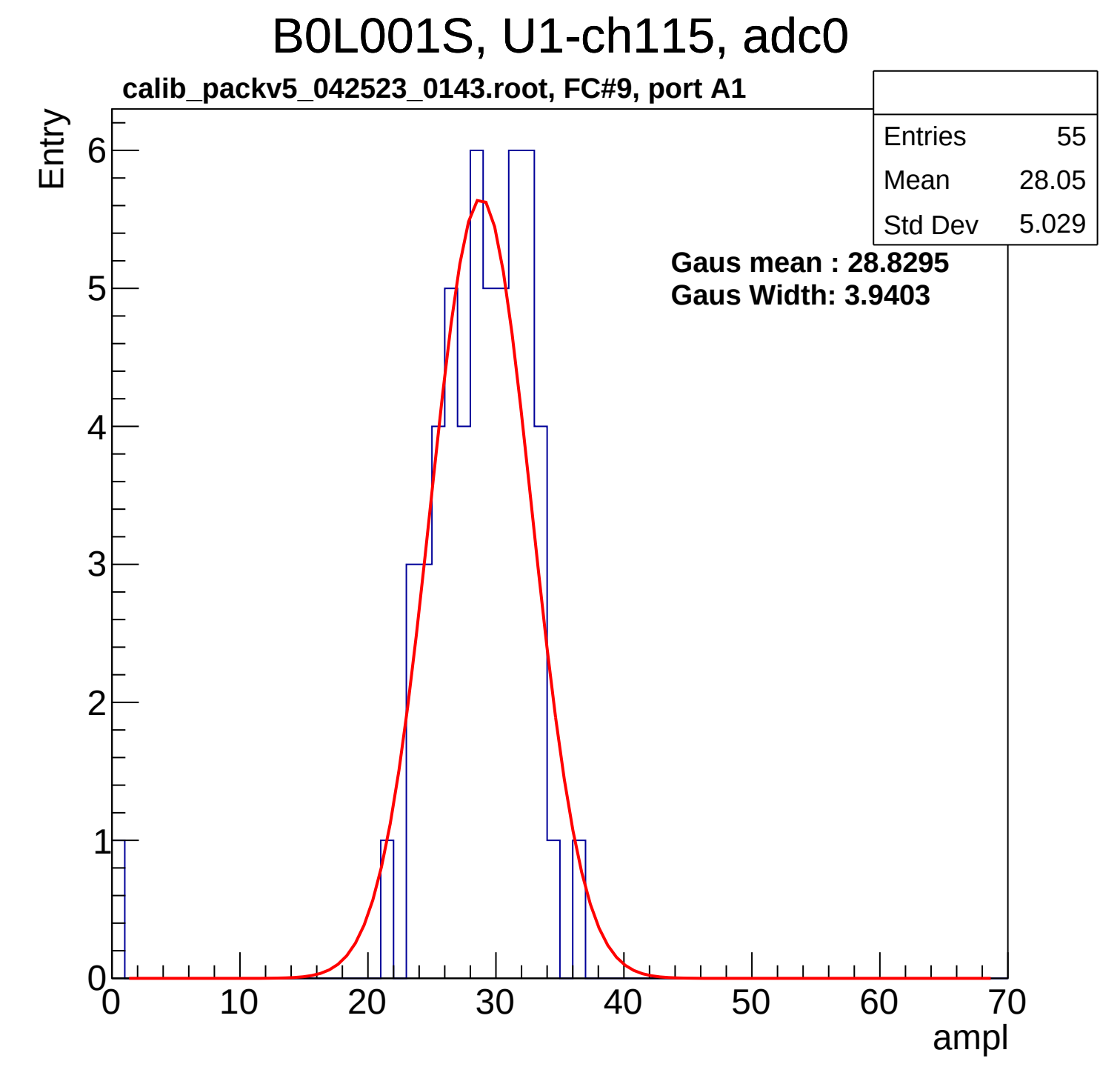
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 28.05
Std Dev 5.029
Gaus mean : 28.8295
Gaus Width: 3.9403
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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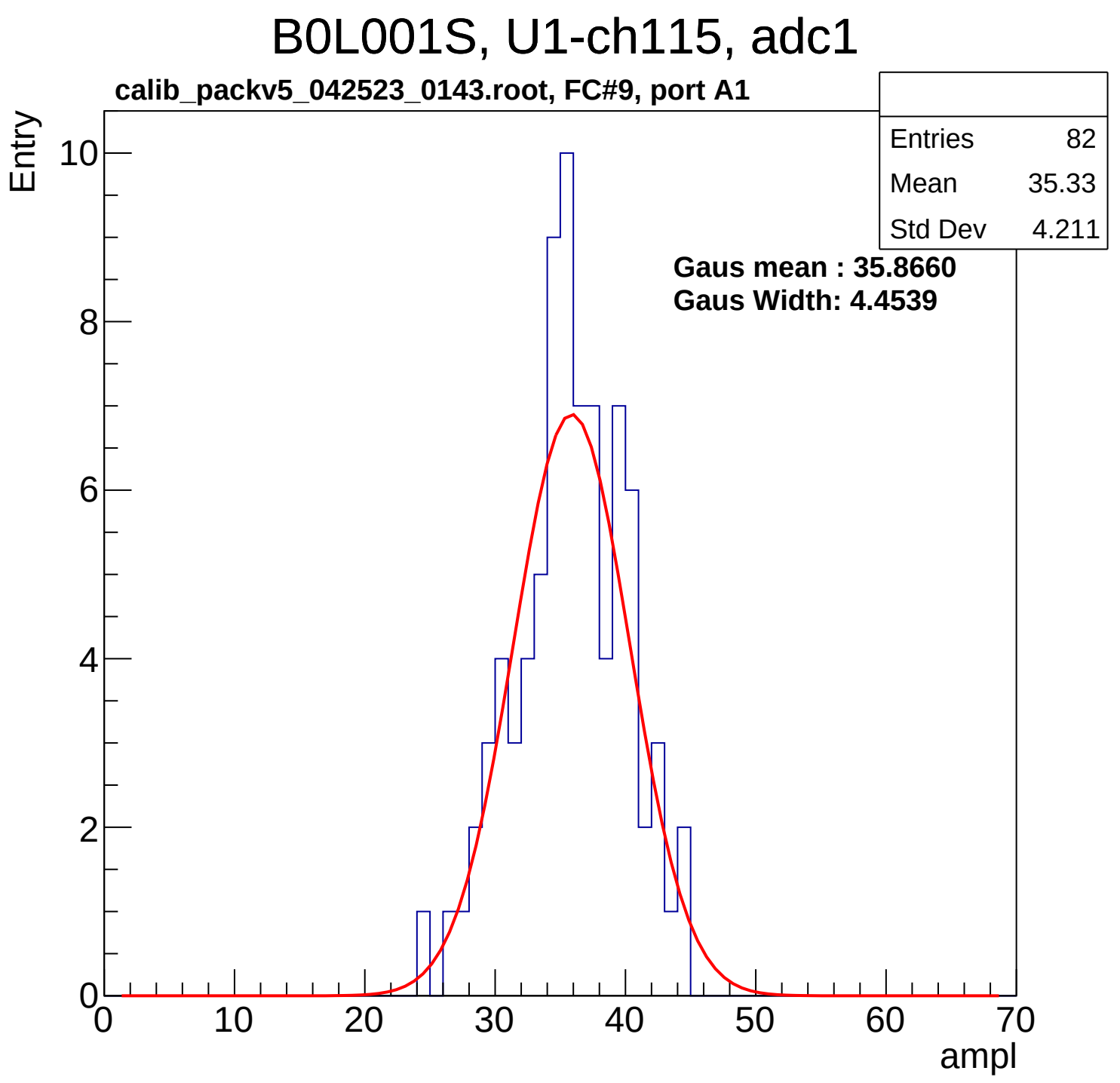
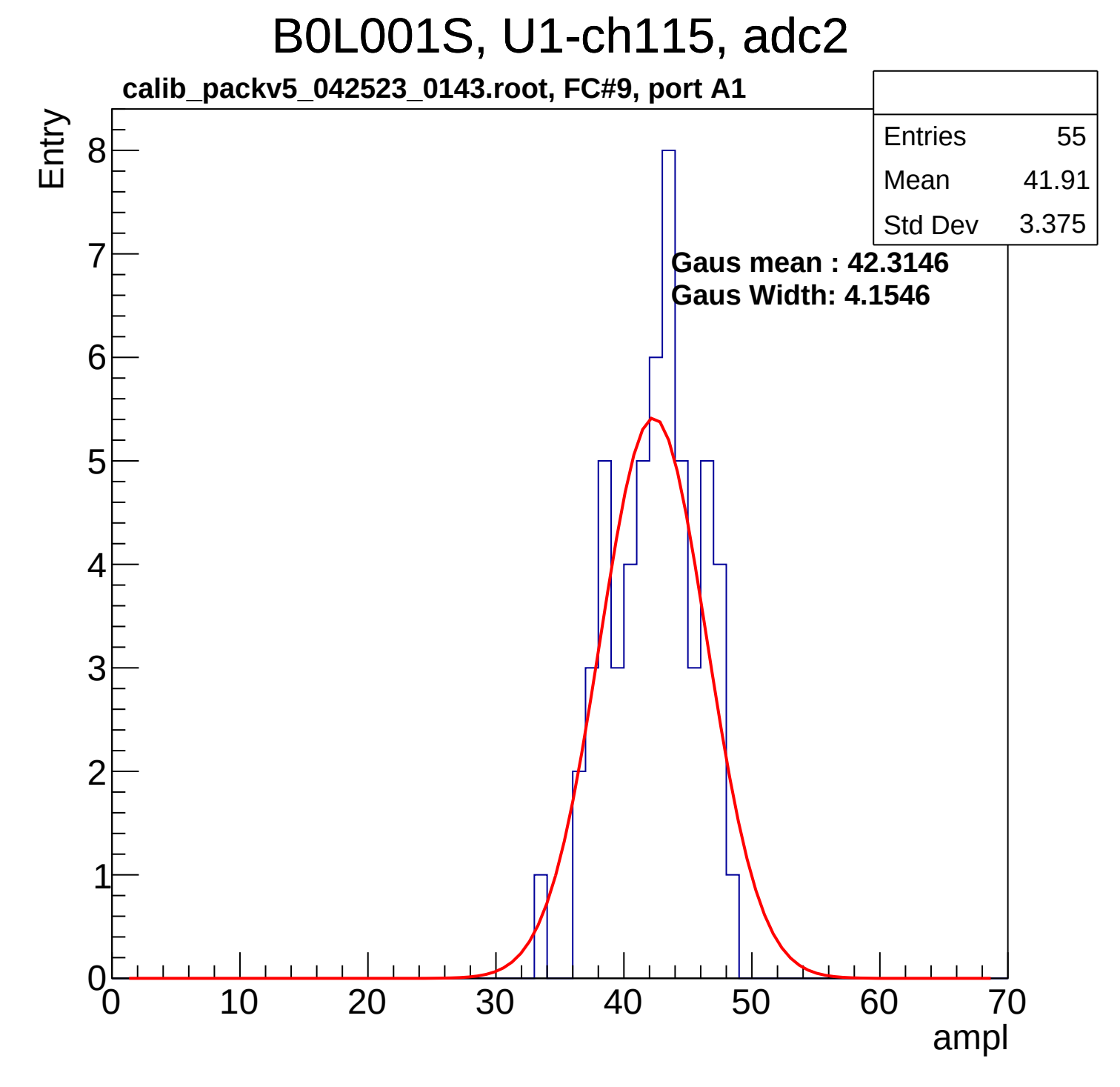
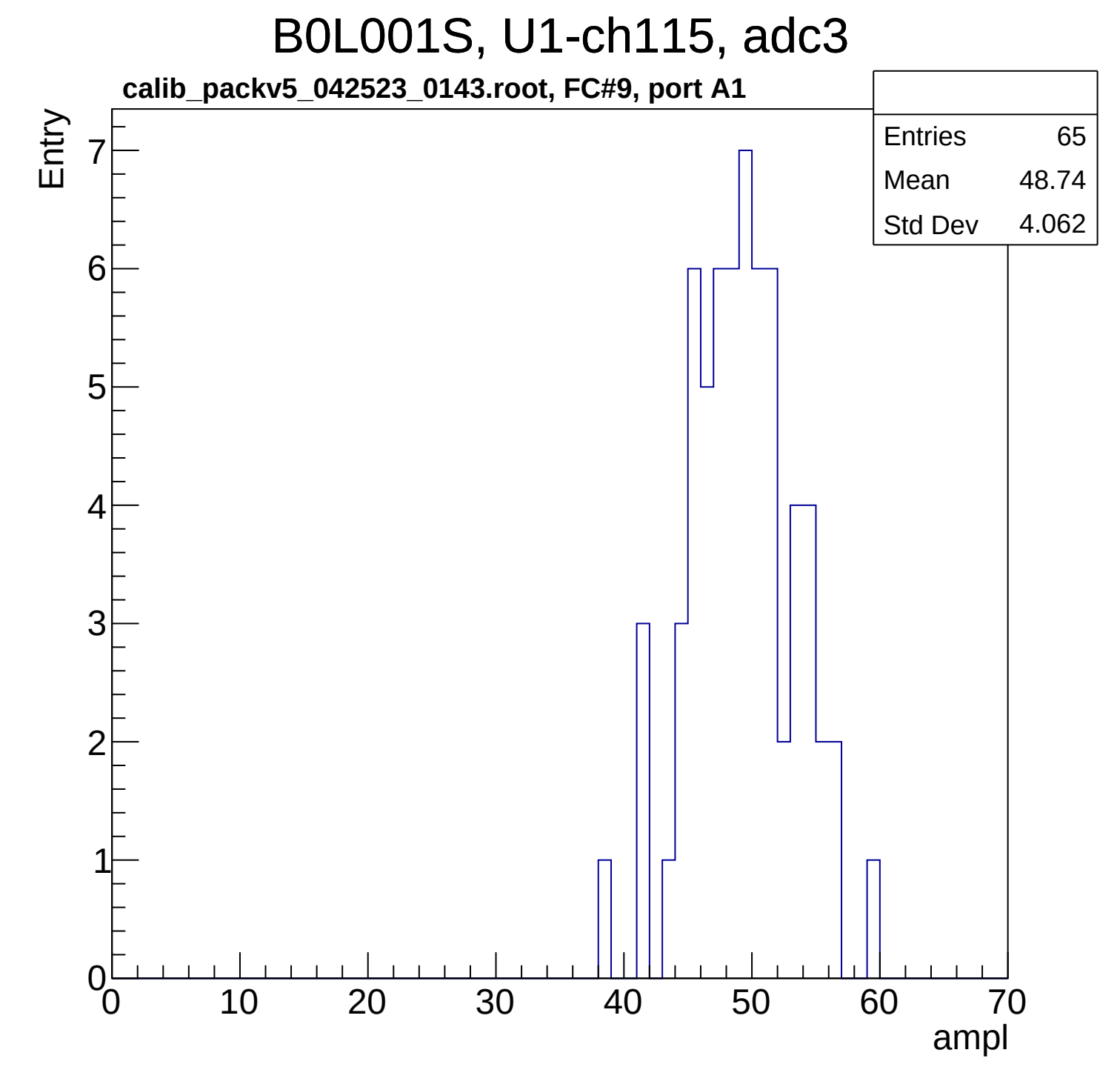

B0L001S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 82
Mean 35.33
Std Dev 4.211
Gaus mean : 35.8660
Gaus Width: 4.4539
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 41.91
Std Dev 3.375
Gaus mean : 42.3146
Gaus Width: 4.1546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 48.74
Std Dev 4.062
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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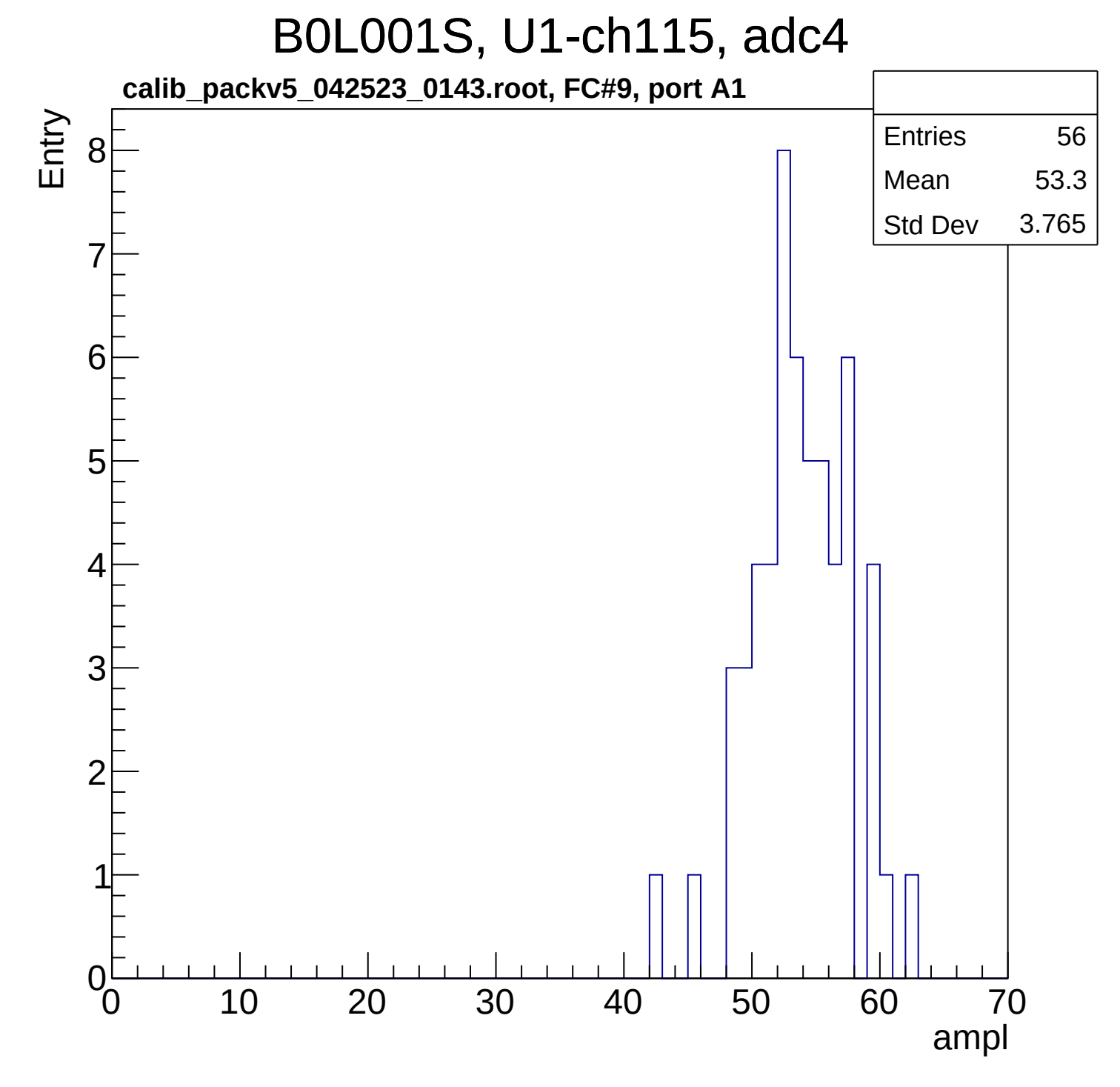
B0L001S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 53.3
Std Dev 3.765
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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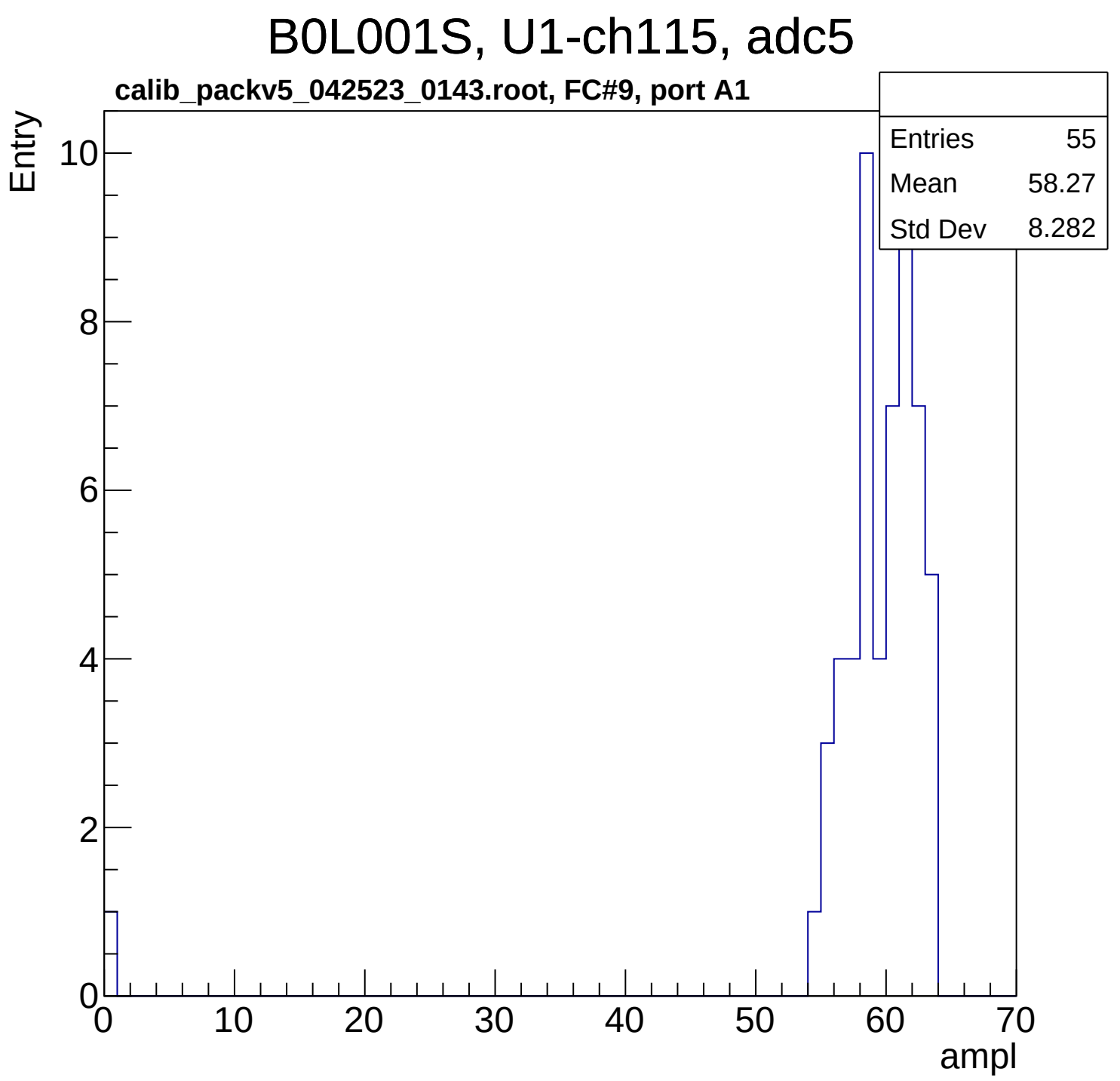
B0L001S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 58.27
Std Dev 8.282
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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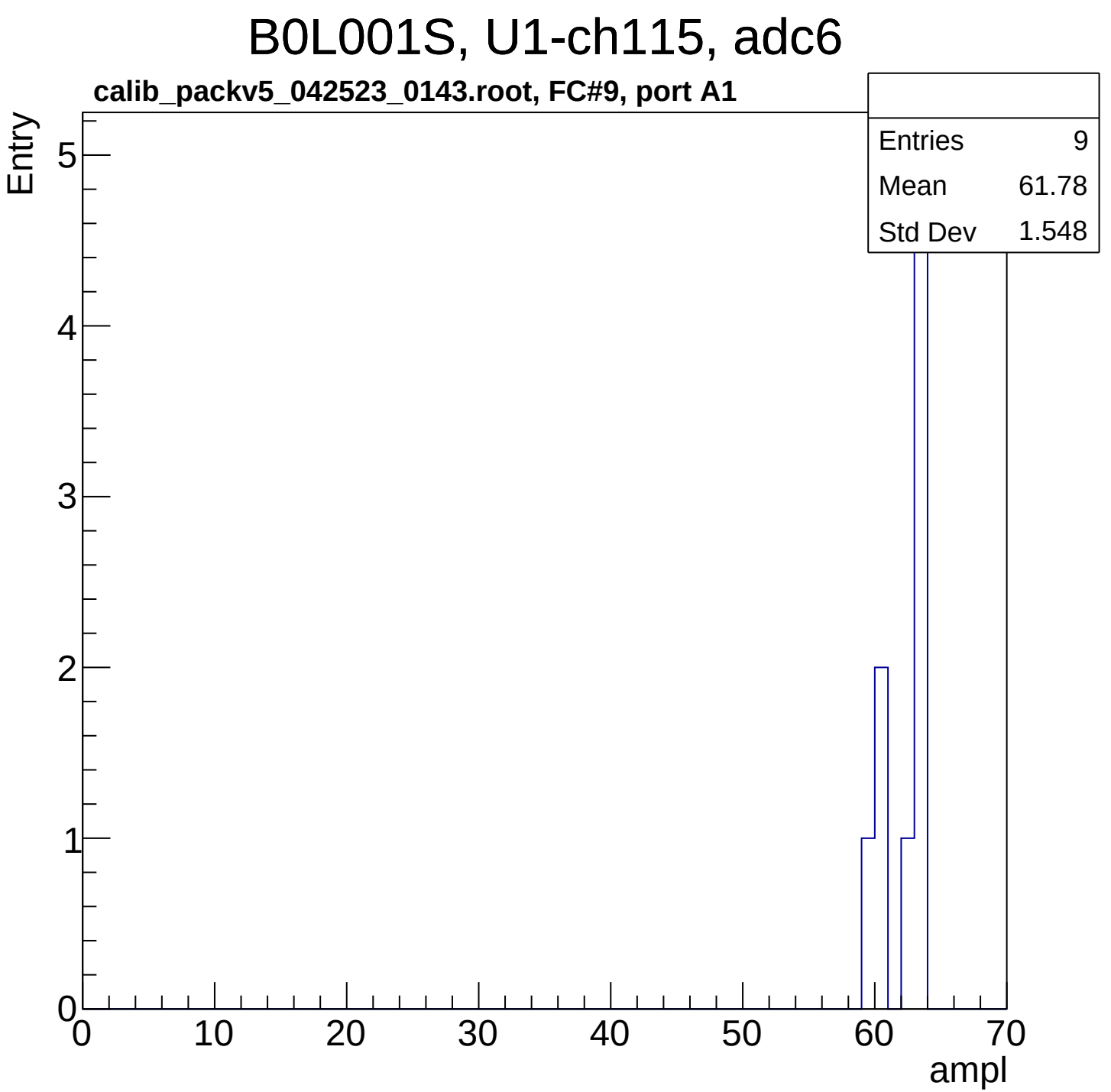
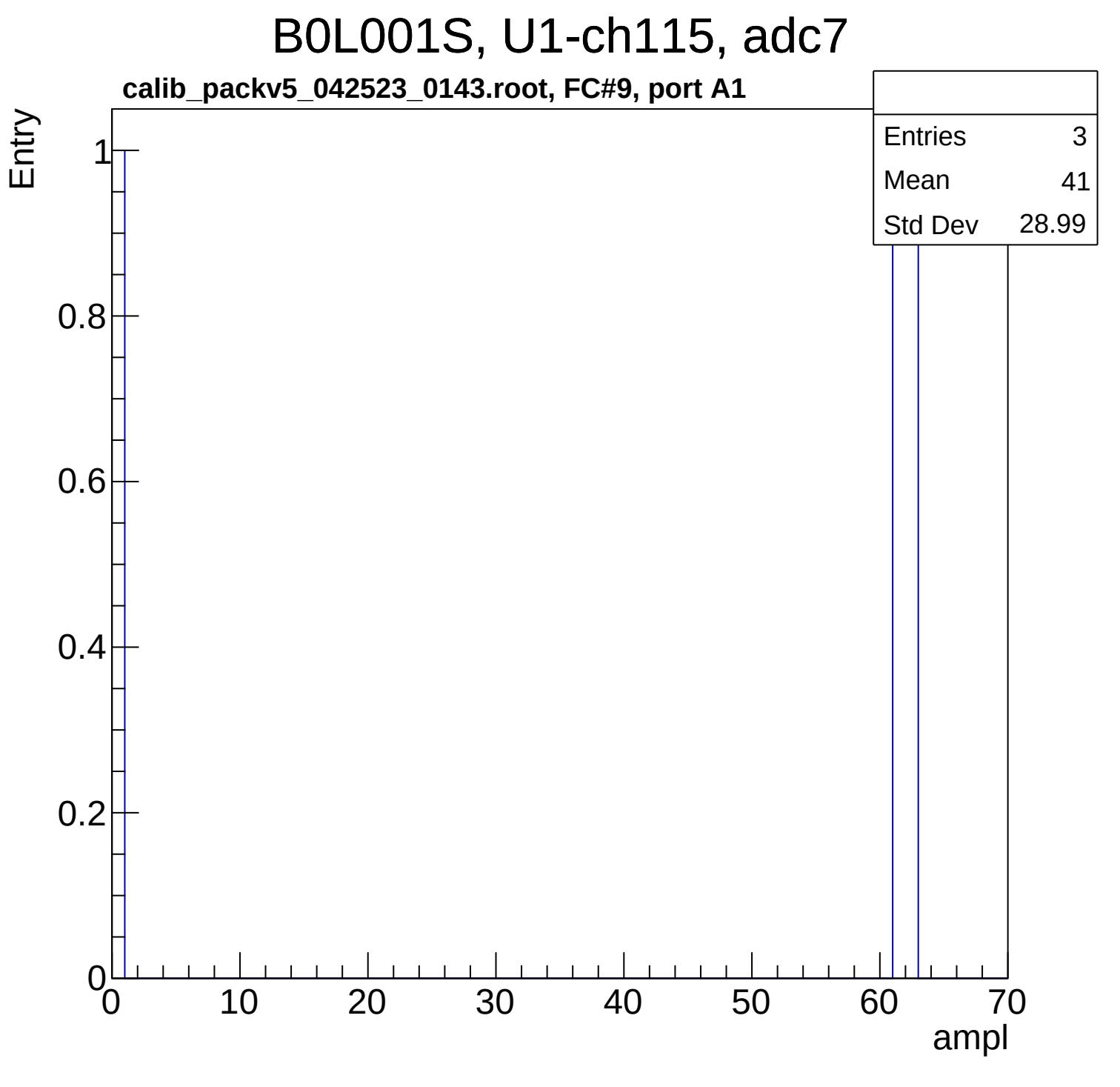

B0L001S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.78
Std Dev 1.548
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 3
Mean 41
Std Dev 28.99
Entry
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0.8
0.6
0.4
0.2
0
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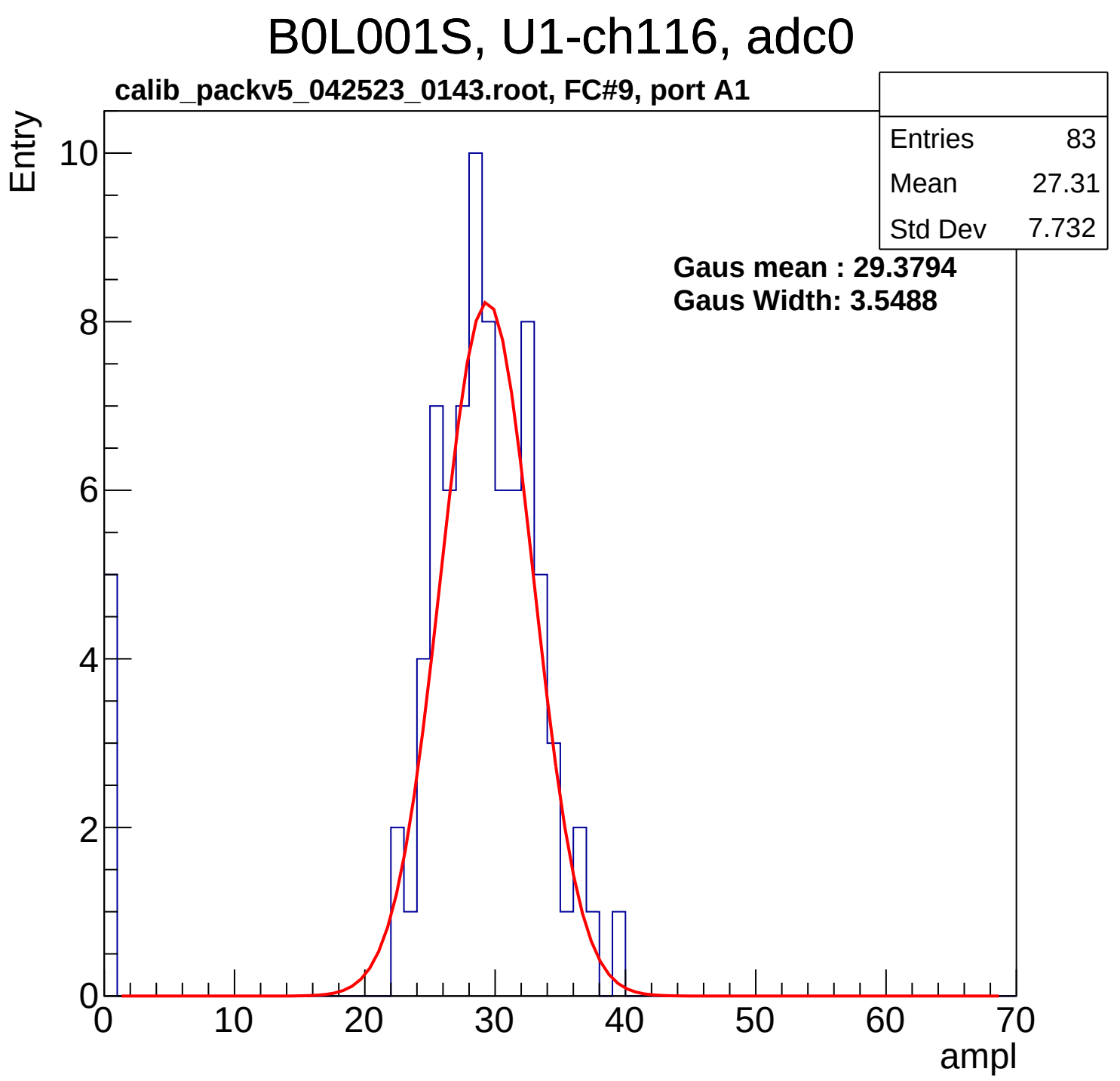
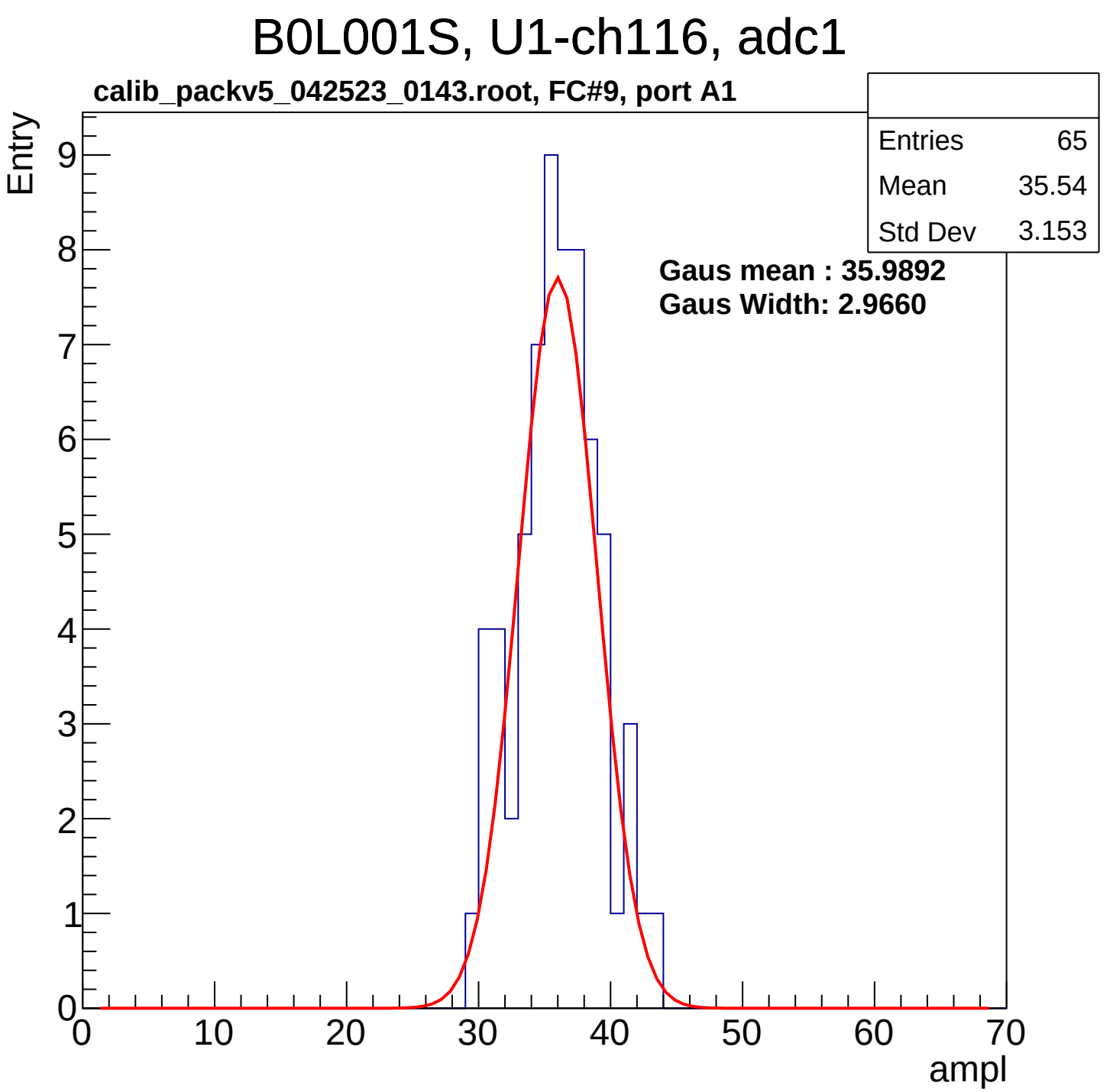
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 83
Mean 27.31
Std Dev 7.732
Gaus mean : 29.3794
Gaus Width: 3.5488
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 35.54
Std Dev 3.153
Gaus mean : 35.9892
Gaus Width: 2.9660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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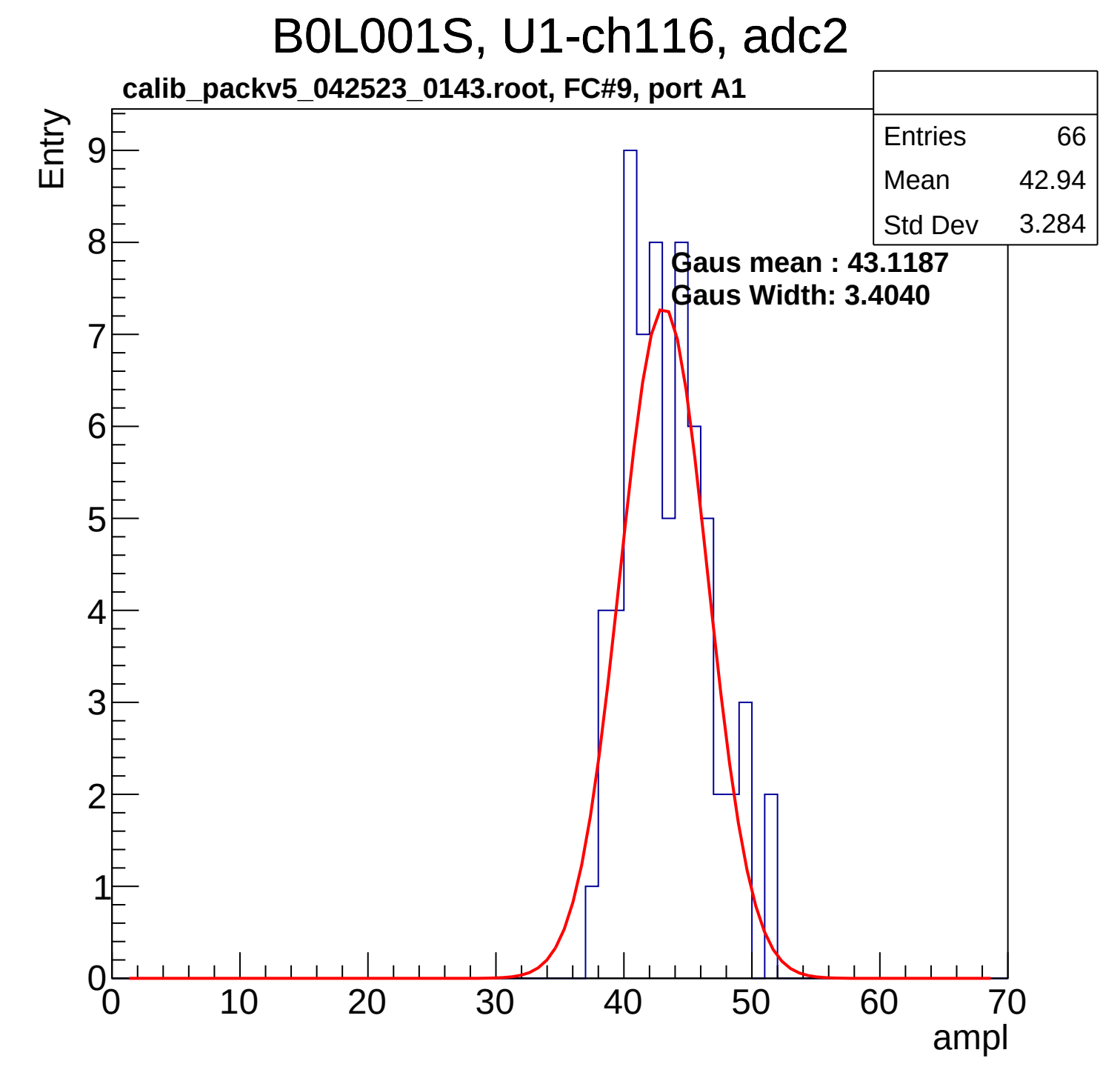
B0L001S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 42.94
Std Dev 3.284
Gaus mean : 43.1187
Gaus Width: 3.4040
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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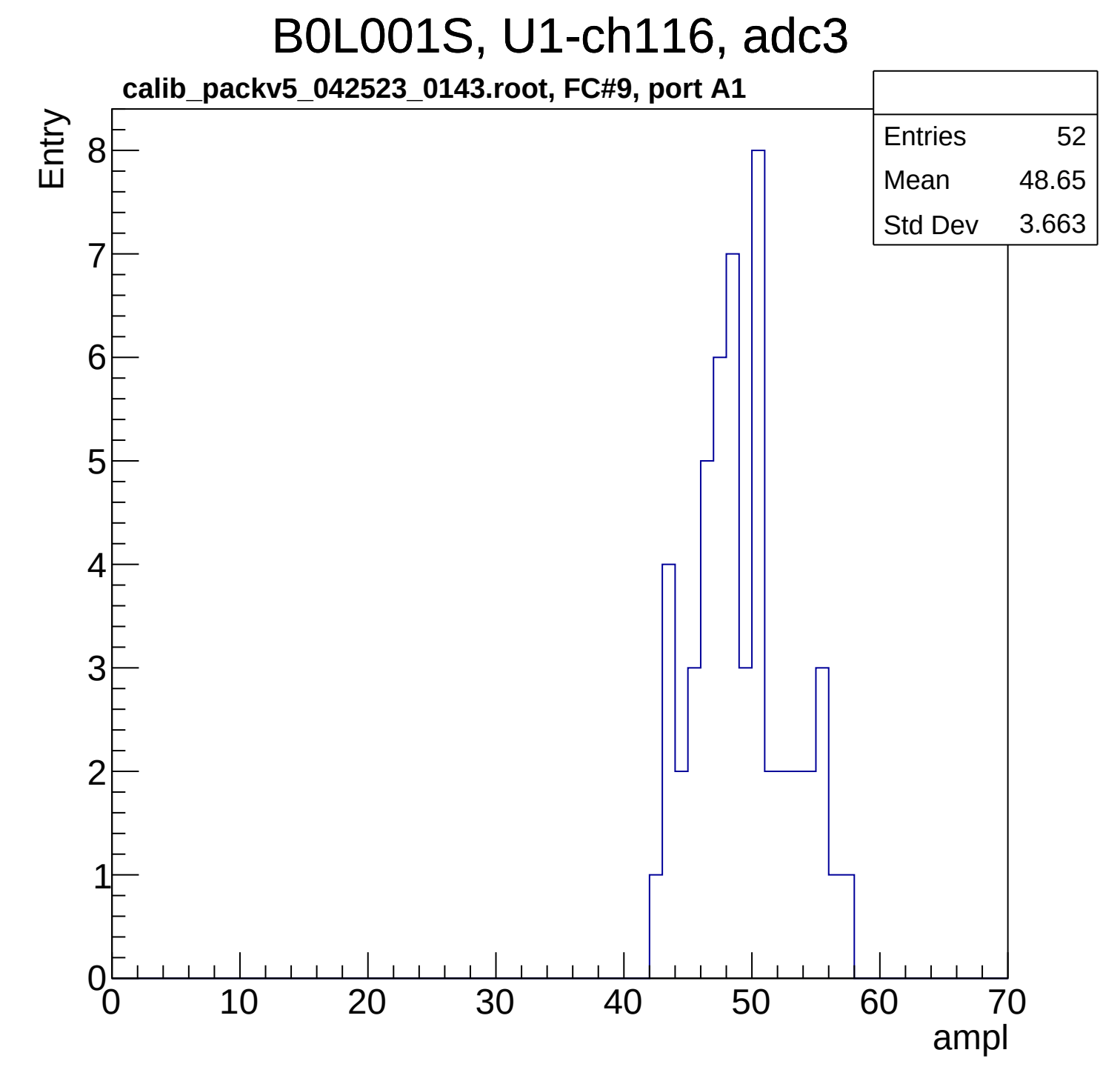
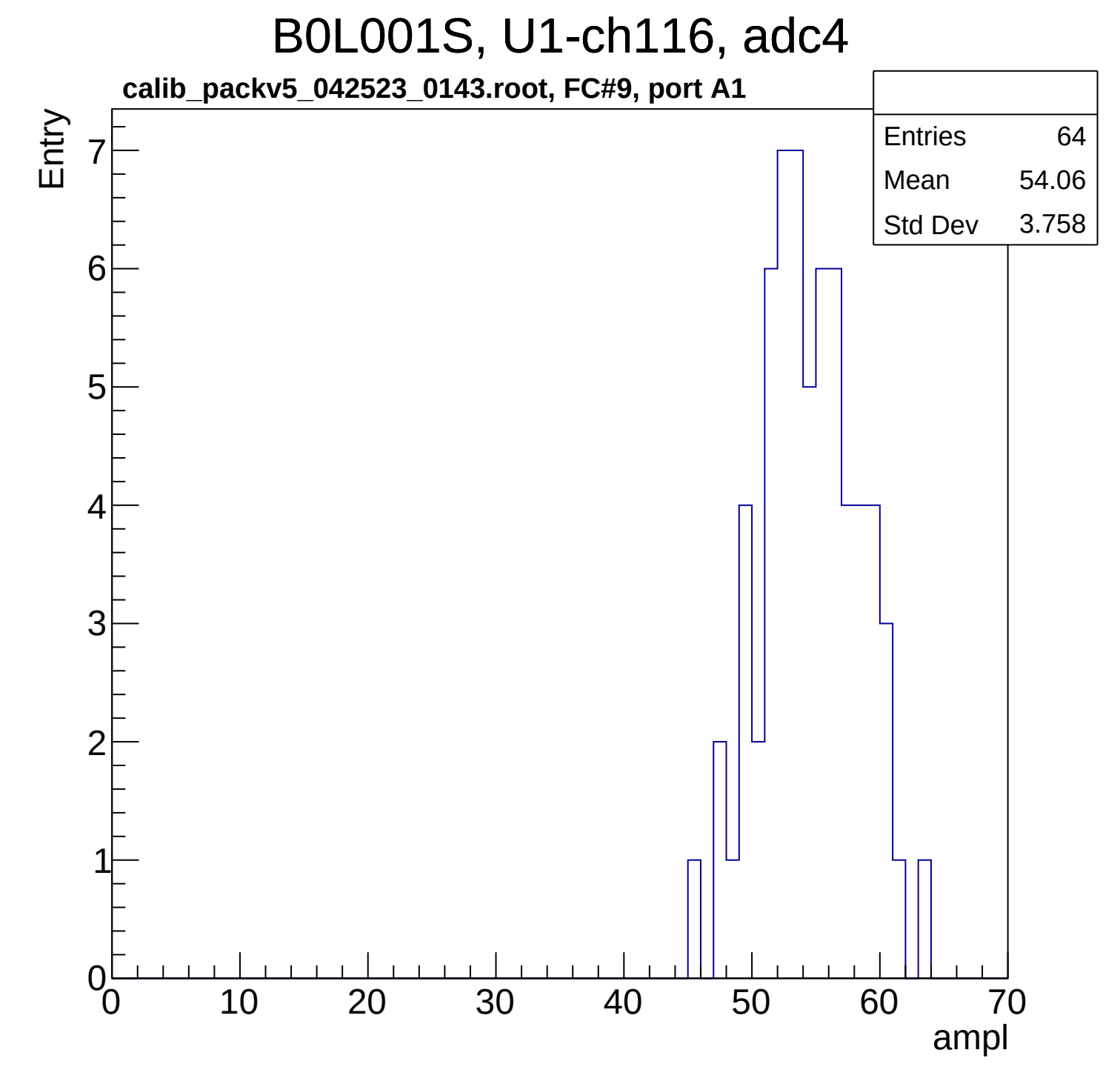
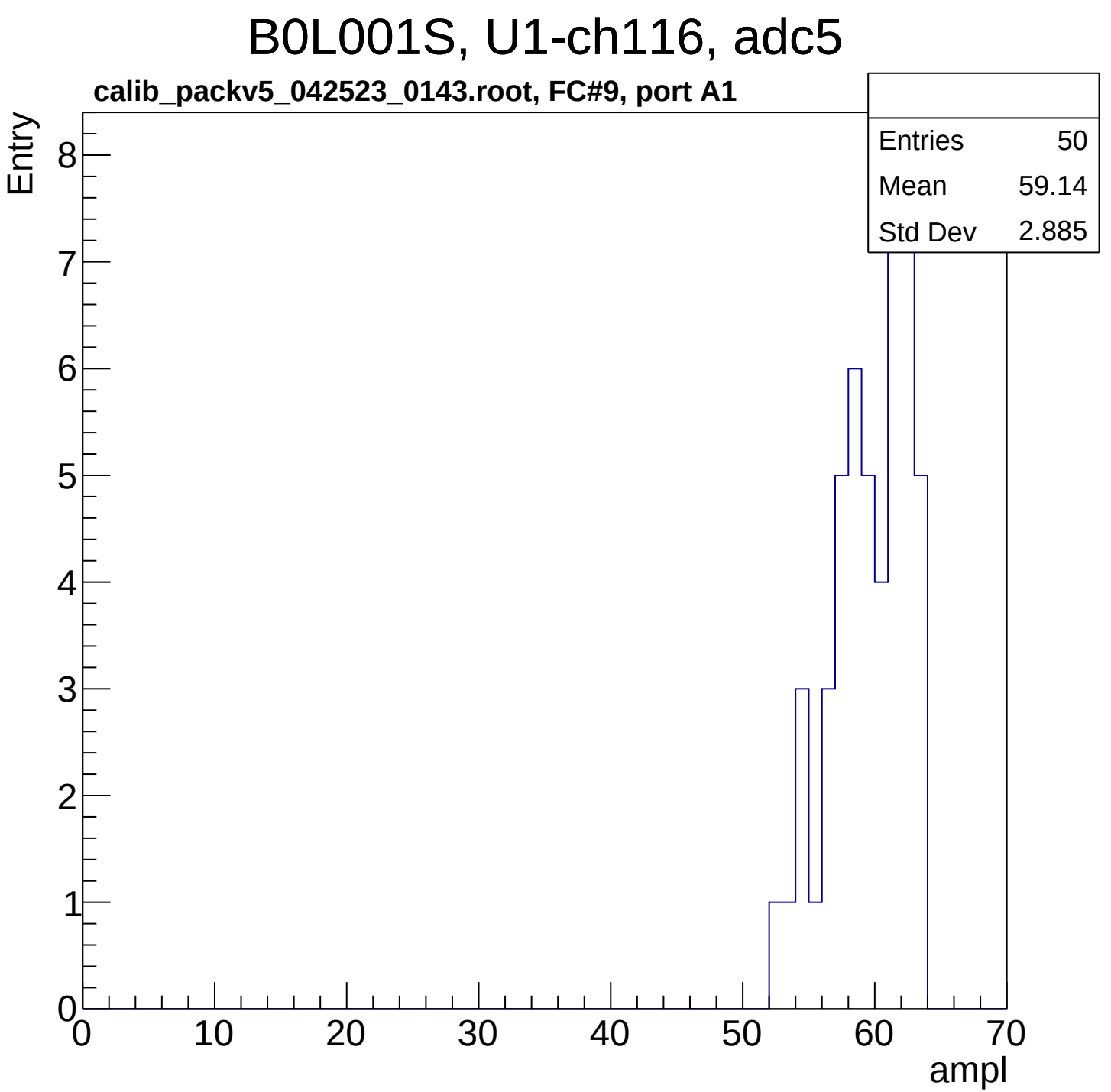
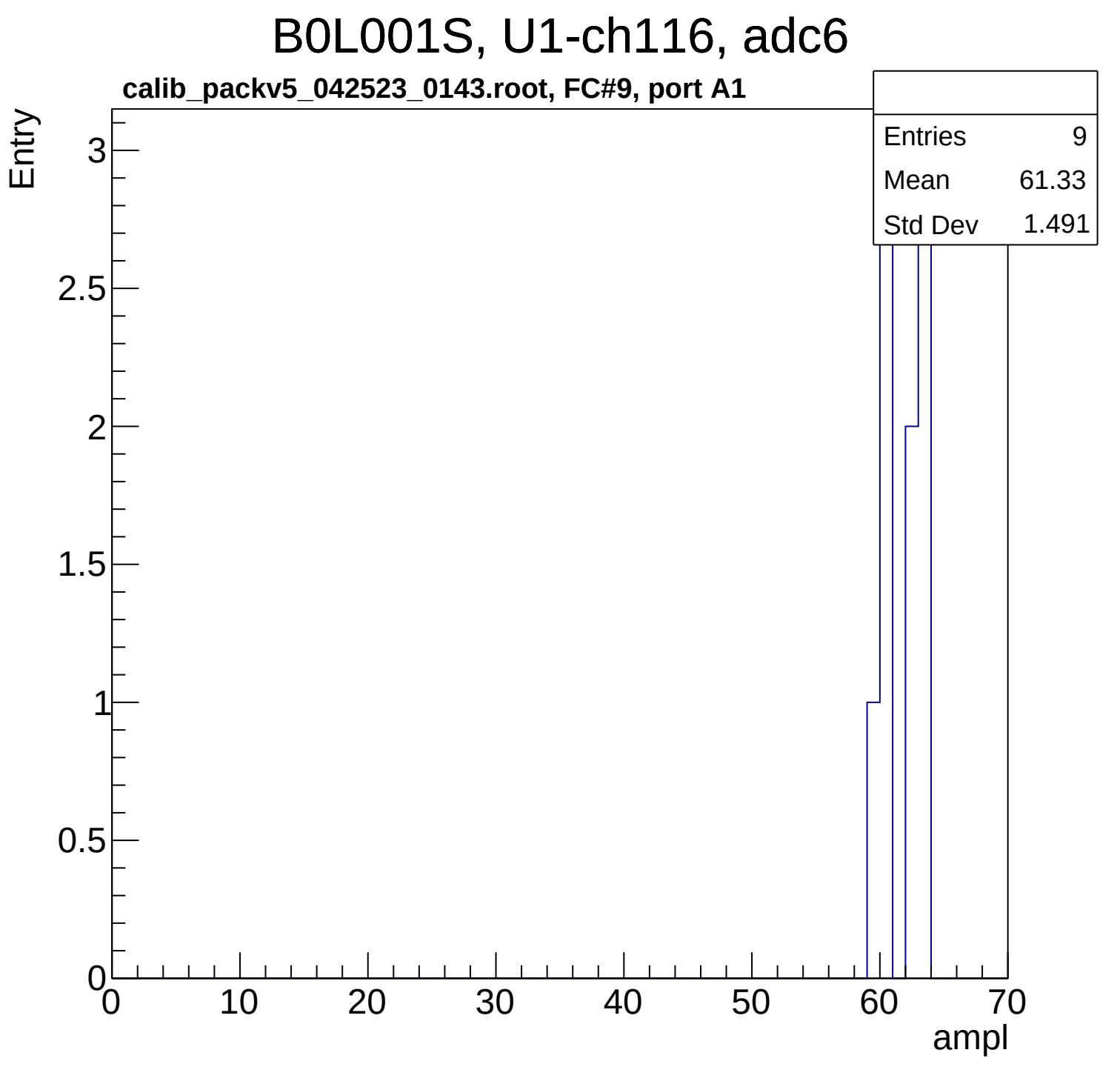

B0L001S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 48.65
Std Dev 3.663
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 54.06
Std Dev 3.758
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 50
Mean 59.14
Std Dev 2.885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 9
Mean 61.33
Std Dev 1.491
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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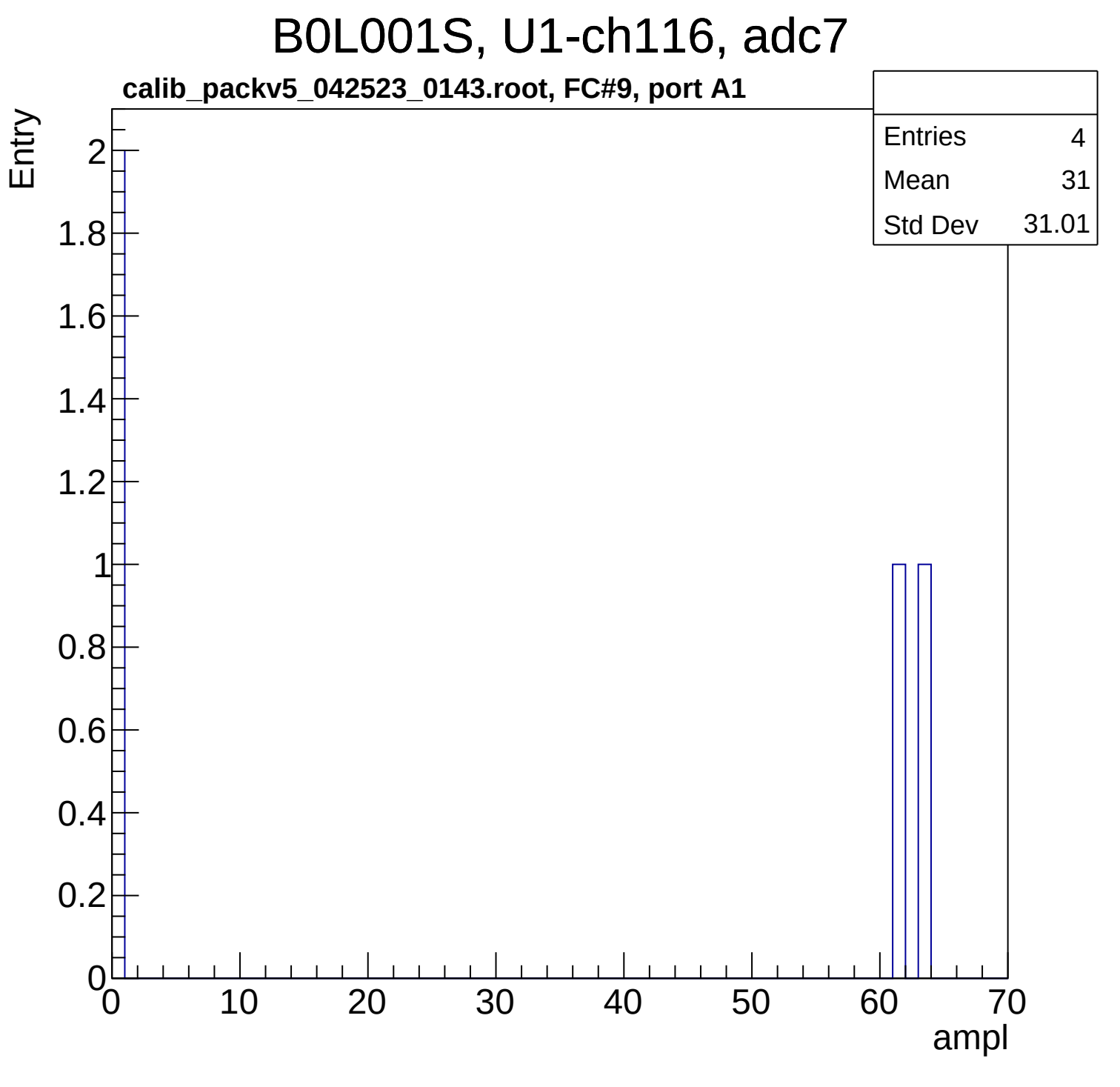
B0L001S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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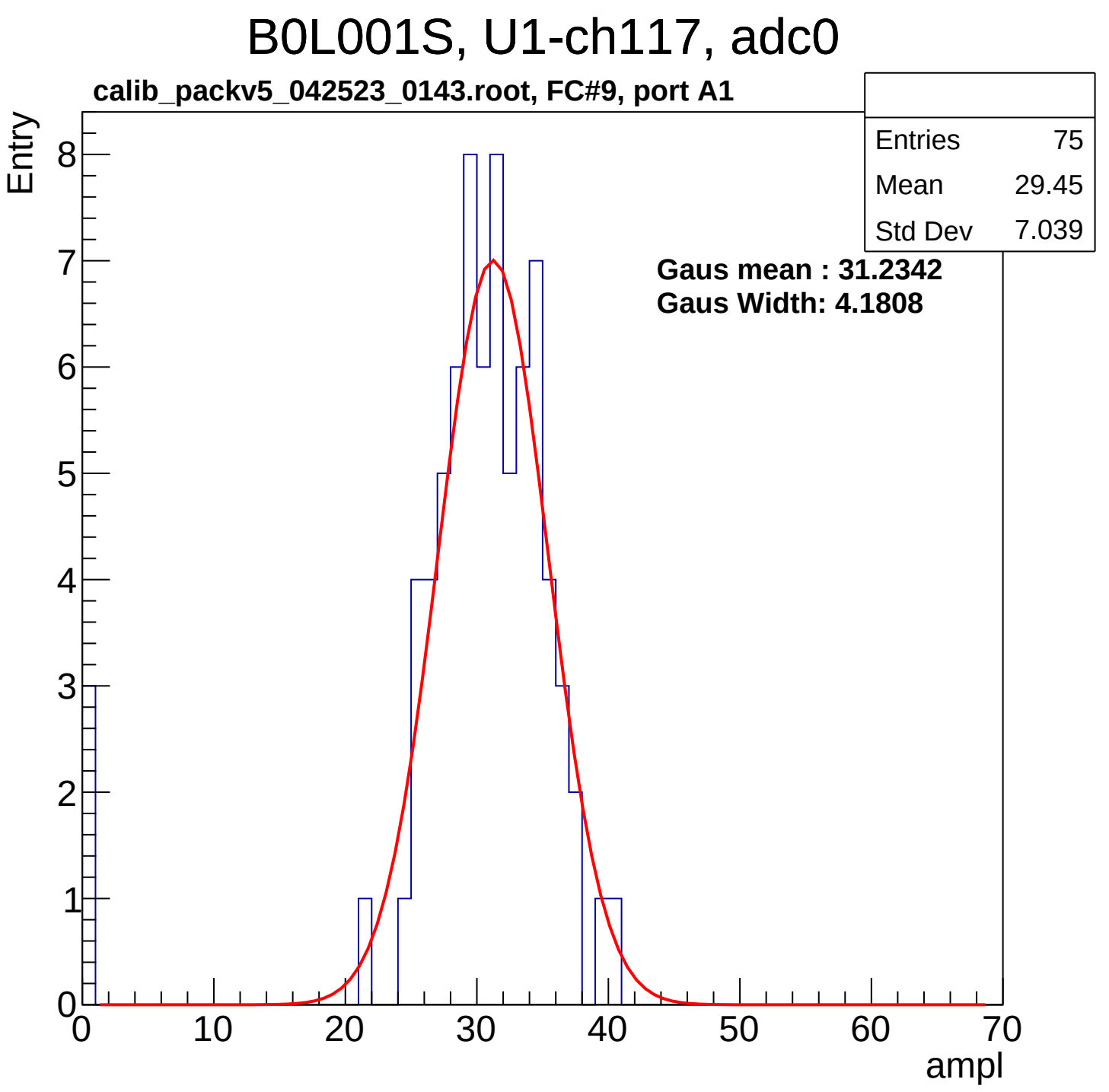
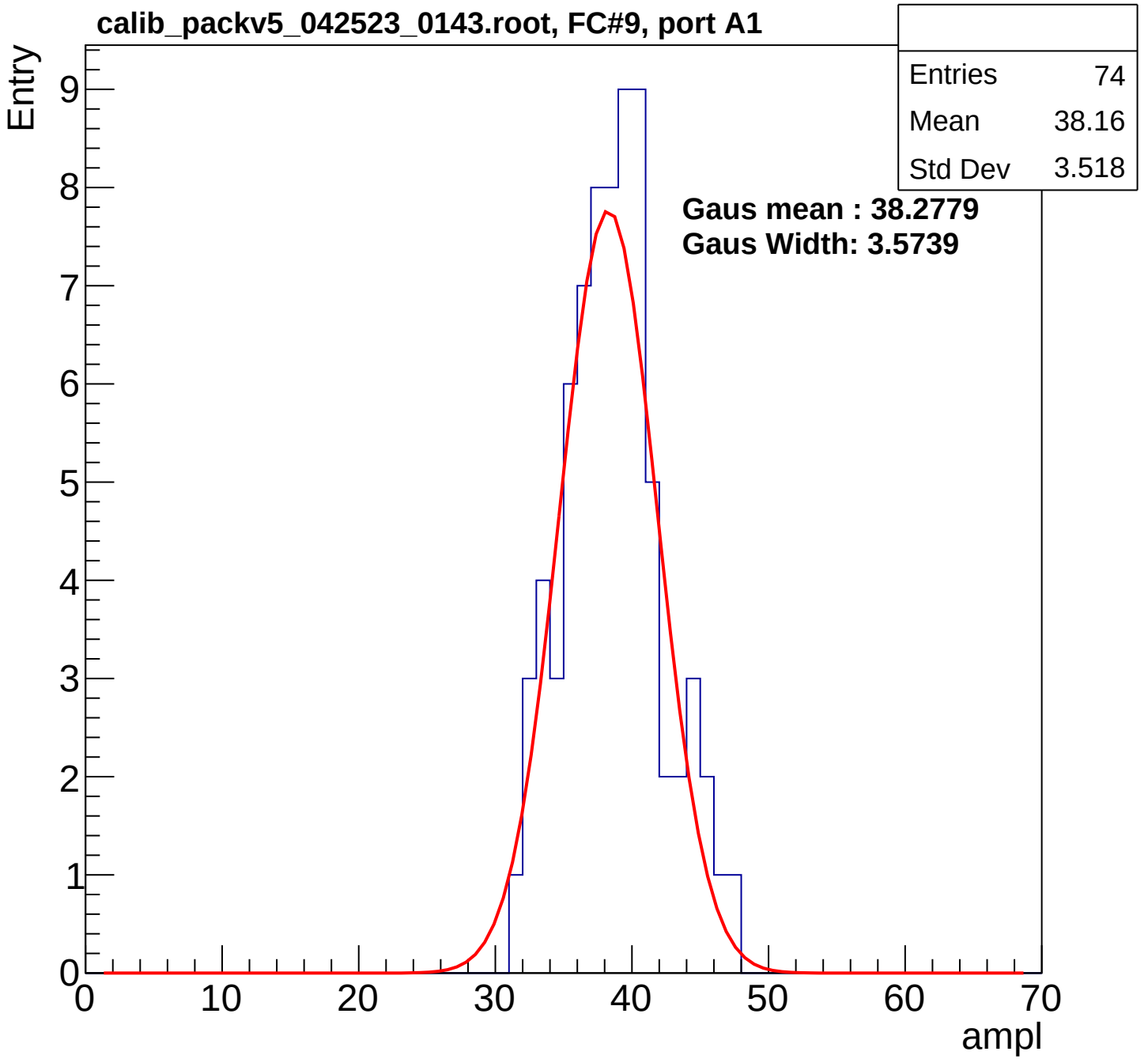

B0L001S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 29.45
Std Dev 7.039
Gaus mean : 31.2342
Gaus Width: 4.1808
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 38.16
Std Dev 3.518
Gaus mean : 38.2779
Gaus Width: 3.5739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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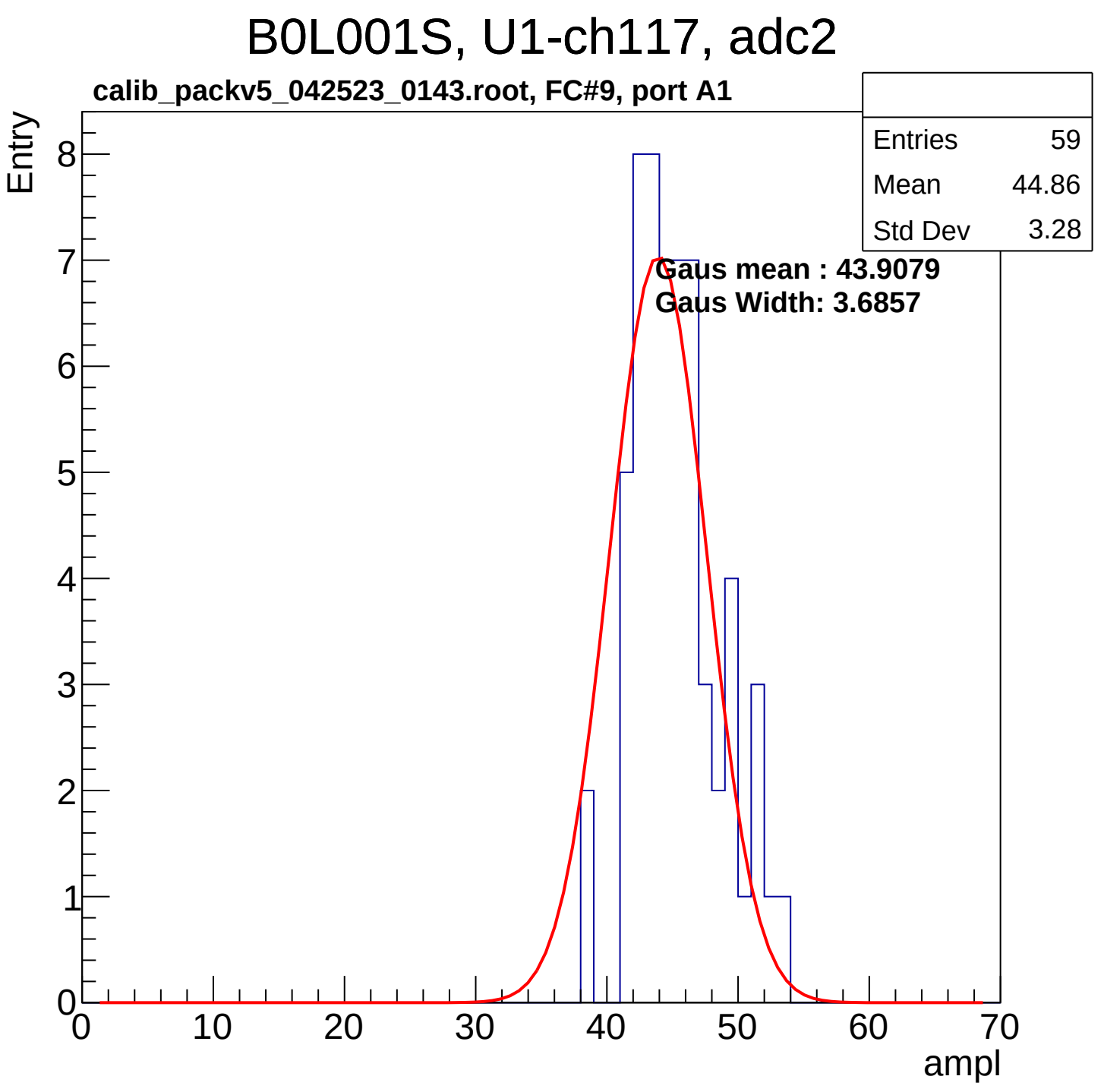
B0L001S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 44.86
Std Dev 3.28
Gaus mean : 43.9079
Gaus Width: 3.6857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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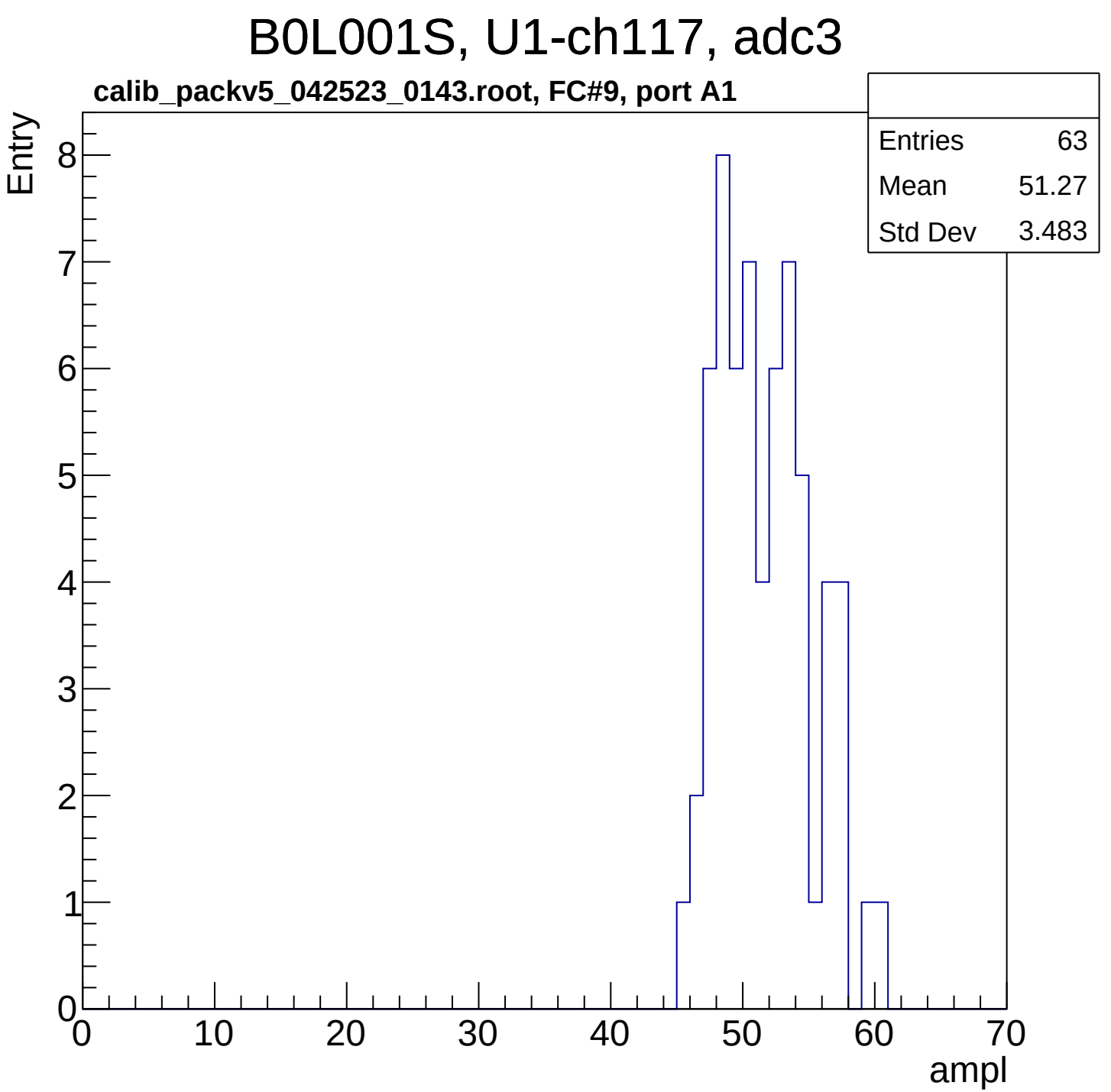
B0L001S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 51.27
Std Dev 3.483
Entry
ampl
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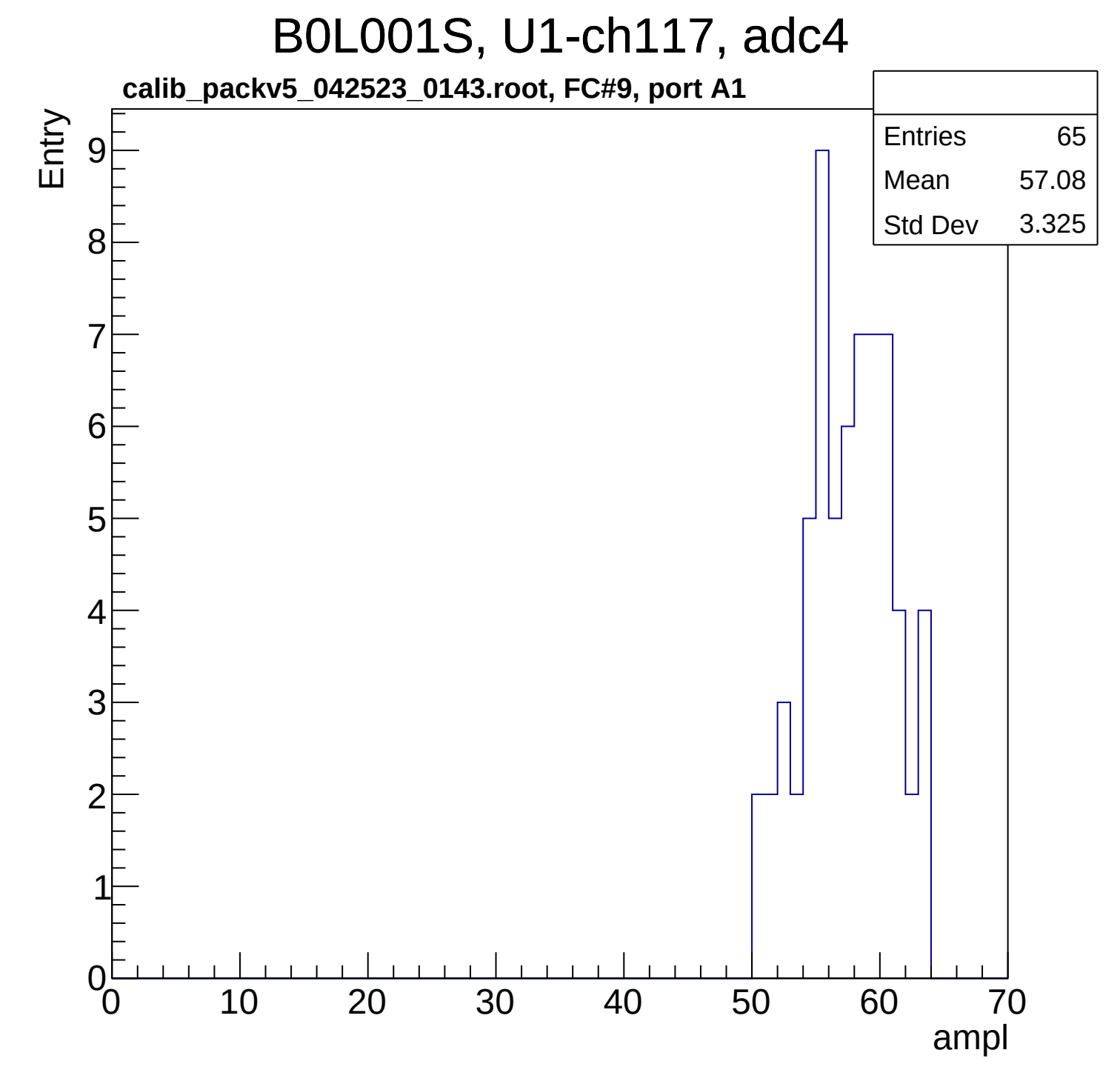
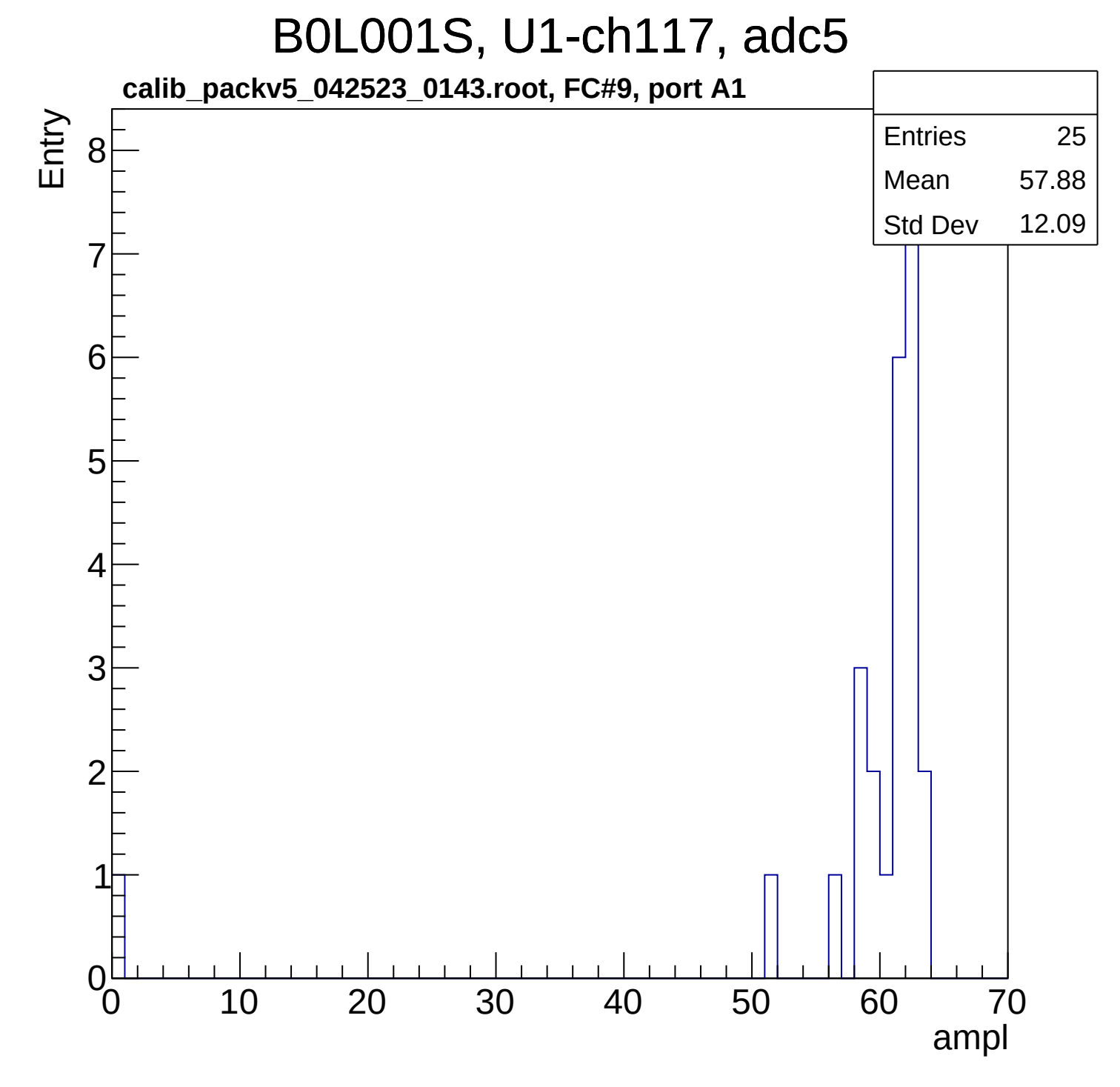
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 57.08
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 25
Mean 57.88
Std Dev 12.09
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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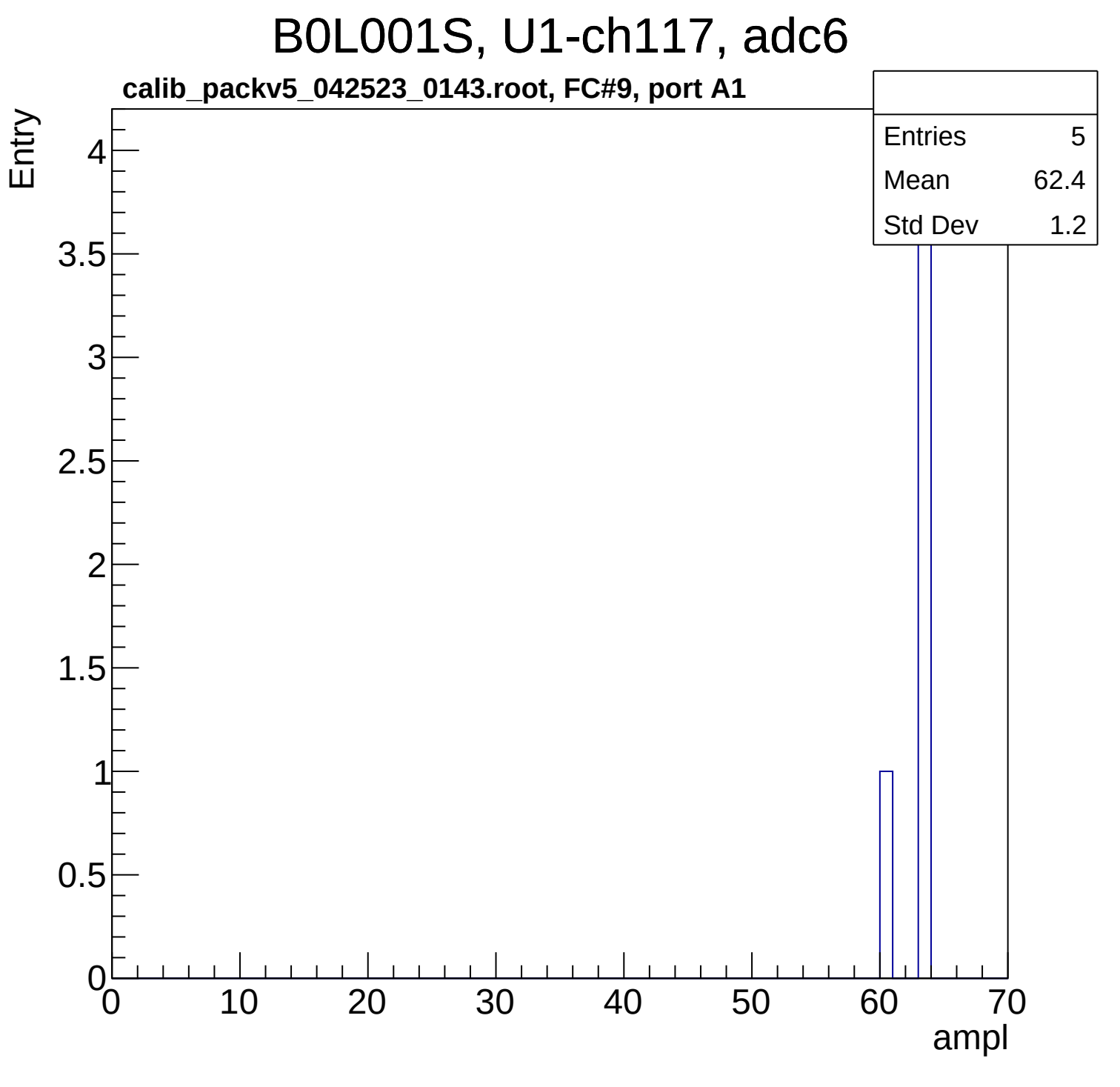
B0L001S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 62.4
Std Dev 1.2
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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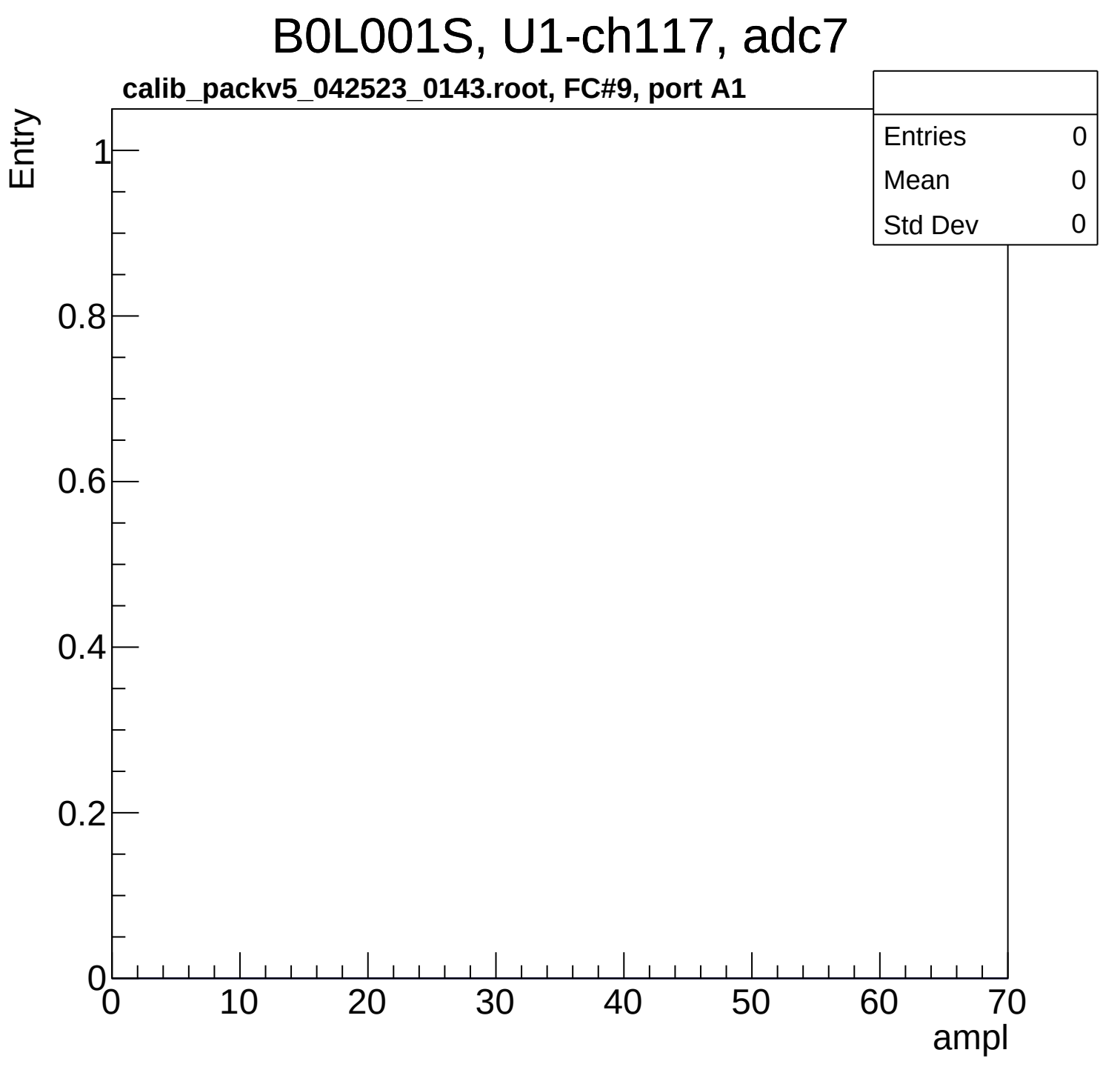
40
50
60
70


B0L001S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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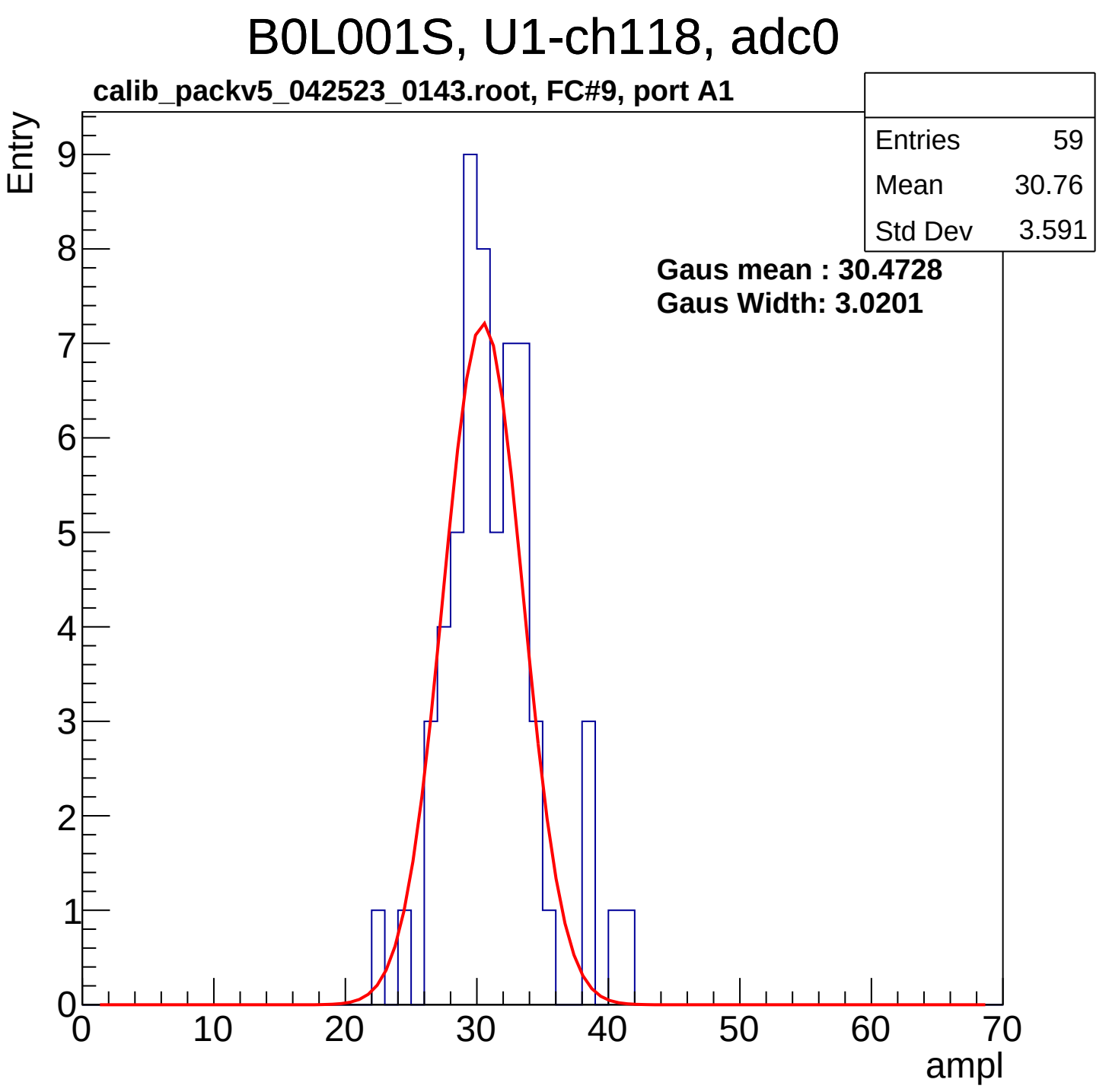
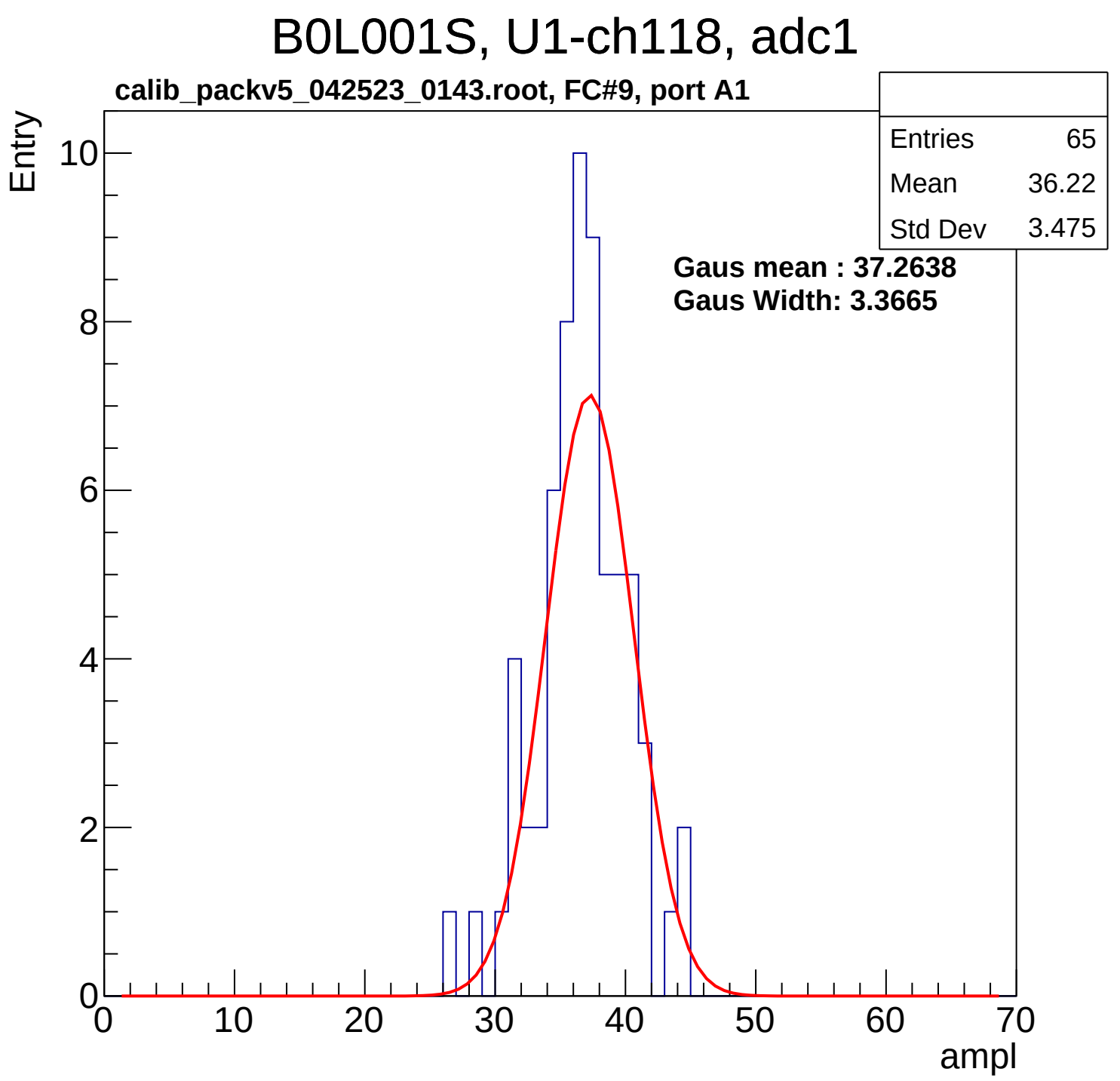
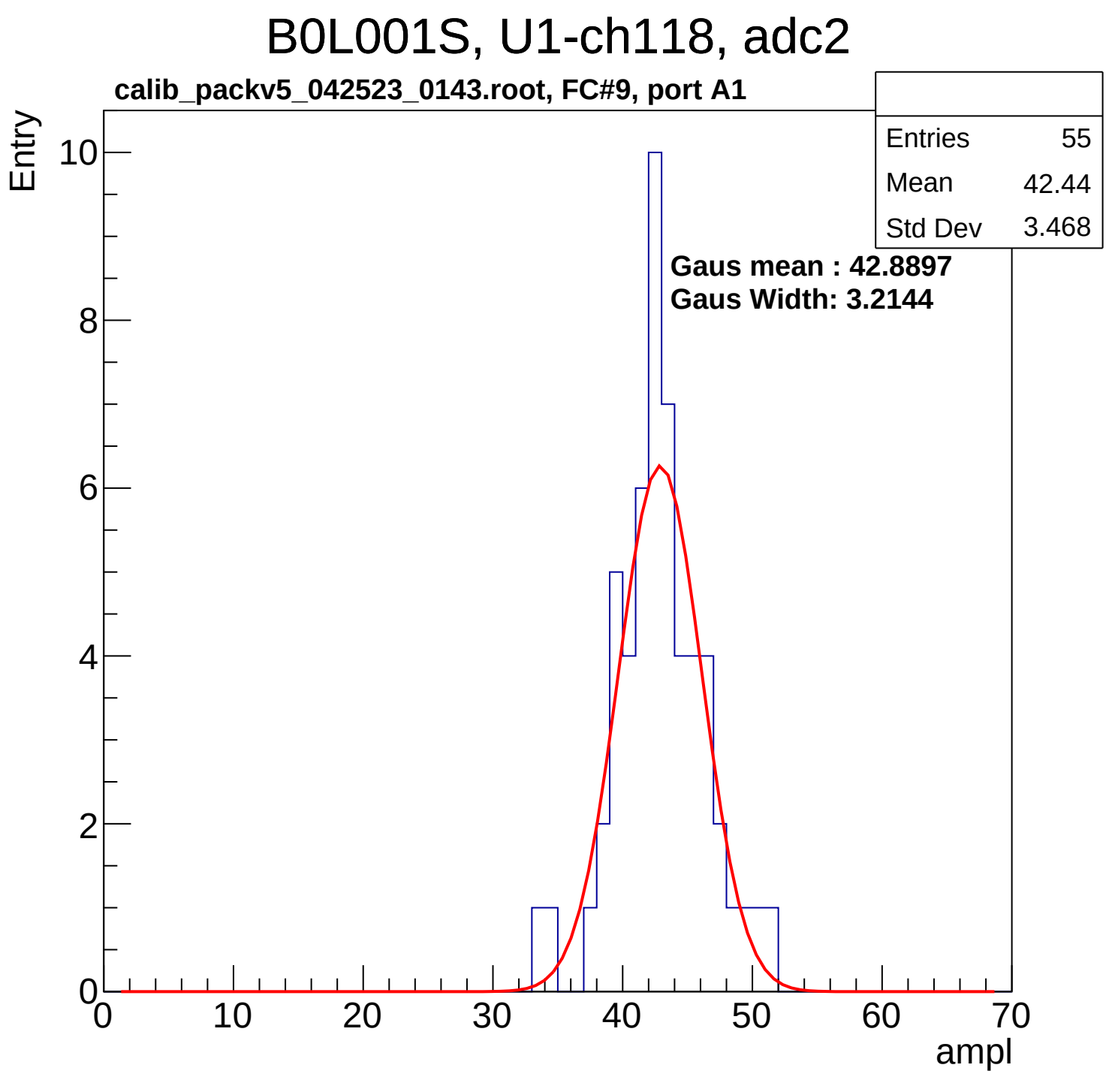
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 30.76
Std Dev 3.591
Gaus mean : 30.4728
Gaus Width: 3.0201
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 36.22
Std Dev 3.475
Gaus mean : 37.2638
Gaus Width: 3.3665
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 42.44
Std Dev 3.468
Gaus mean : 42.8897
Gaus Width: 3.2144
Entry
ampl
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2
4
6
8
10
0
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40
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60
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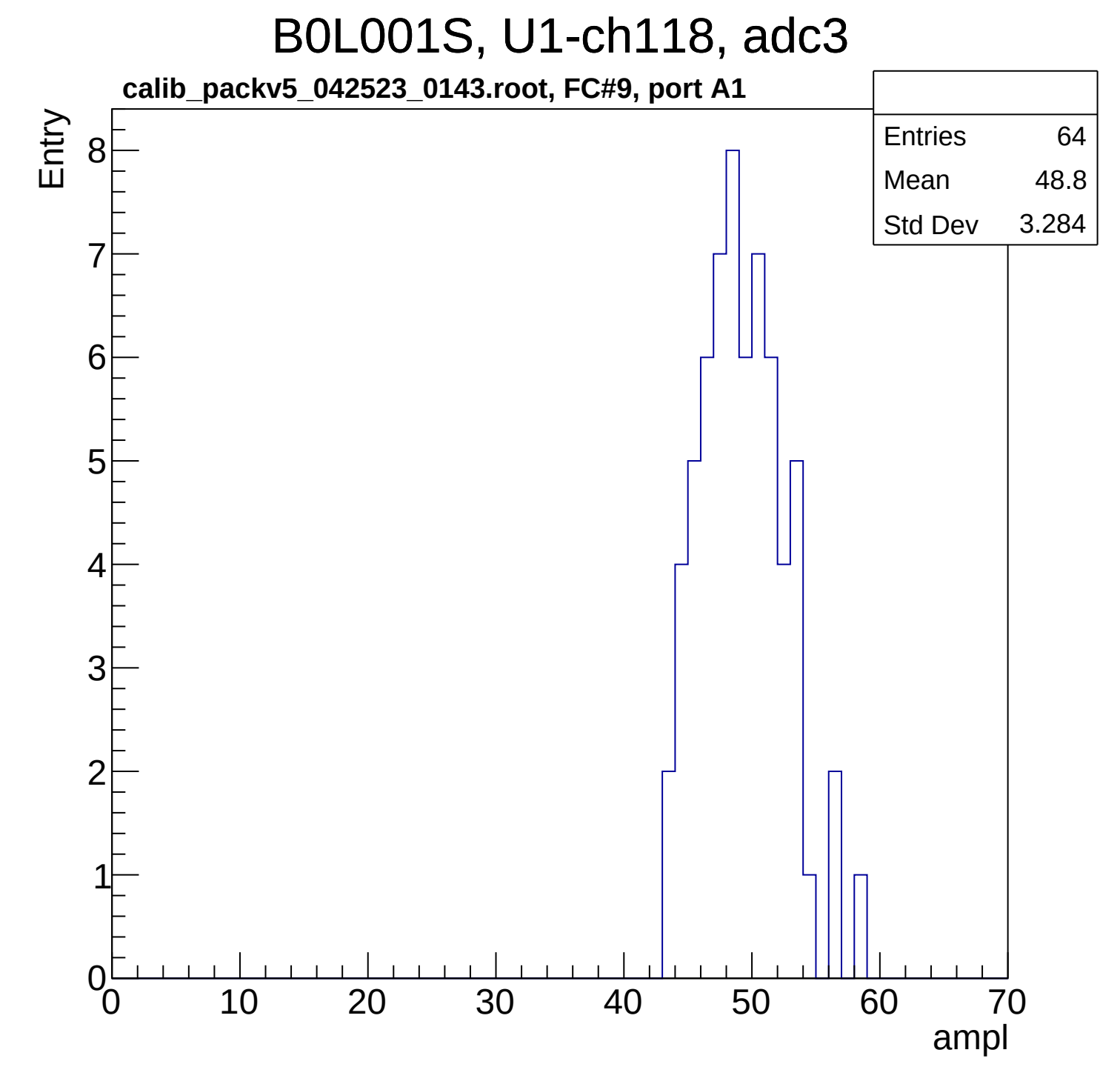
B0L001S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 48.8
Std Dev 3.284
Entry
ampl
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2
3
4
5
6
7
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0
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60
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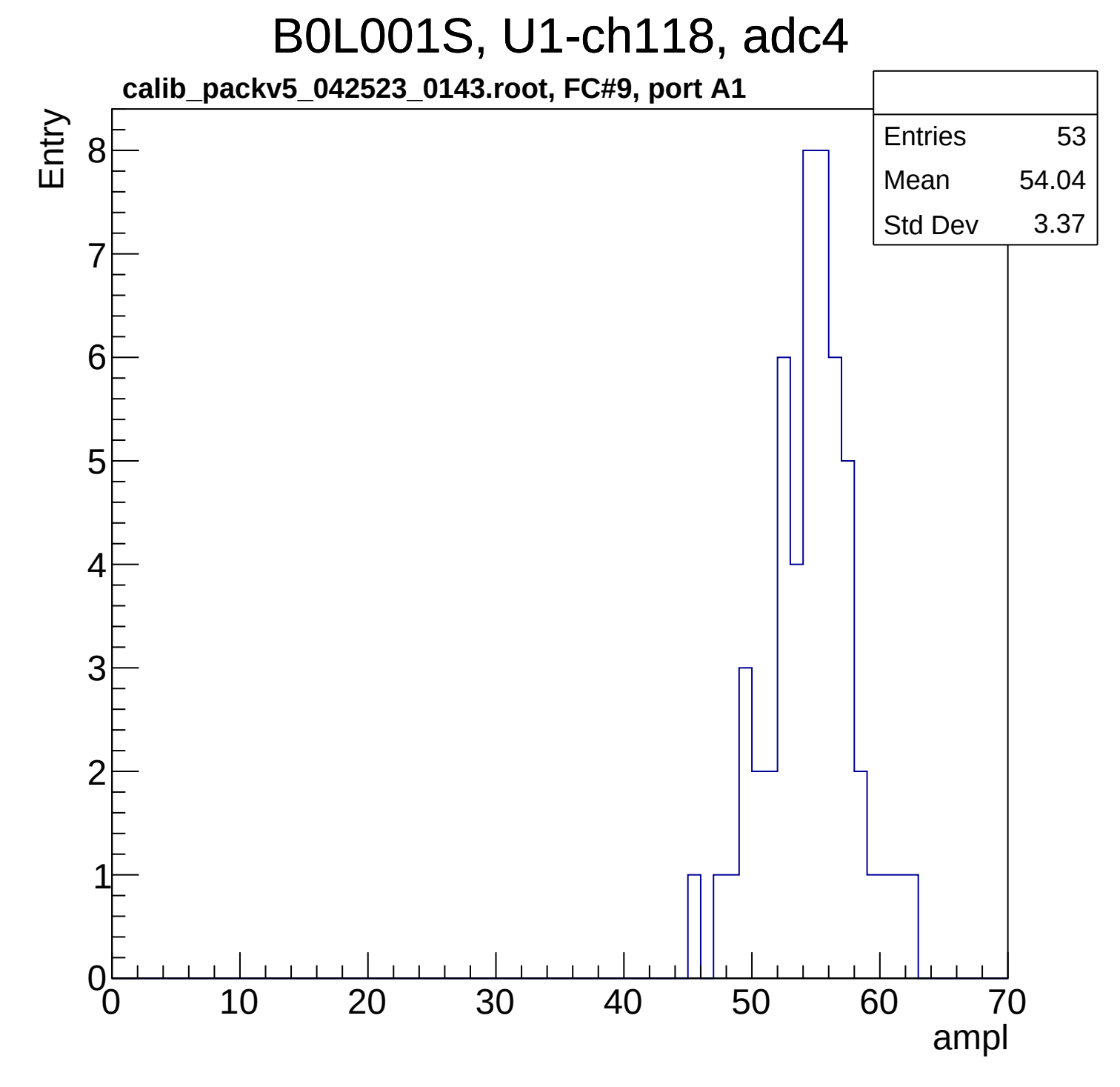
B0L001S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 54.04
Std Dev 3.37
Entry
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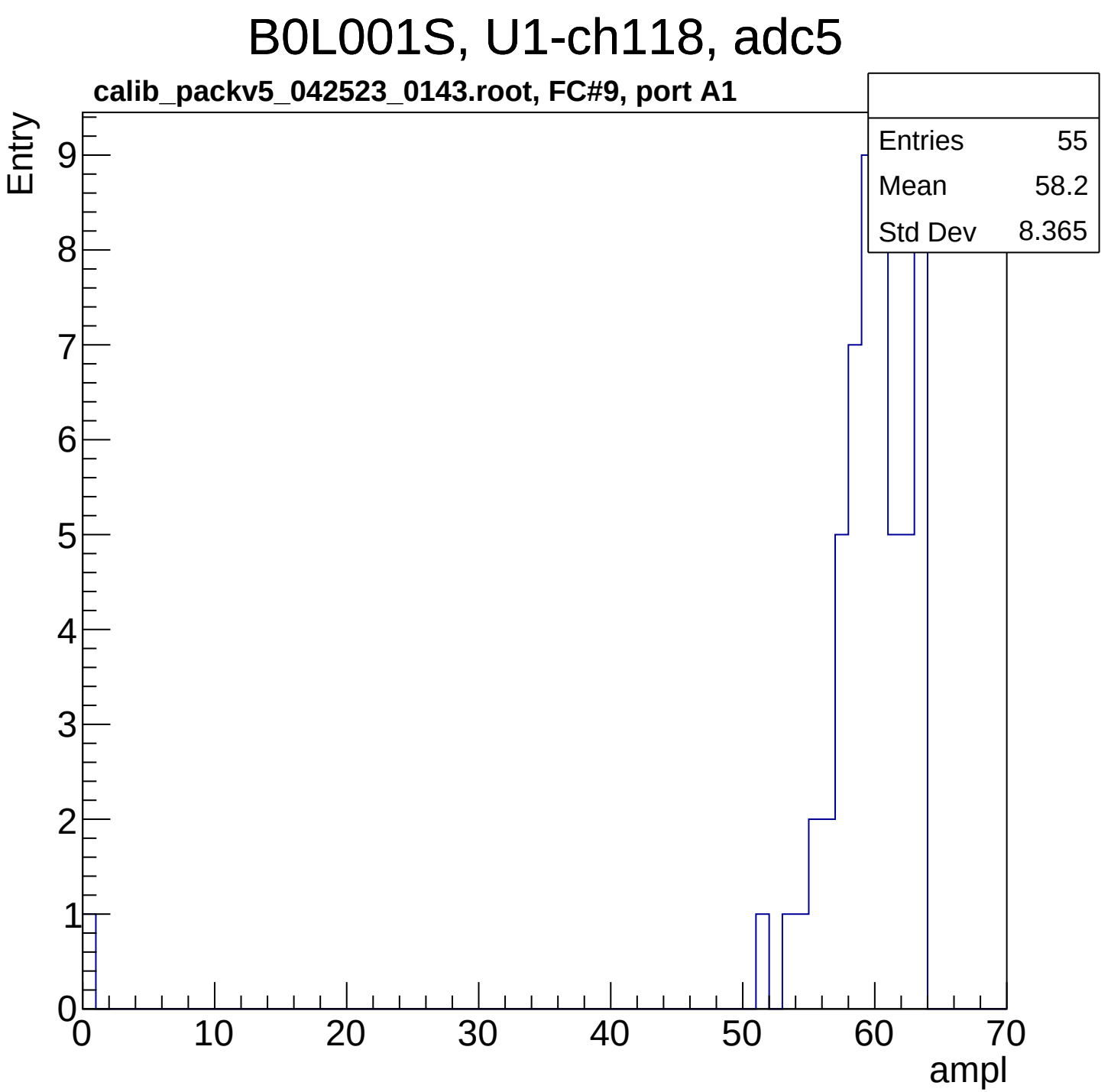
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 55
Mean 58.2
Std Dev 8.365
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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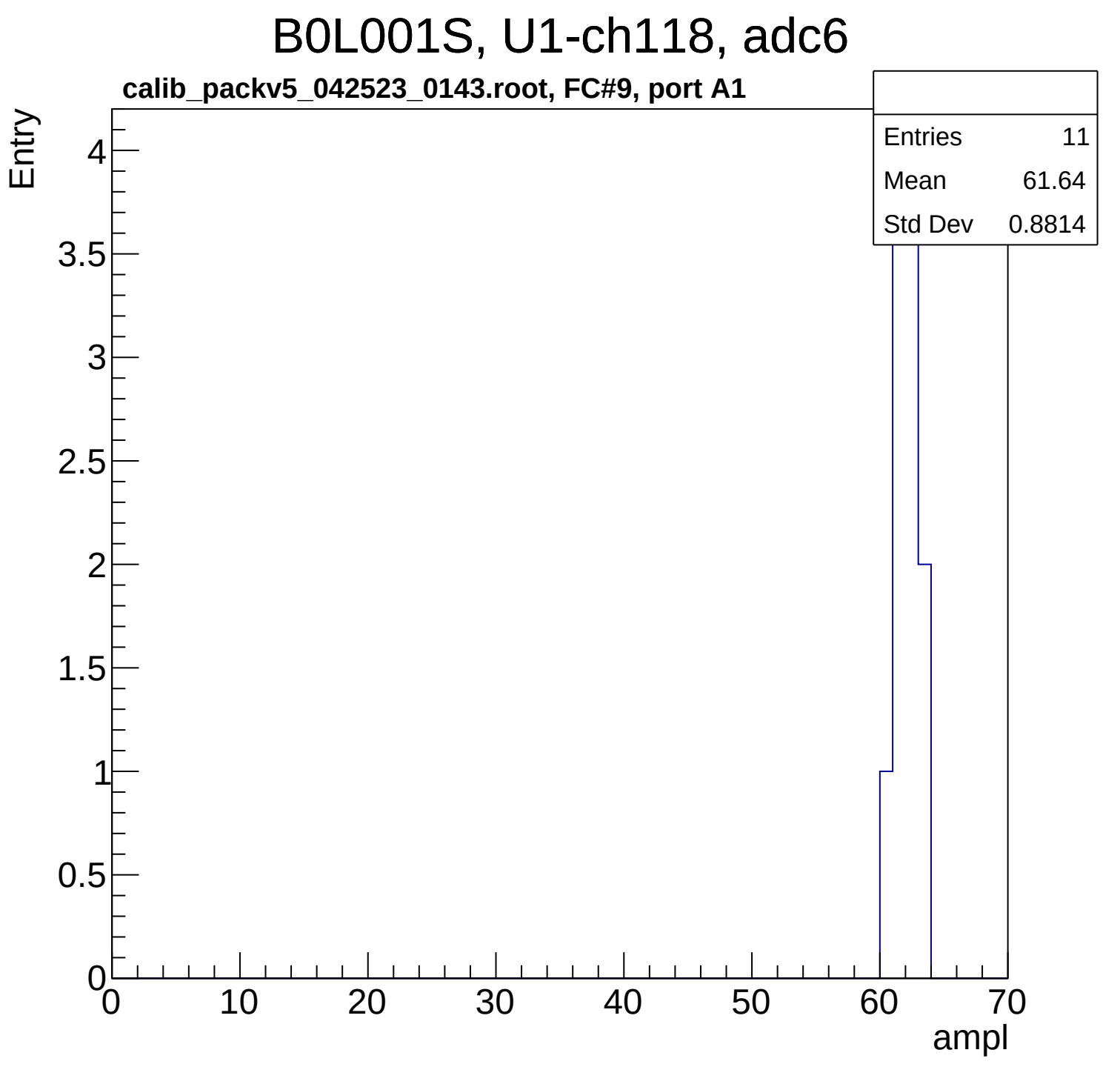
B0L001S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 11
Mean 61.64
Std Dev 0.8814
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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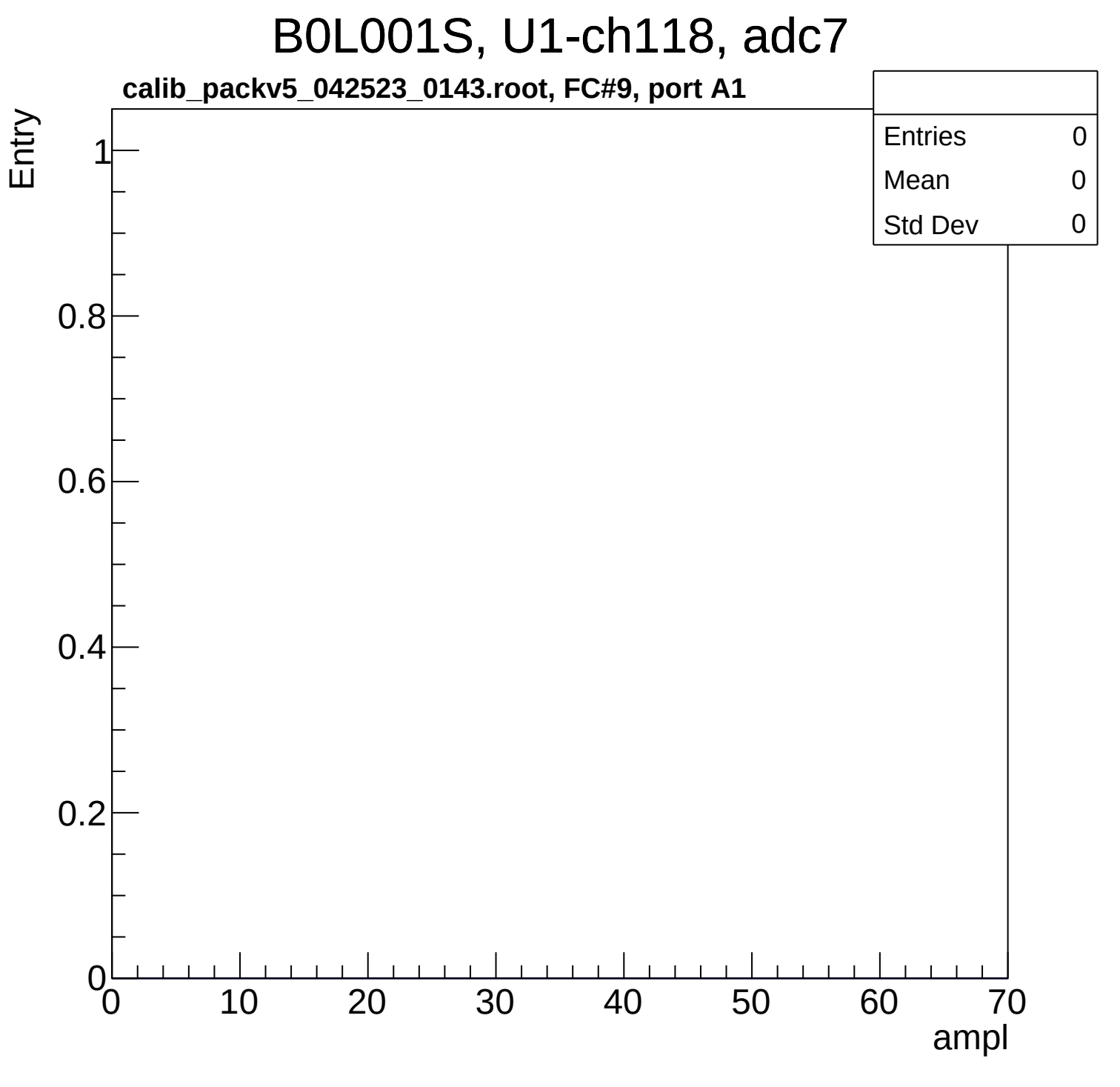
40
50
60
70


B0L001S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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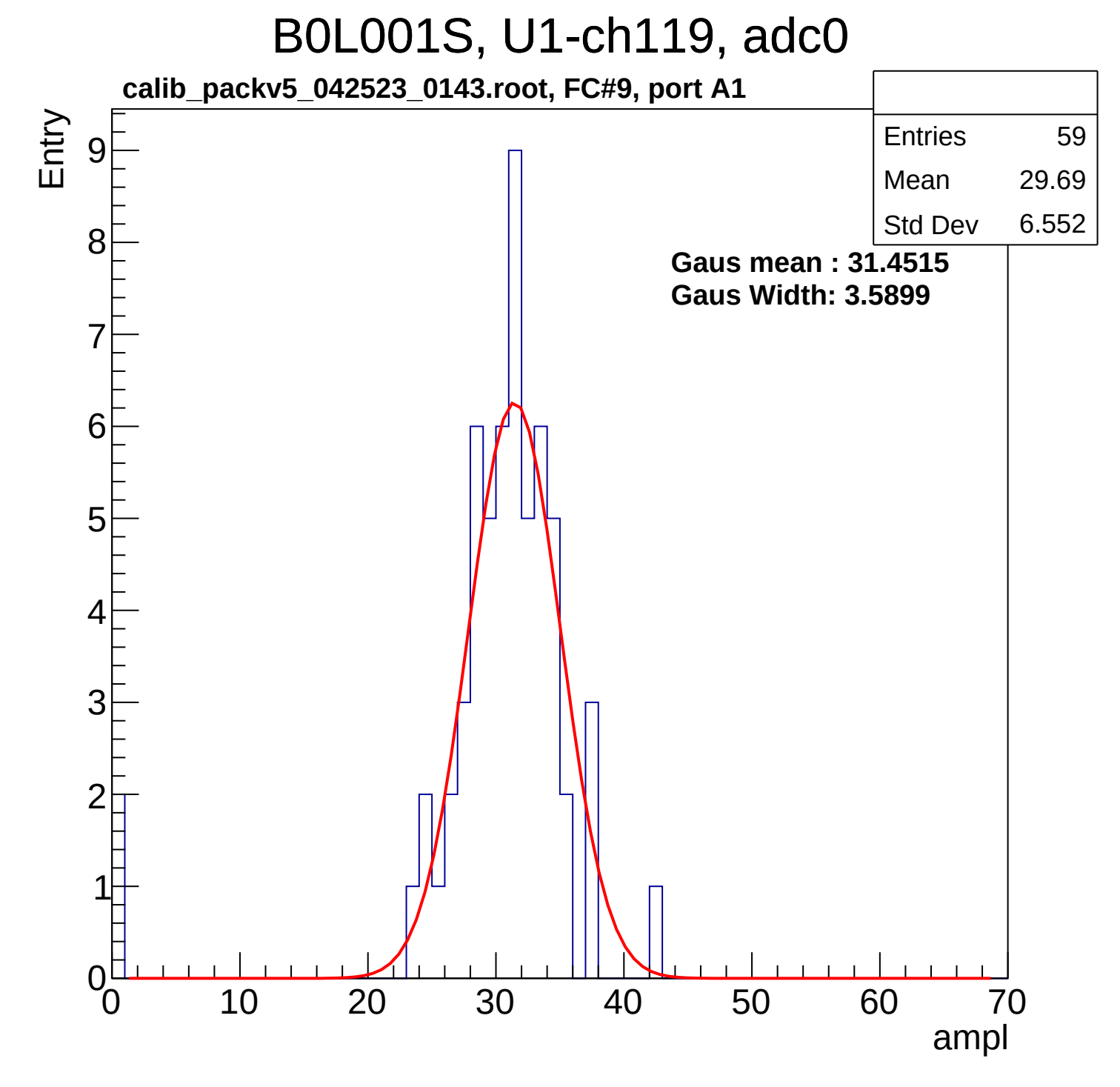
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 29.69
Std Dev 6.552
Gaus mean : 31.4515
Gaus Width: 3.5899
Entry
ampl
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3
4
5
6
7
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0
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60
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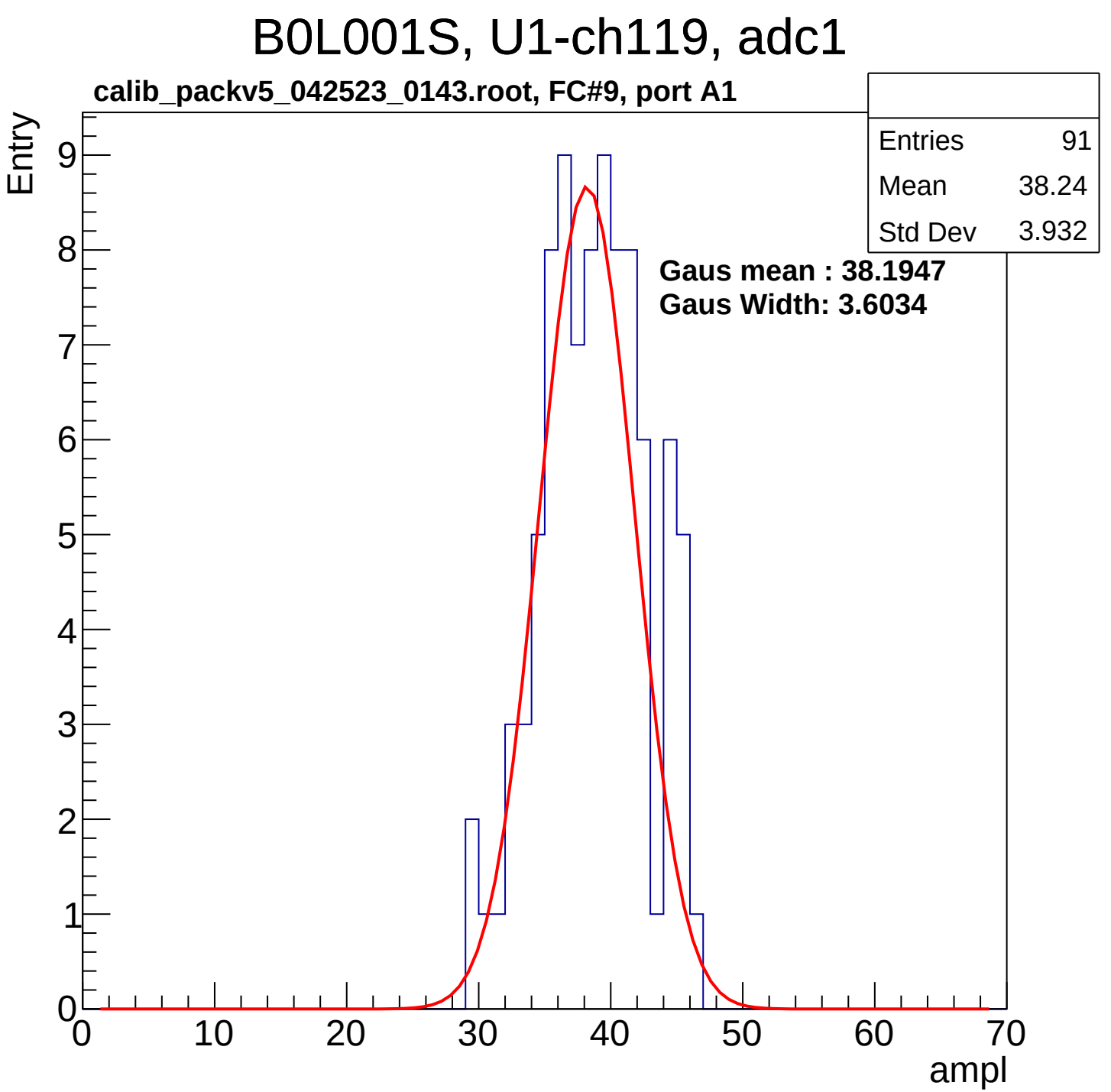
B0L001S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 91
Mean 38.24
Std Dev 3.932
Gaus mean : 38.1947
Gaus Width: 3.6034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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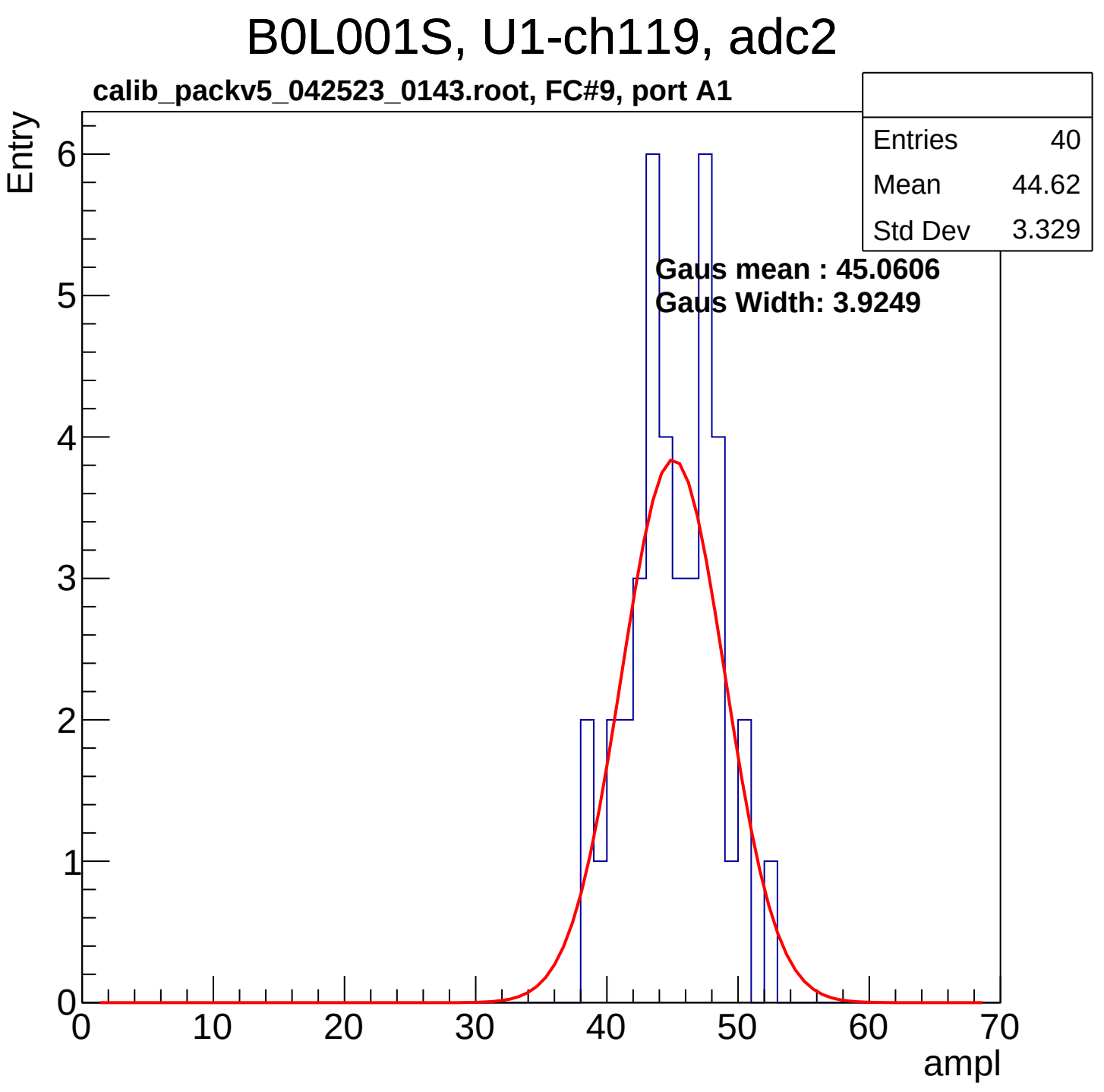
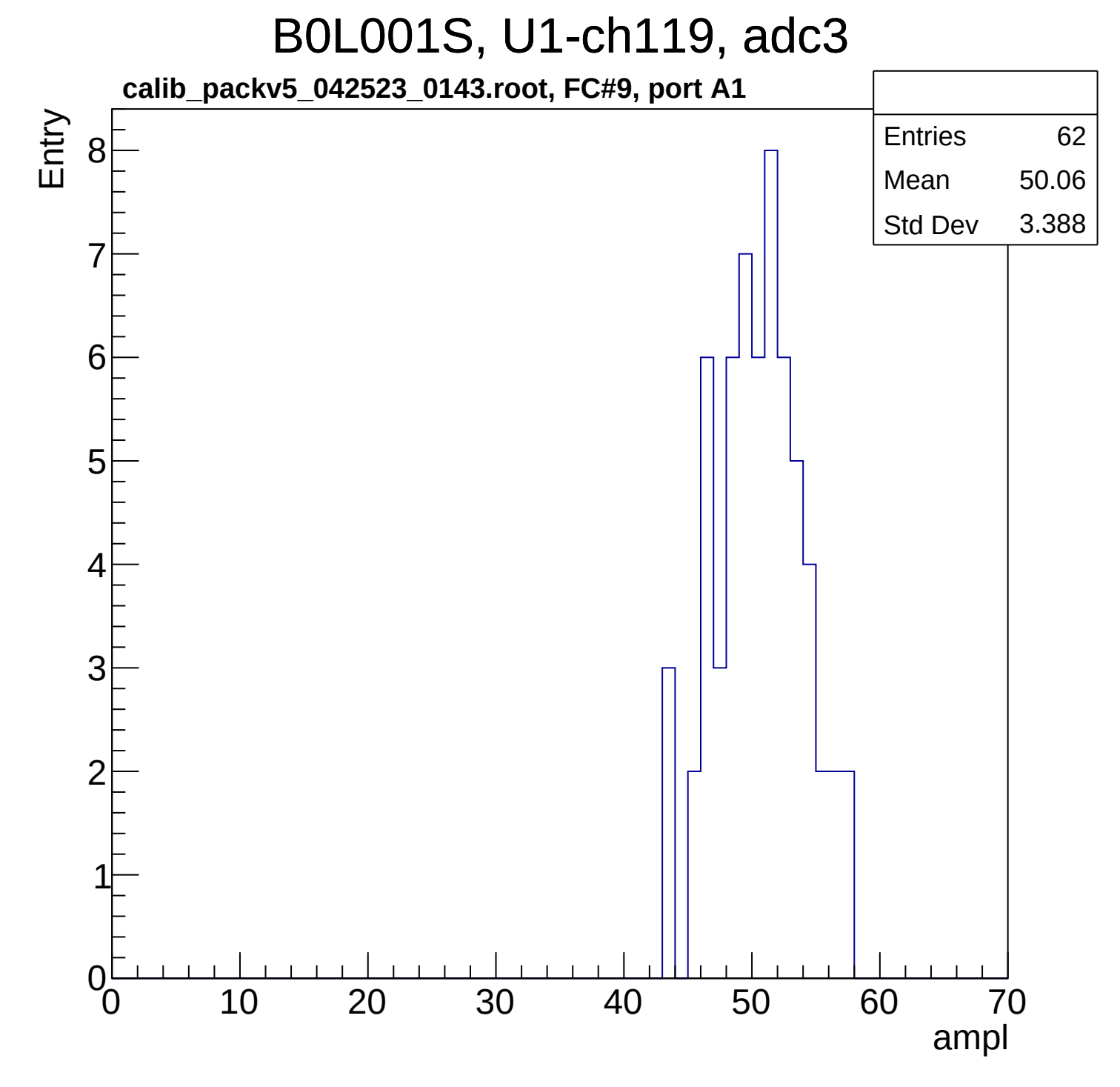

B0L001S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 44.62
Std Dev 3.329
Gaus mean : 45.0606
Gaus Width: 3.9249
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L001S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.06
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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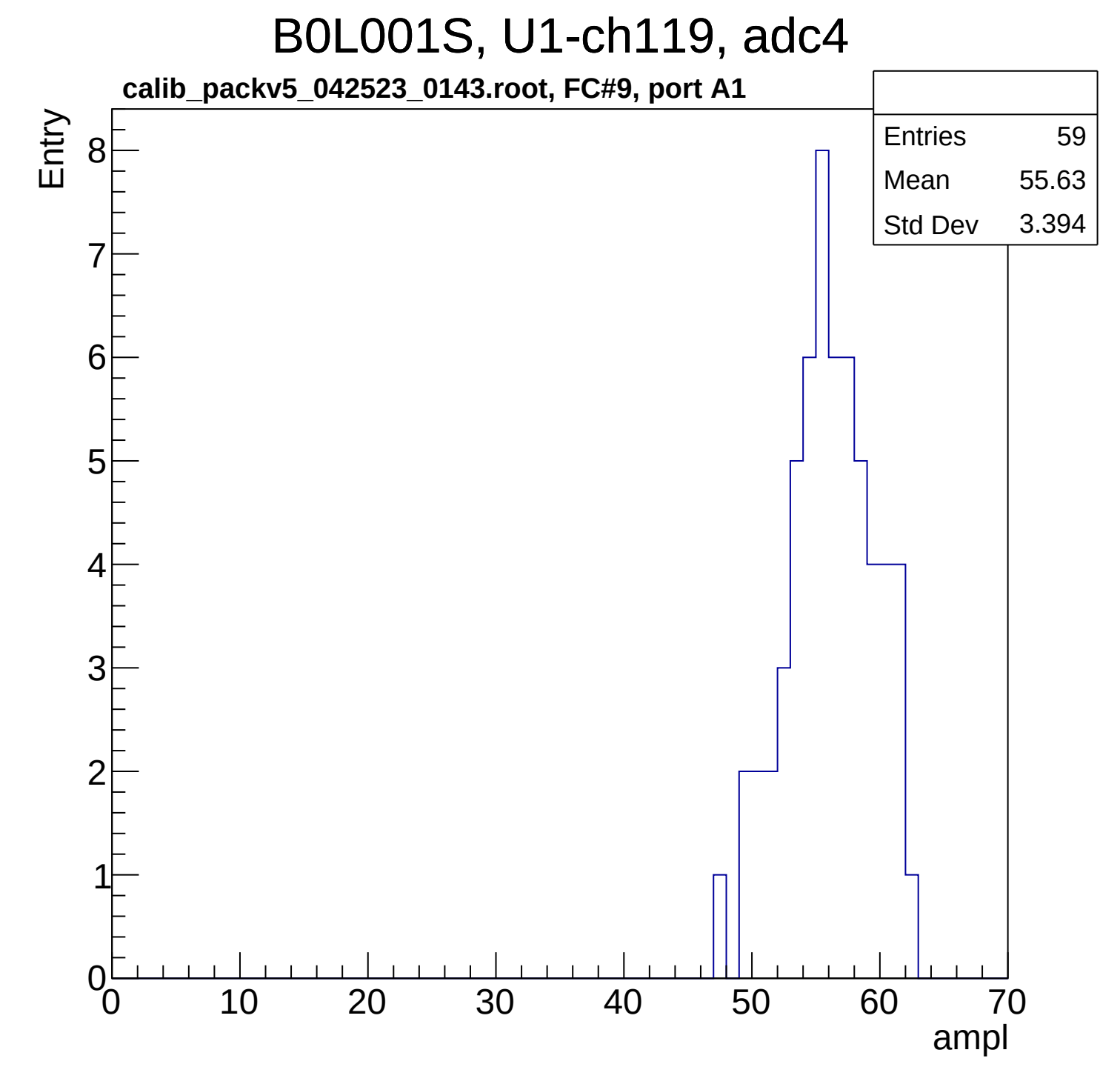
B0L001S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 55.63
Std Dev 3.394
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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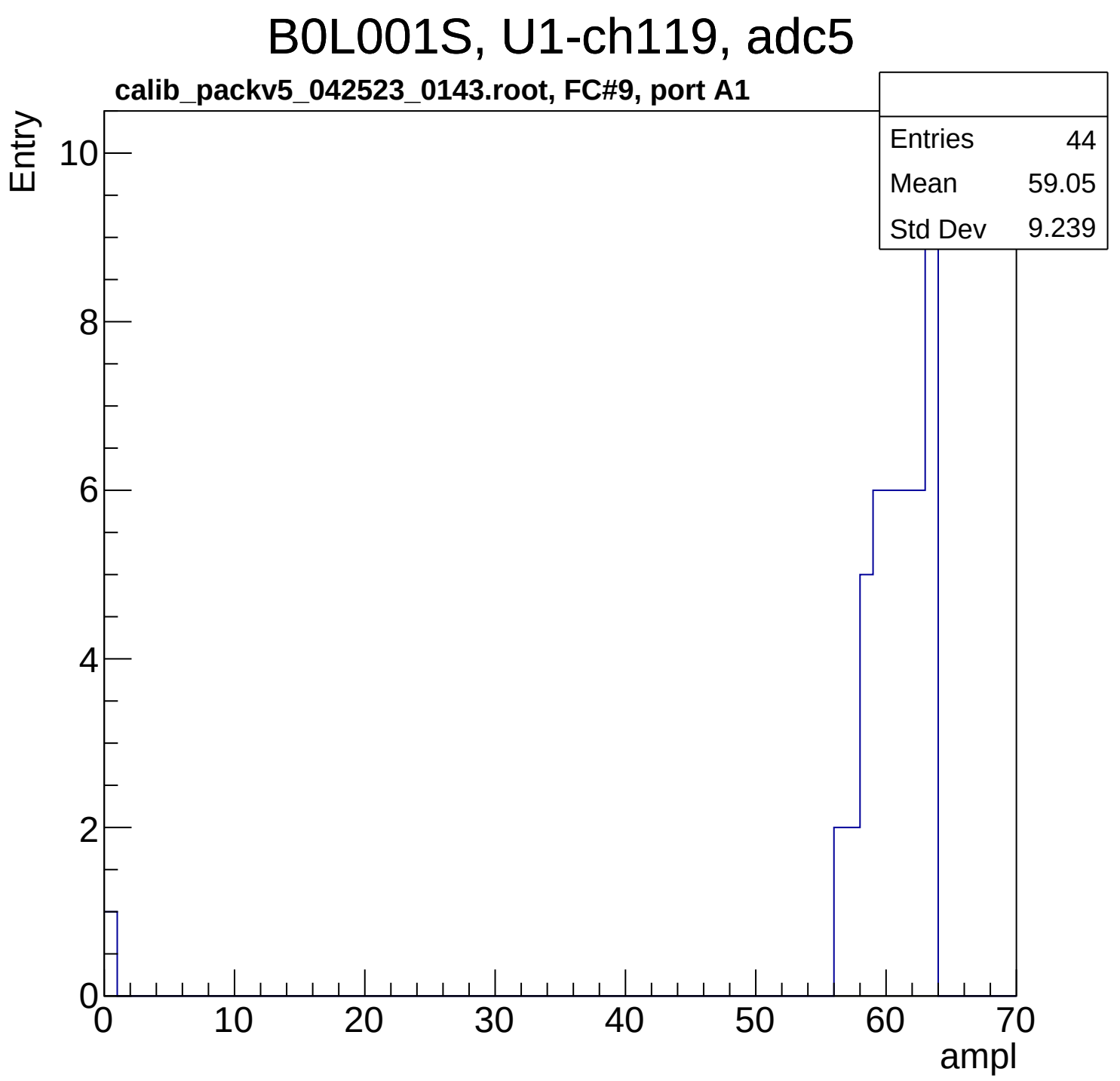
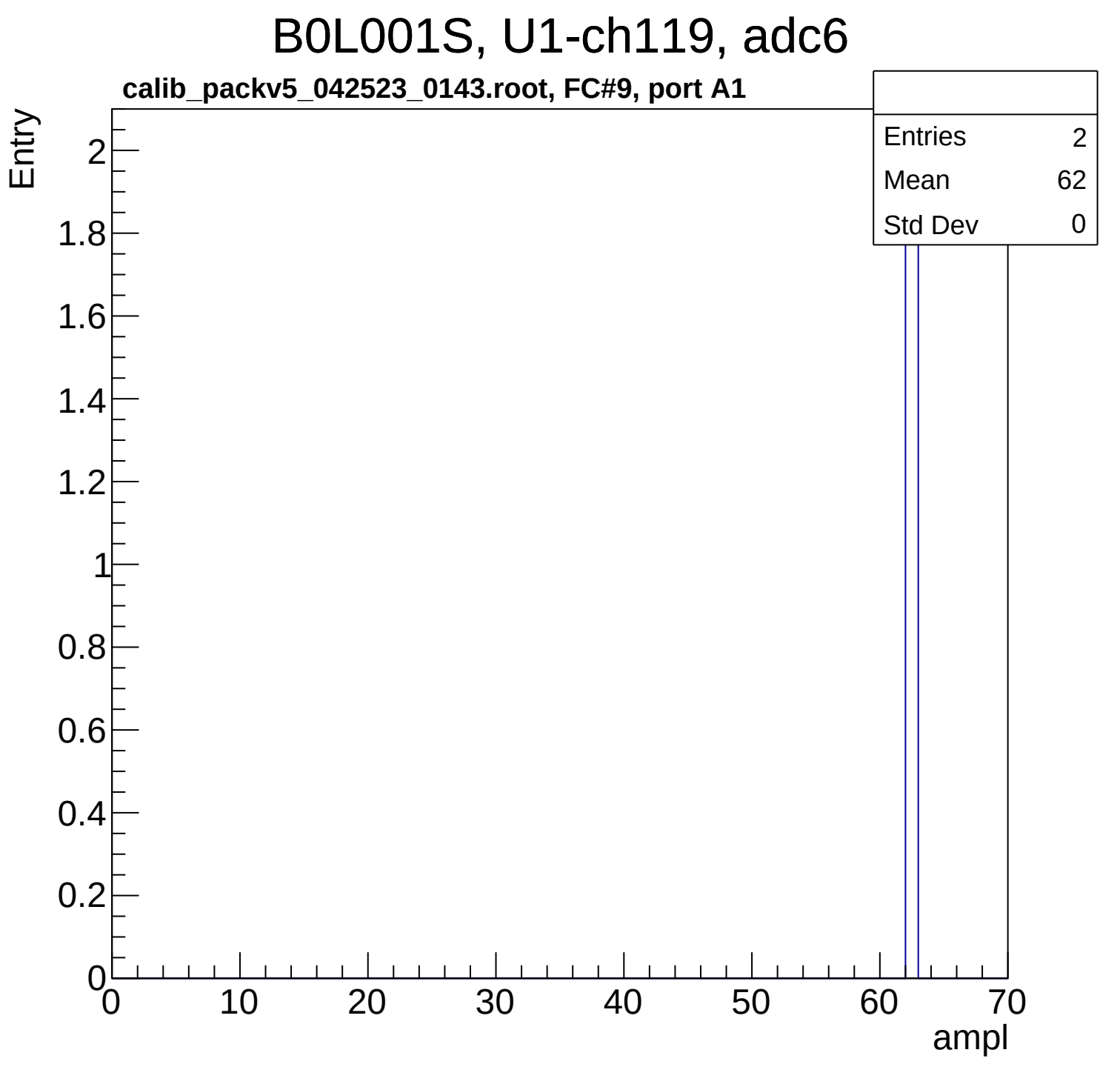
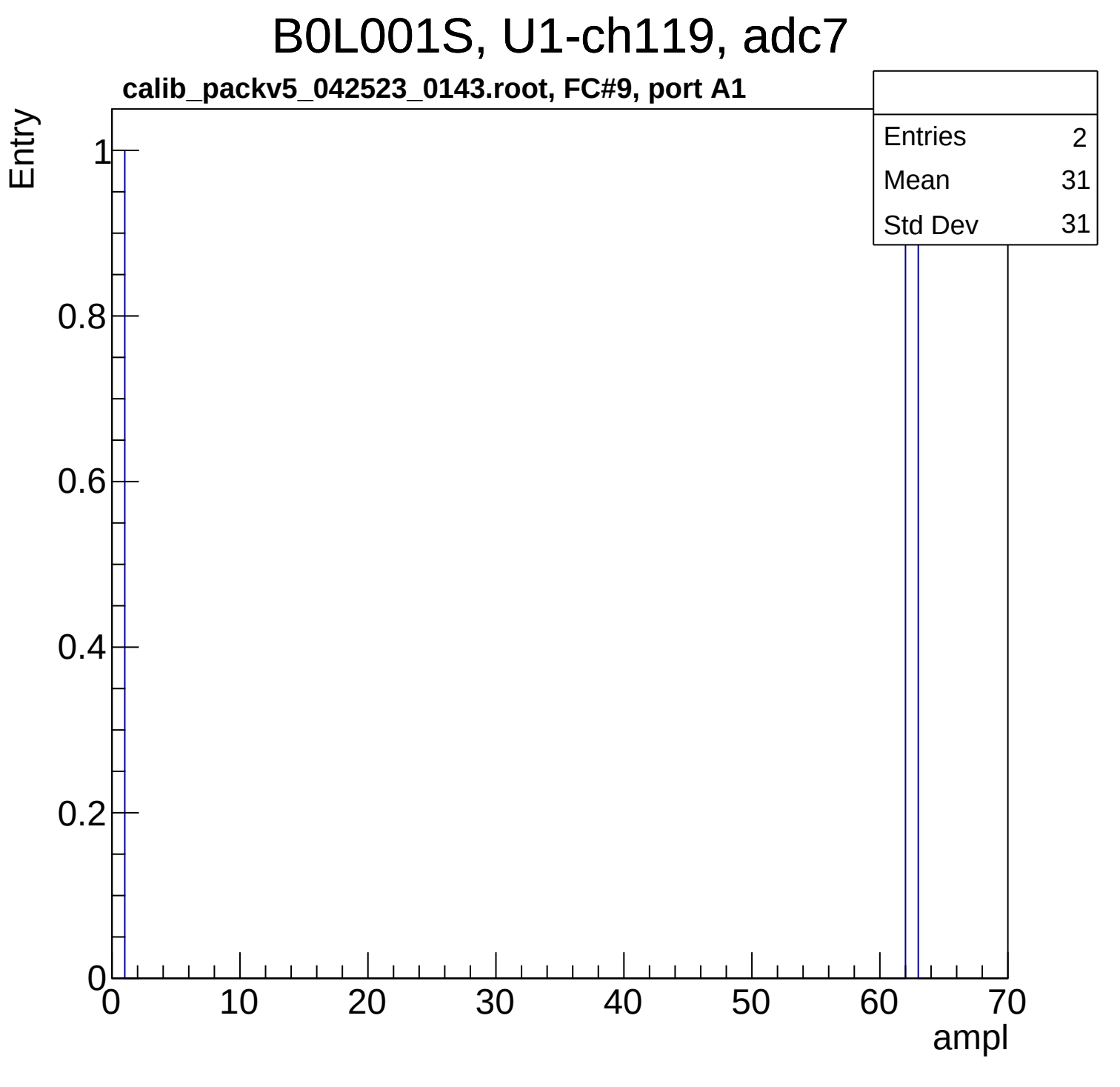
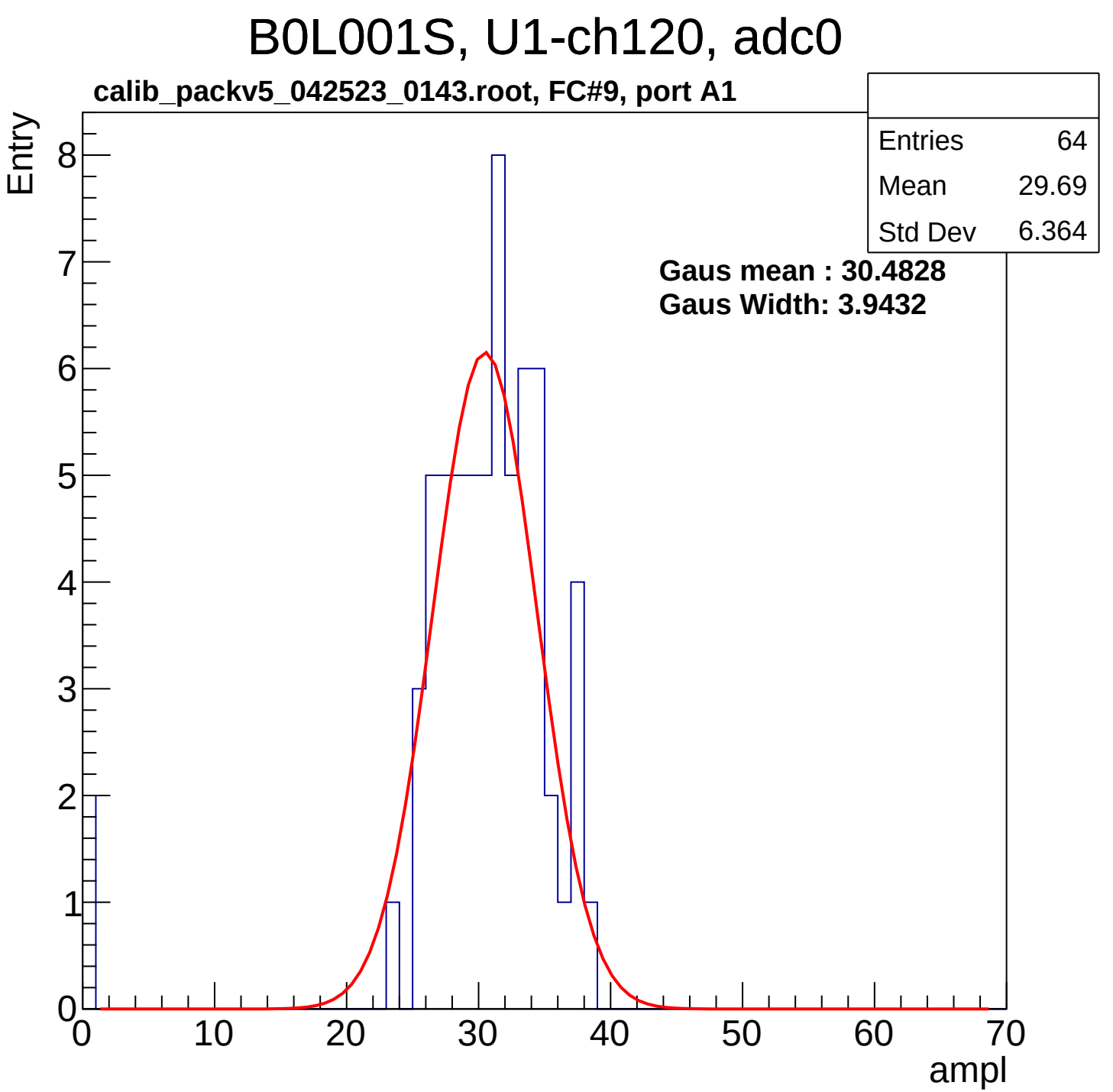
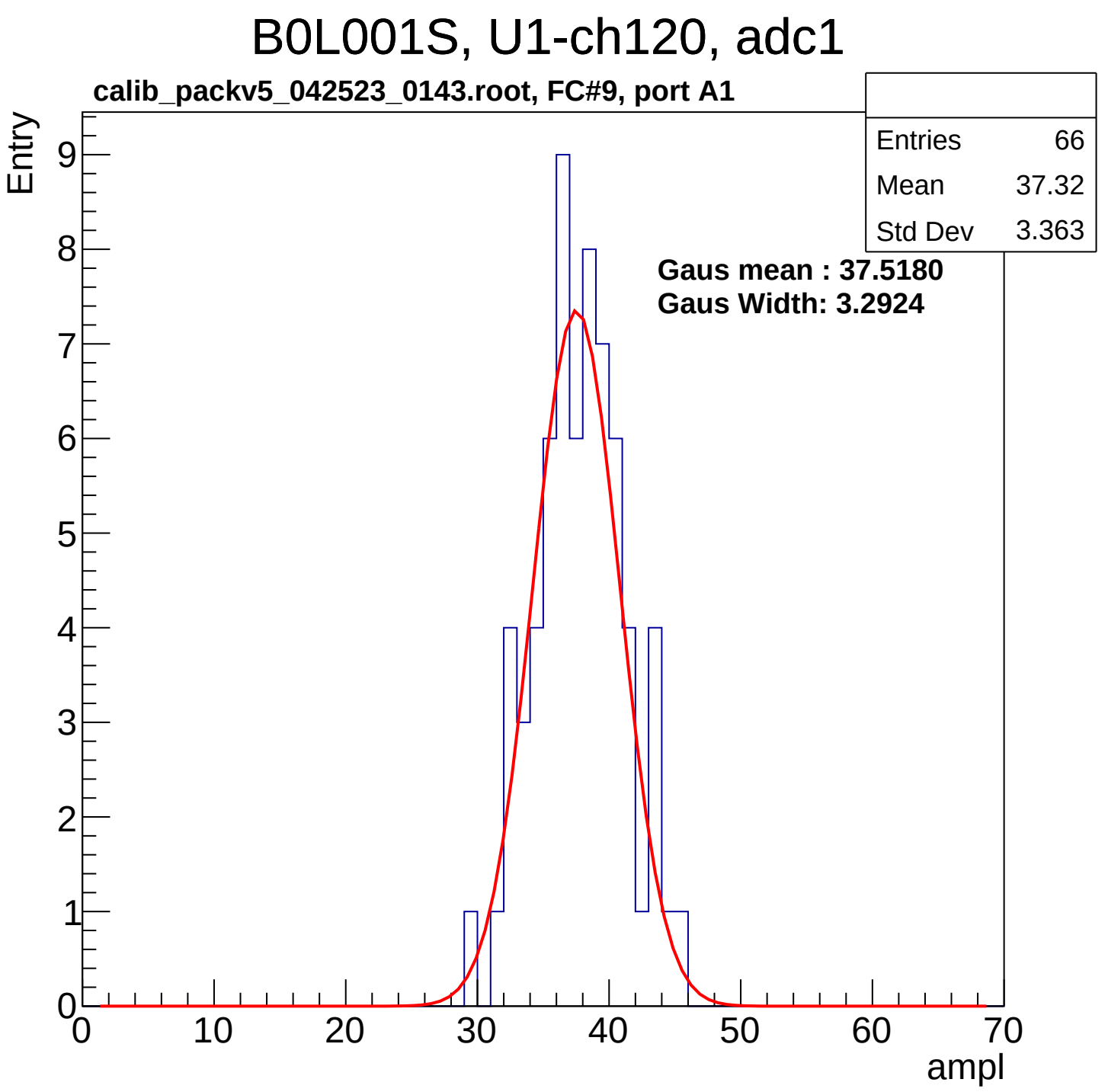
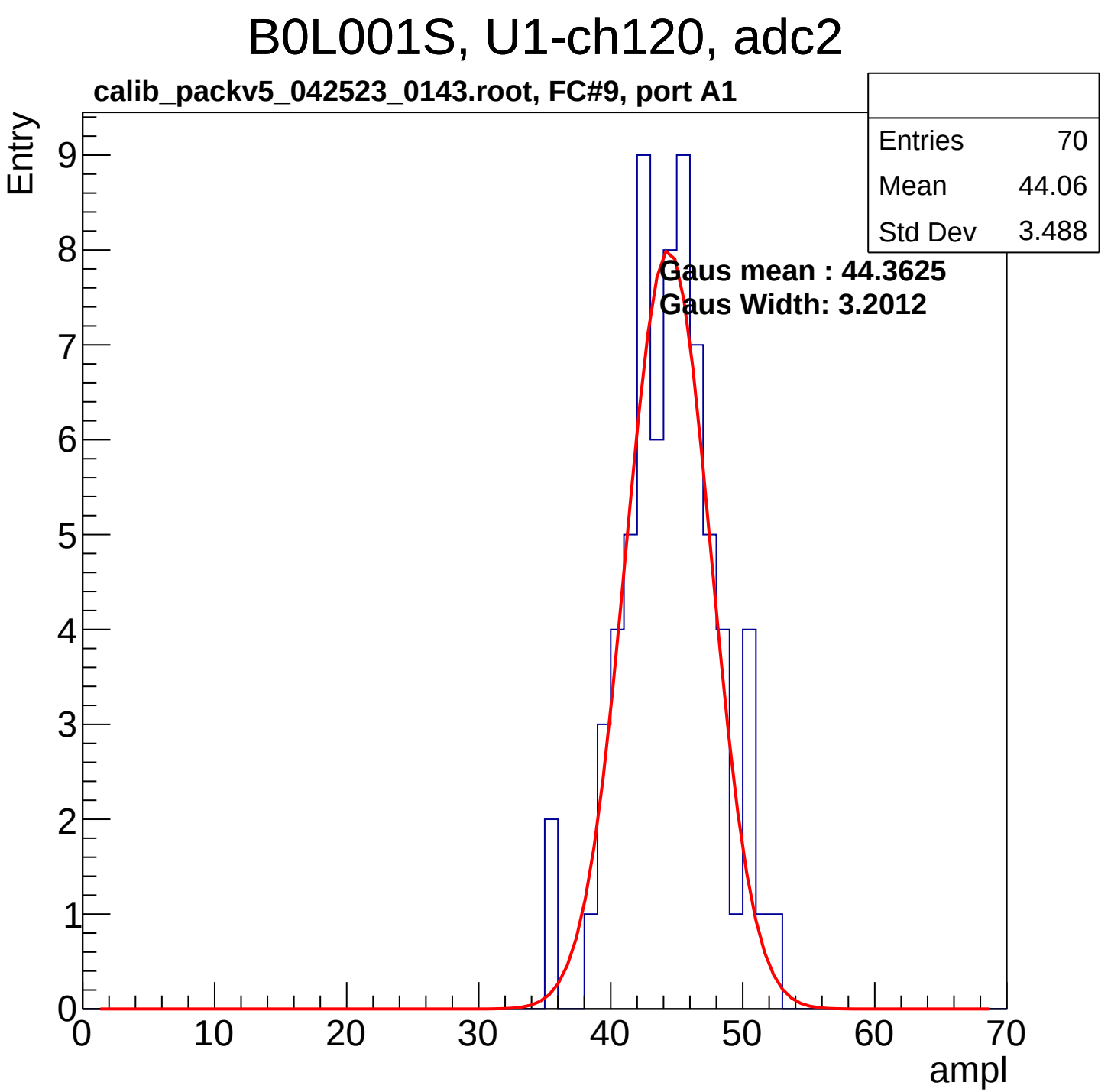

B0L001S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 44
Mean 59.05
Std Dev 9.239
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

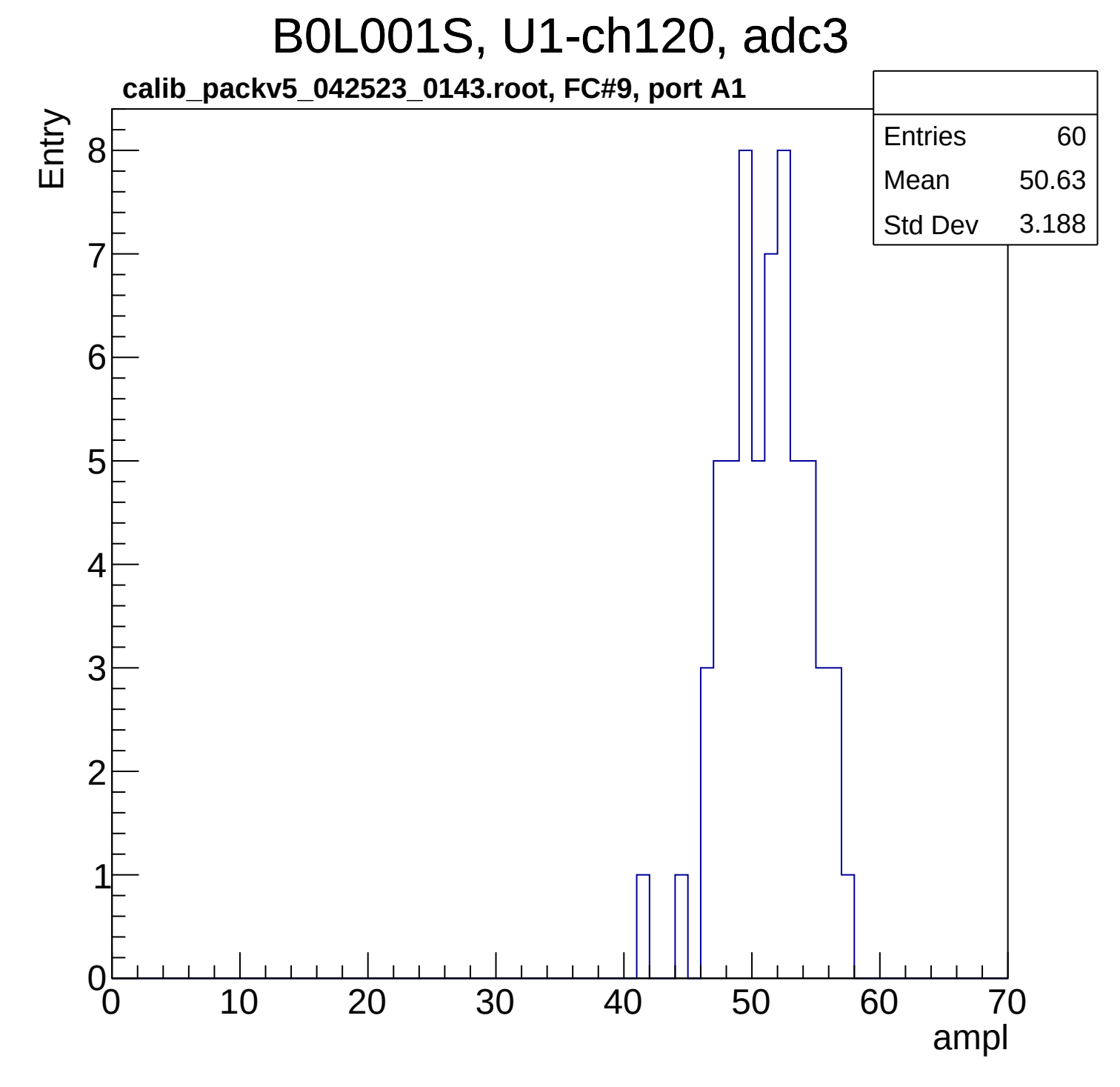

B0L001S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

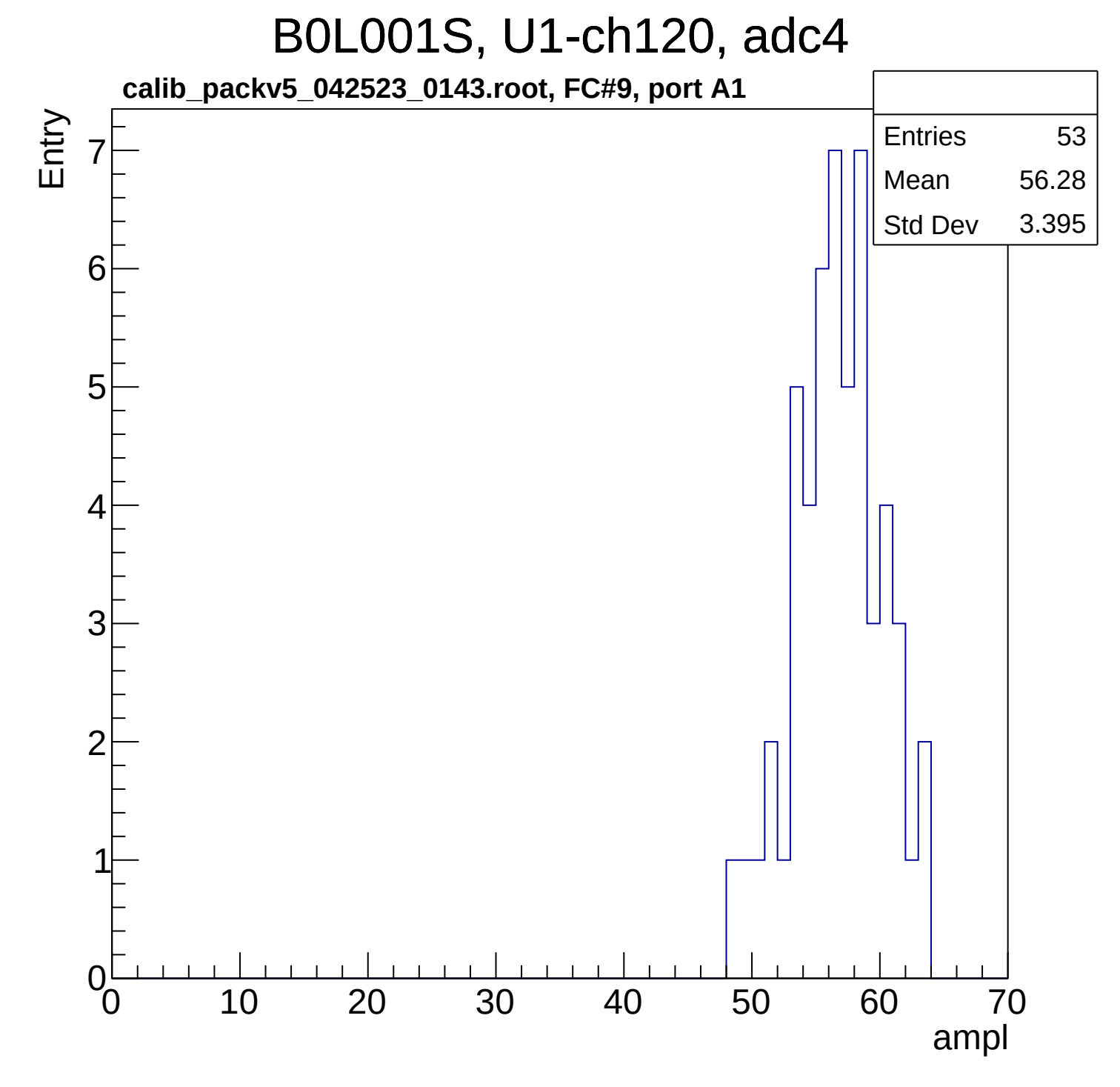

B0L001S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

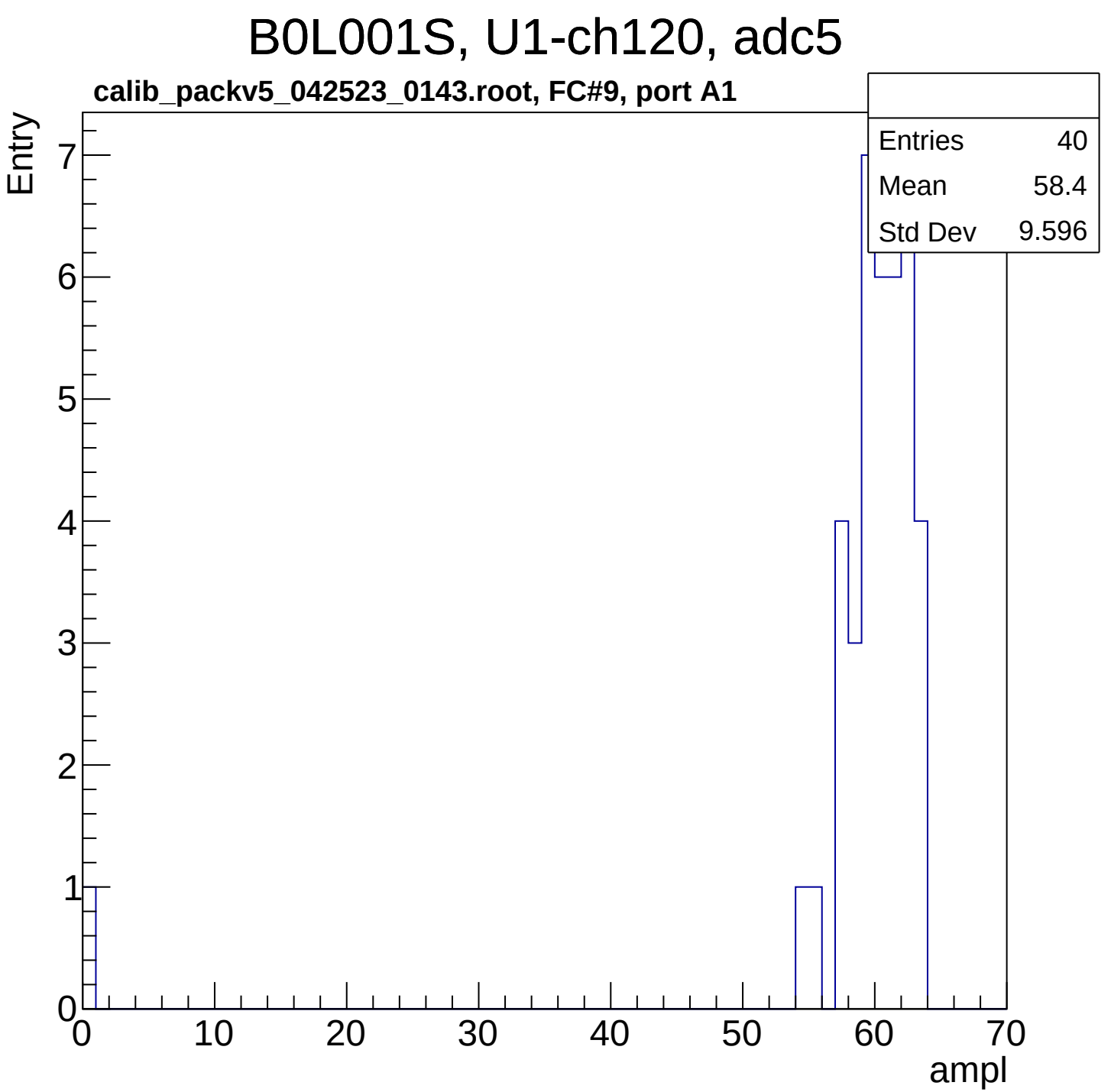

B0L001S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 29.69
Std Dev 6.364
Gaus mean : 30.4828
Gaus Width: 3.9432
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

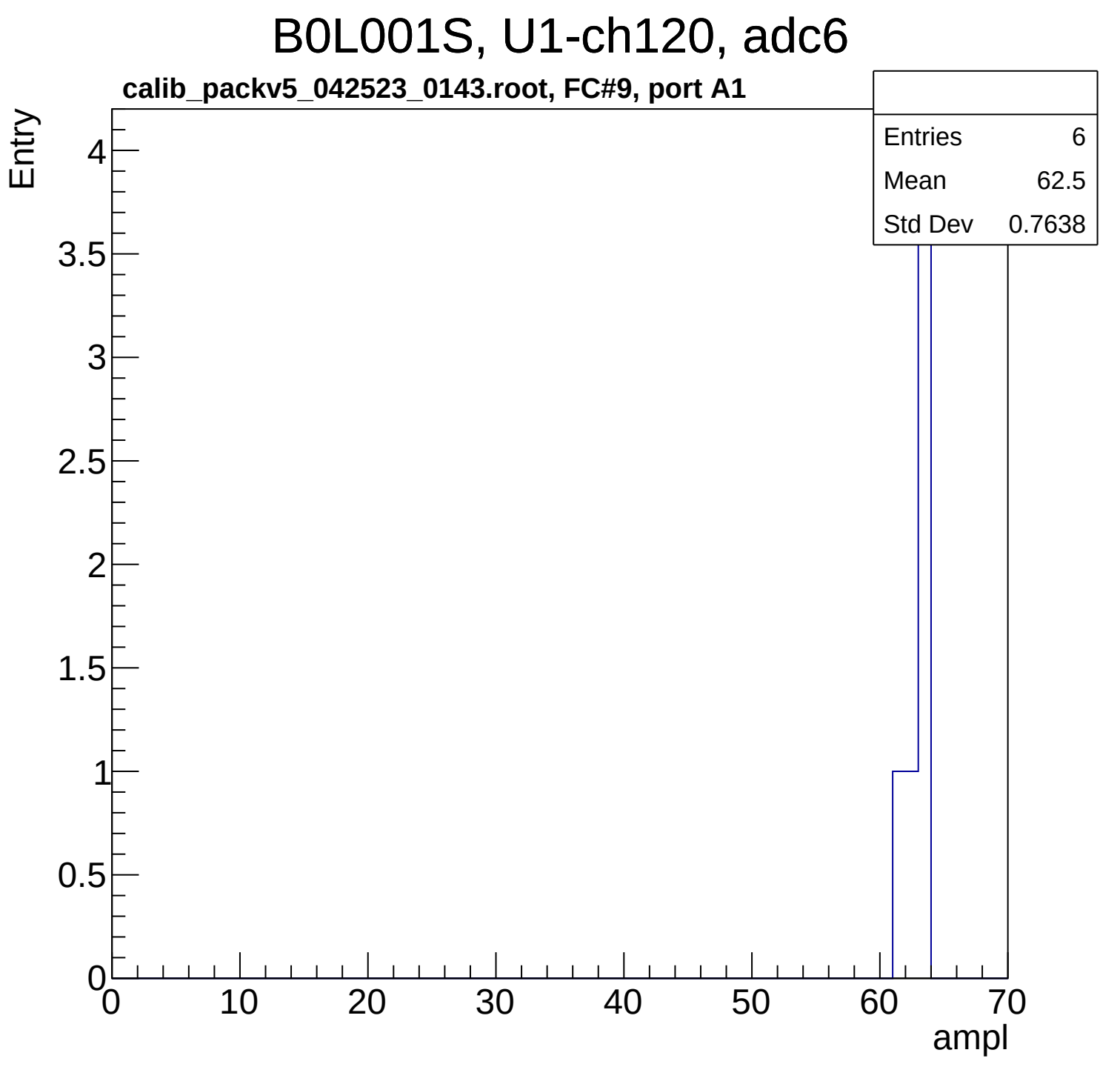

B0L001S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 37.32
Std Dev 3.363
Gaus mean : 37.5180
Gaus Width: 3.2924
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

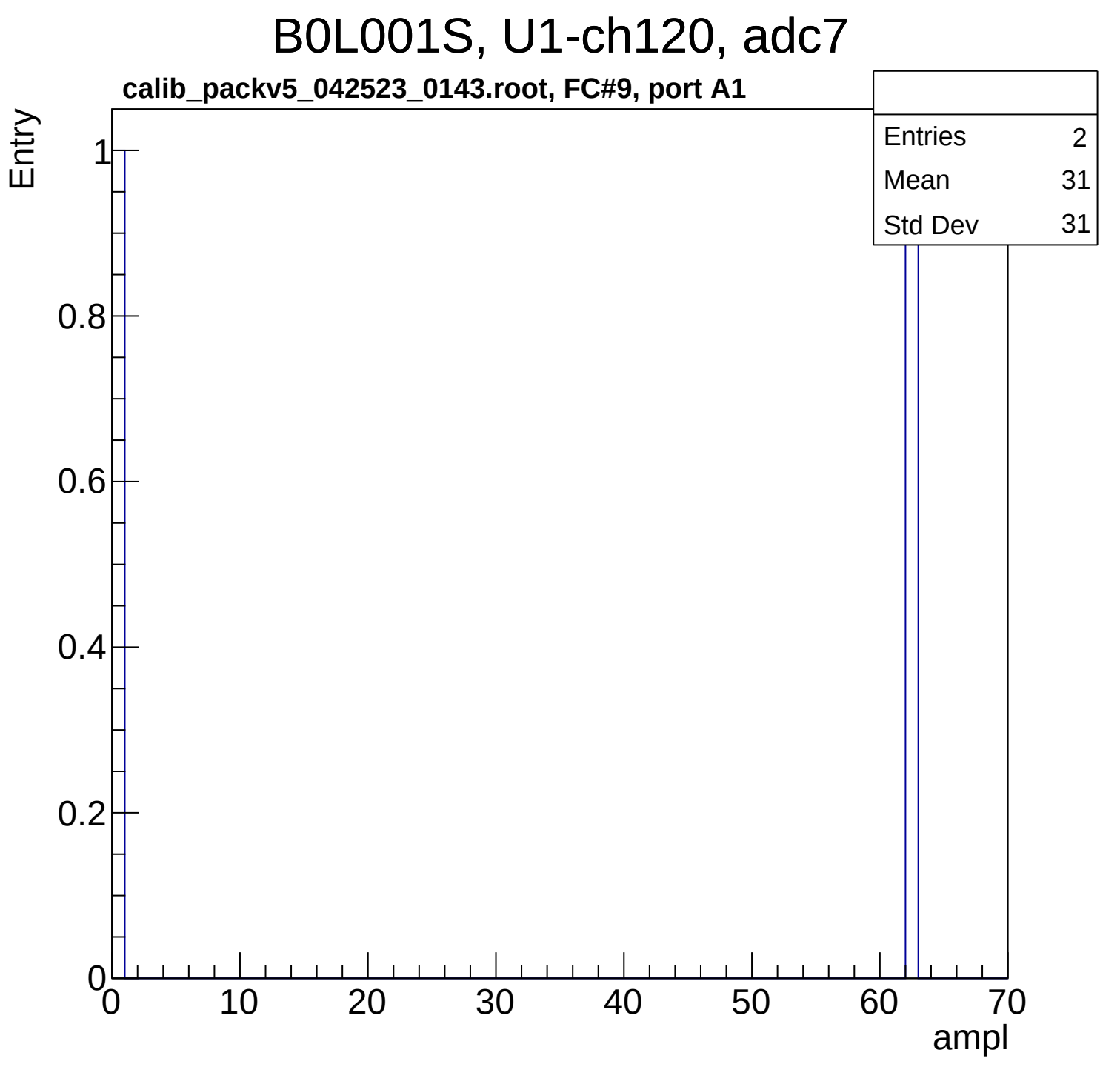

B0L001S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 70
Mean 44.06
Std Dev 3.488
Gaus mean : 44.3625
Gaus Width: 3.2012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

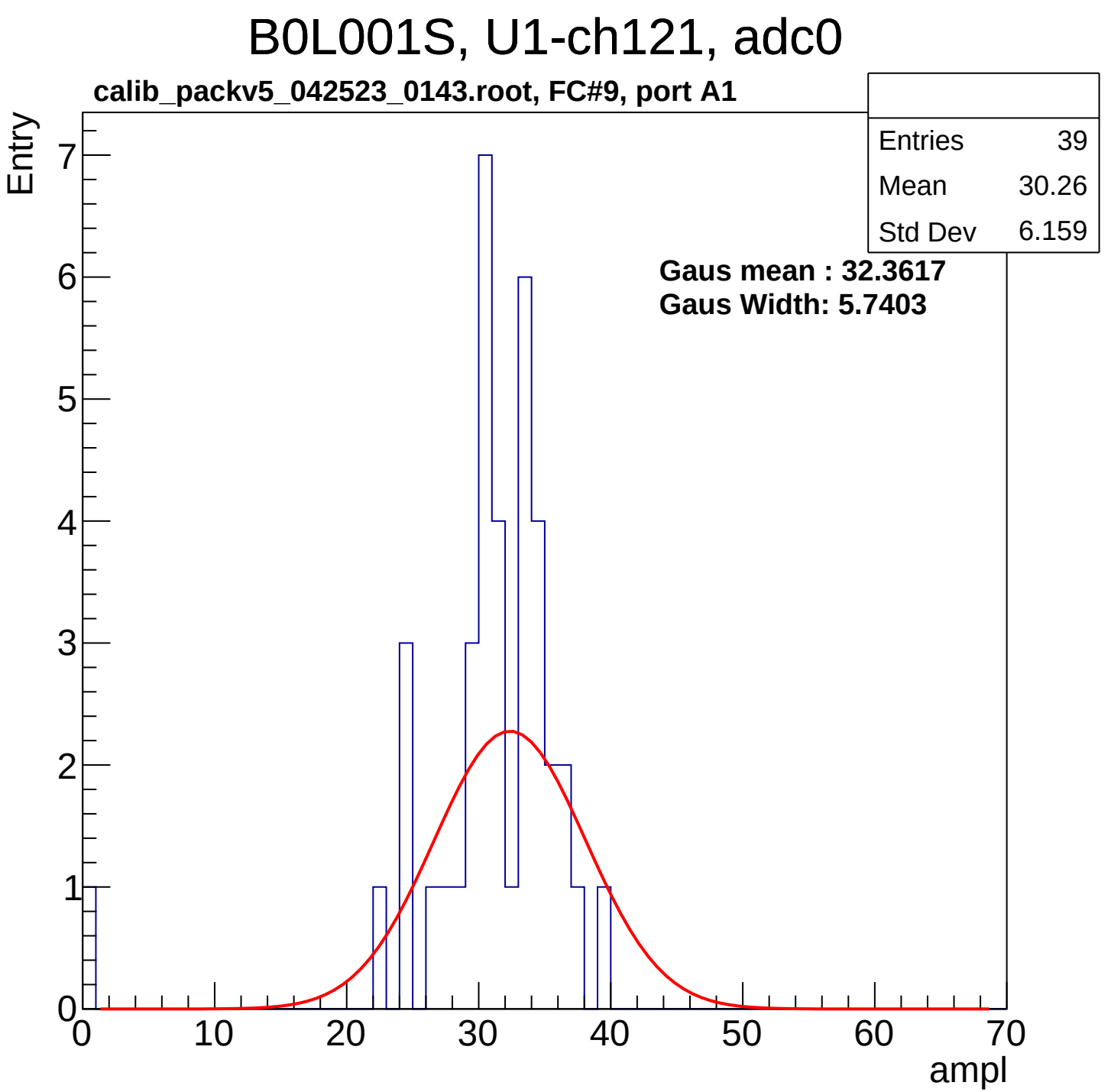

B0L001S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 50.63
Std Dev 3.188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

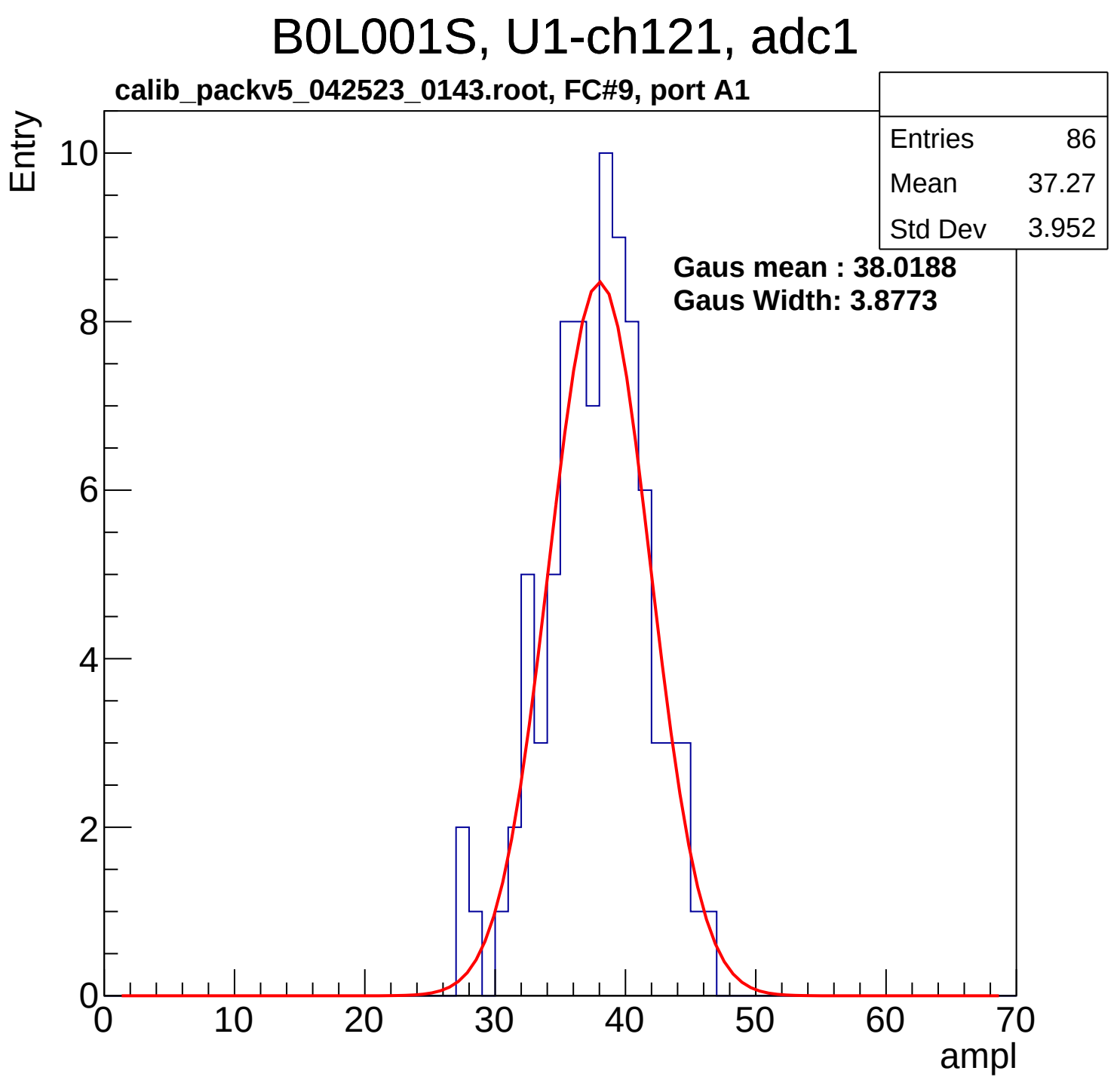

B0L001S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 53
Mean 56.28
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

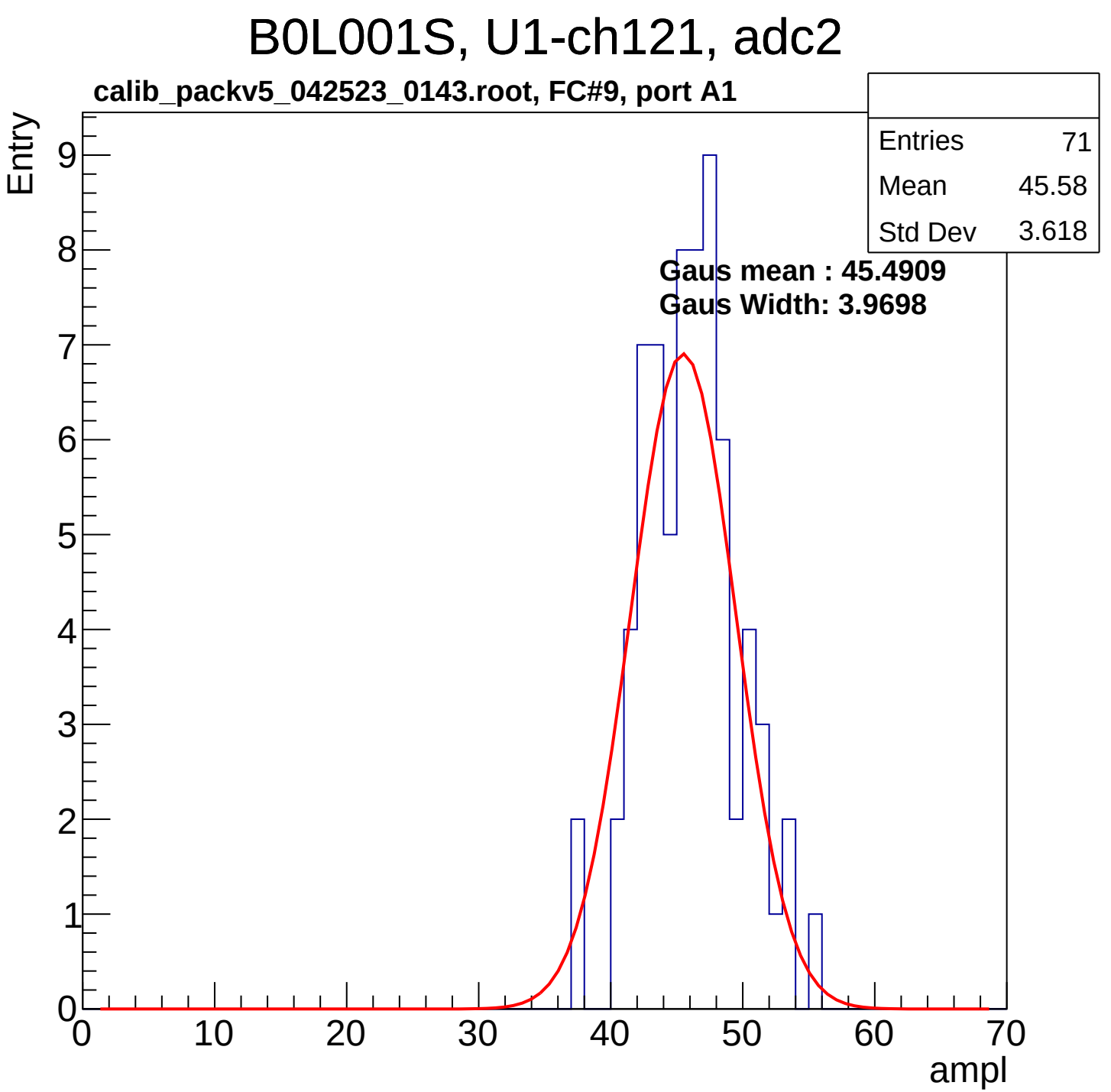

B0L001S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 58.4
Std Dev 9.596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

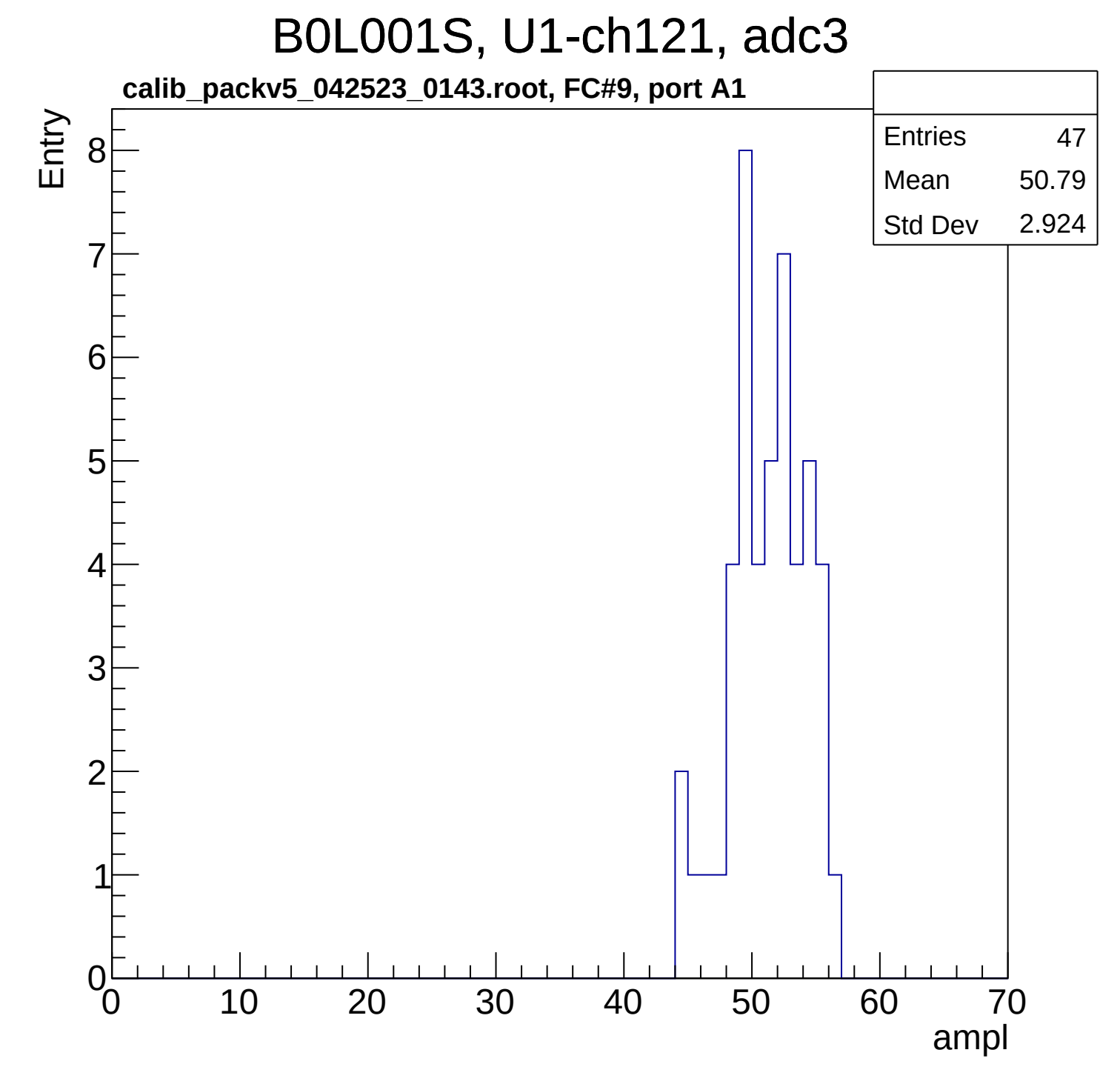

B0L001S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 6
Mean 62.5
Std Dev 0.7638
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

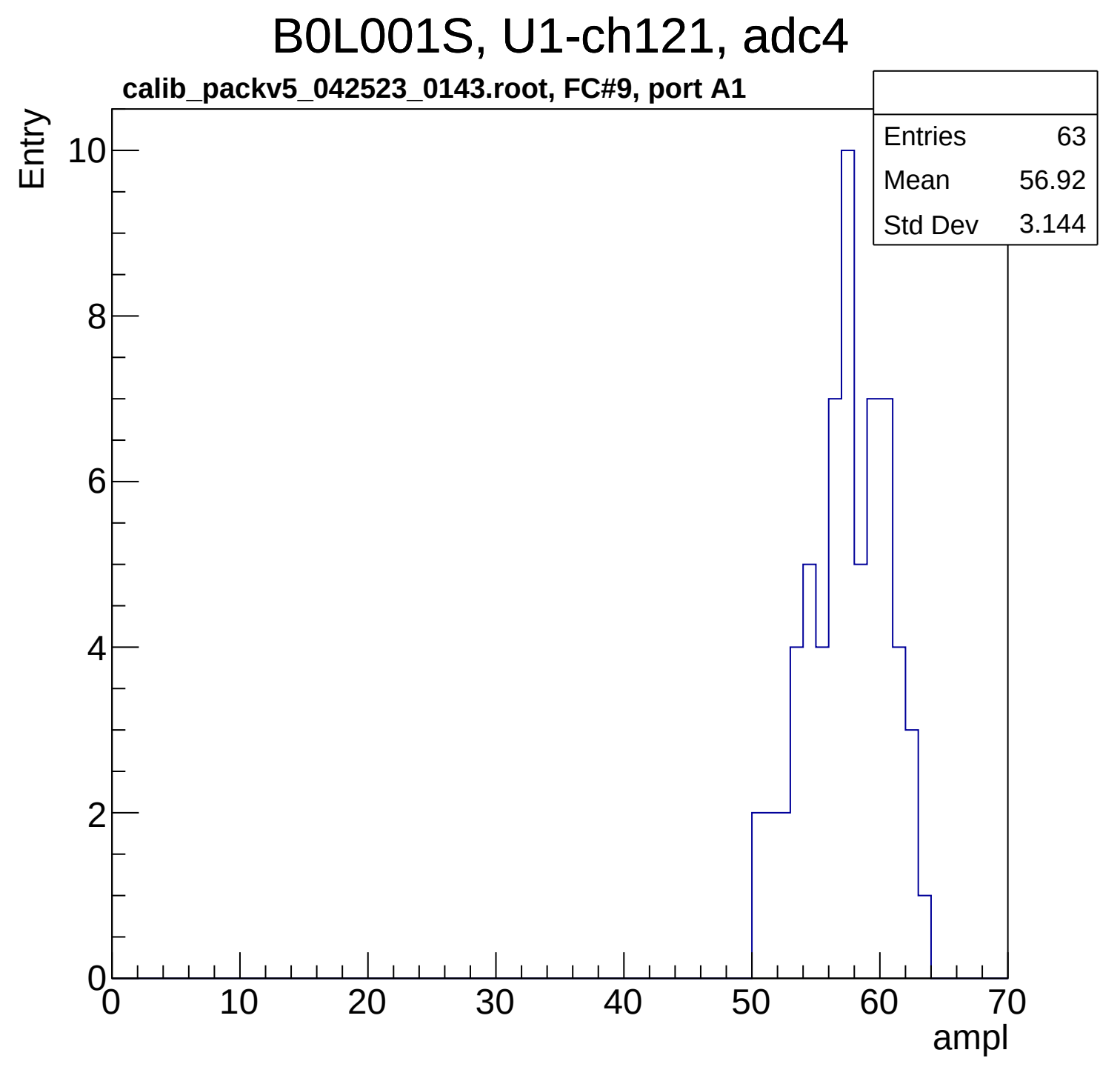

B0L001S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

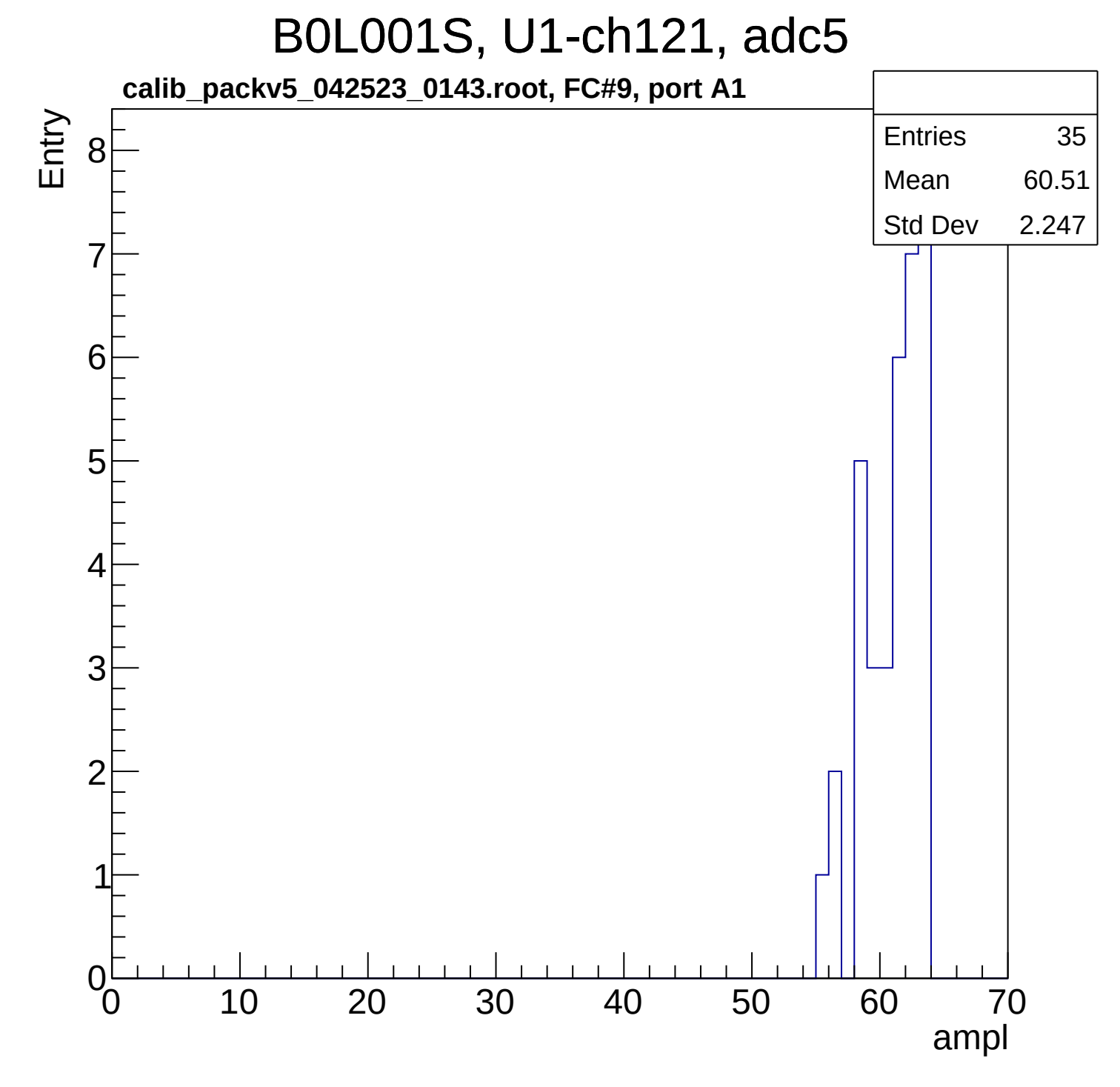

B0L001S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 30.26
Std Dev 6.159
Gaus mean : 32.3617
Gaus Width: 5.7403
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 86
Mean 37.27
Std Dev 3.952
Gaus mean : 38.0188
Gaus Width: 3.8773
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 45.58
Std Dev 3.618
Gaus mean : 45.4909
Gaus Width: 3.9698
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 47
Mean 50.79
Std Dev 2.924
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 56.92
Std Dev 3.144
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 35
Mean 60.51
Std Dev 2.247
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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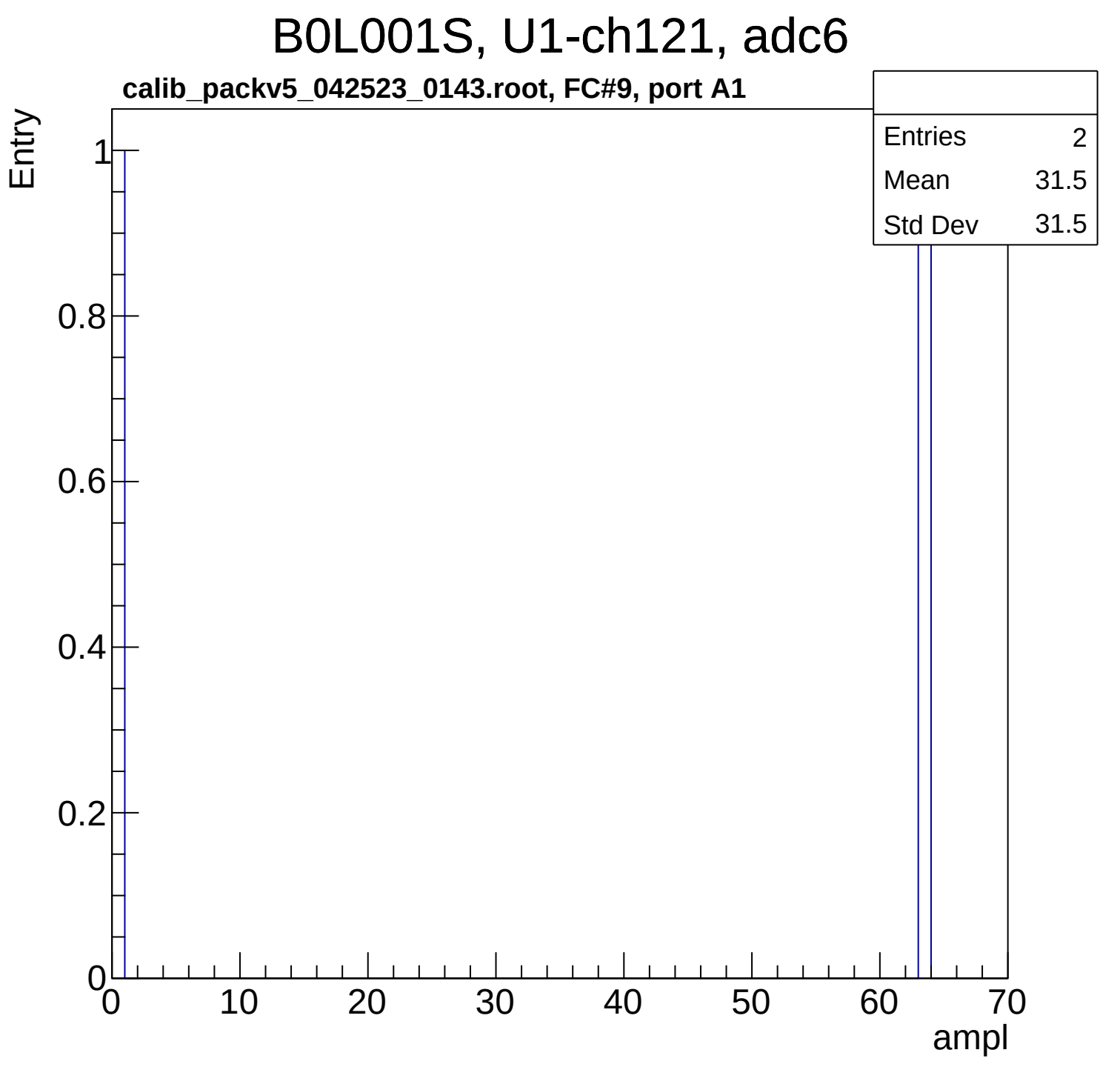
40
50
60
70


B0L001S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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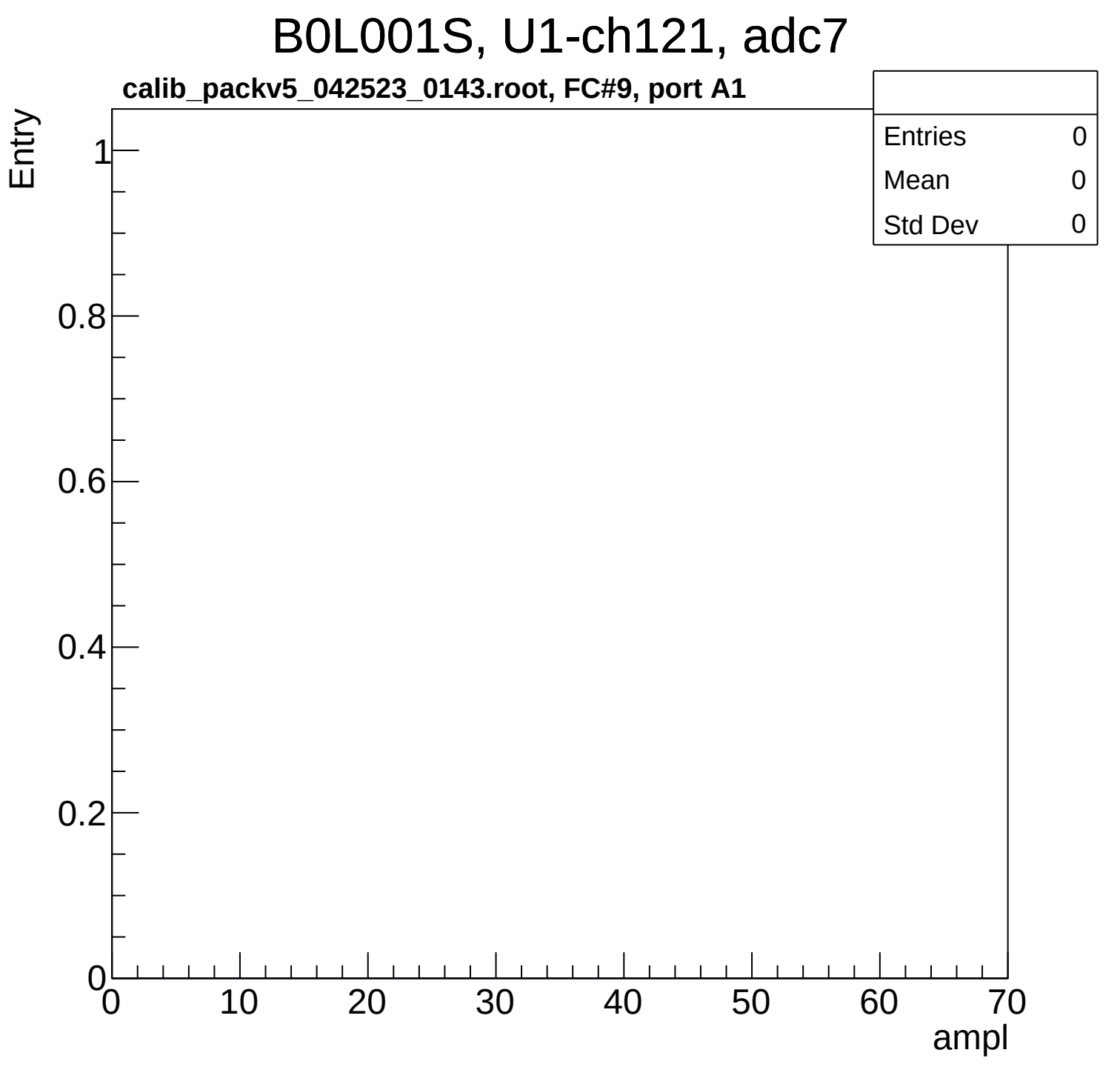
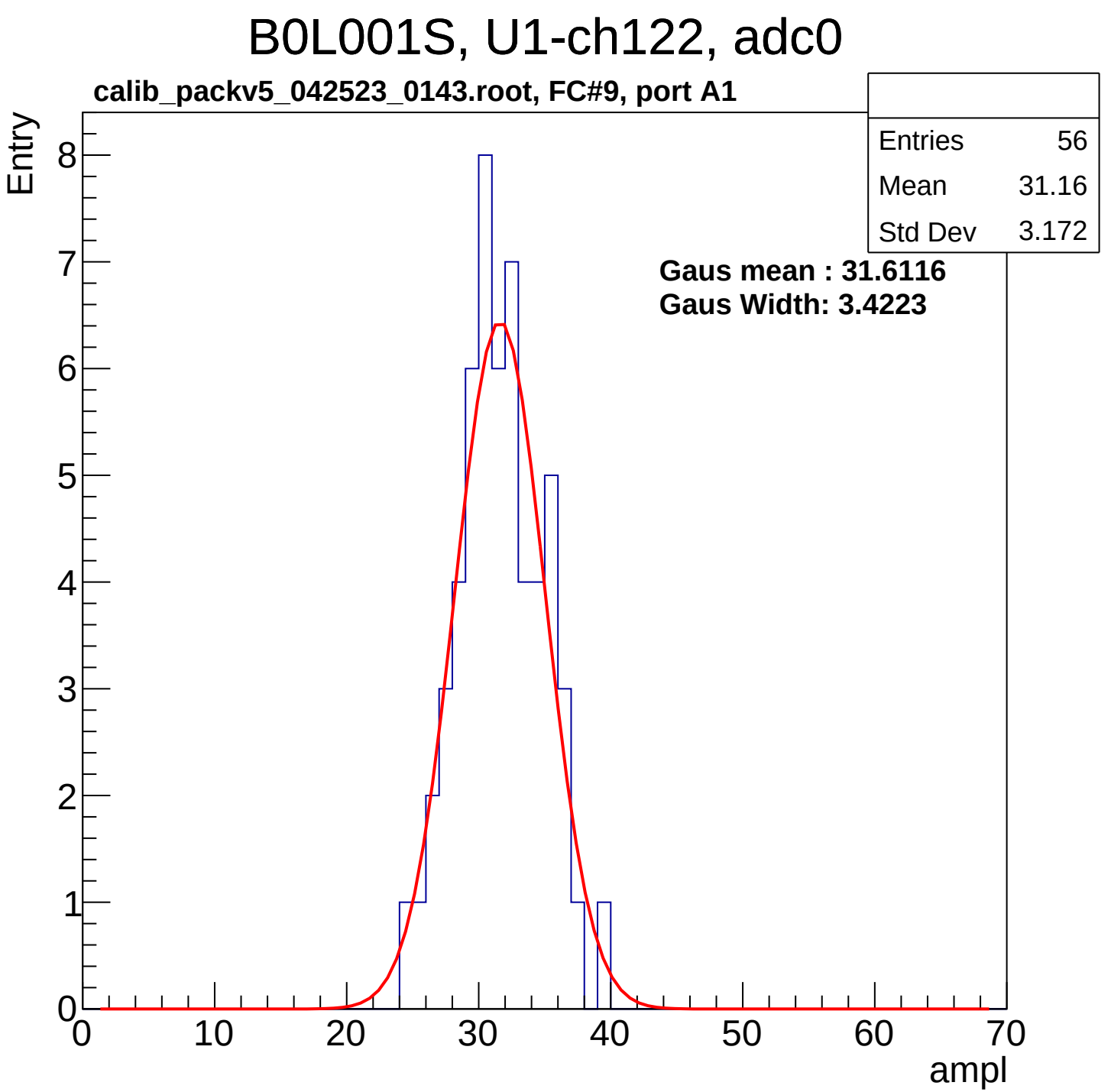
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 56
Mean 31.16
Std Dev 3.172
Gaus mean : 31.6116
Gaus Width: 3.4223
Entry
ampl
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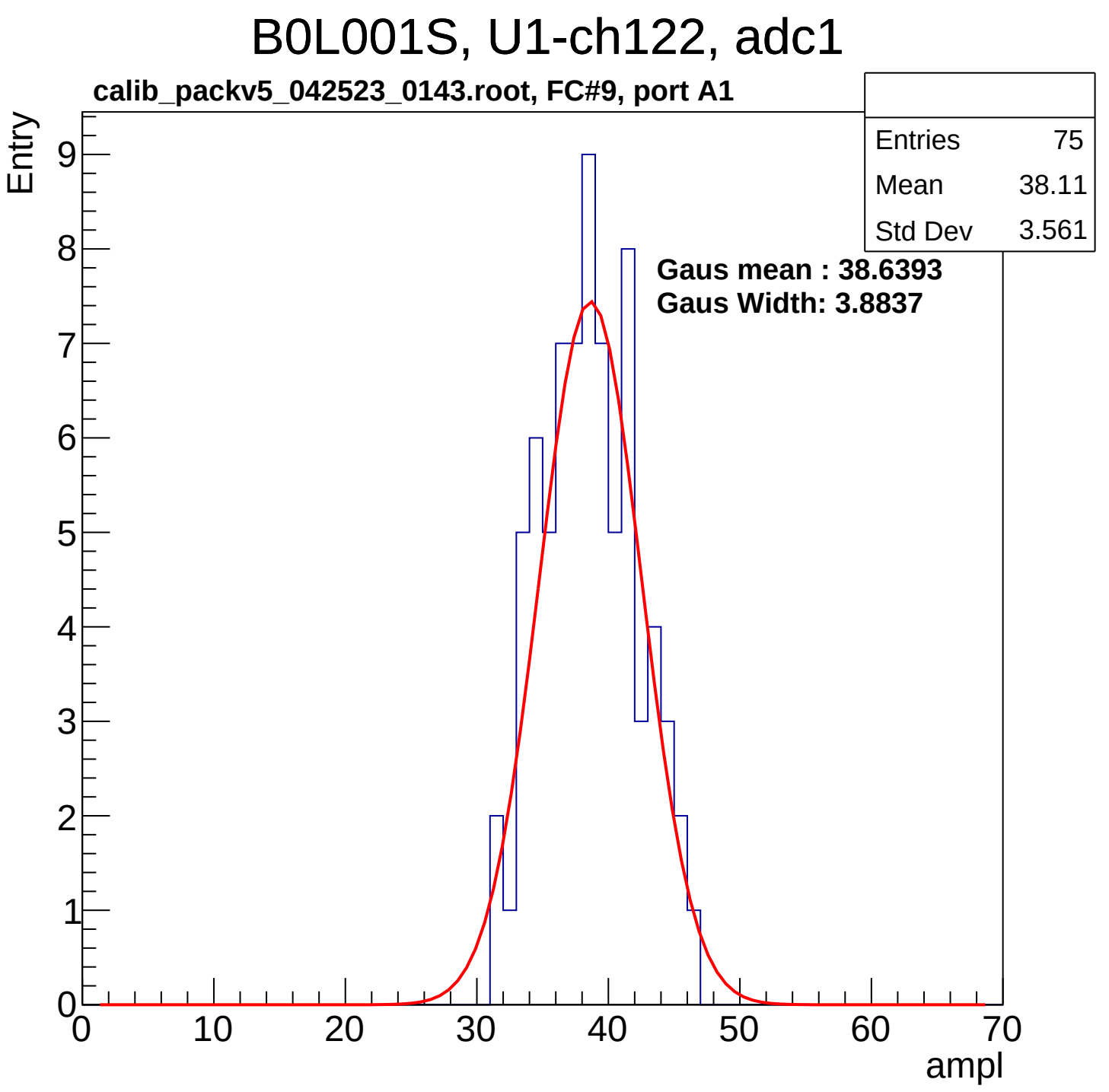
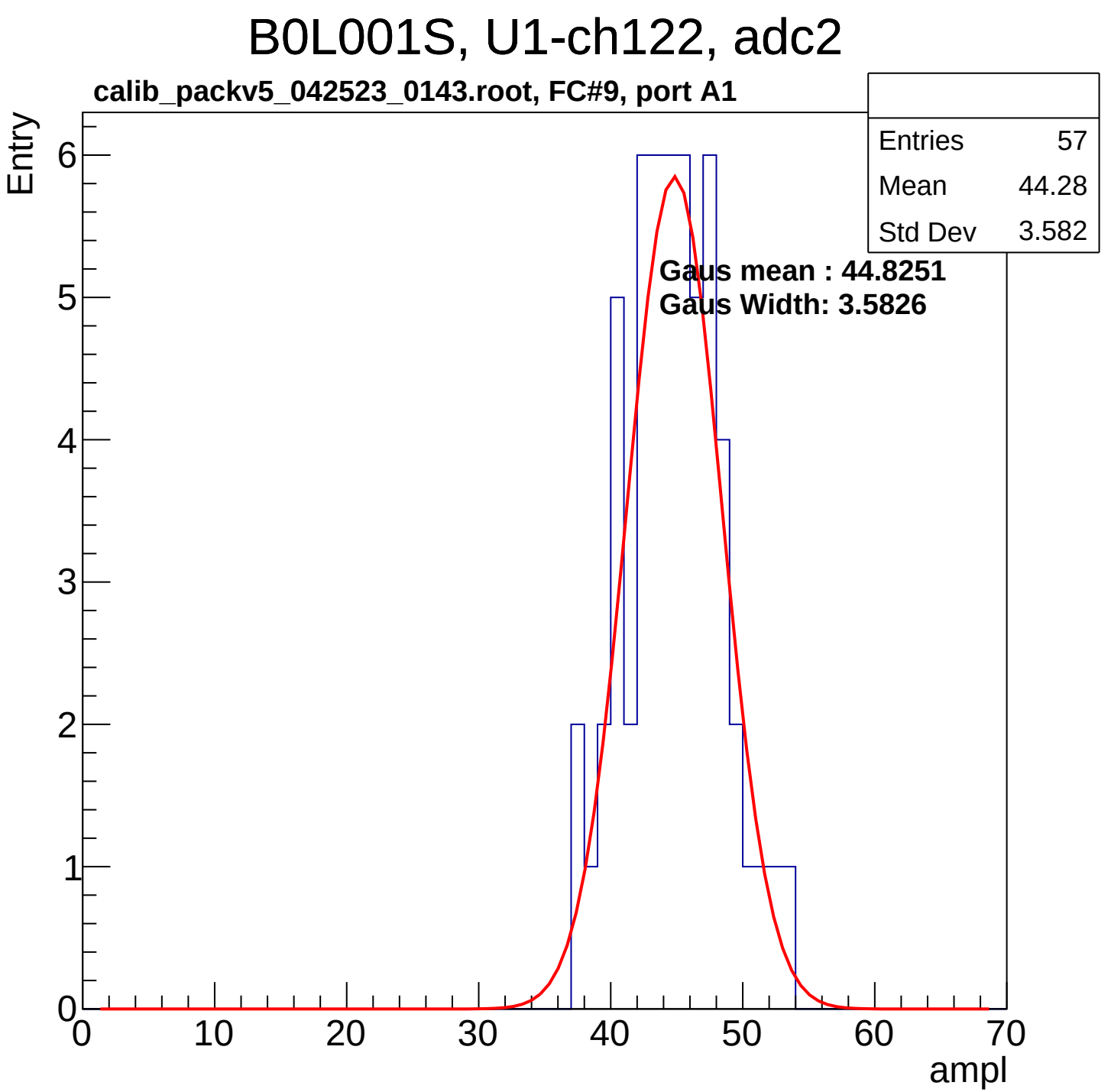
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 75
Mean 38.11
Std Dev 3.561
Gaus mean : 38.6393
Gaus Width: 3.8837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 57
Mean 44.28
Std Dev 3.582
Gaus mean : 44.8251
Gaus Width: 3.5826
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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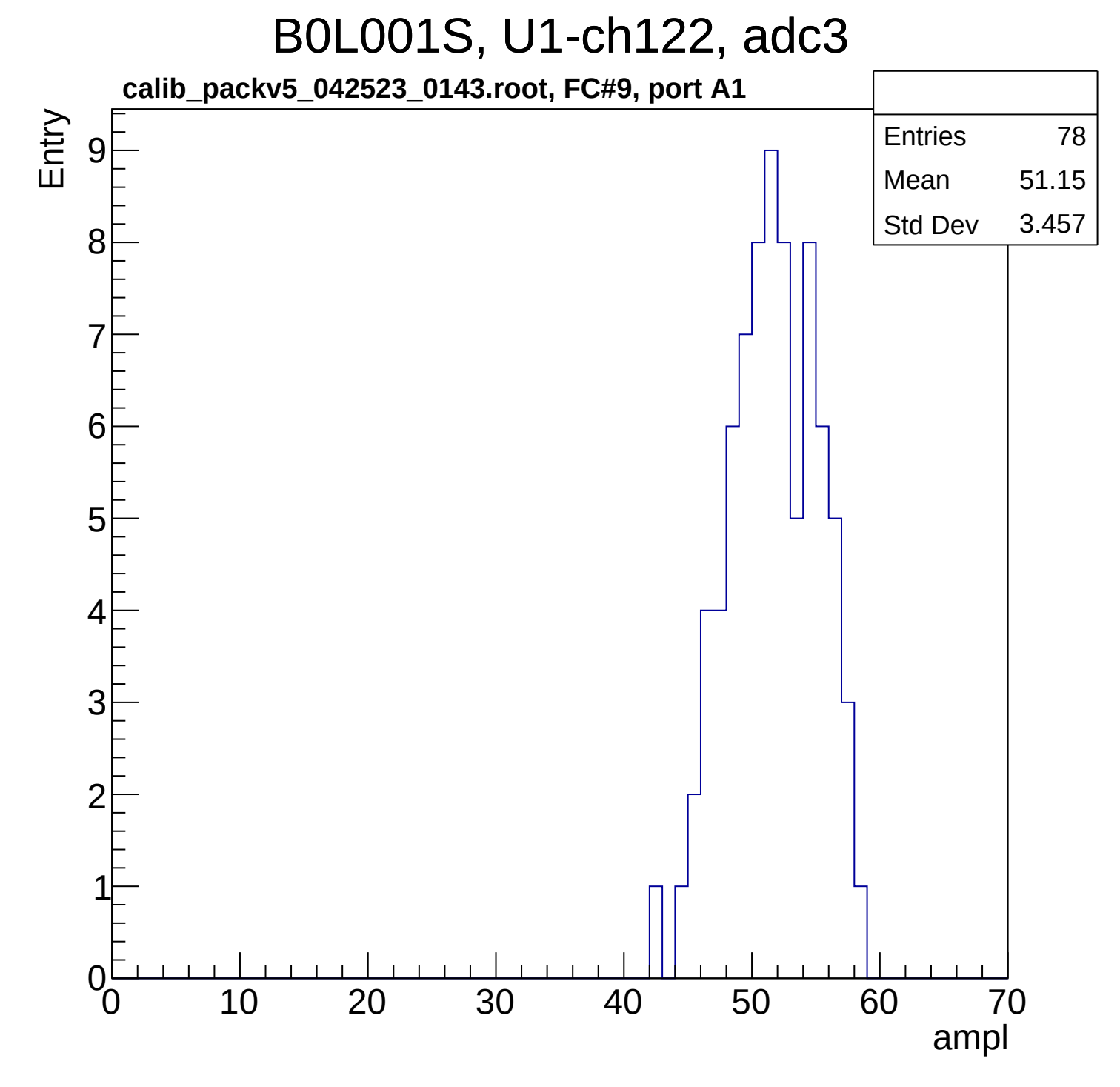
40
50
60
70


B0L001S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 51.15
Std Dev 3.457
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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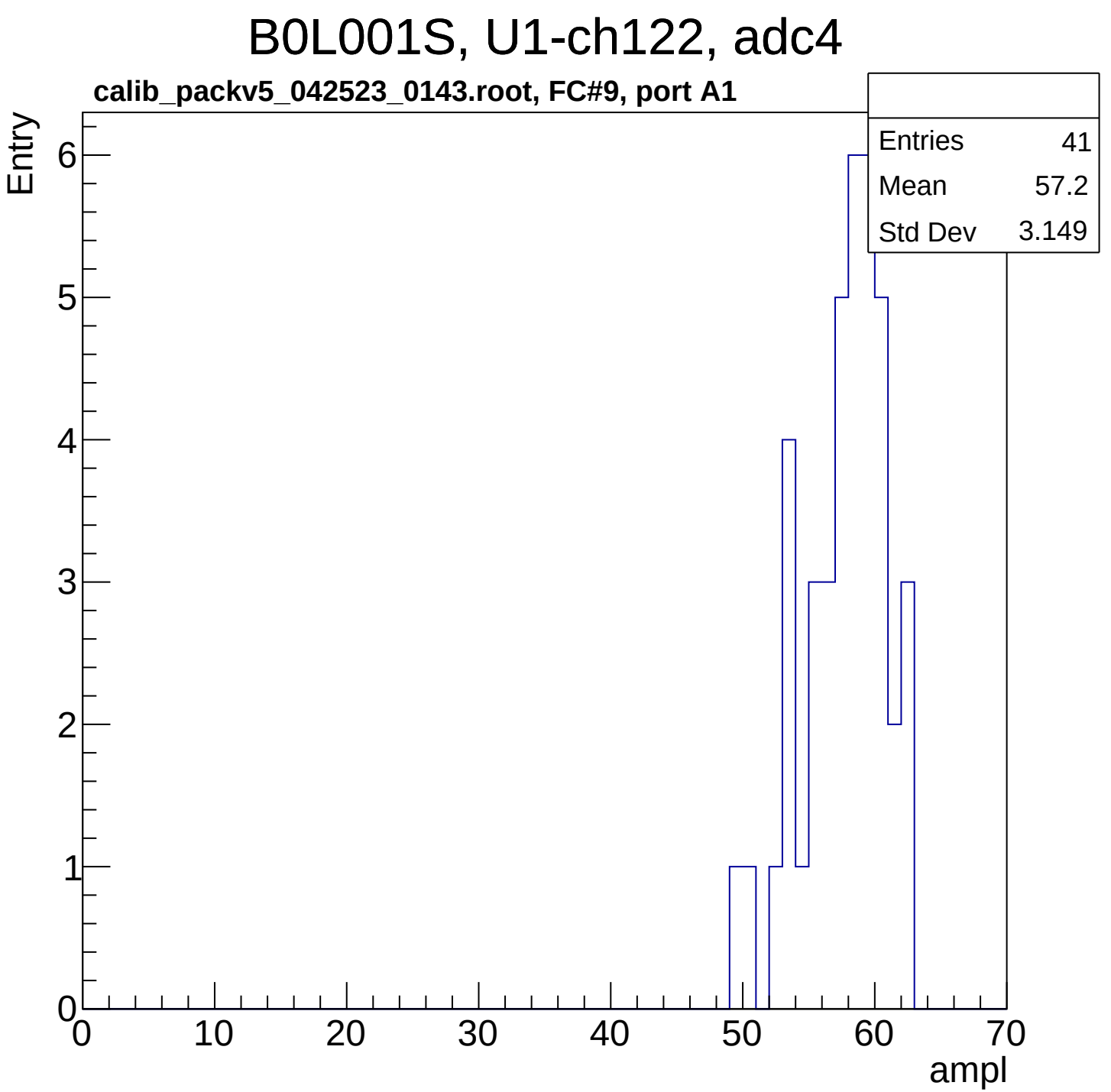
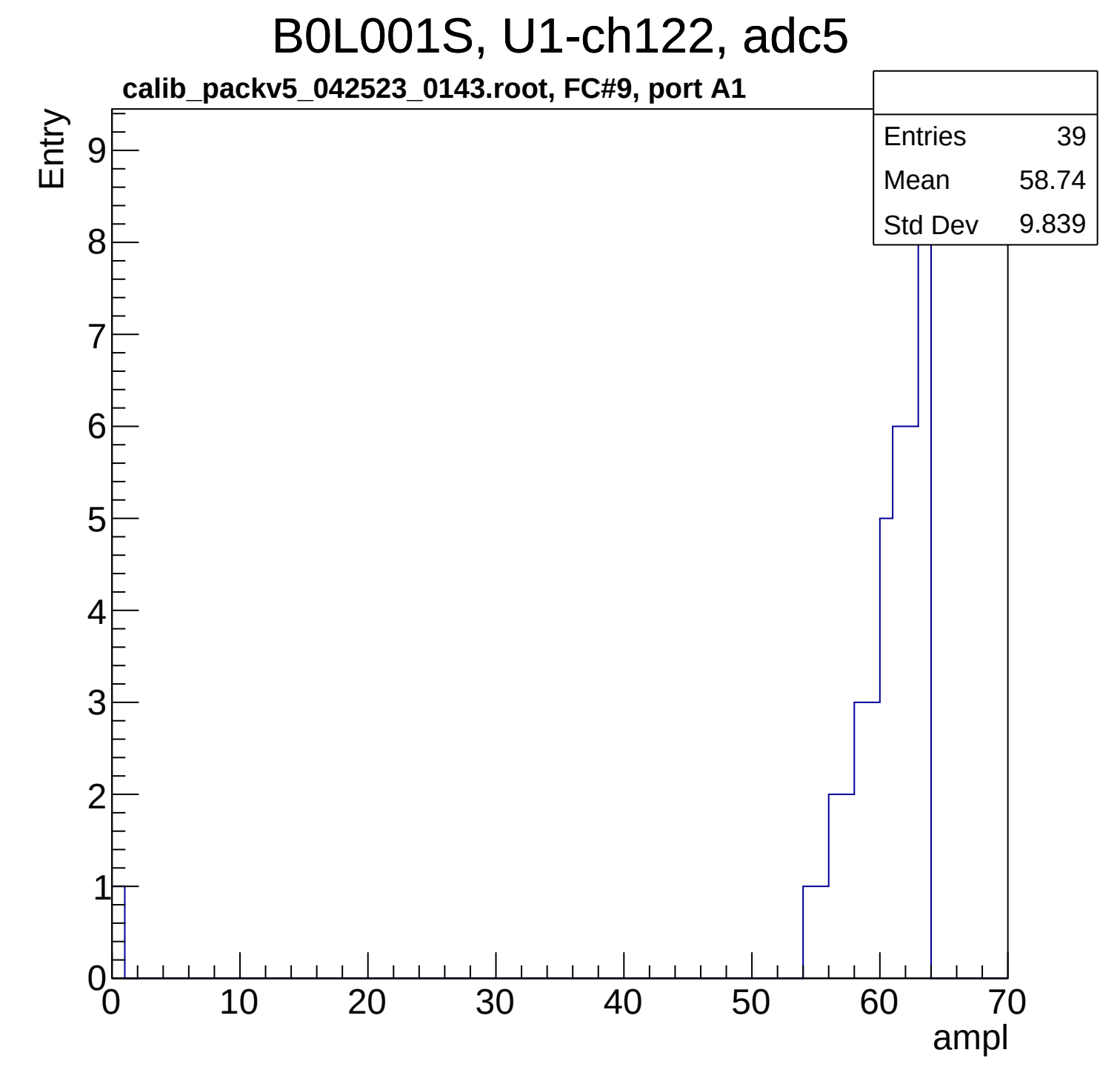
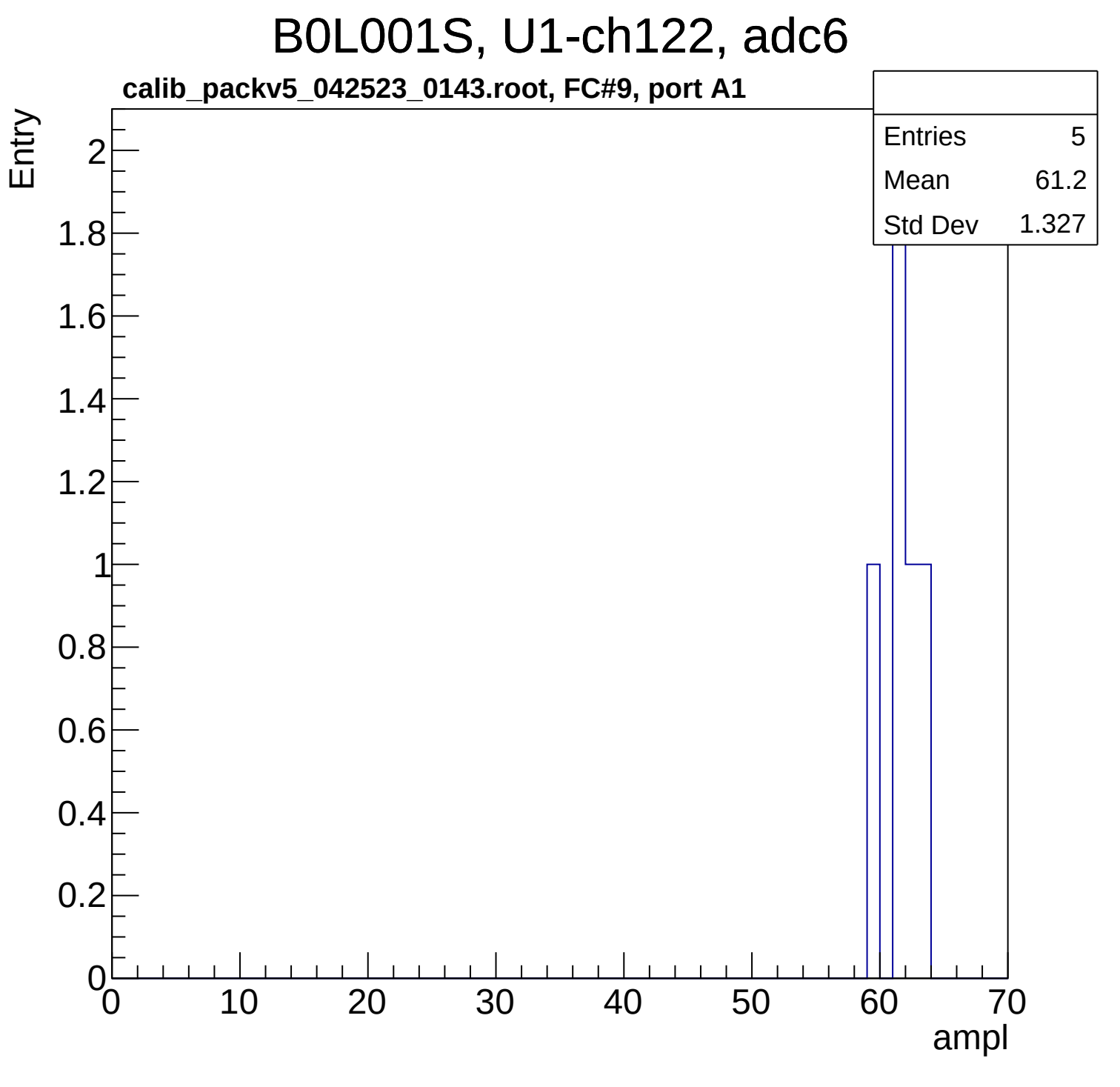
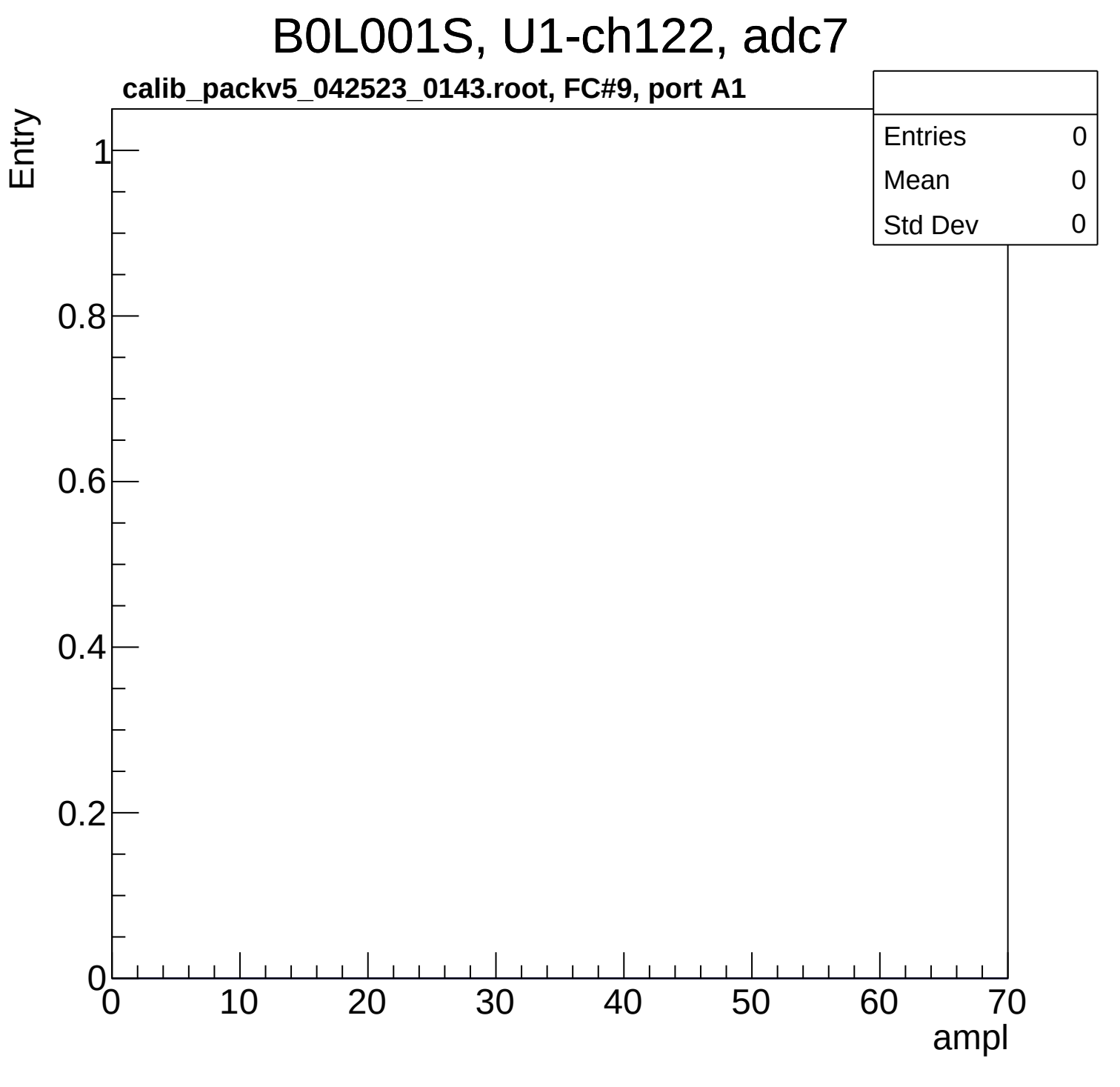

B0L001S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 41
Mean 57.2
Std Dev 3.149
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 39
Mean 58.74
Std Dev 9.839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 5
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 0
Mean 0
Std Dev 0
Entry
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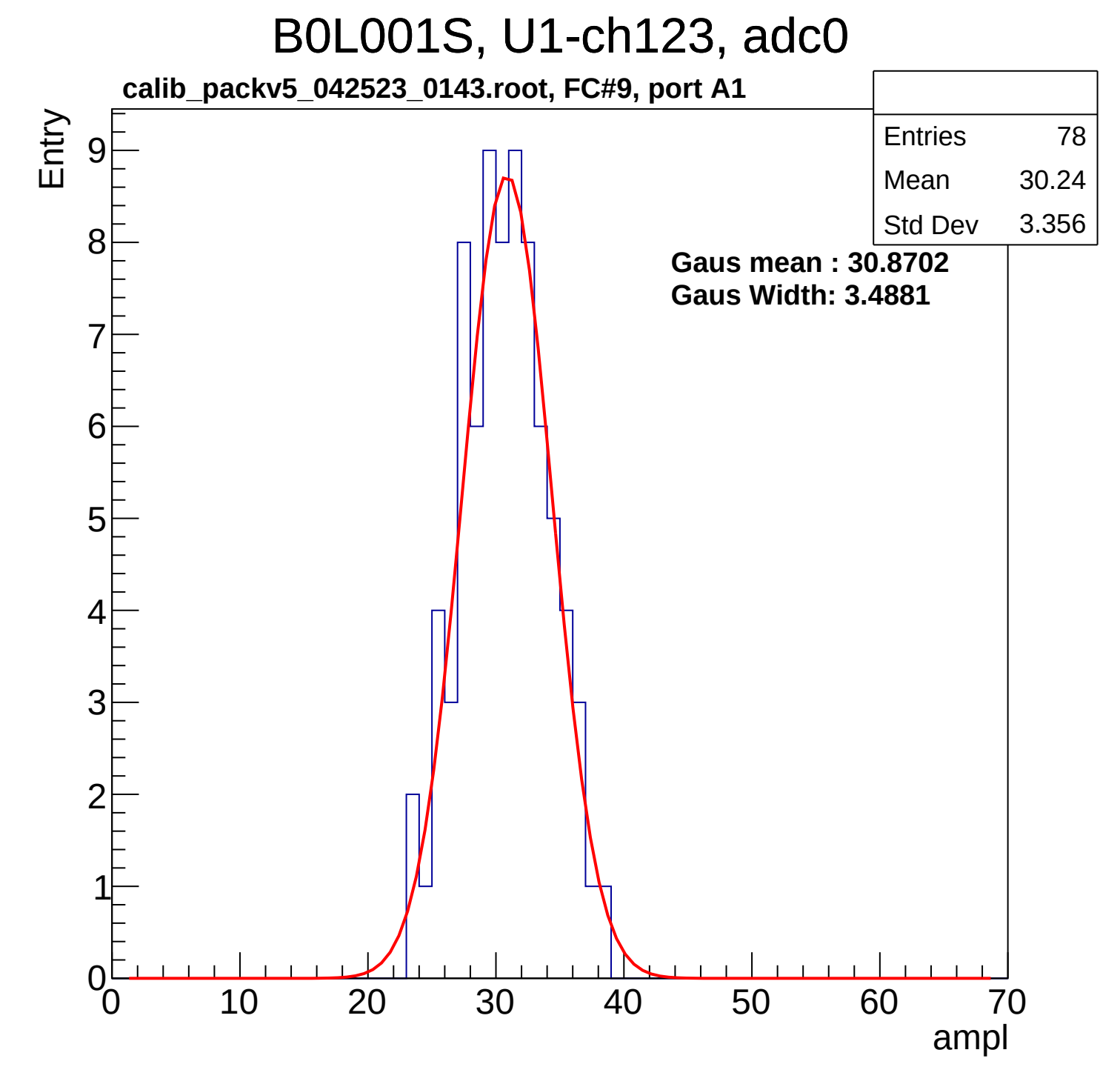
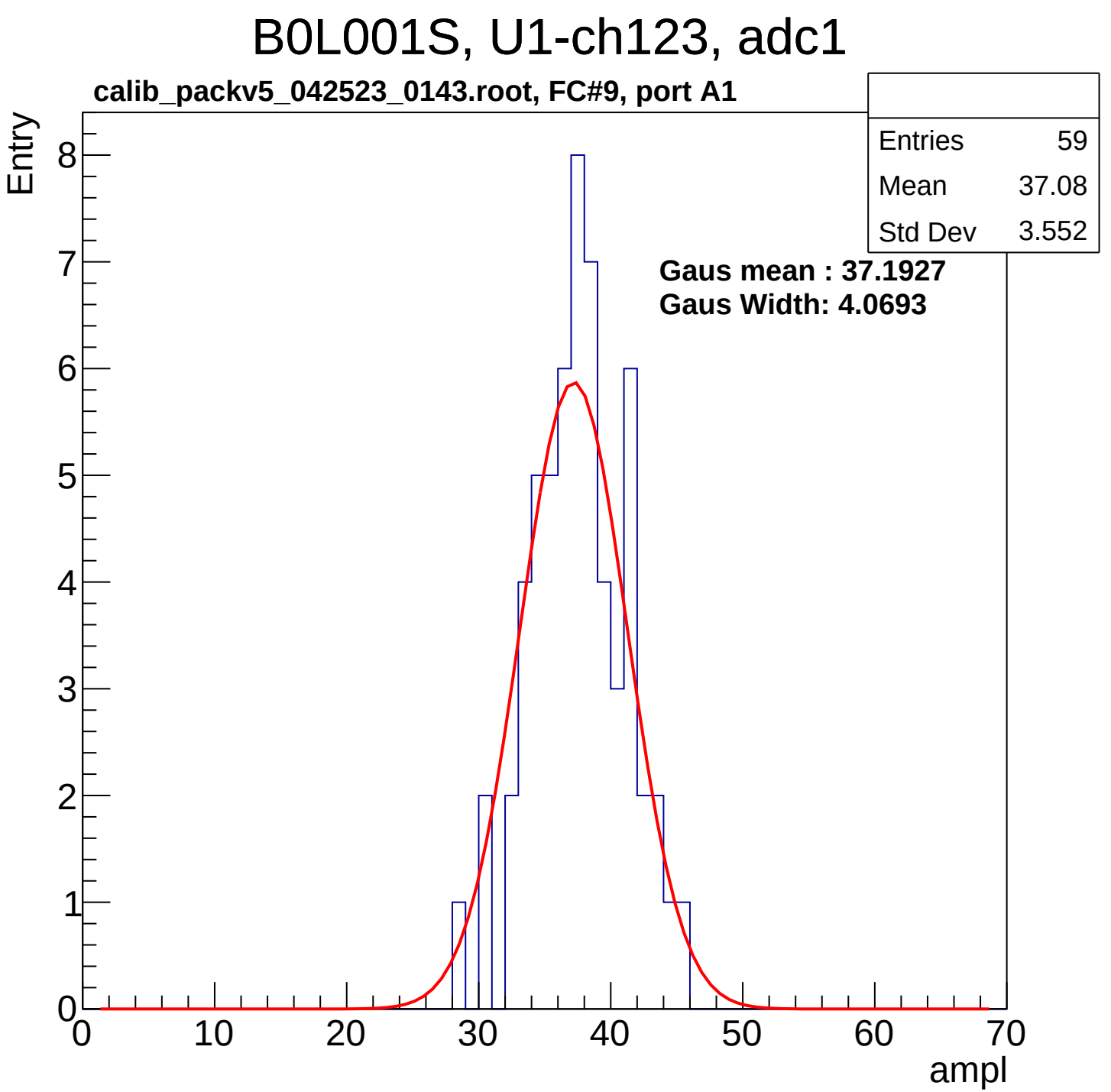
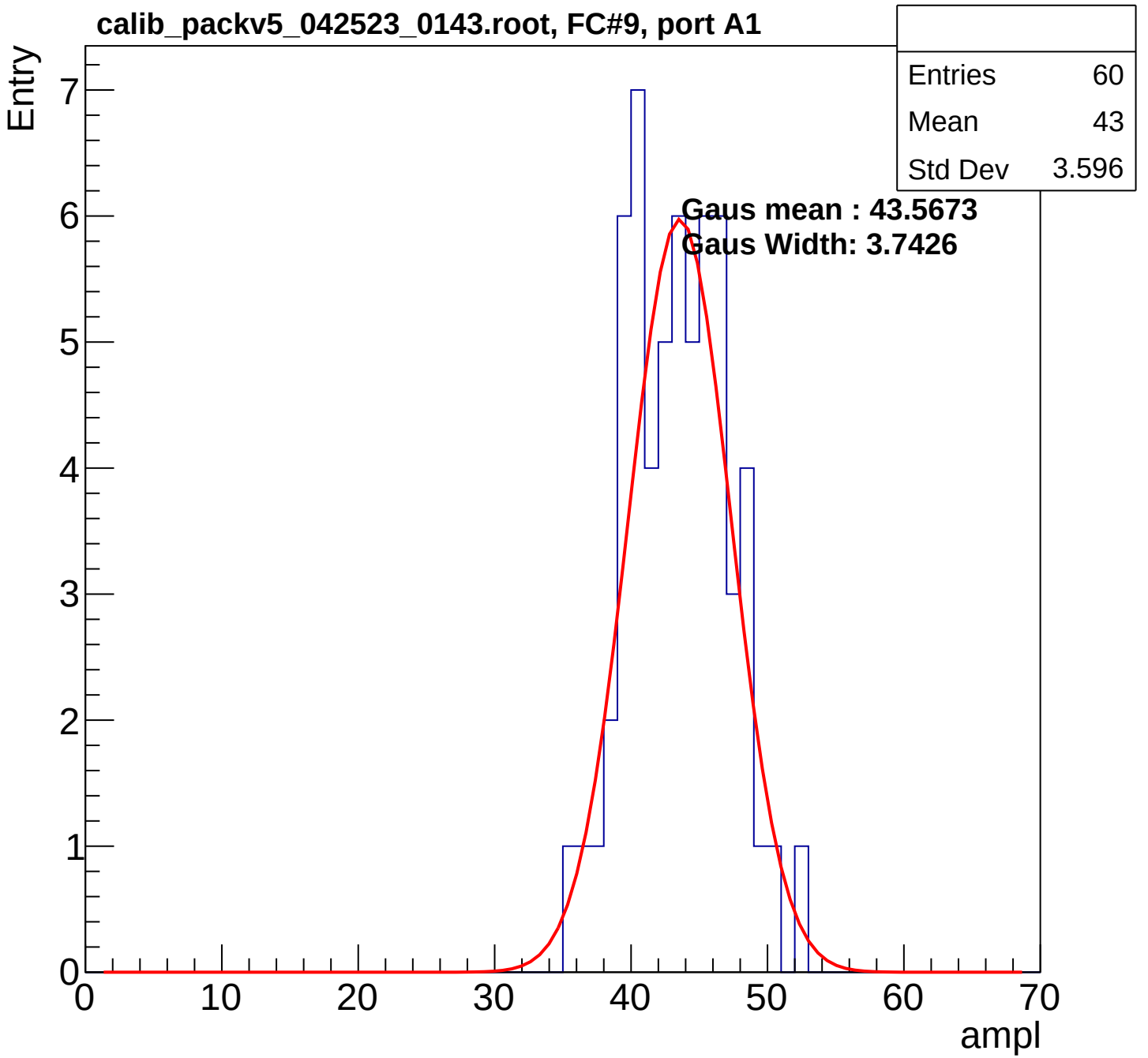
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 30.24
Std Dev 3.356
Gaus mean : 30.8702
Gaus Width: 3.4881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 37.08
Std Dev 3.552
Gaus mean : 37.1927
Gaus Width: 4.0693
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 43
Std Dev 3.596
Gaus mean : 43.5673
Gaus Width: 3.7426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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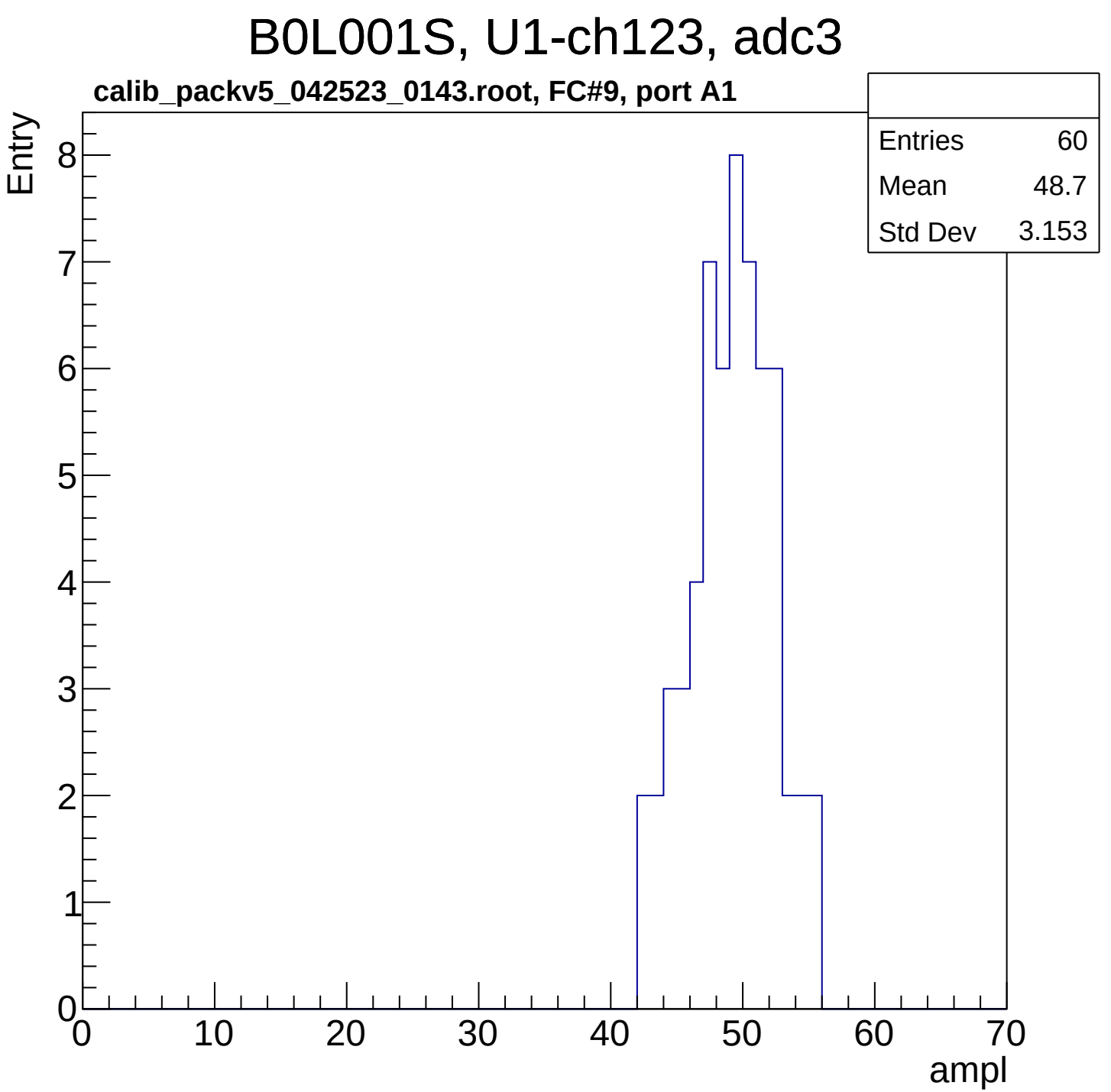
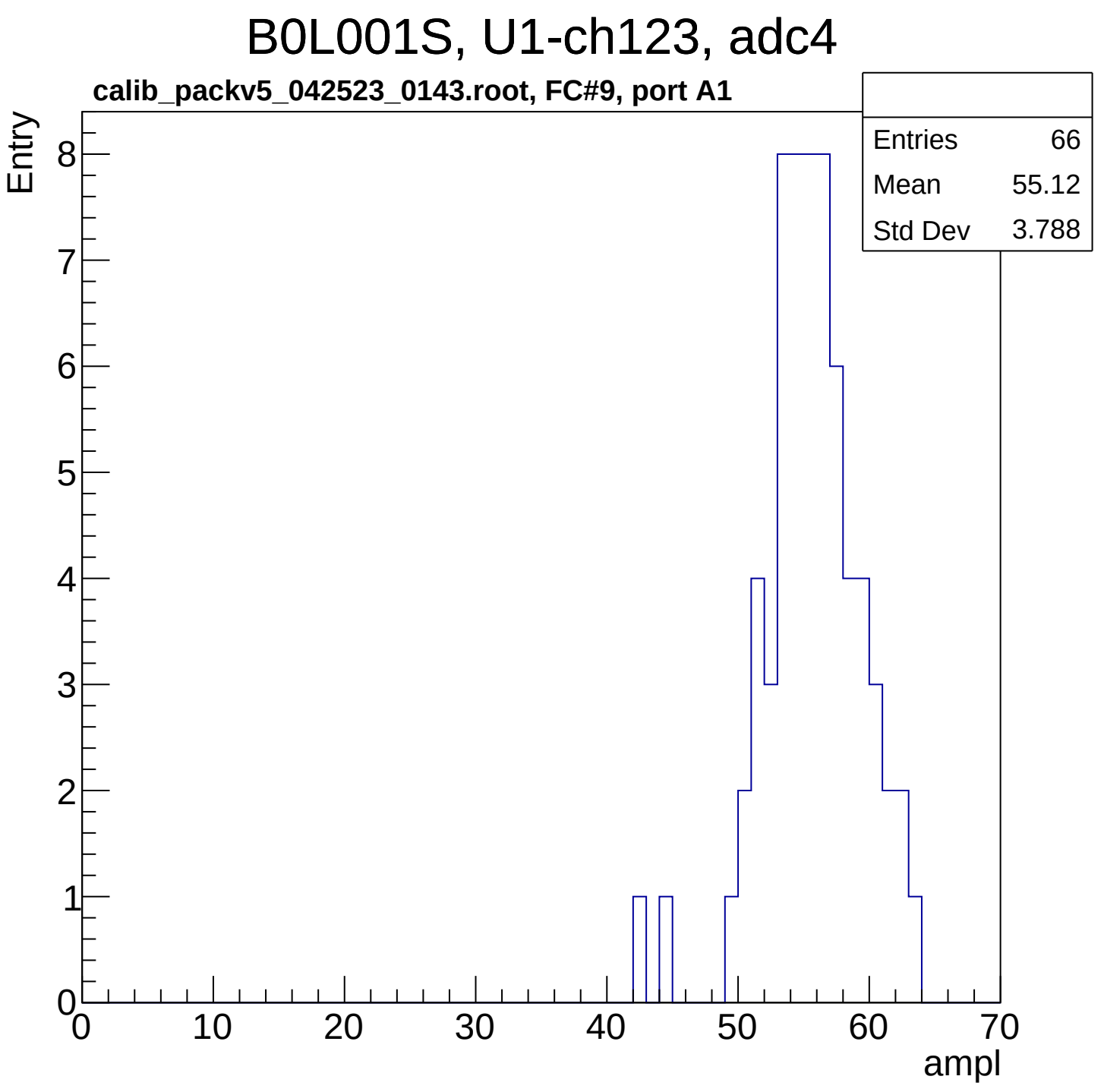
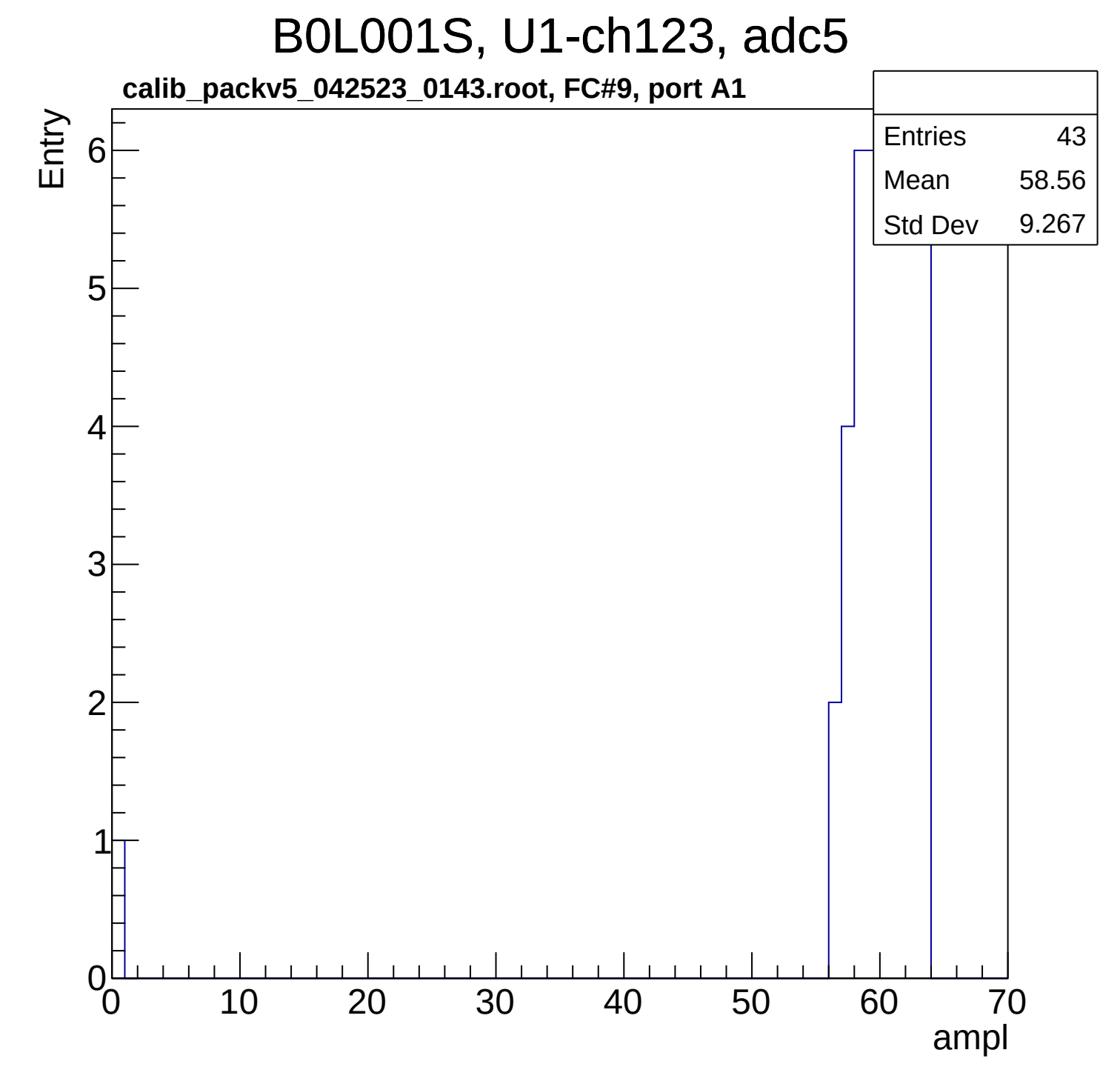
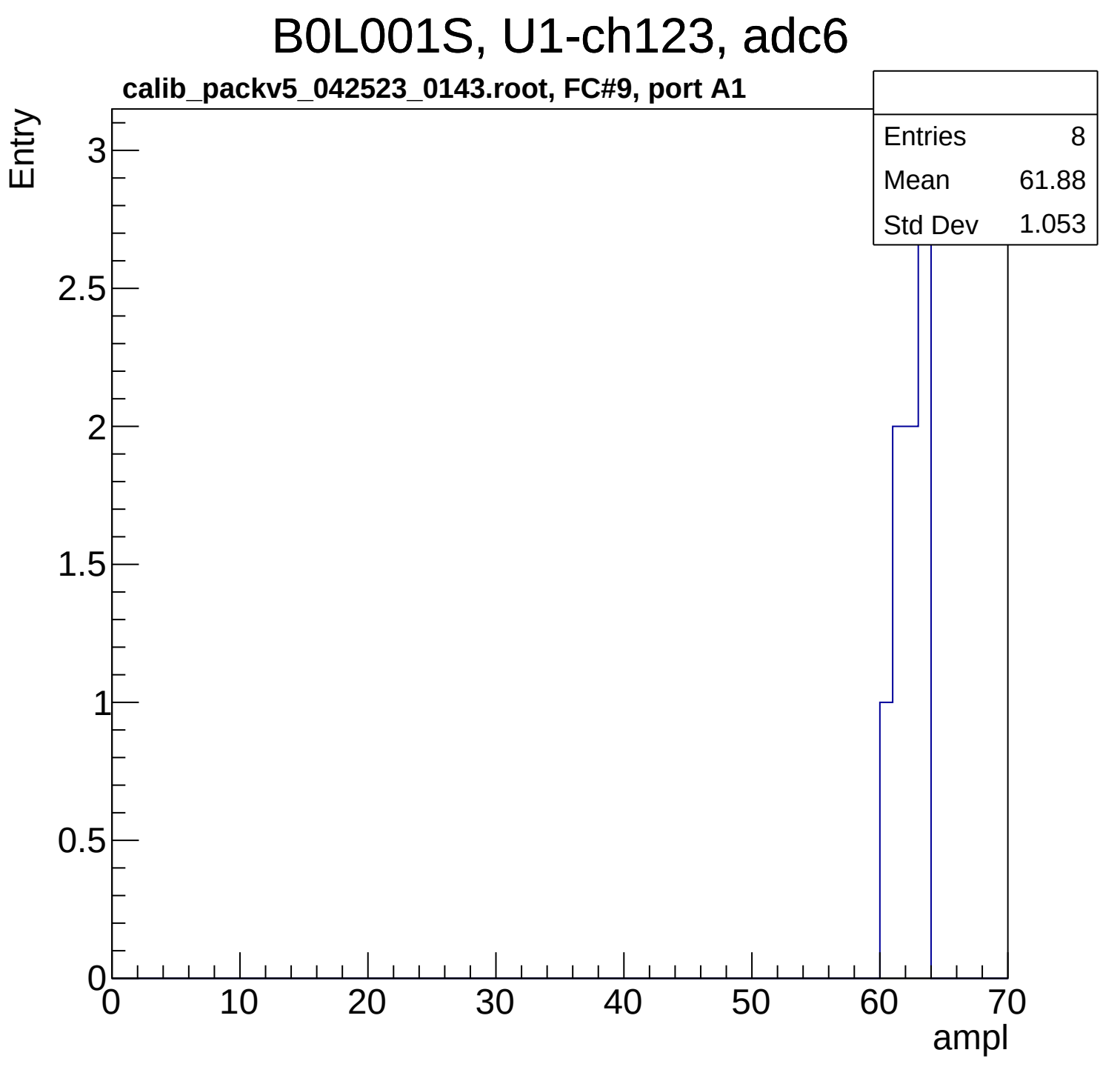
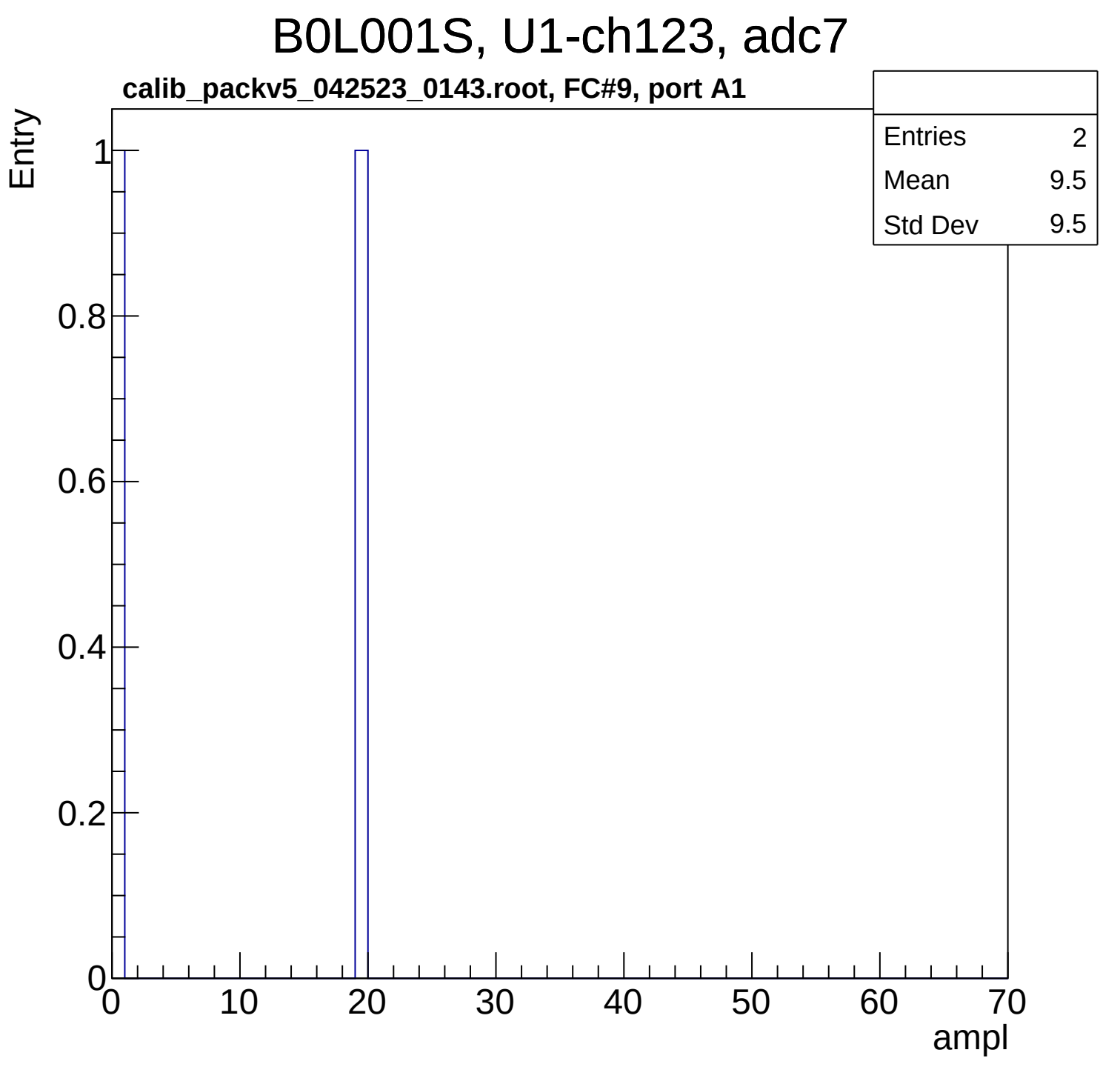
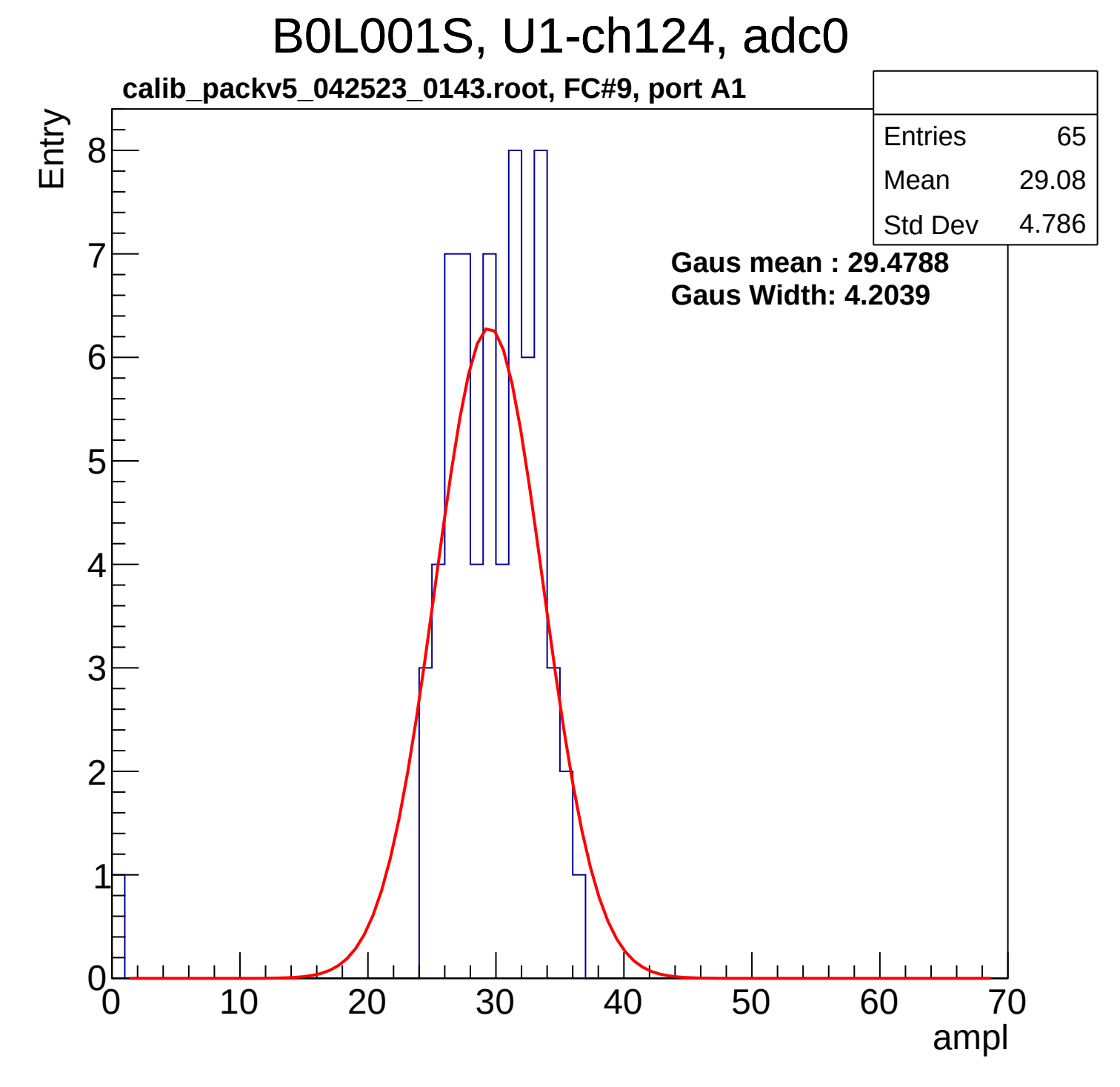

B0L001S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 60
Mean 48.7
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

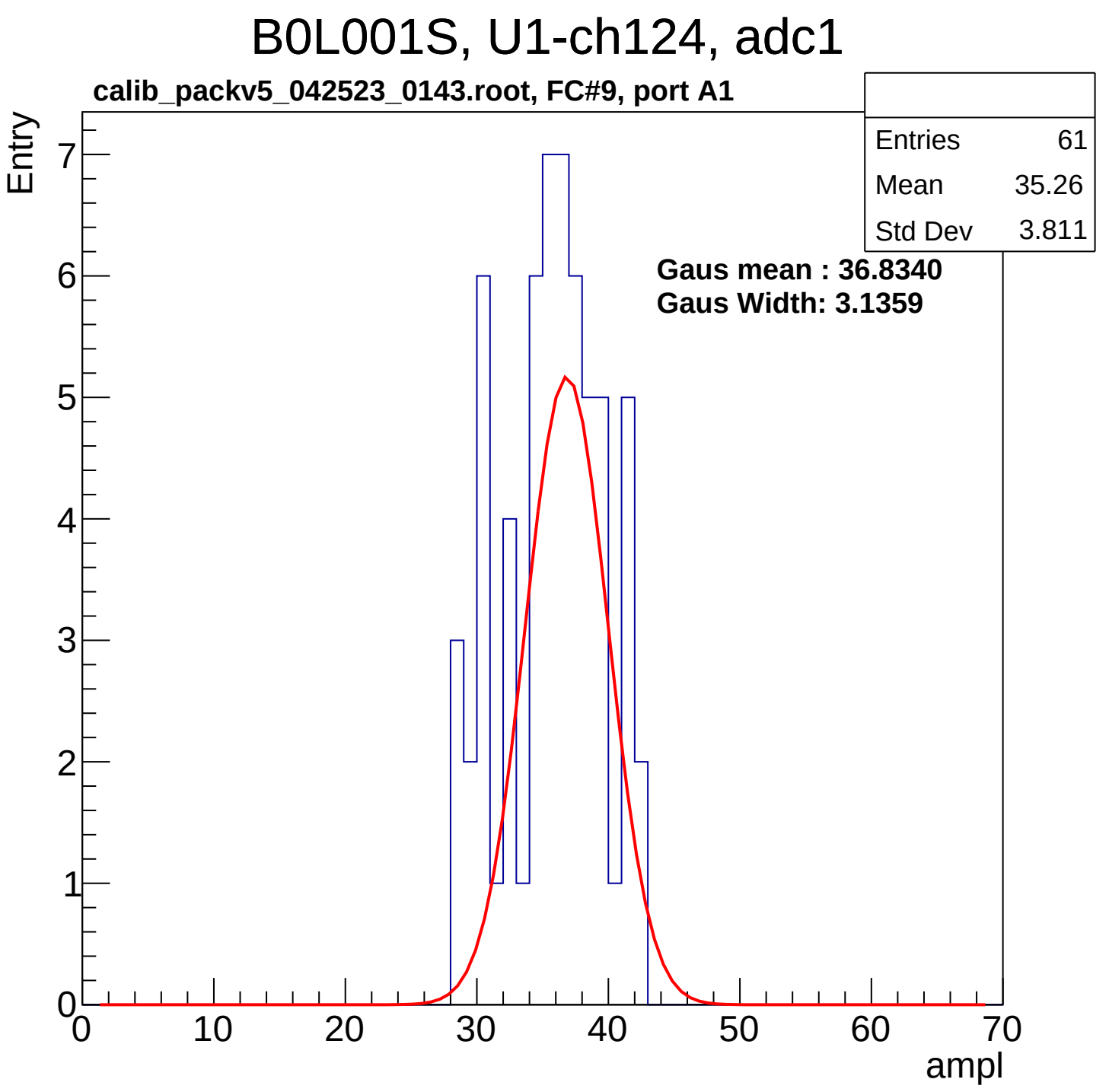

B0L001S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 55.12
Std Dev 3.788
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

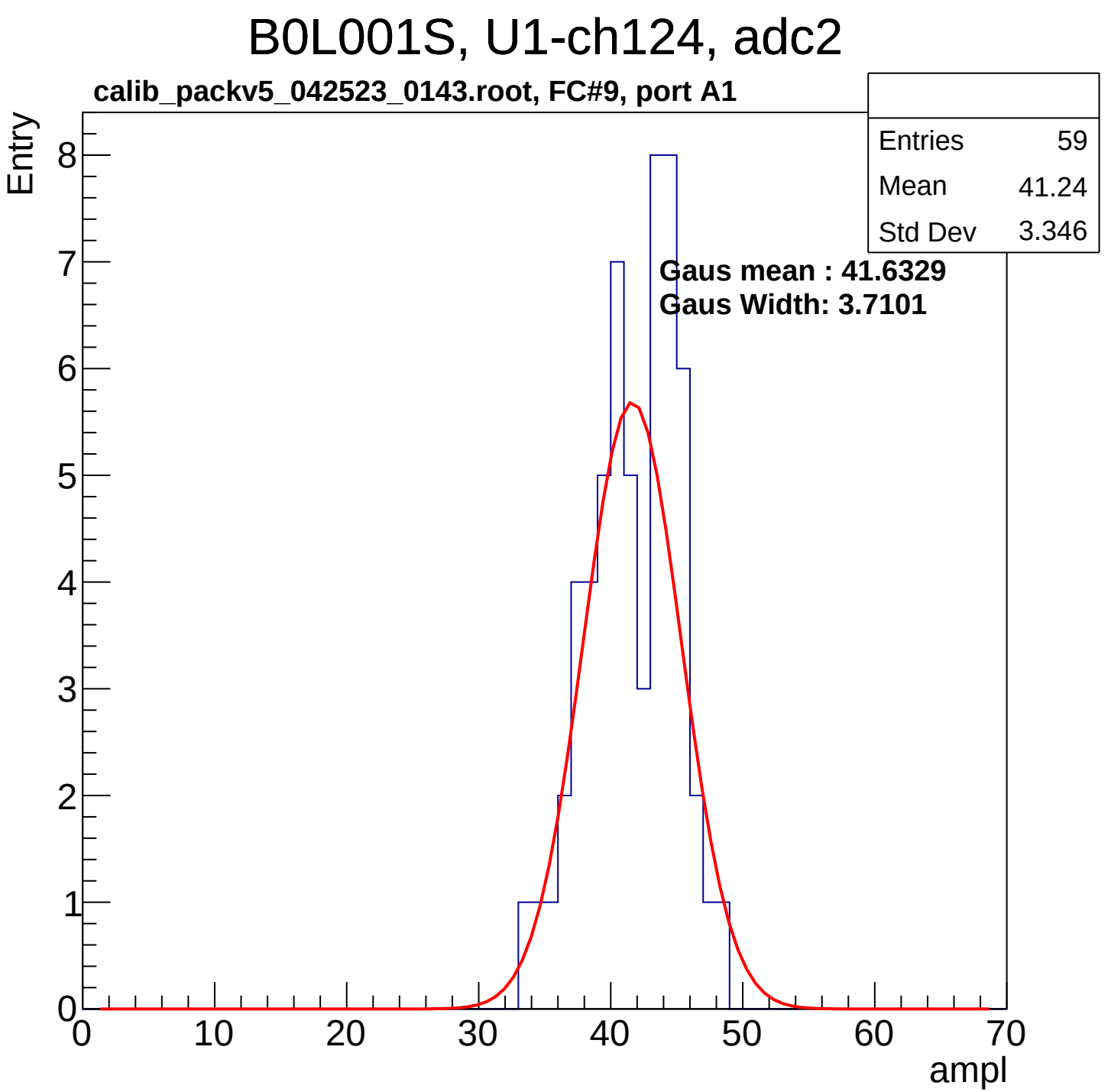

B0L001S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.56
Std Dev 9.267
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

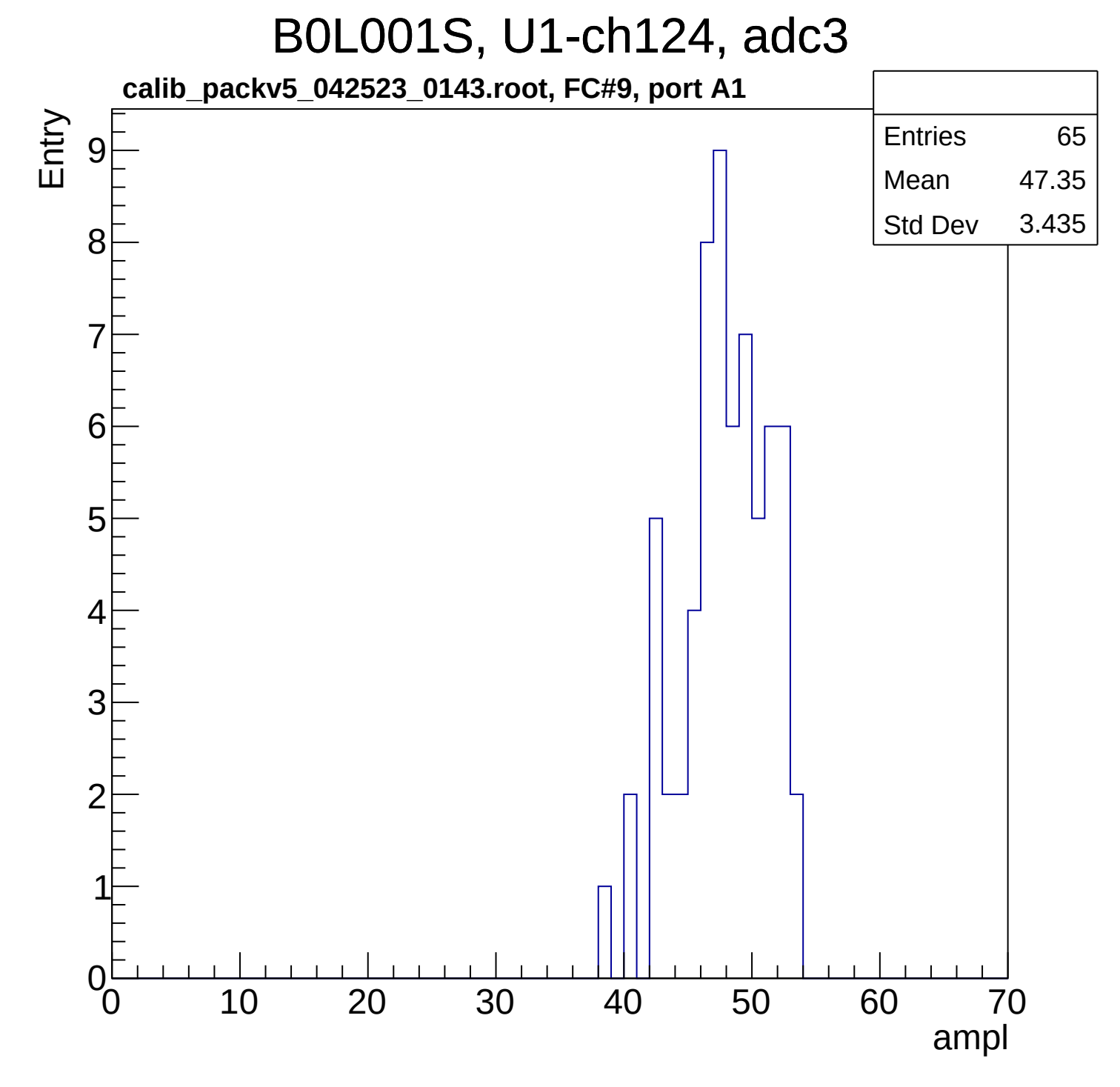

B0L001S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 8
Mean 61.88
Std Dev 1.053
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

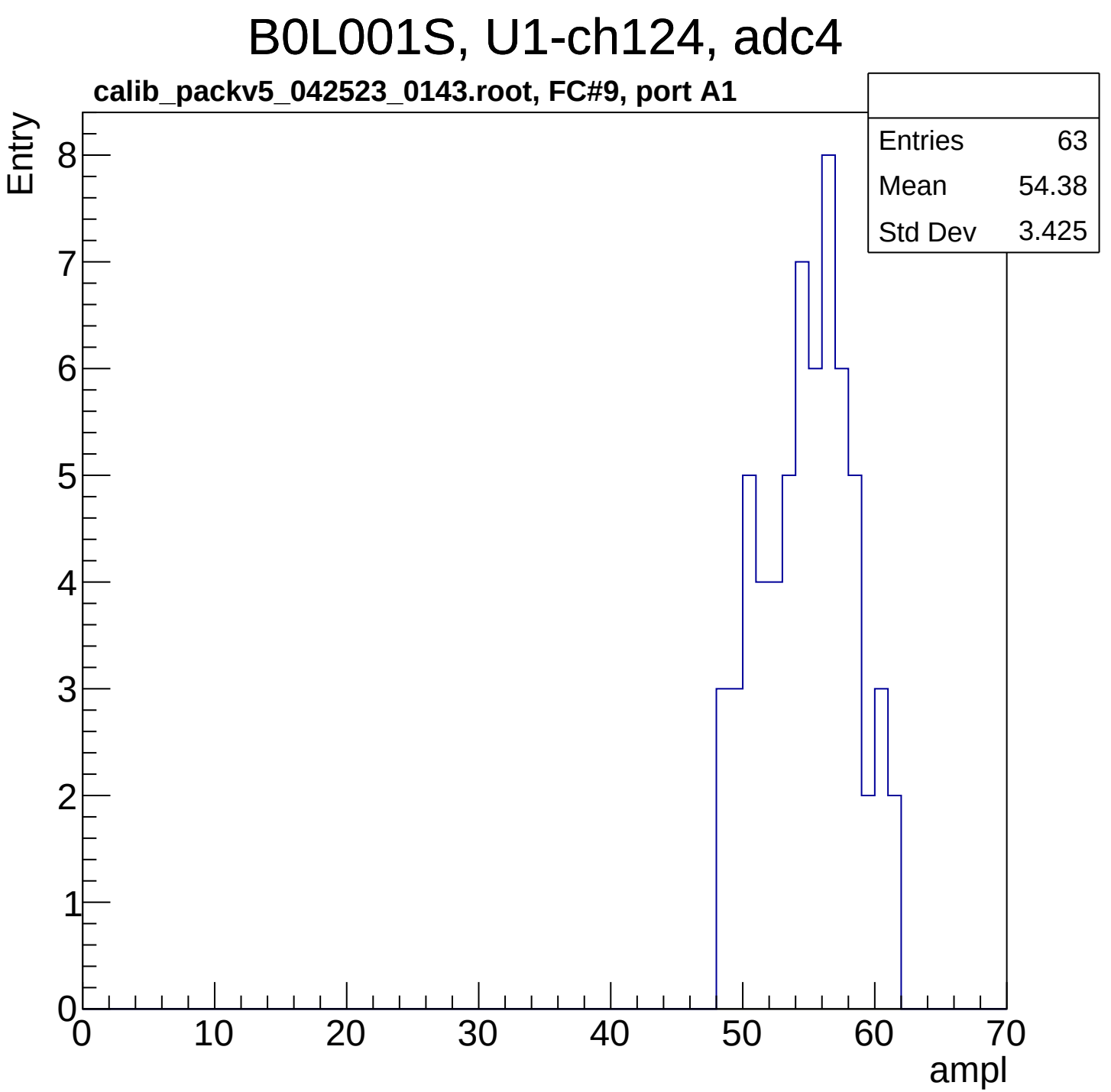

B0L001S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

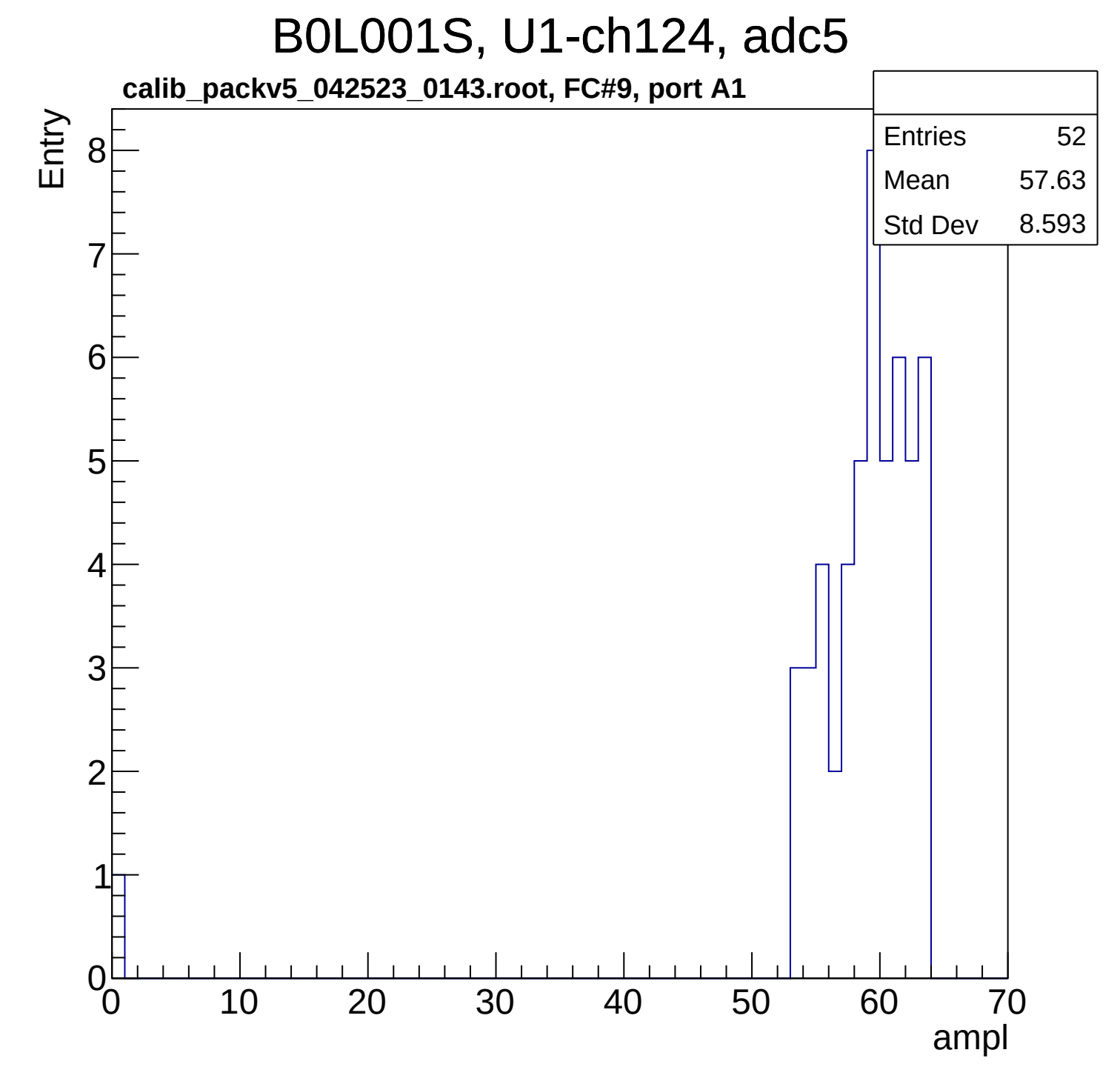

B0L001S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 29.08
Std Dev 4.786
Gaus mean : 29.4788
Gaus Width: 4.2039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

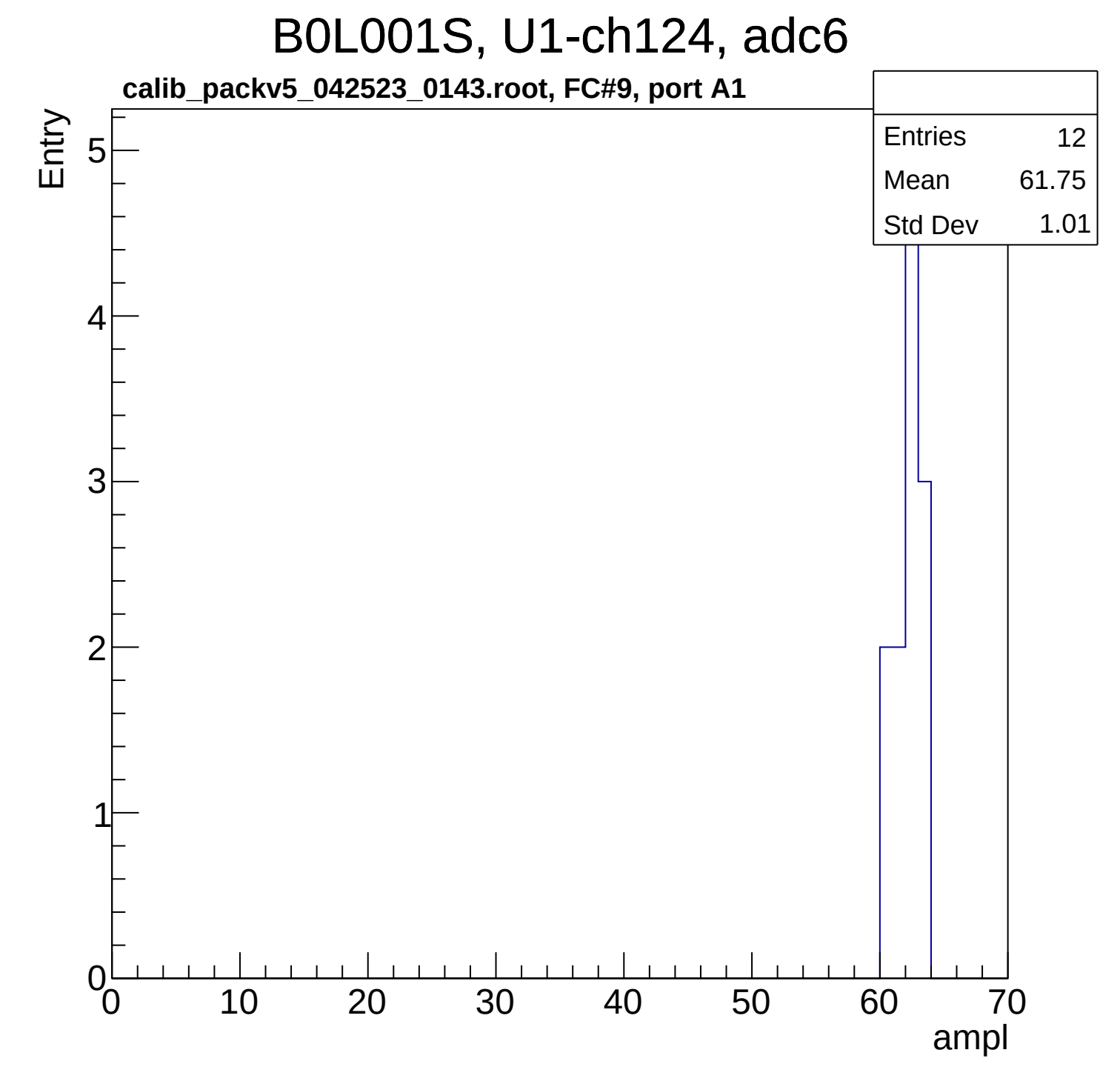

B0L001S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 35.26
Std Dev 3.811
Gaus mean : 36.8340
Gaus Width: 3.1359
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

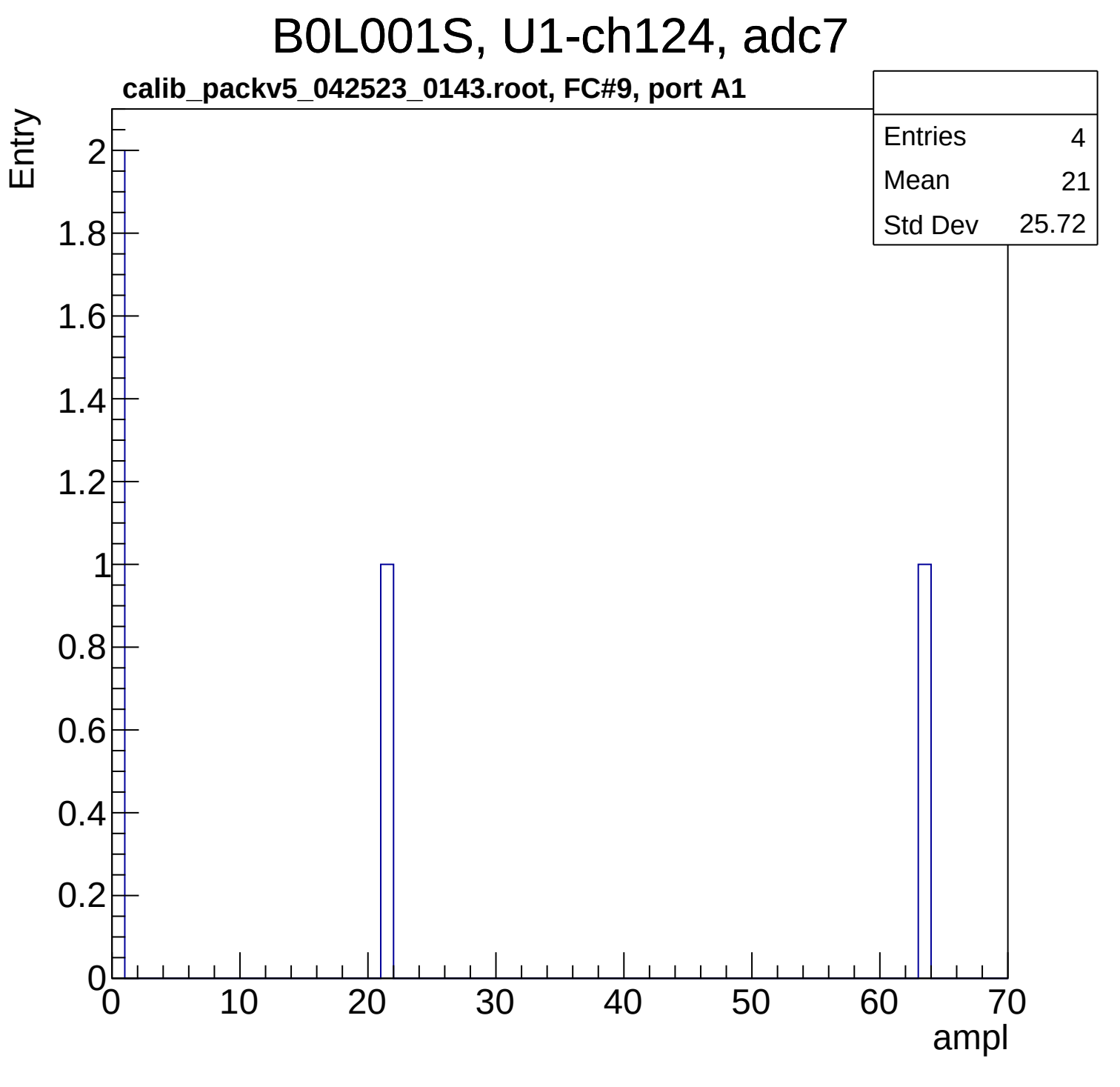

B0L001S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 59
Mean 41.24
Std Dev 3.346
Gaus mean : 41.6329
Gaus Width: 3.7101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

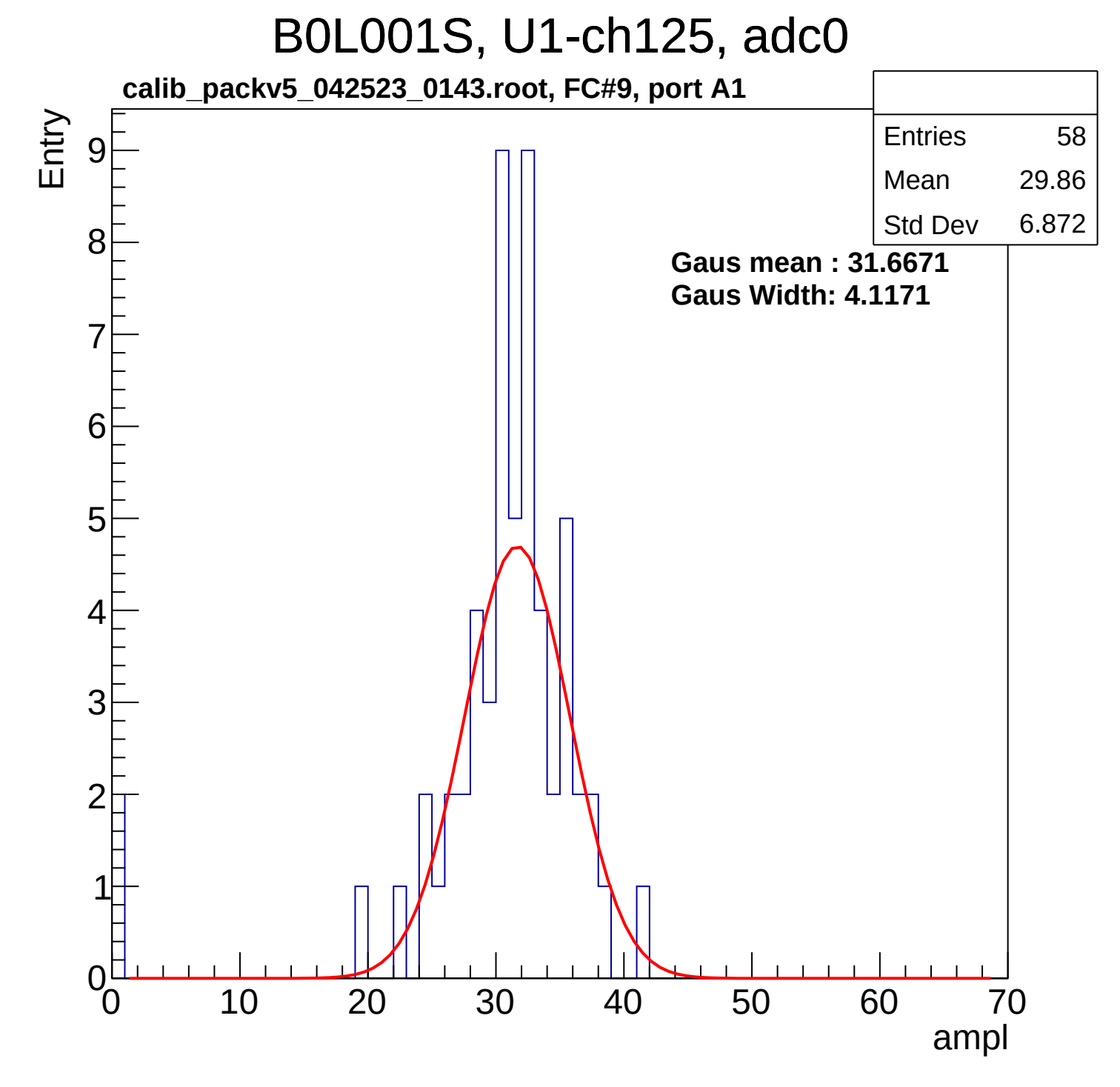

B0L001S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 65
Mean 47.35
Std Dev 3.435
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

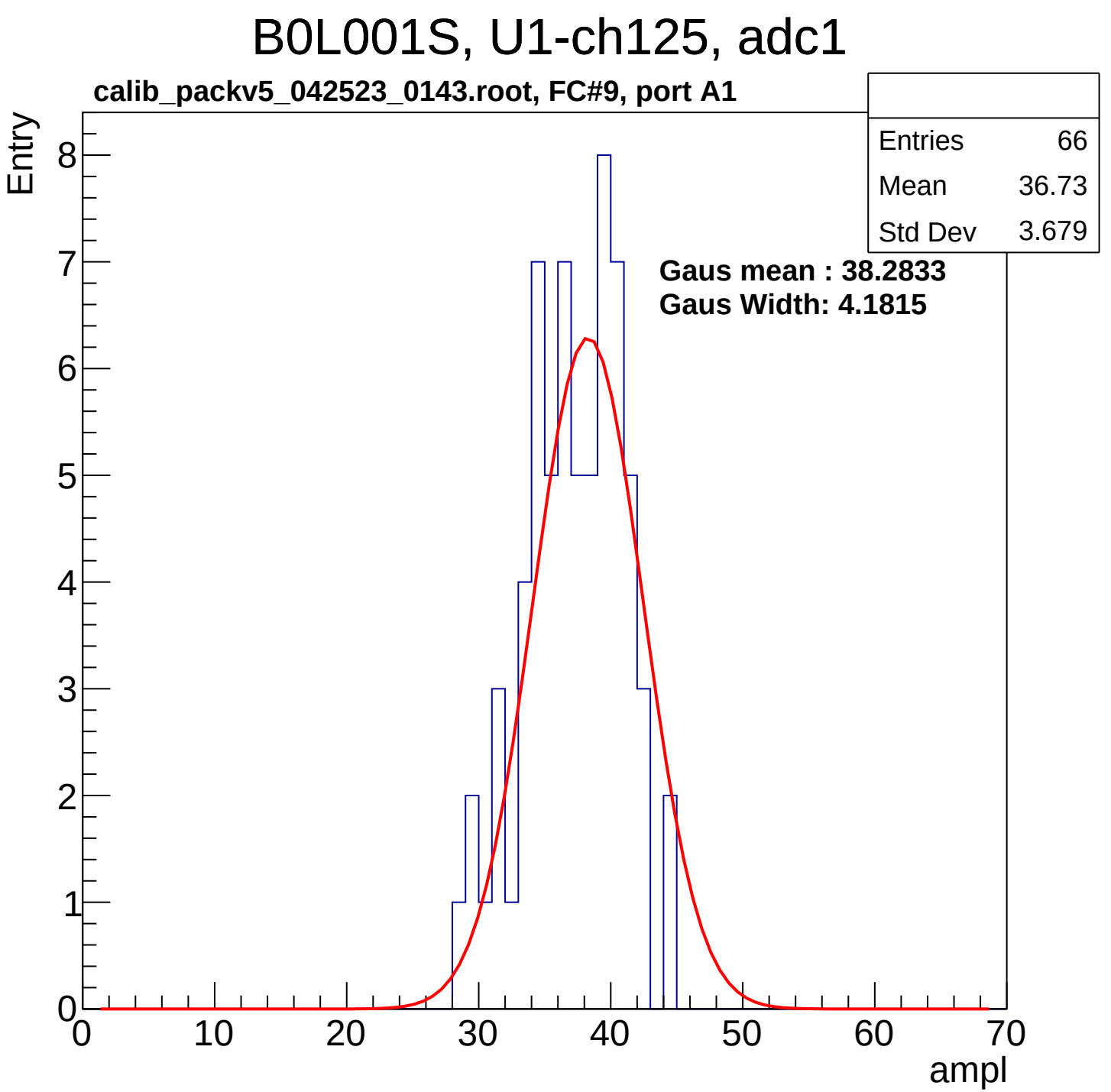

B0L001S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 63
Mean 54.38
Std Dev 3.425
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

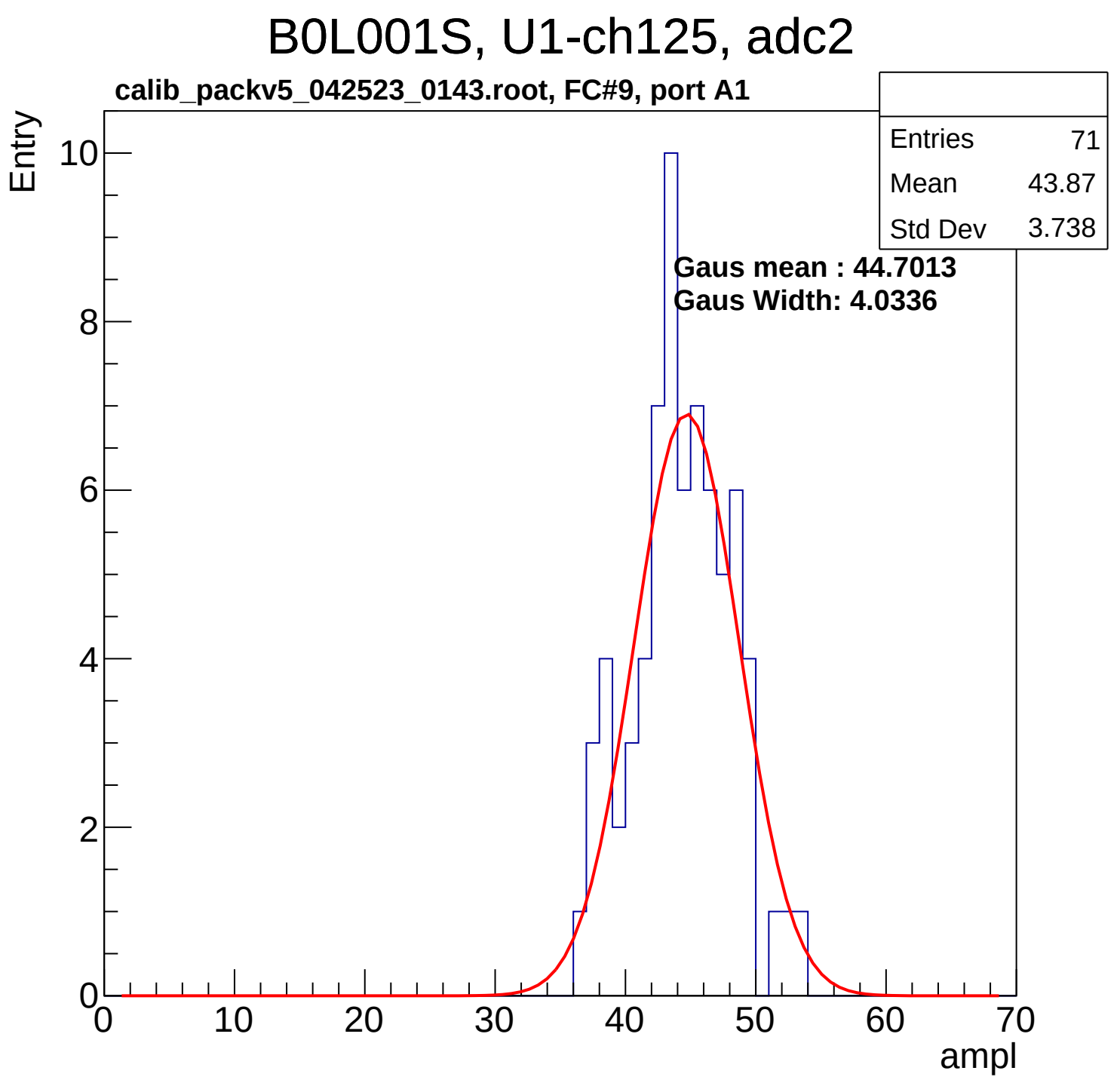

B0L001S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 57.63
Std Dev 8.593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

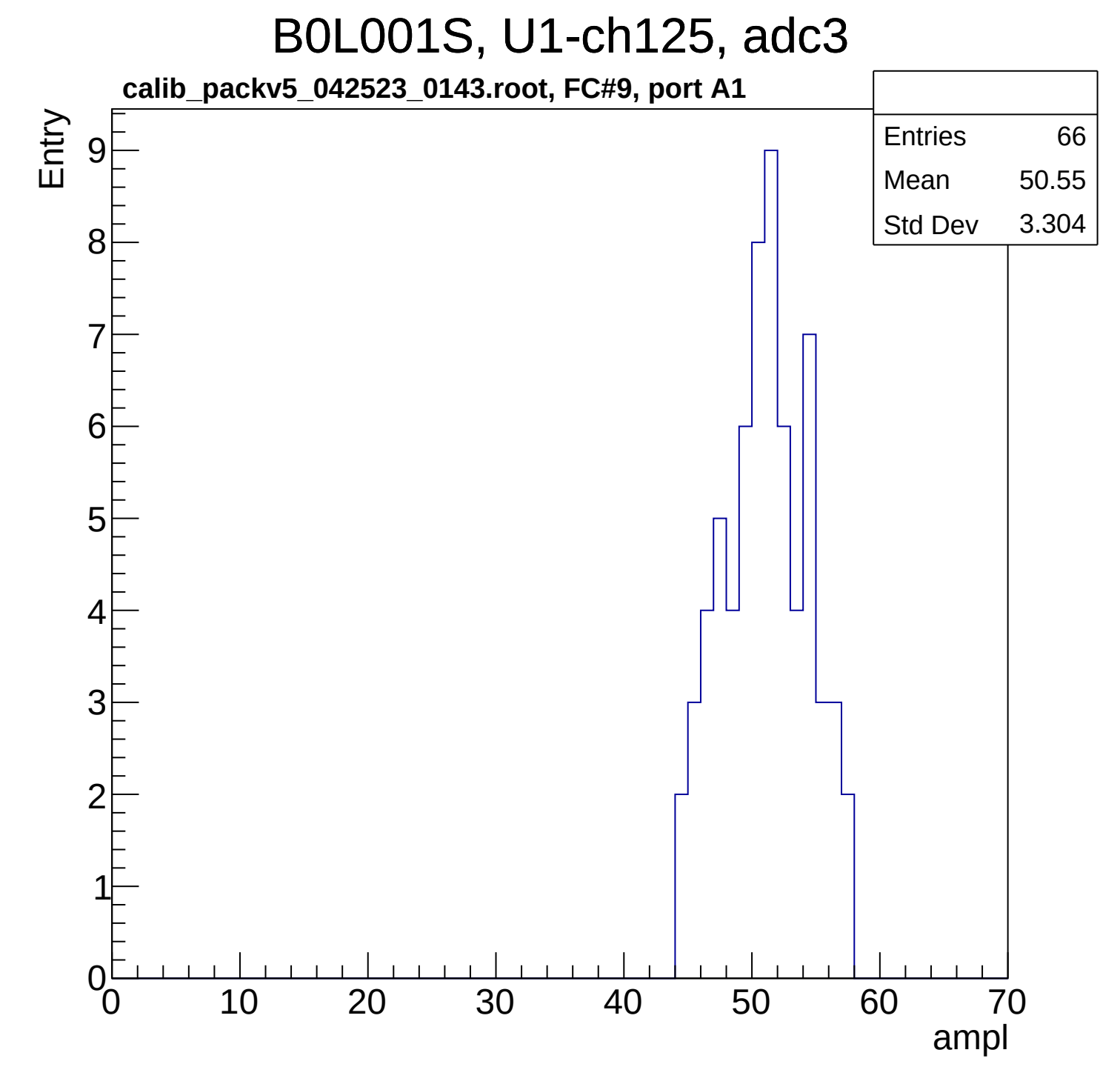

B0L001S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 12
Mean 61.75
Std Dev 1.01
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

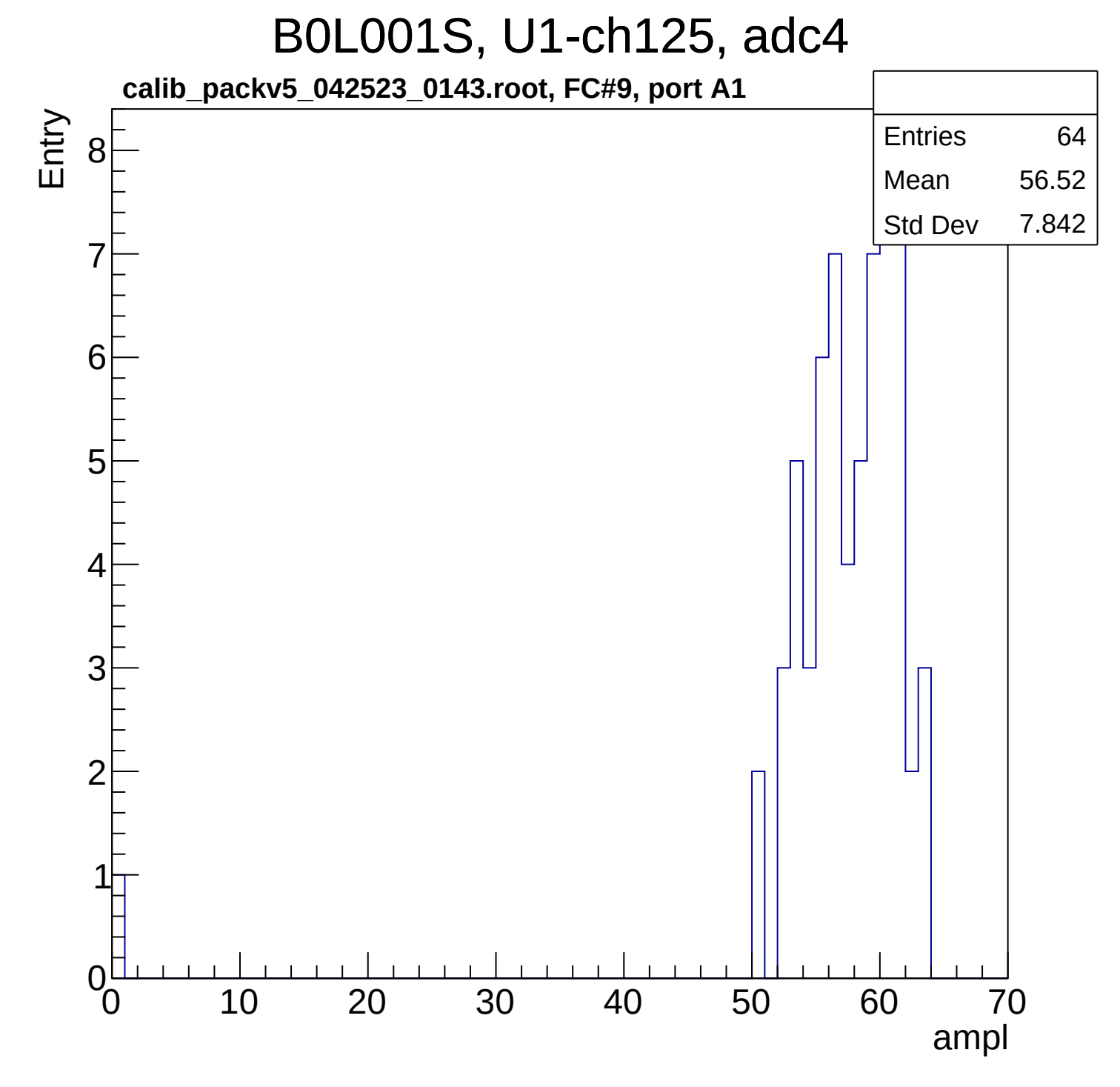

B0L001S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 29.86
Std Dev 6.872
Gaus mean : 31.6671
Gaus Width: 4.1171
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 36.73
Std Dev 3.679
Gaus mean : 38.2833
Gaus Width: 4.1815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 71
Mean 43.87
Std Dev 3.738
Gaus mean : 44.7013
Gaus Width: 4.0336
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 66
Mean 50.55
Std Dev 3.304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 56.52
Std Dev 7.842
Entry
ampl
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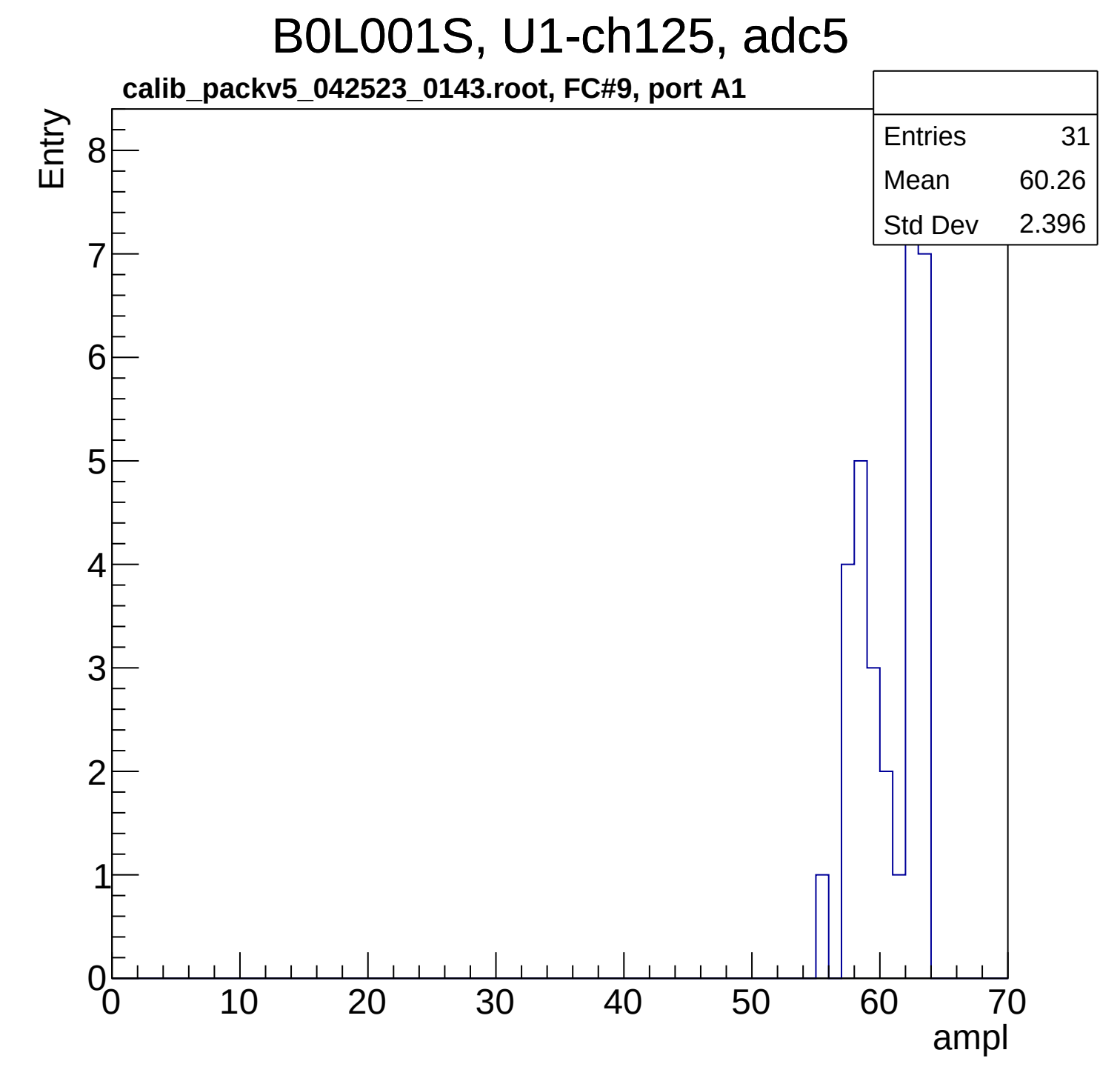
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 31
Mean 60.26
Std Dev 2.396
Entry
ampl
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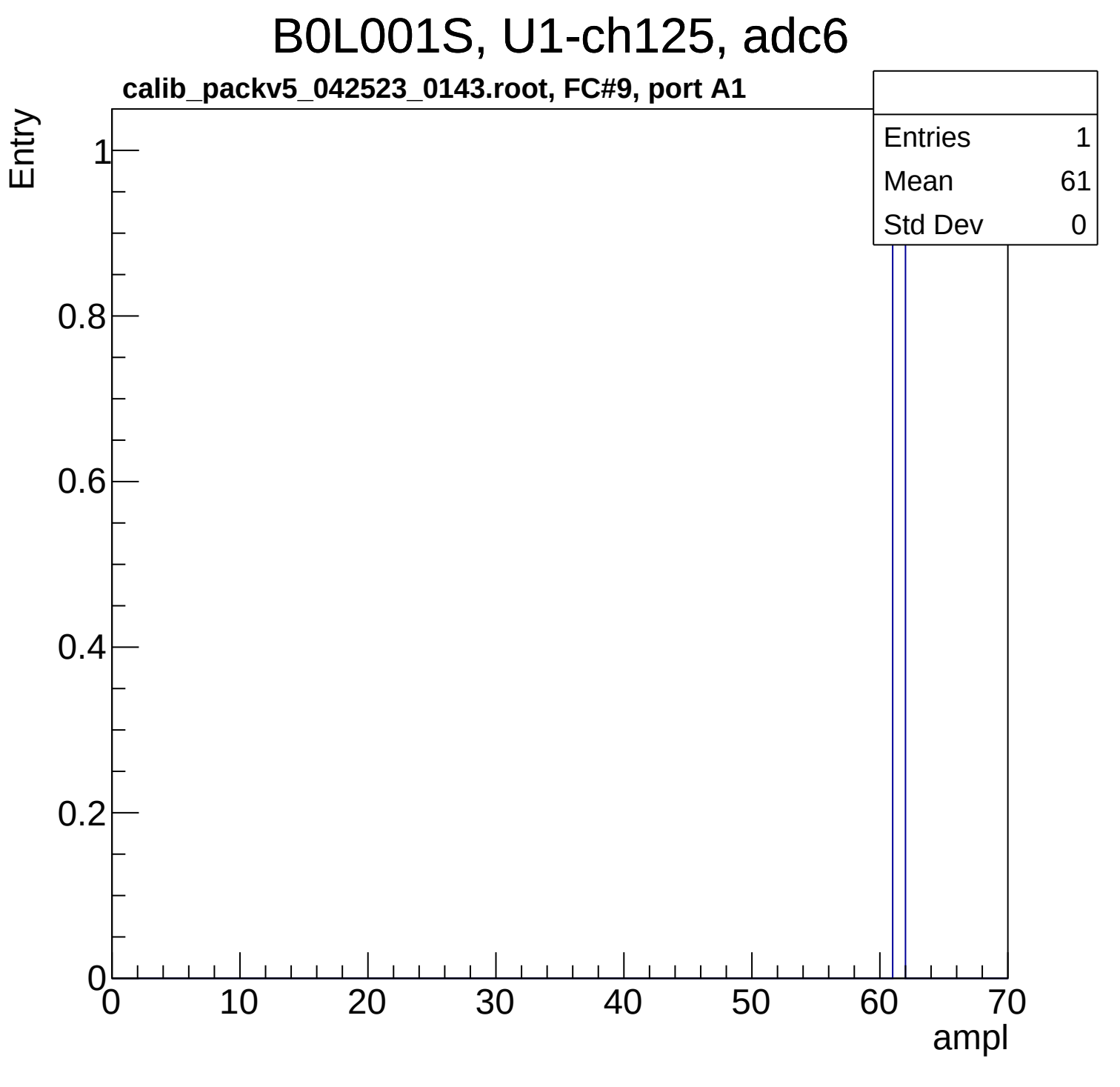
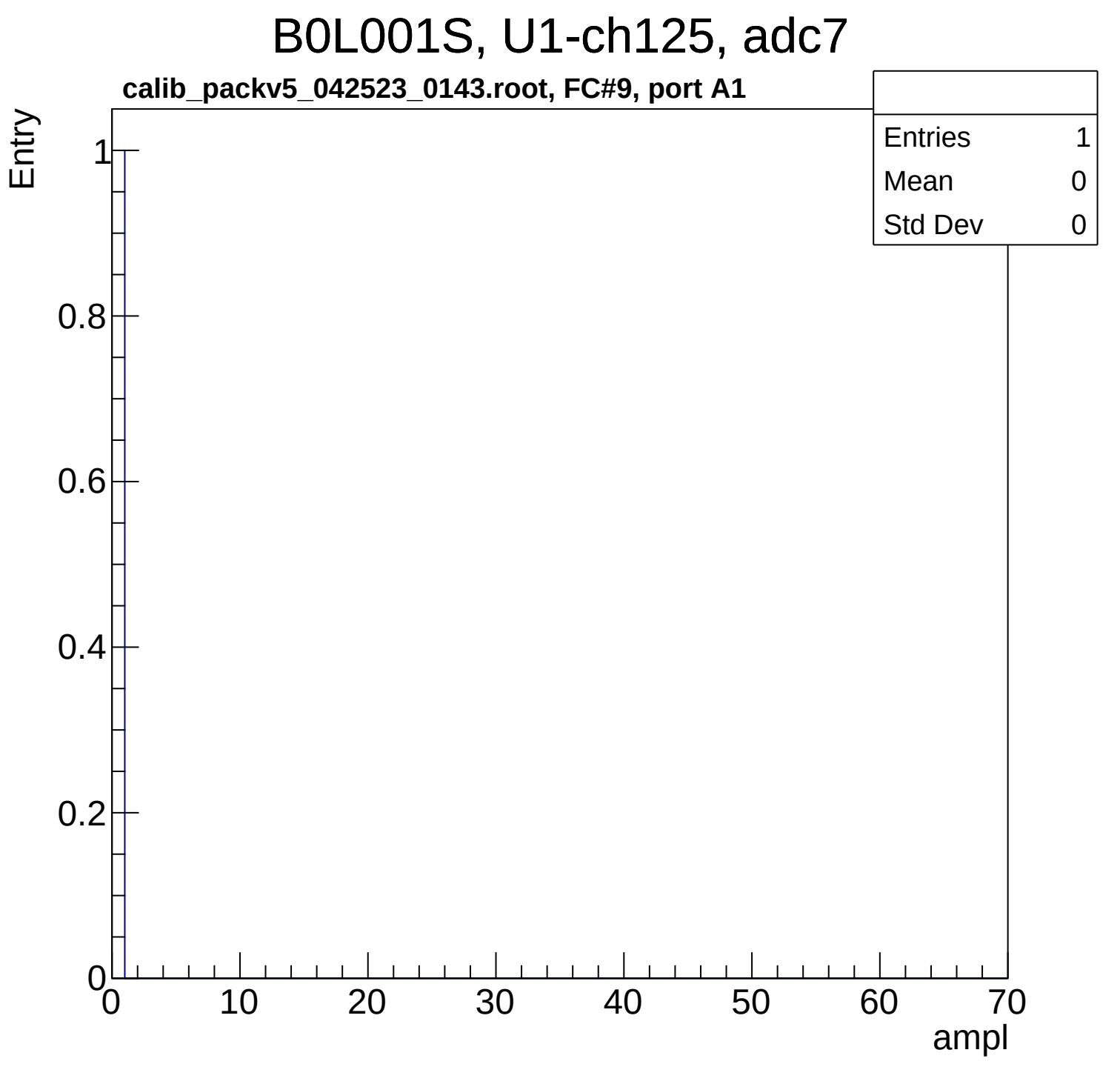
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 1
Mean 0
Std Dev 0
Entry
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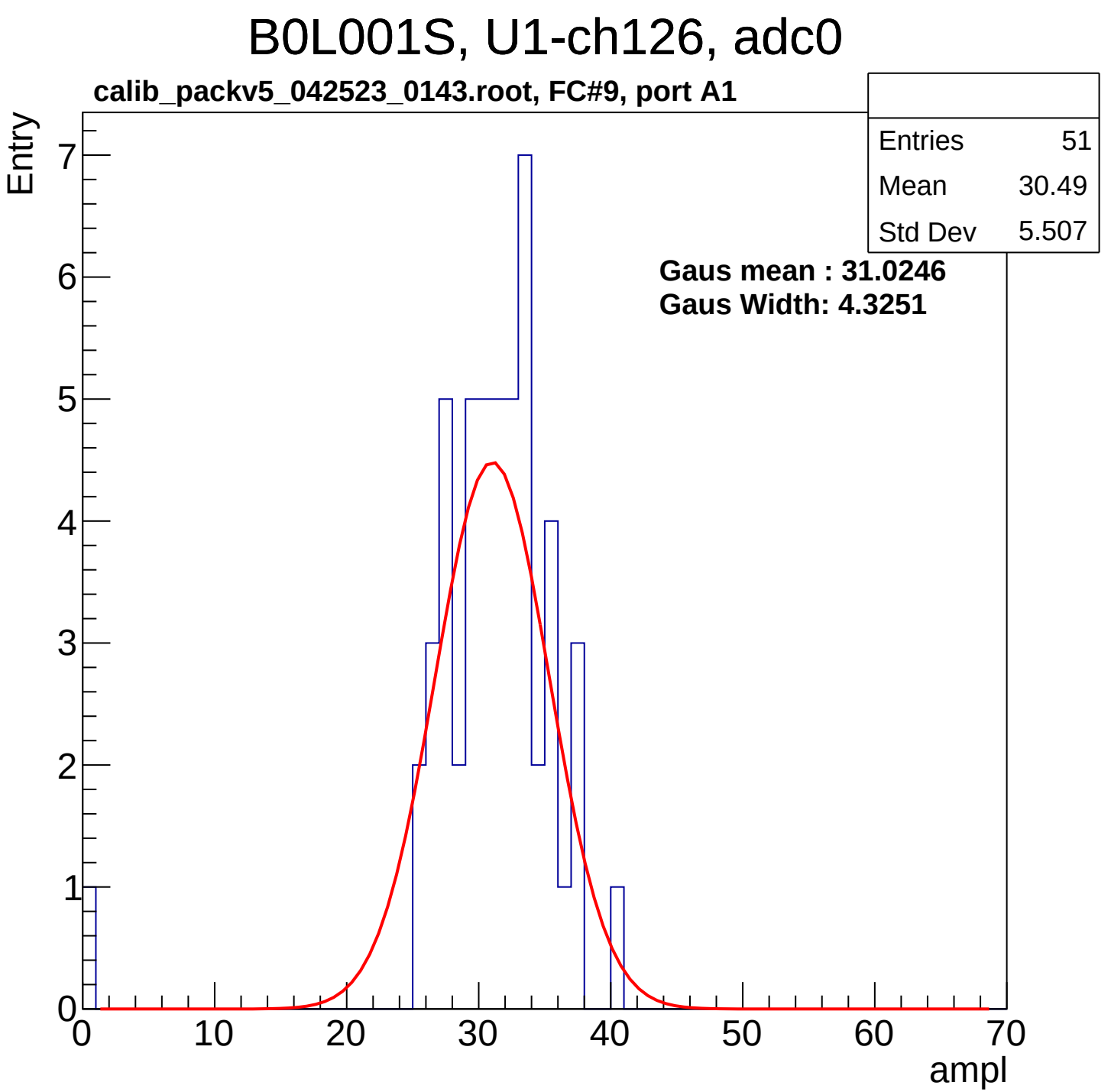
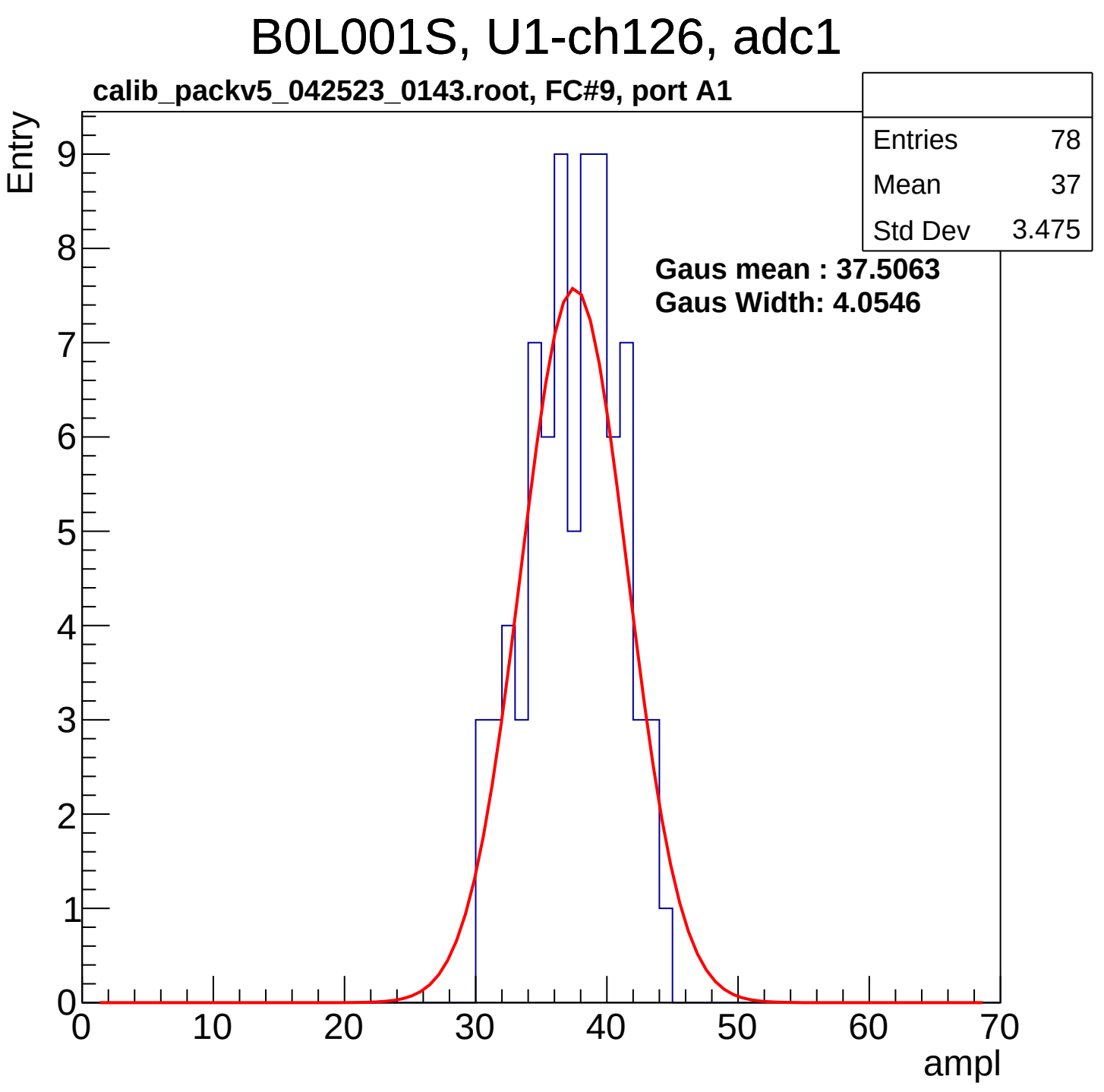
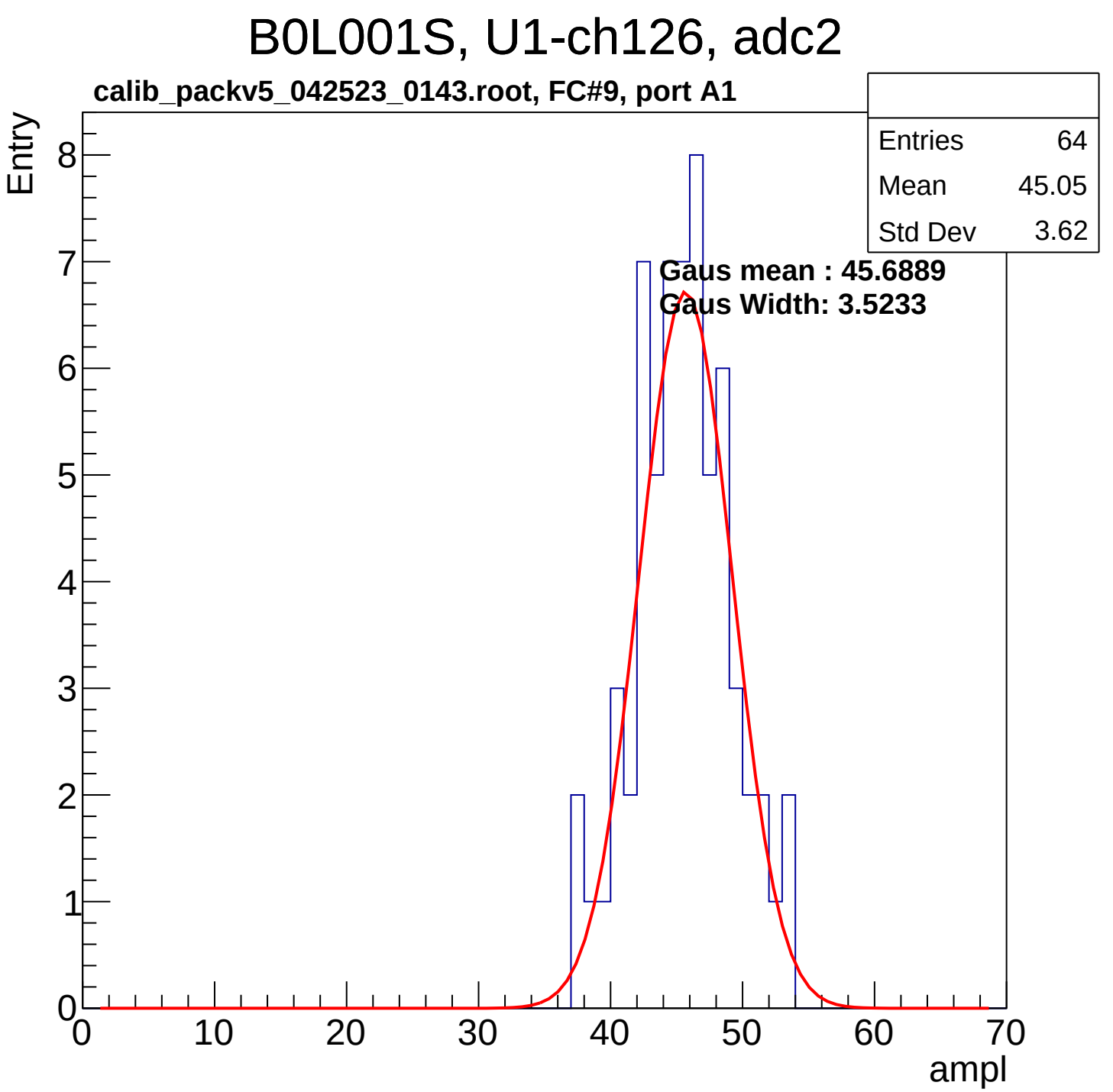
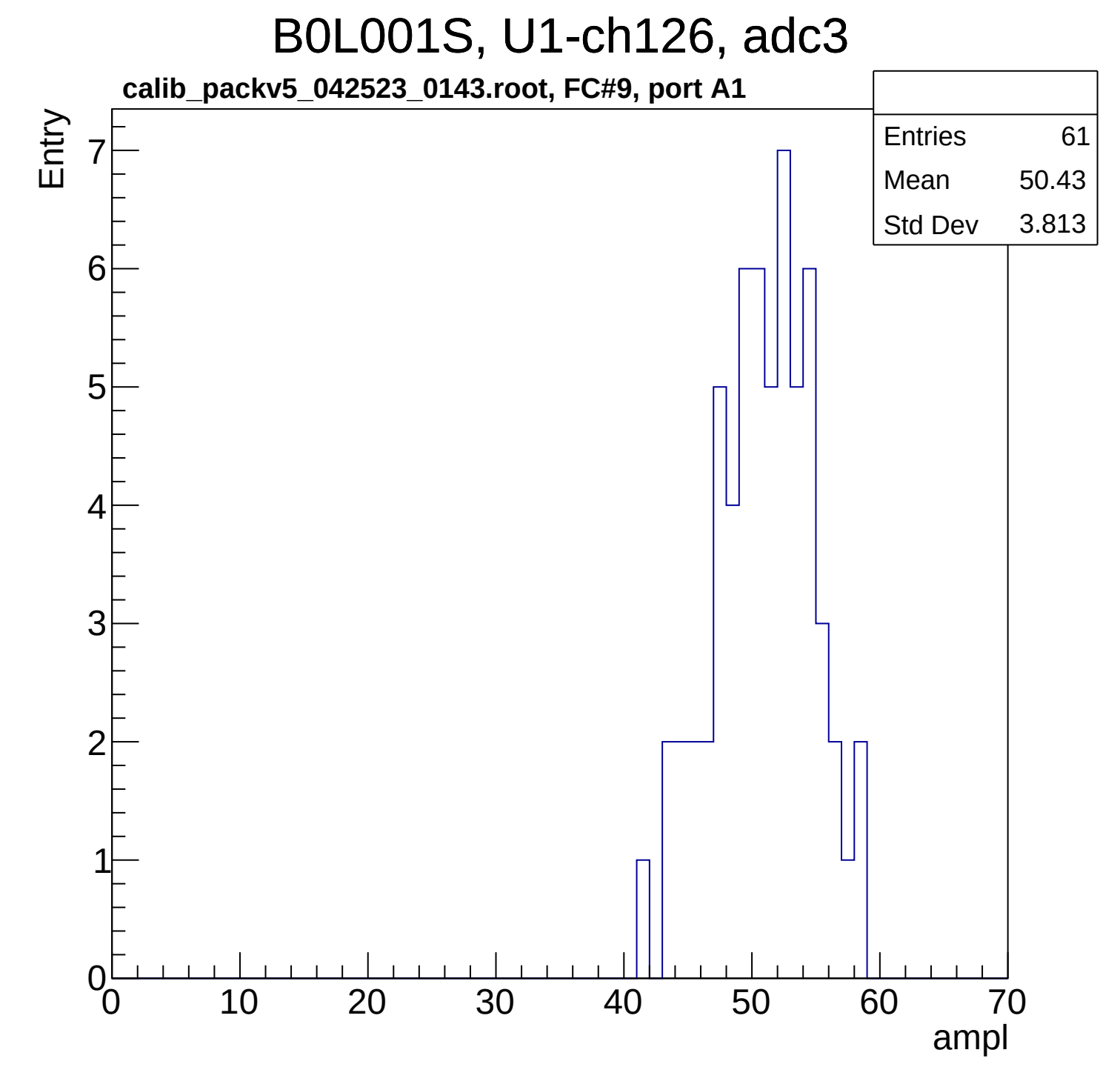
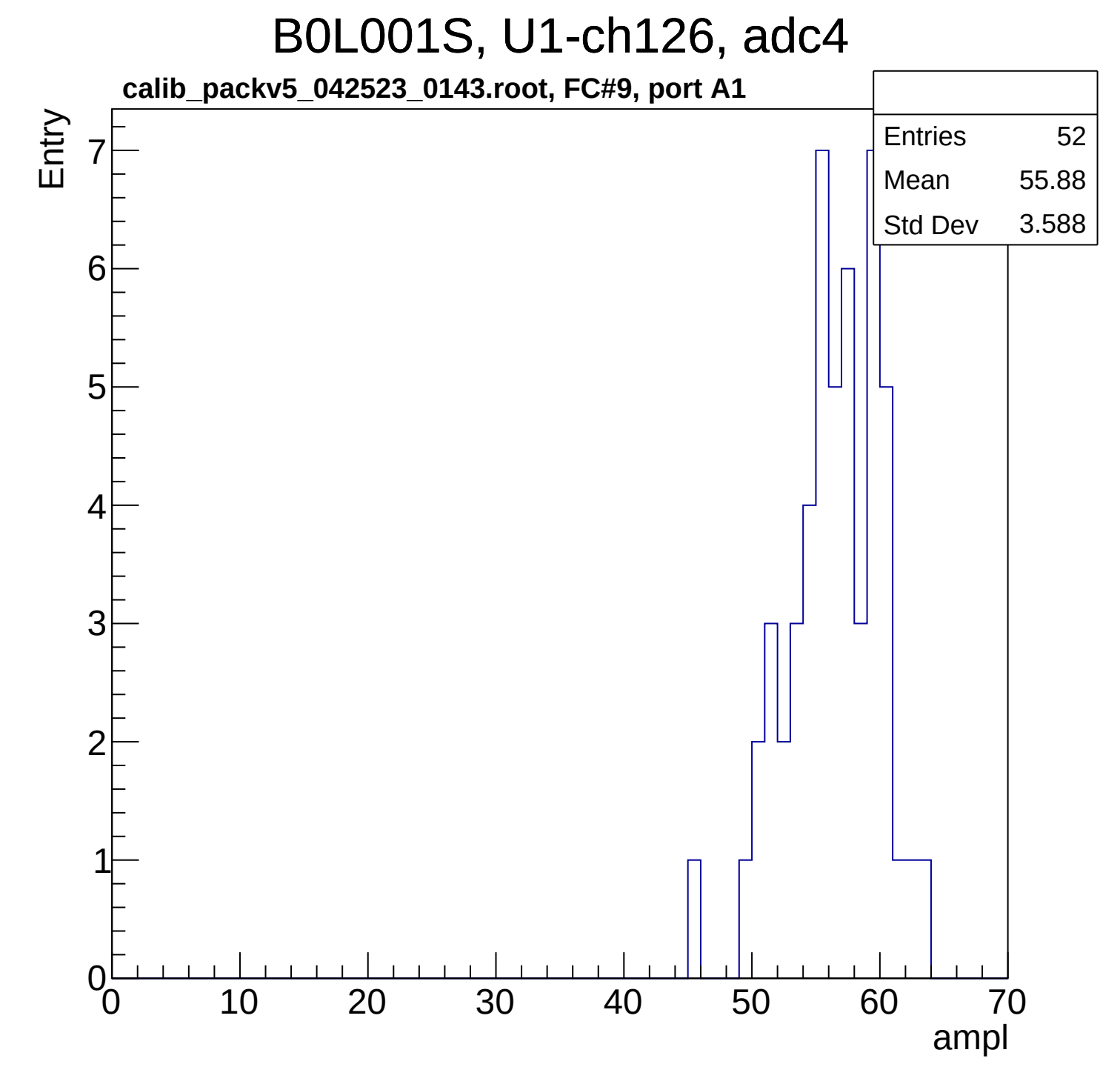
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

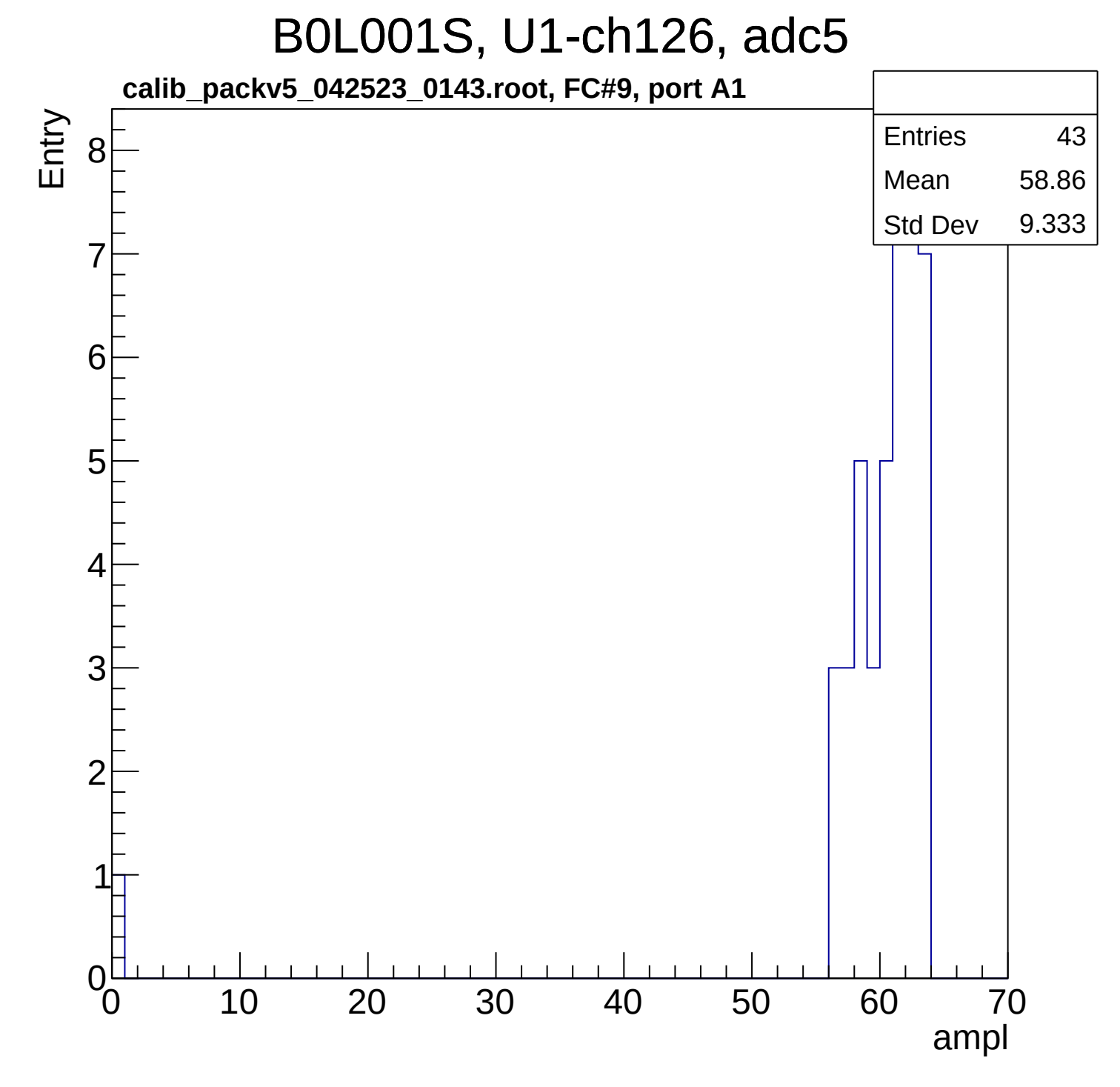

B0L001S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 51
Mean 30.49
Std Dev 5.507
Gaus mean : 31.0246
Gaus Width: 4.3251
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 78
Mean 37
Std Dev 3.475
Gaus mean : 37.5063
Gaus Width: 4.0546
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 45.05
Std Dev 3.62
Gaus mean : 45.6889
Gaus Width: 3.5233
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 50.43
Std Dev 3.813
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 52
Mean 55.88
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L001S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 43
Mean 58.86
Std Dev 9.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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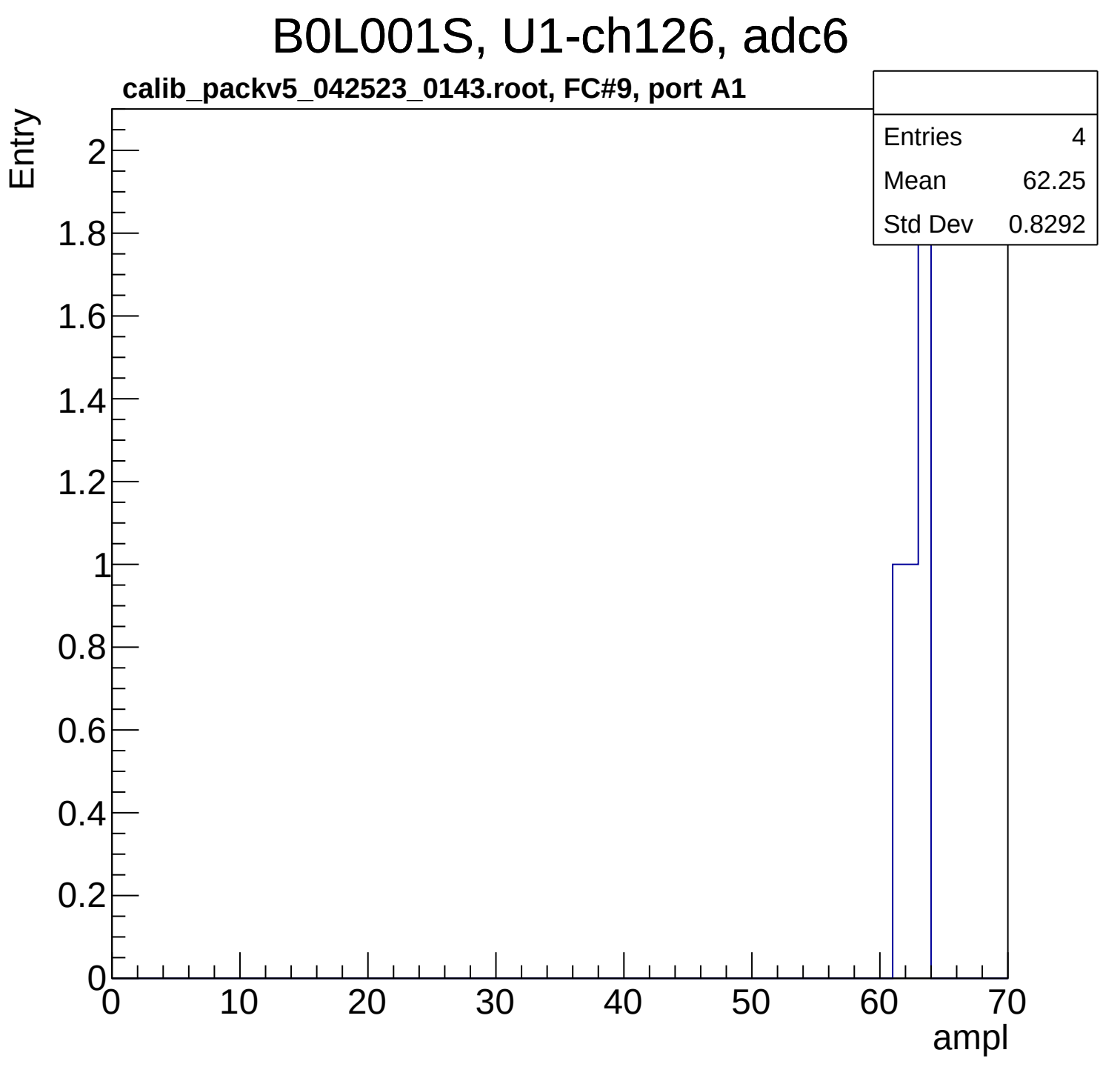
B0L001S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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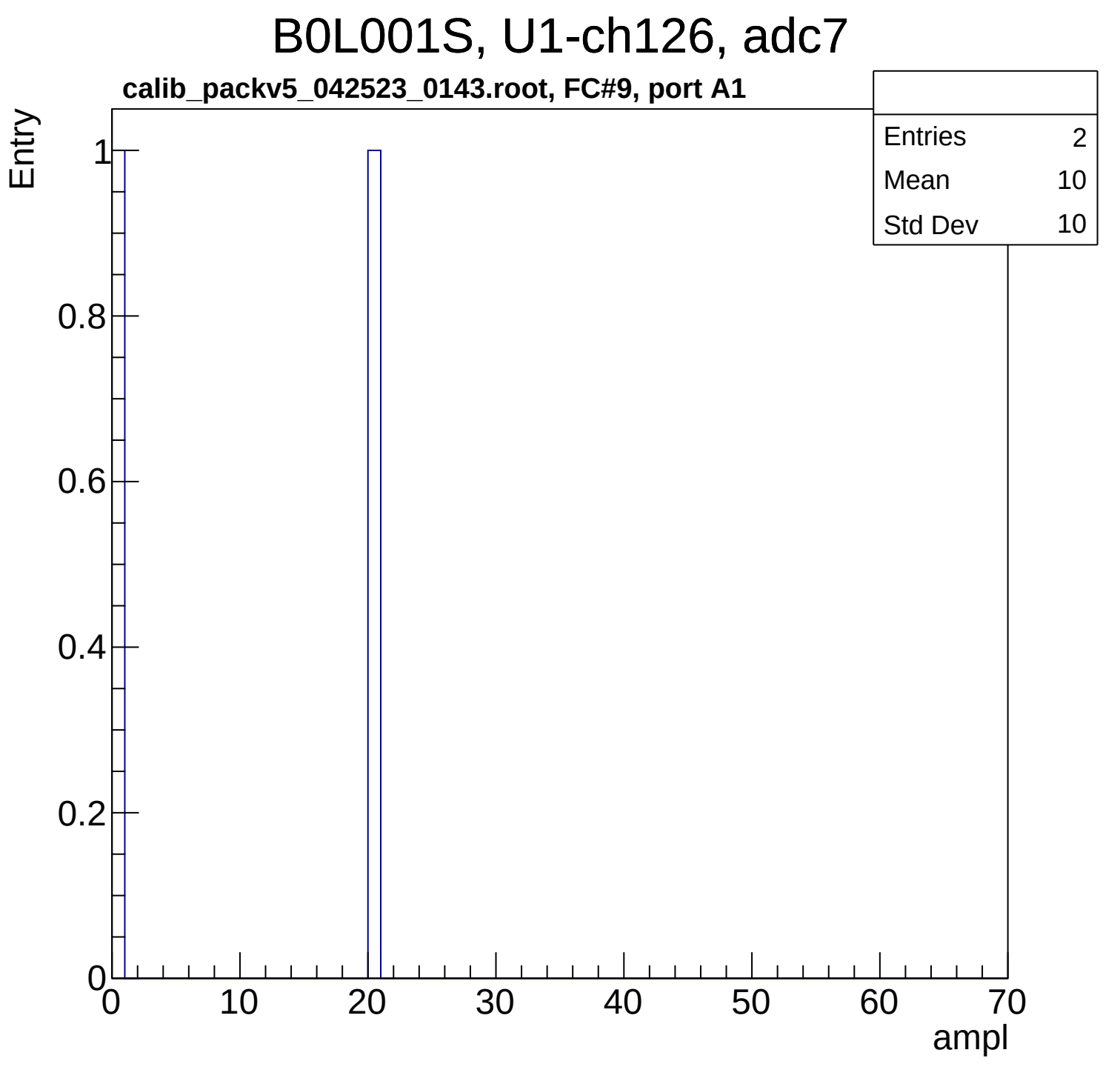
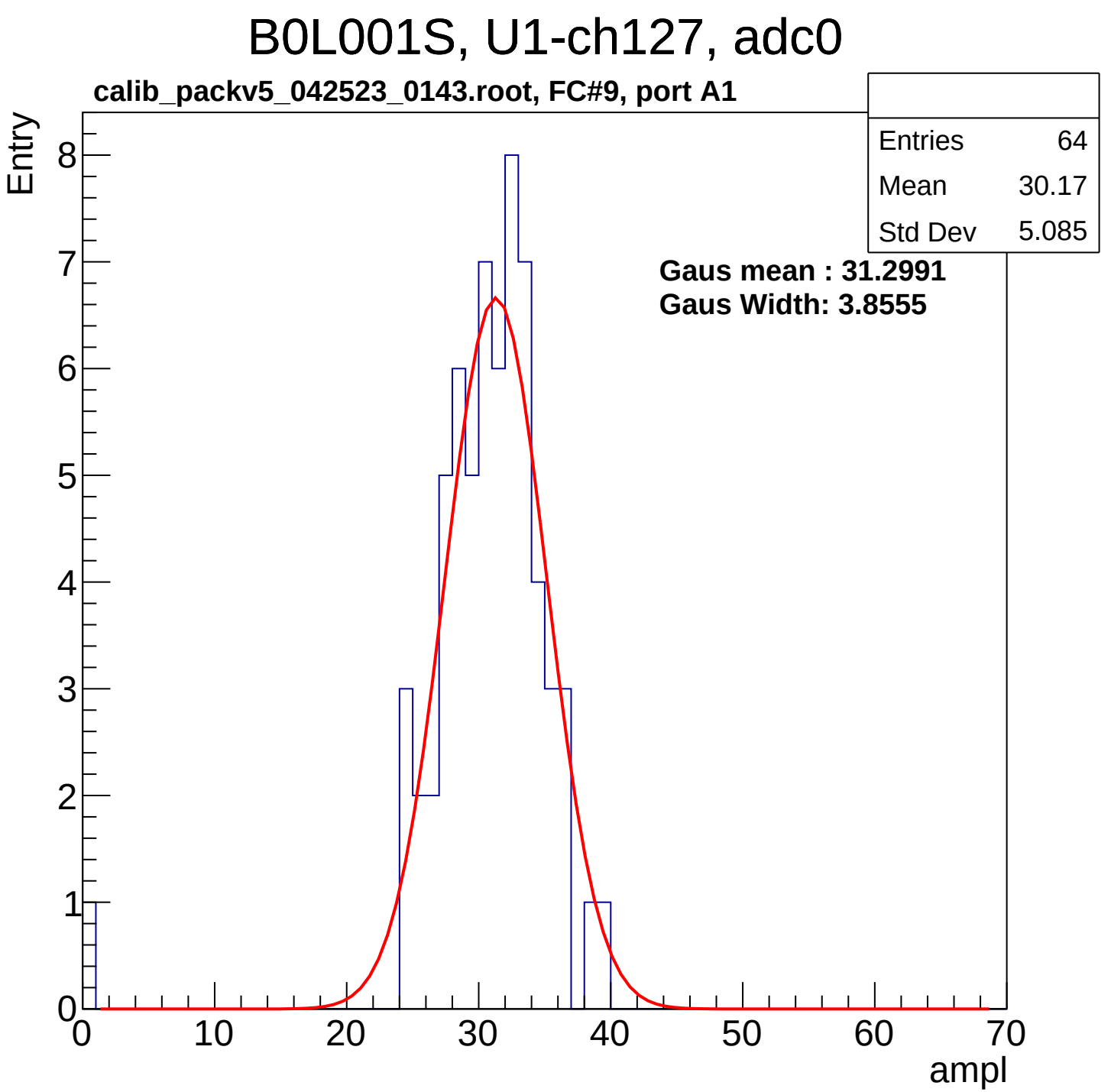
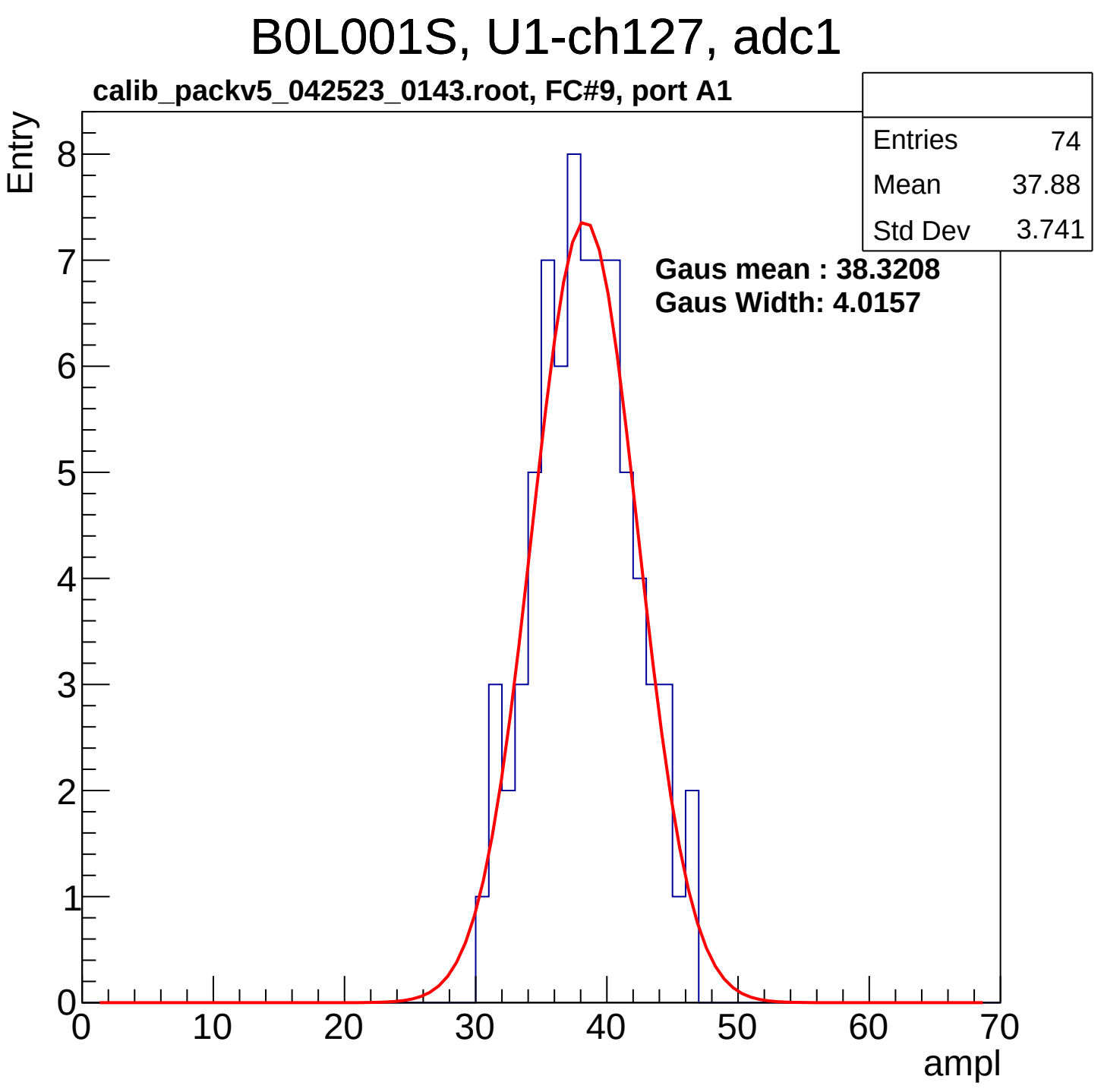
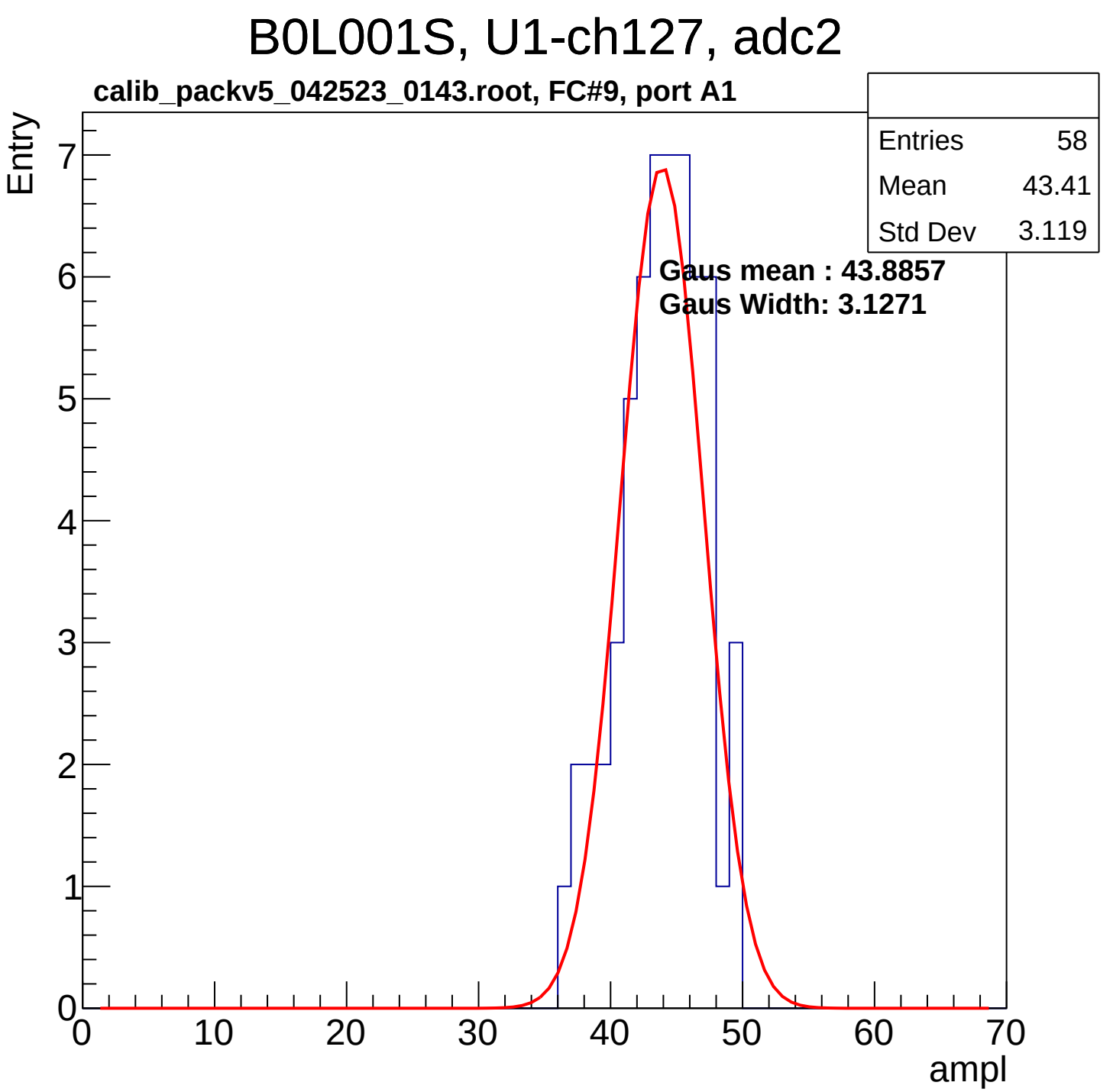
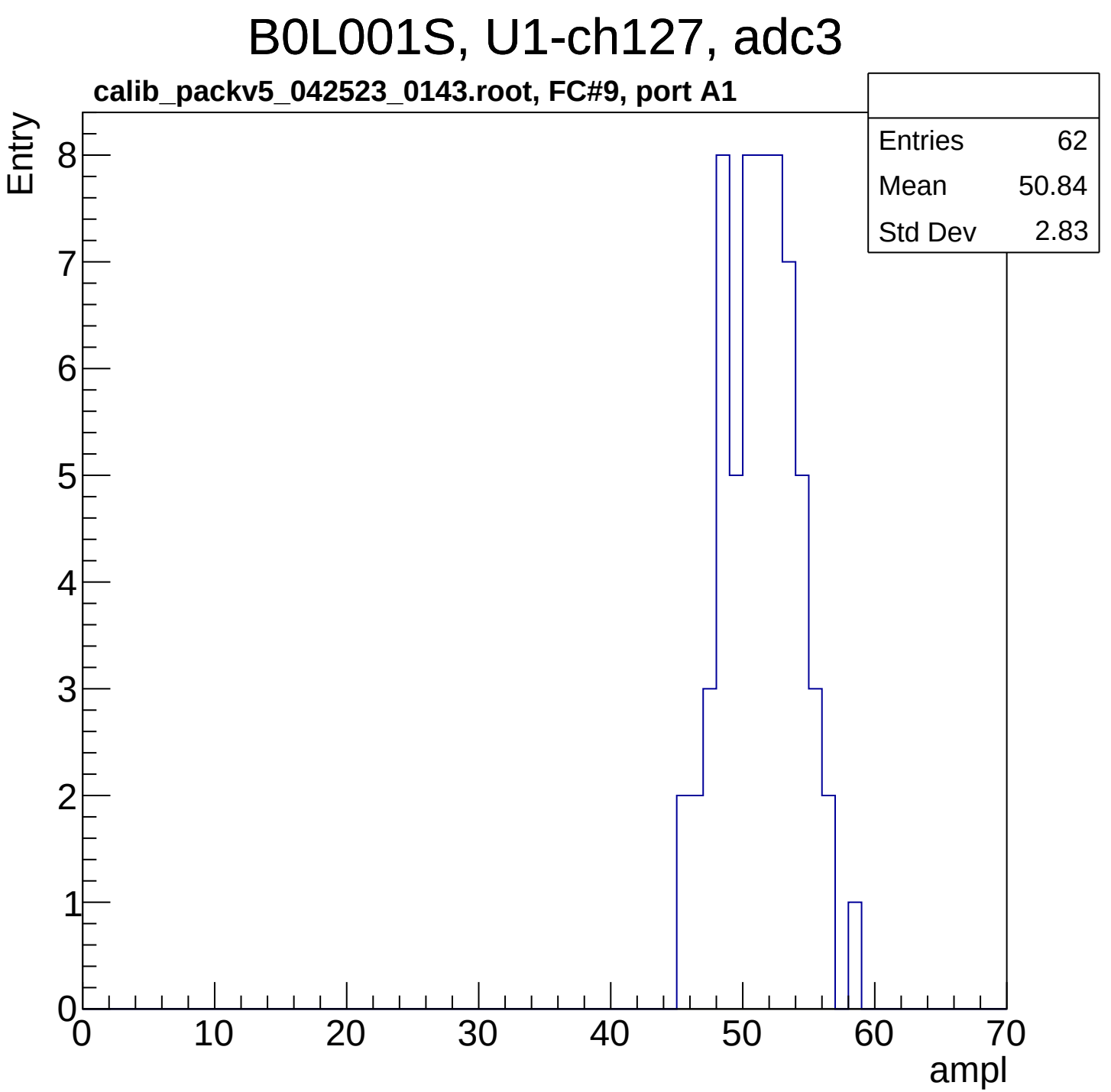
30
40
50
60
70

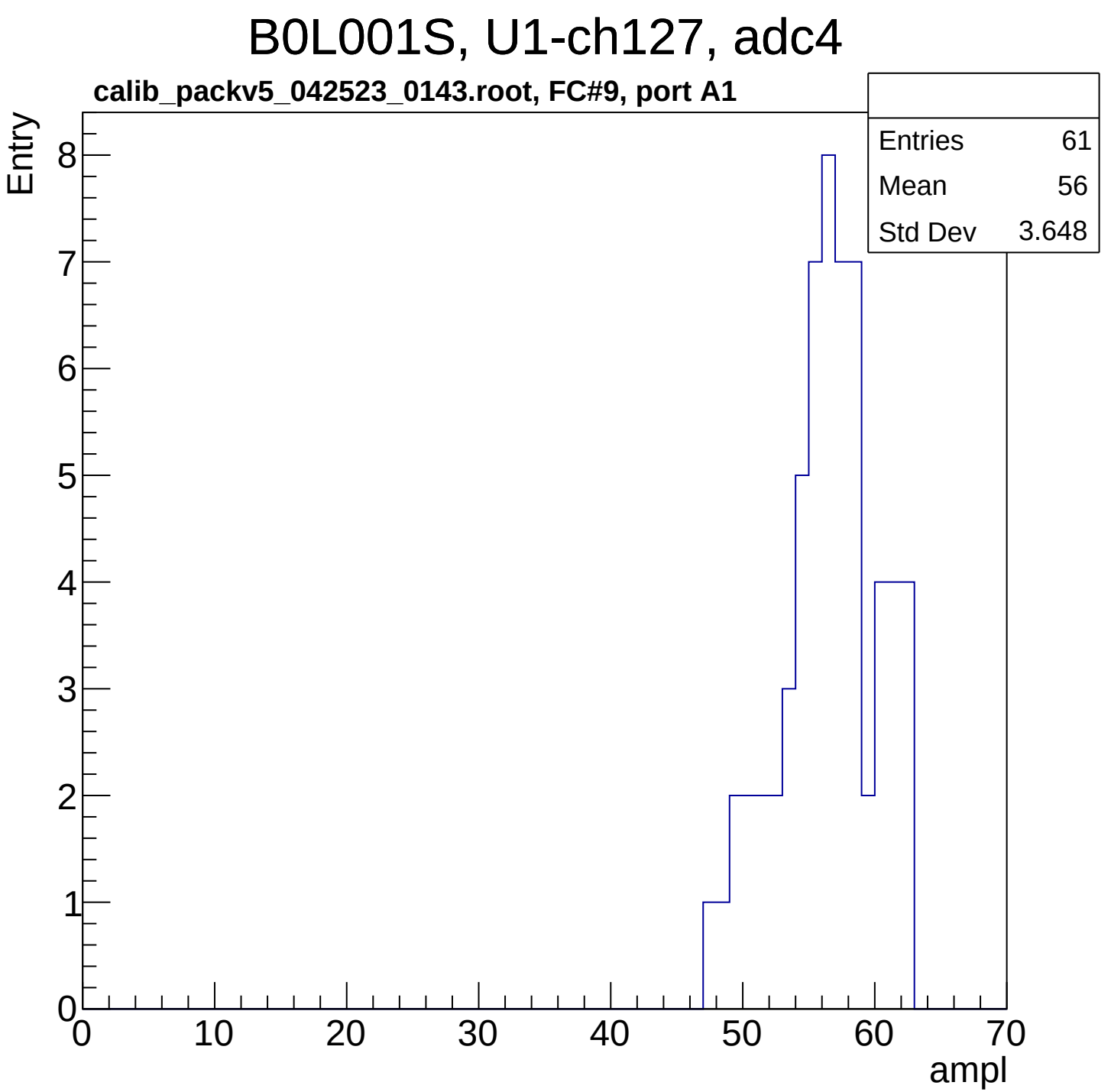

B0L001S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

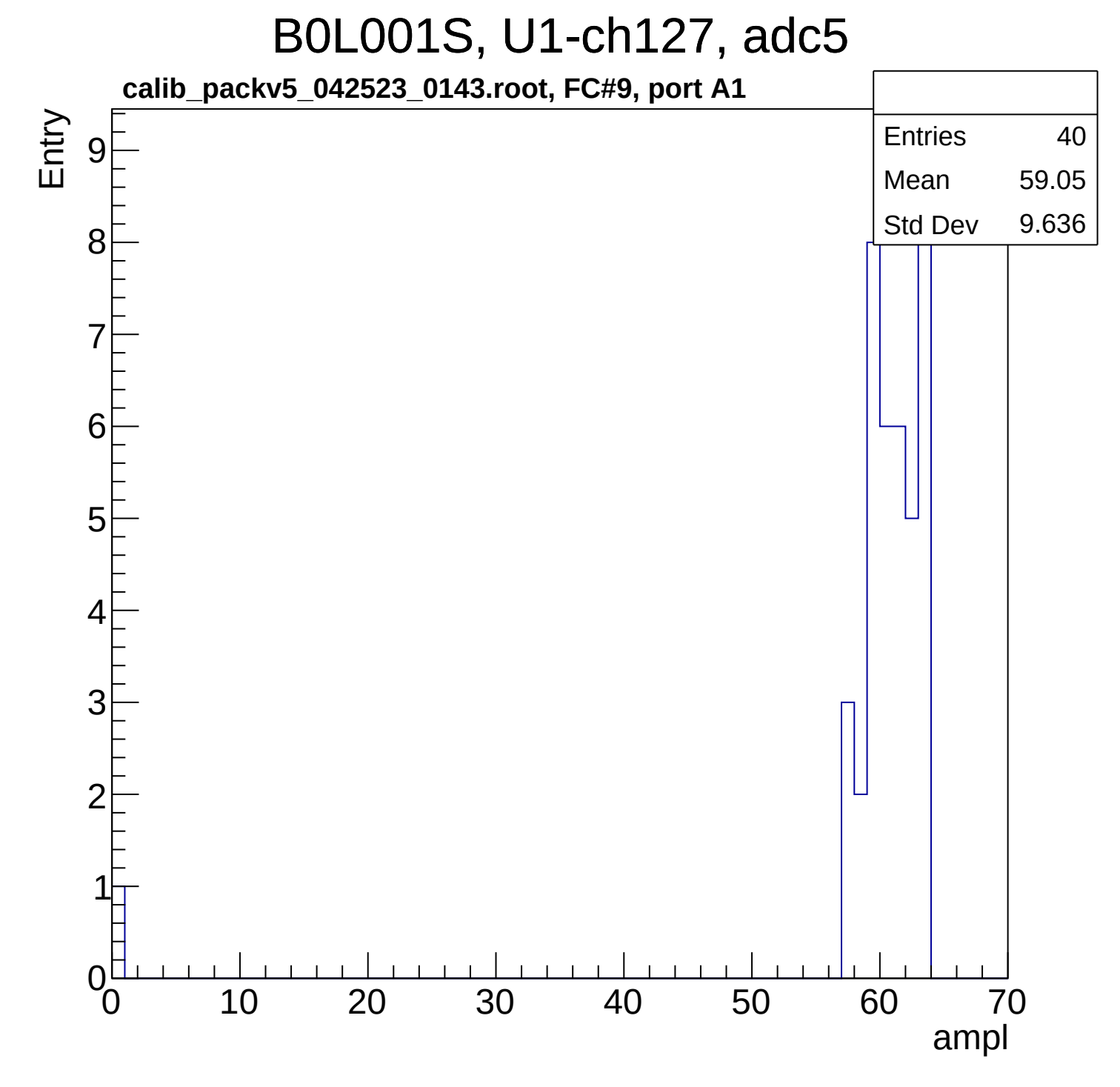

B0L001S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#9, port A1
Entries 64
Mean 30.17
Std Dev 5.085
Gaus mean : 31.2991
Gaus Width: 3.8555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

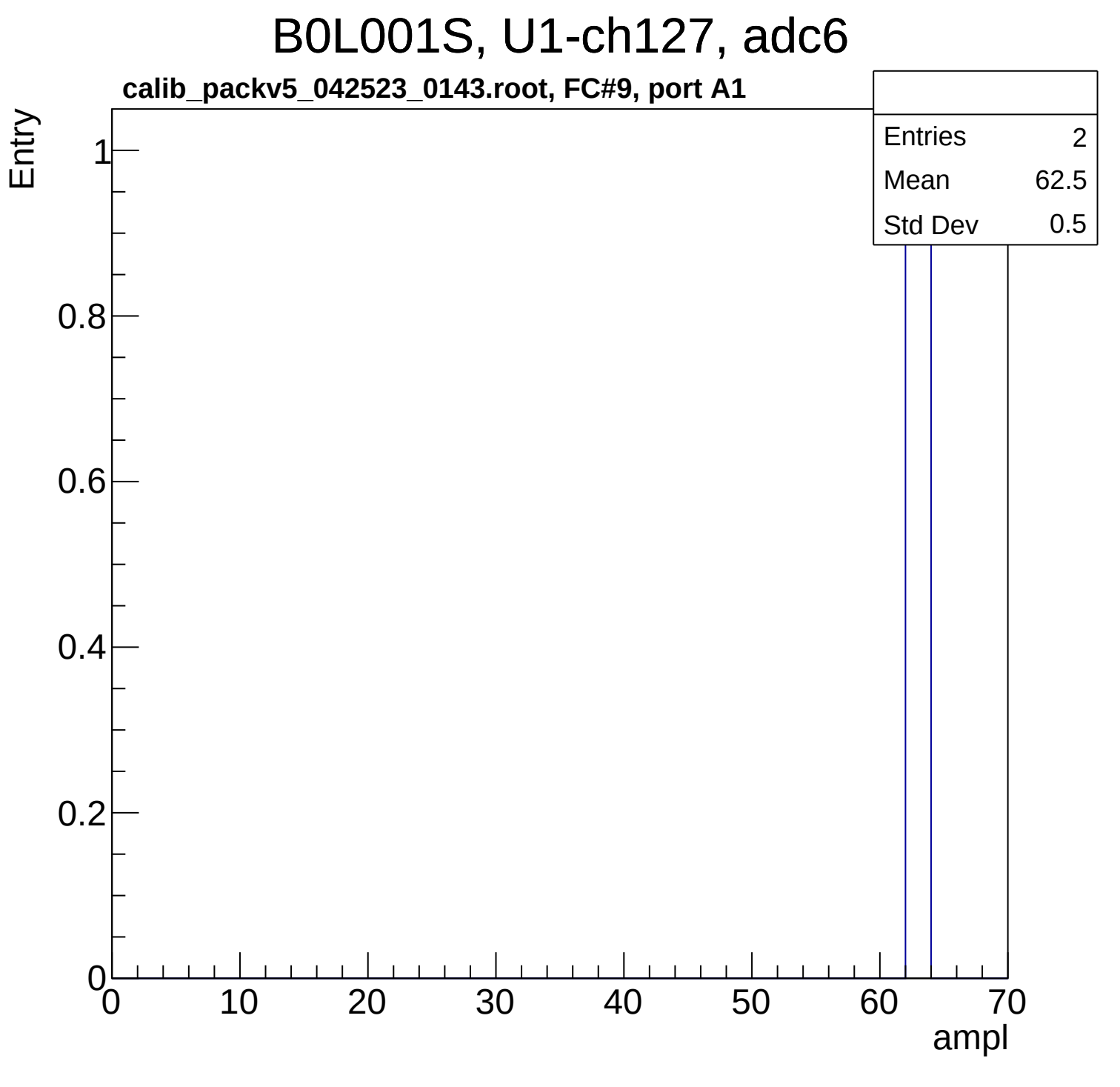

B0L001S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#9, port A1
Entries 74
Mean 37.88
Std Dev 3.741
Gaus mean : 38.3208
Gaus Width: 4.0157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

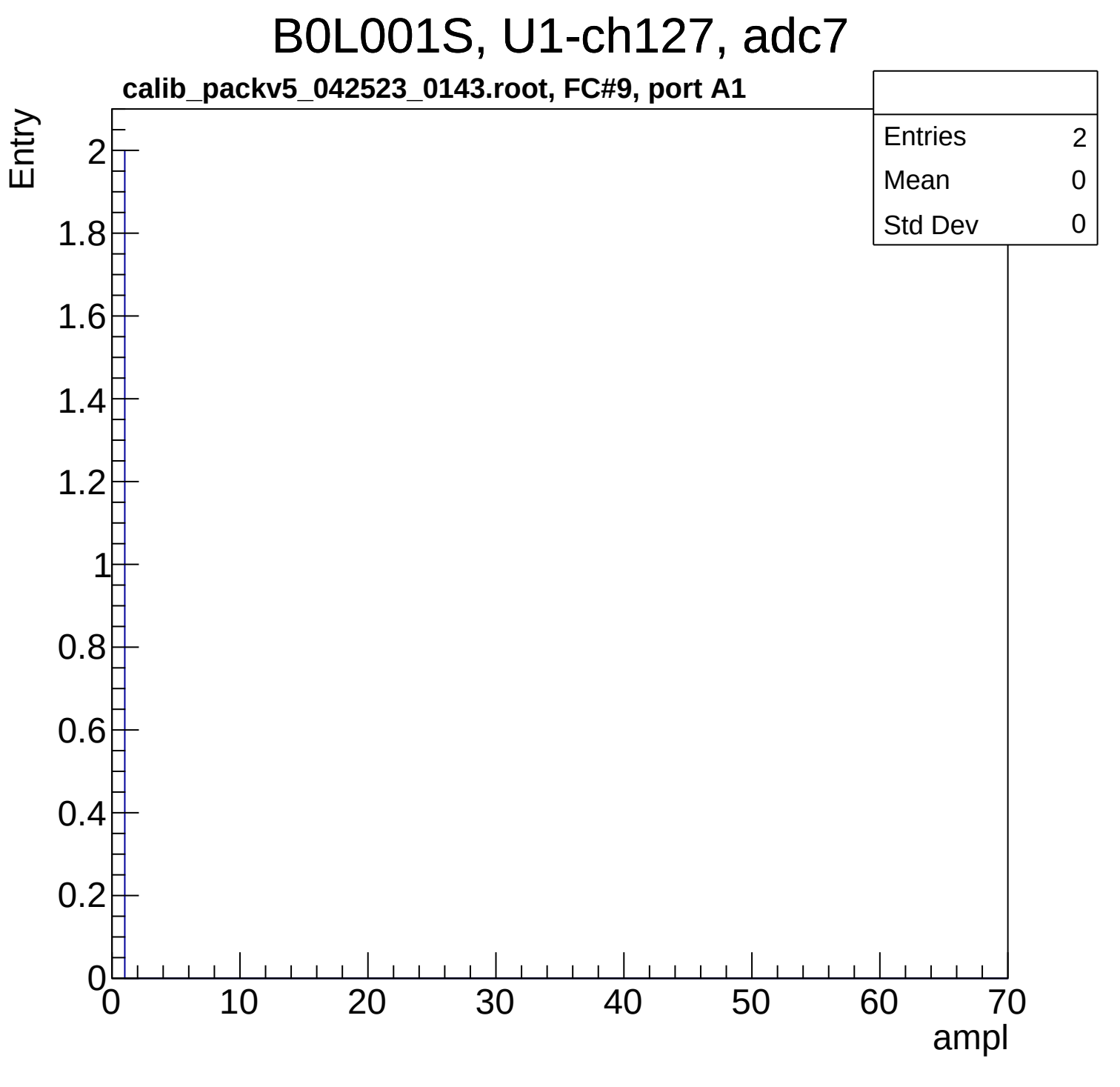

B0L001S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#9, port A1
Entries 58
Mean 43.41
Std Dev 3.119
Gaus mean : 43.8857
Gaus Width: 3.1271
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

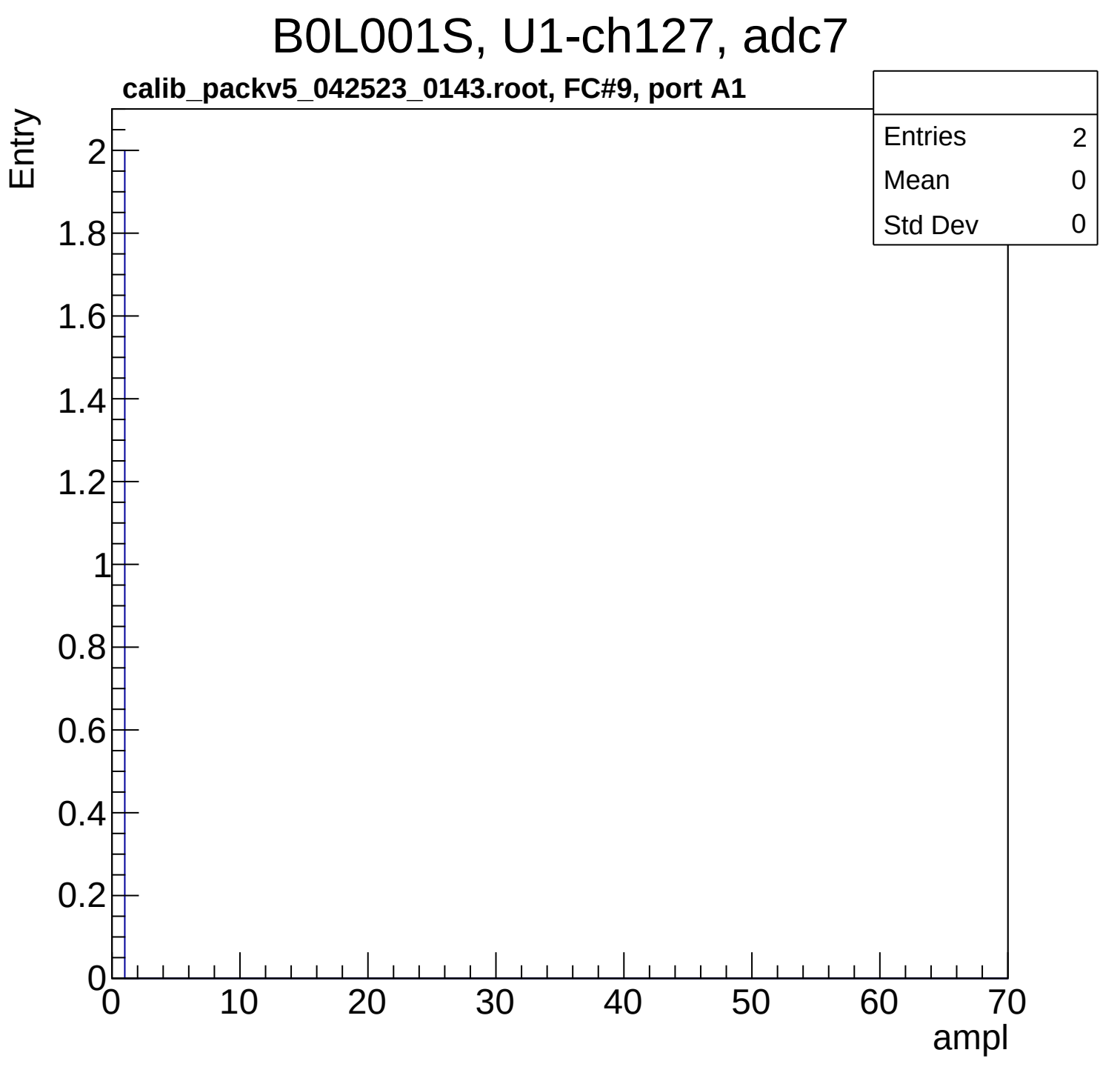

B0L001S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#9, port A1
Entries 62
Mean 50.84
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B0L001S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#9, port A1
Entries 61
Mean 56
Std Dev 3.648
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#9, port A1
Entries 40
Mean 59.05
Std Dev 9.636
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L001S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L001S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#9, port A1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl