

B0L102S, U3-ch0

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.1
Std Dev	12.28

Turn on : 24.4251

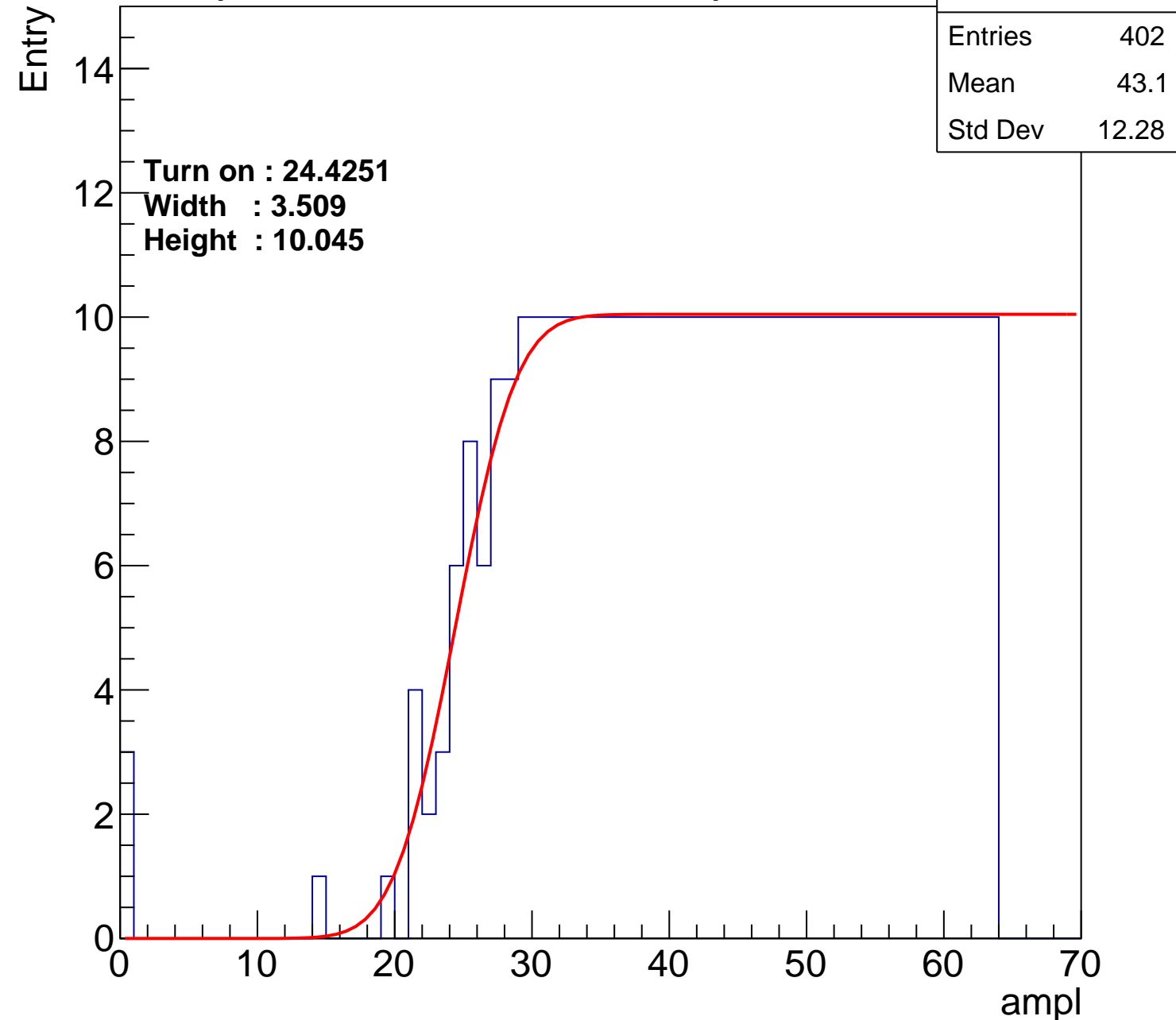
Width : 3.509

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.5
Std Dev	11.4

Turn on : 27.4865

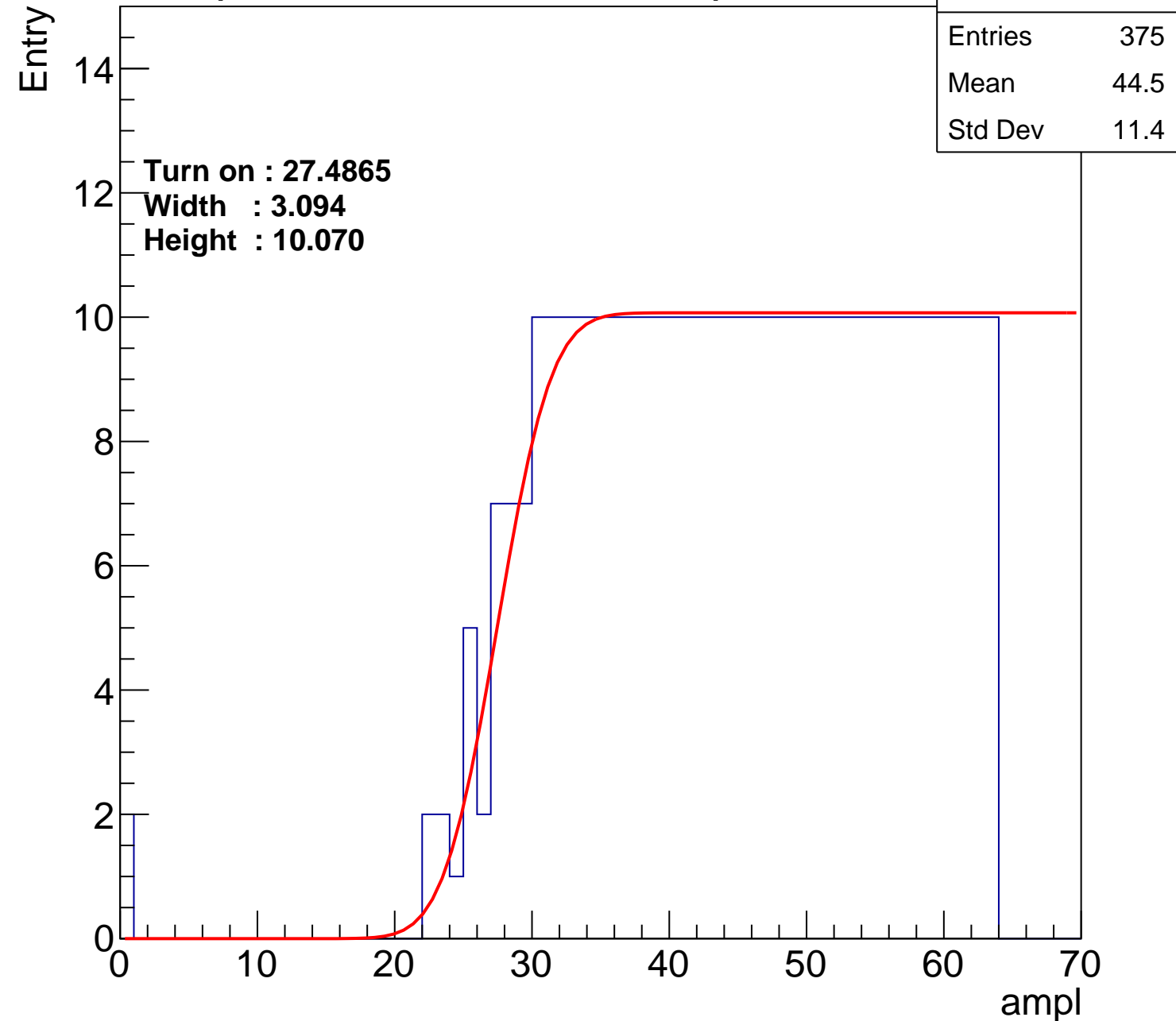
Width : 3.094

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch2

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.84
Std Dev	11.66

Turn on : 25.7416

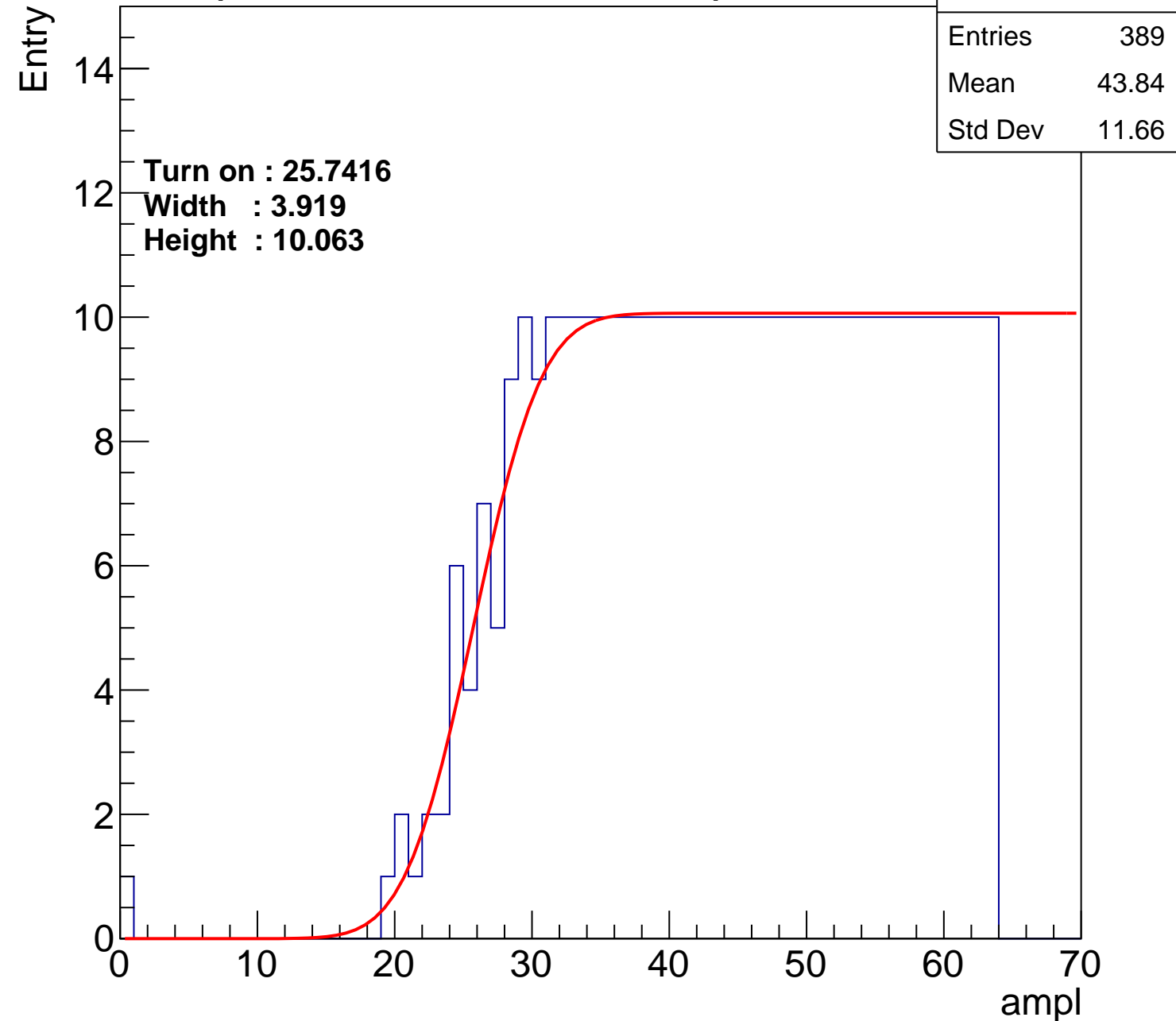
Width : 3.919

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.08
Std Dev	12.08

Turn on : 27.4907

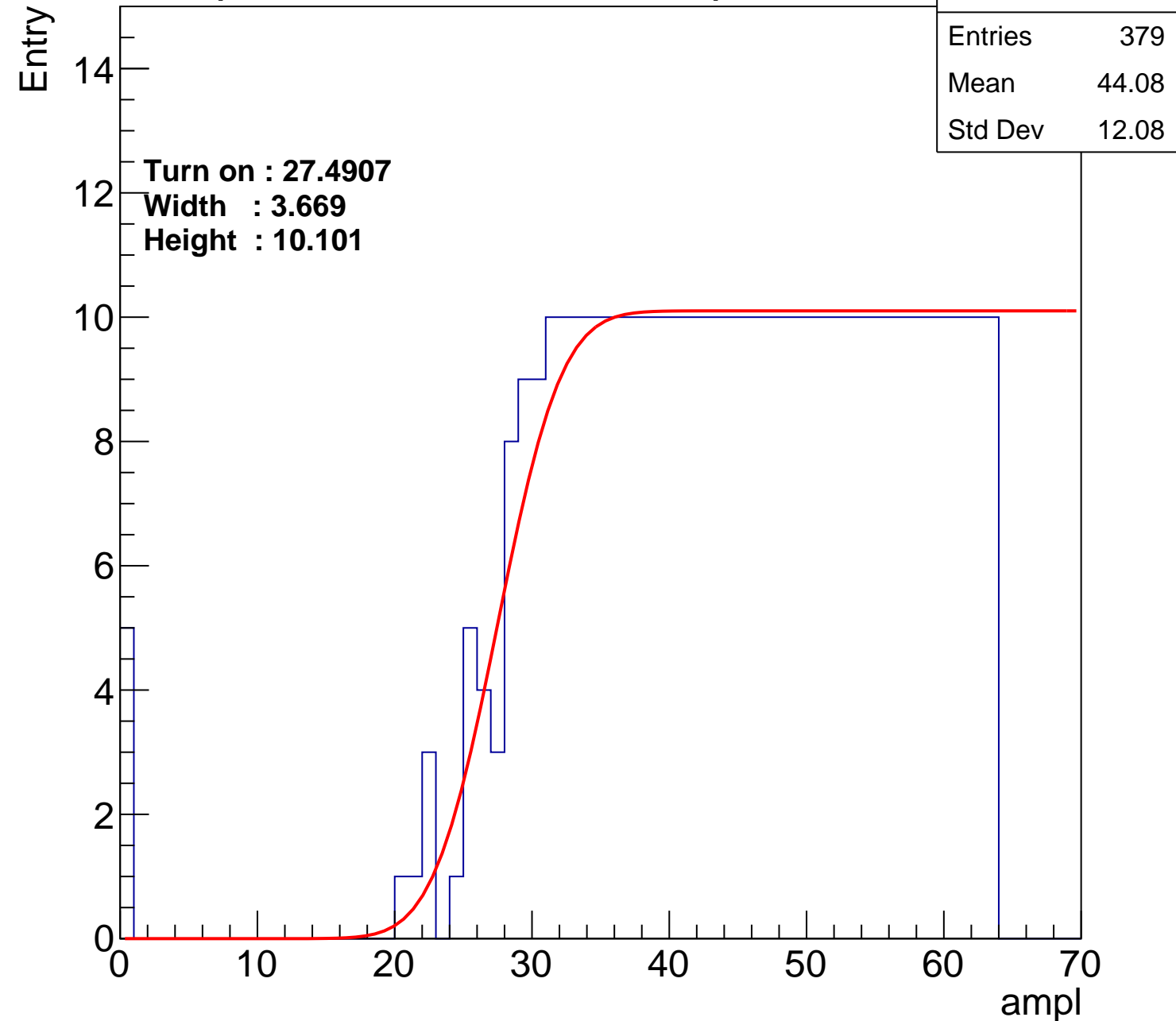
Width : 3.669

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch4

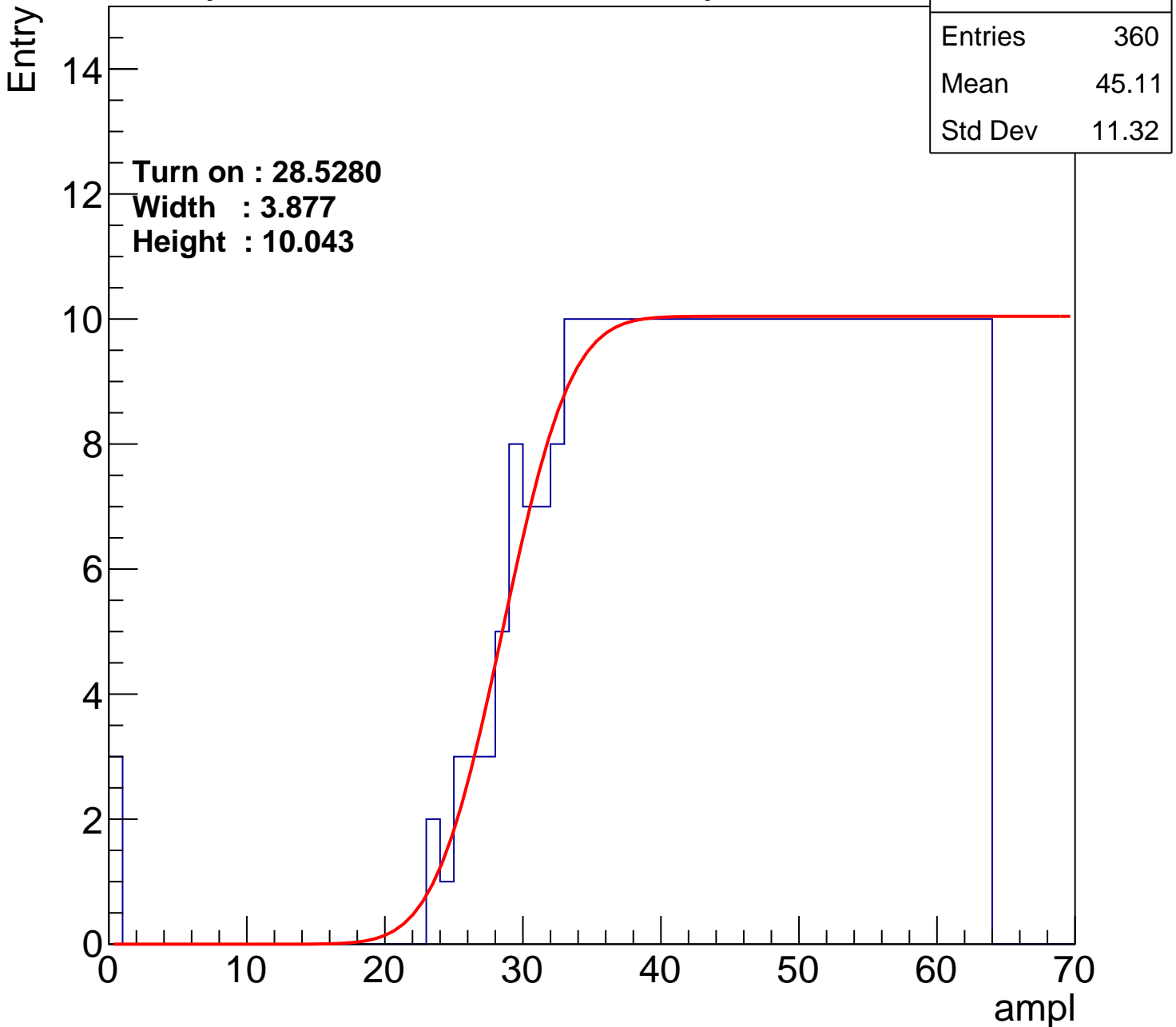
calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.11
Std Dev	11.32

Turn on : 28.5280

Width : 3.877

Height : 10.043



B0L102S, U3-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.5682

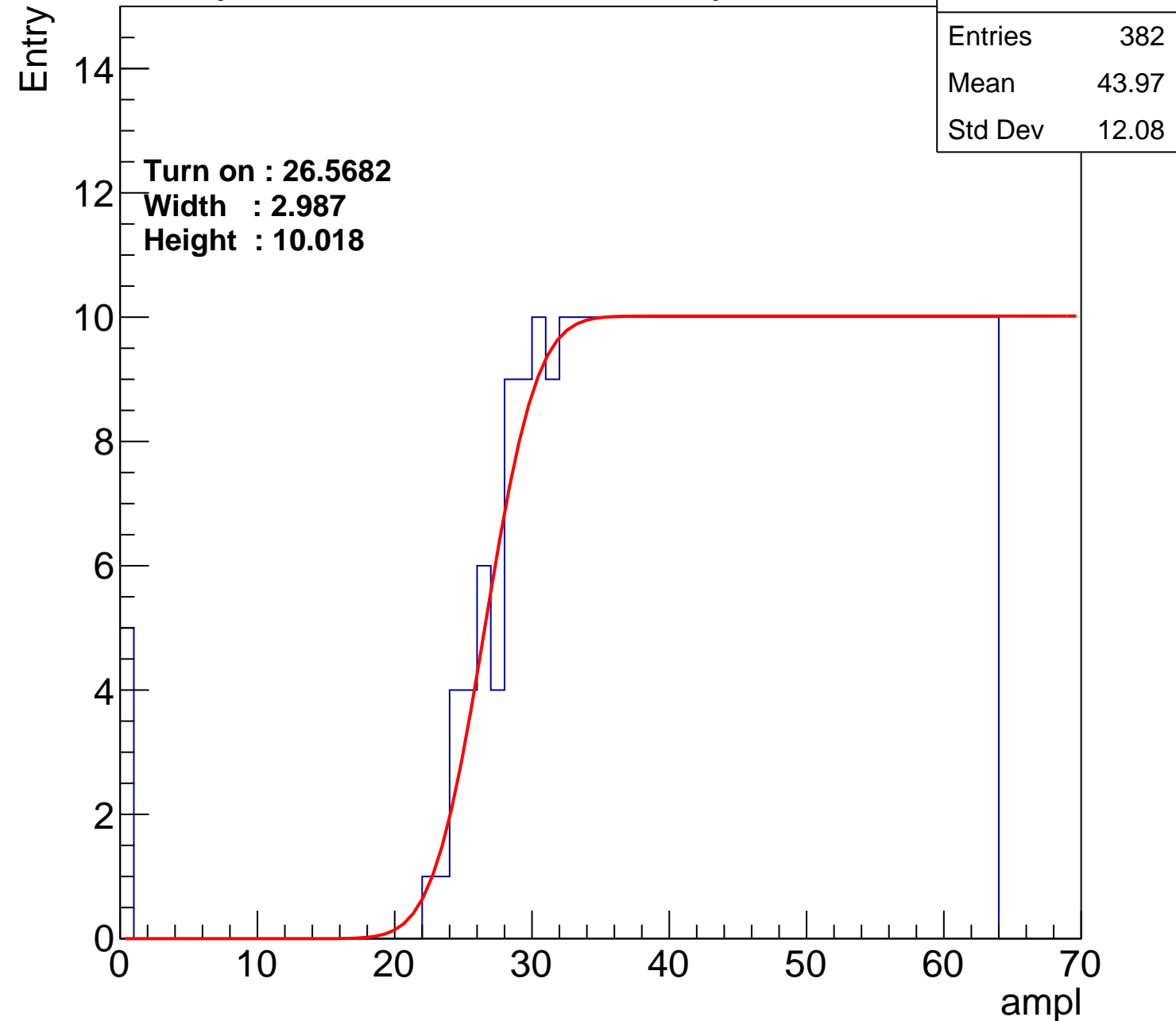
Width : 2.987

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch6

calib_packv5_042523_0143.root, FC#12, port B1

Entries	377
Mean	44.47
Std Dev	11.27

Turn on : 27.0605

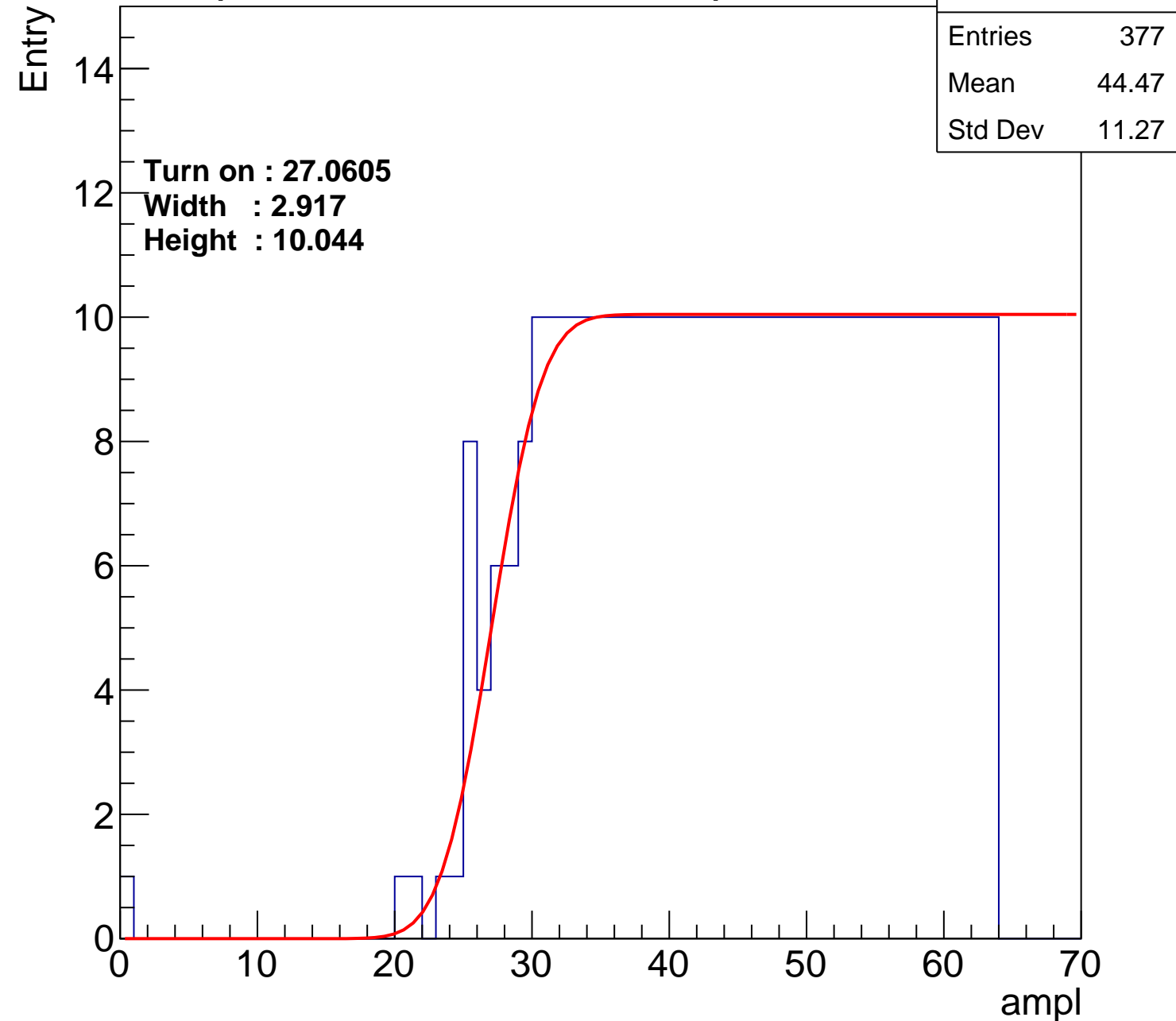
Width : 2.917

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch7

calib_packv5_042523_0143.root, FC#12, port B1

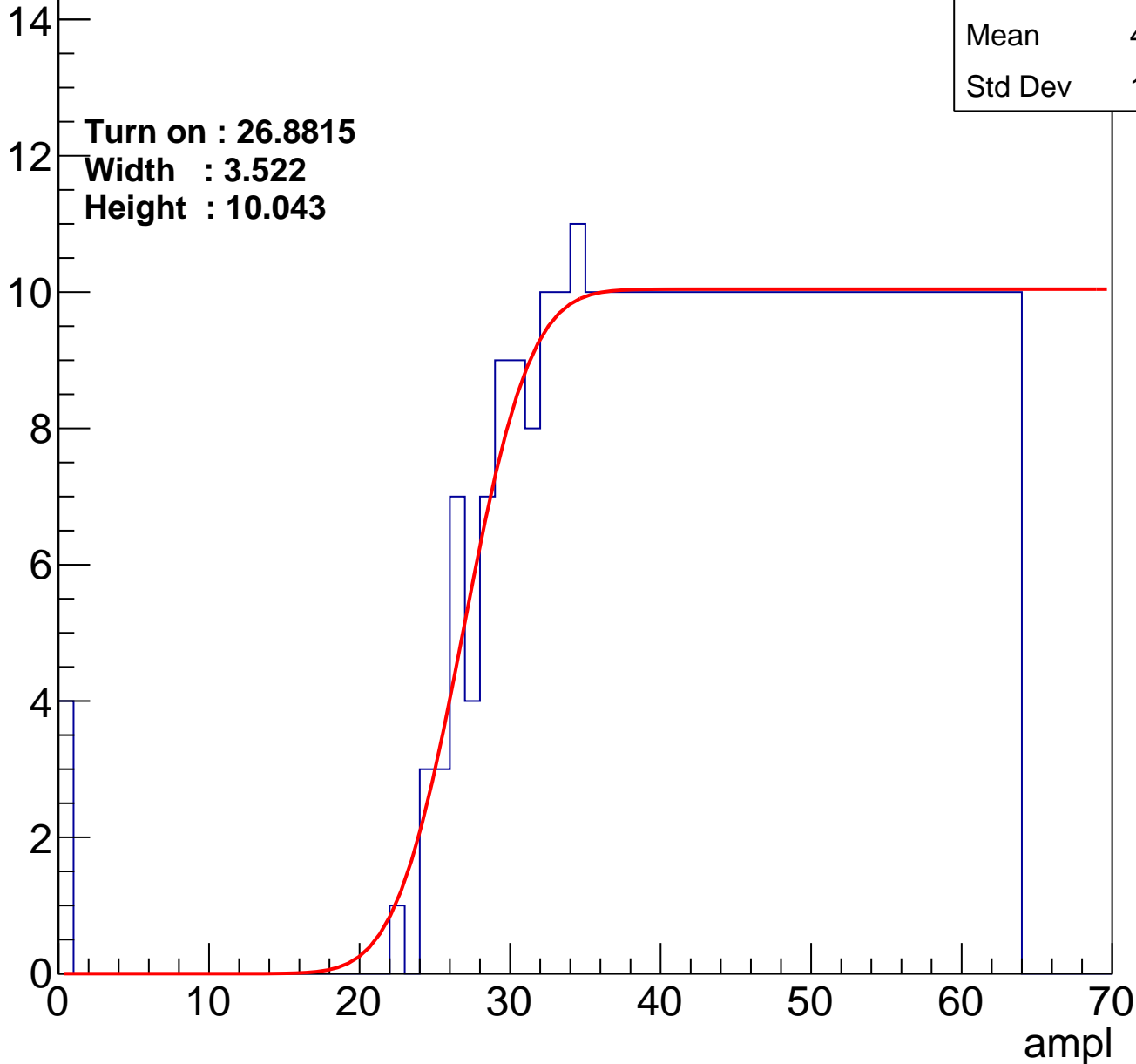
Entries	376
Mean	44.33
Std Dev	11.77

Turn on : 26.8815

Width : 3.522

Height : 10.043

Entry



B0L102S, U3-ch8

calib_packv5_042523_0143.root, FC#12, port B1

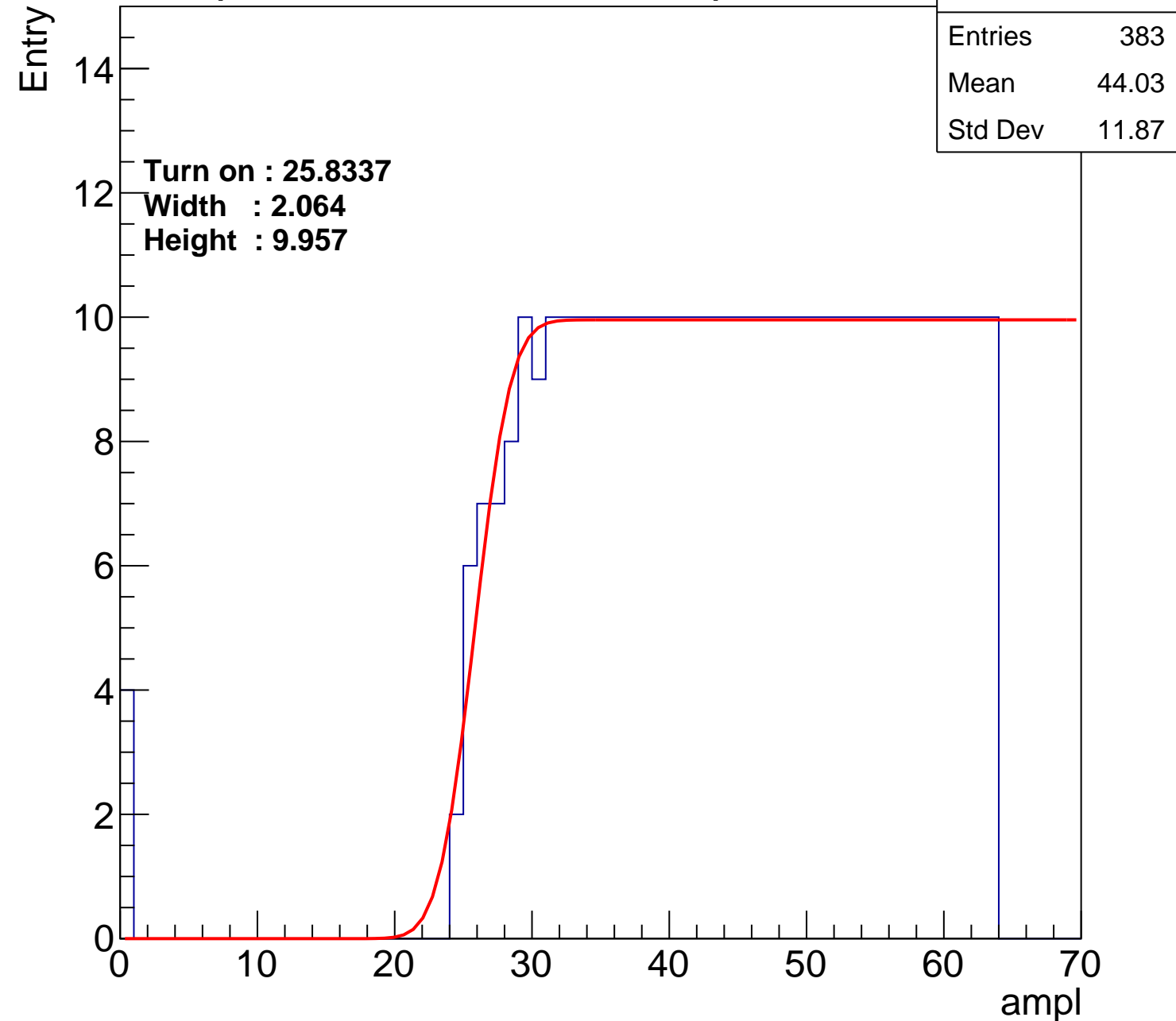
Entry

14
12
10
8
6
4
2
0

Turn on : 25.8337
Width : 2.064
Height : 9.957

Entries	383
Mean	44.03
Std Dev	11.87

ampl



B0L102S, U3-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.26
Std Dev	12.57

Turn on : 25.7706

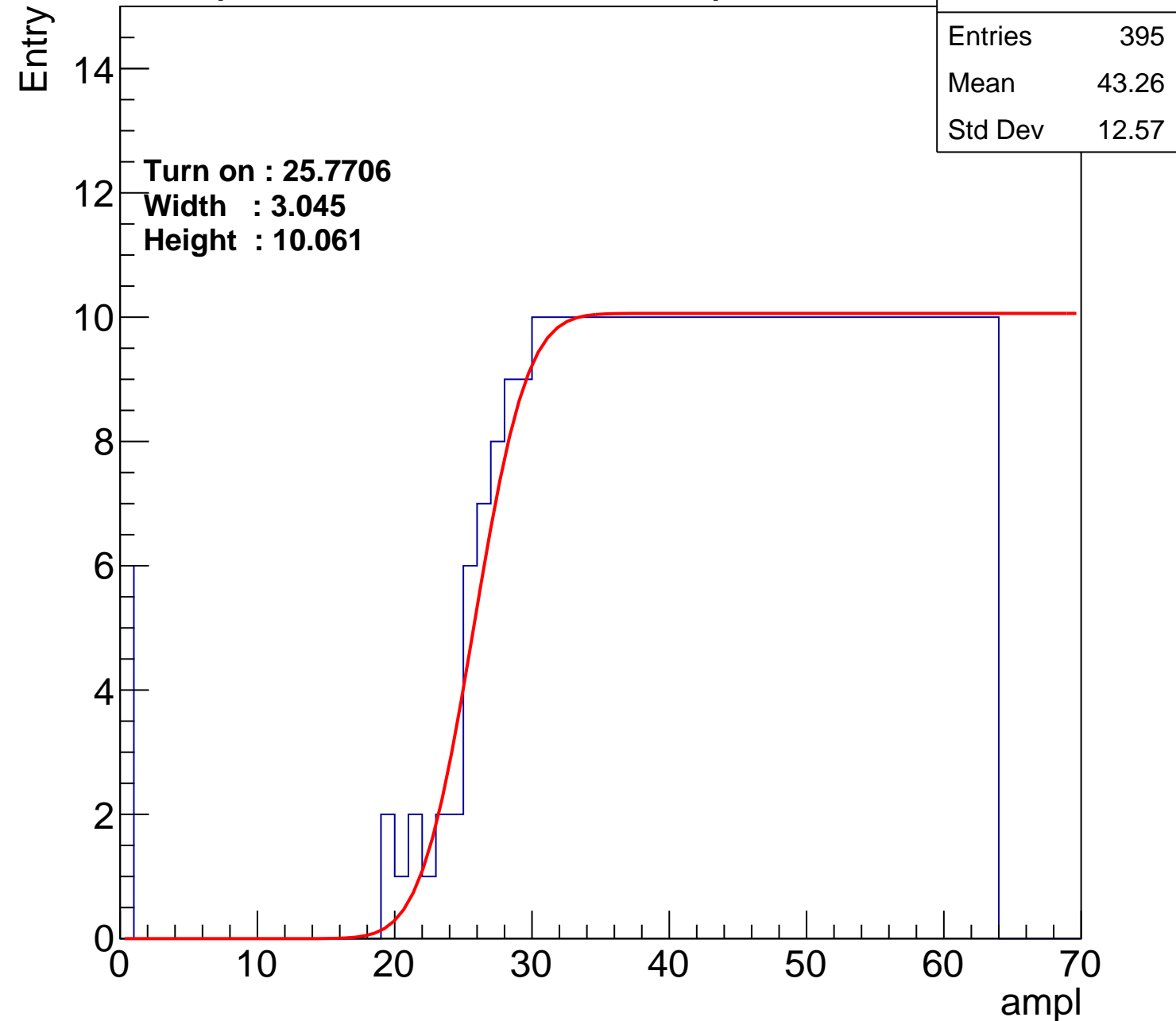
Width : 3.045

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch10

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.67
Std Dev	11.71

Turn on : 25.7648

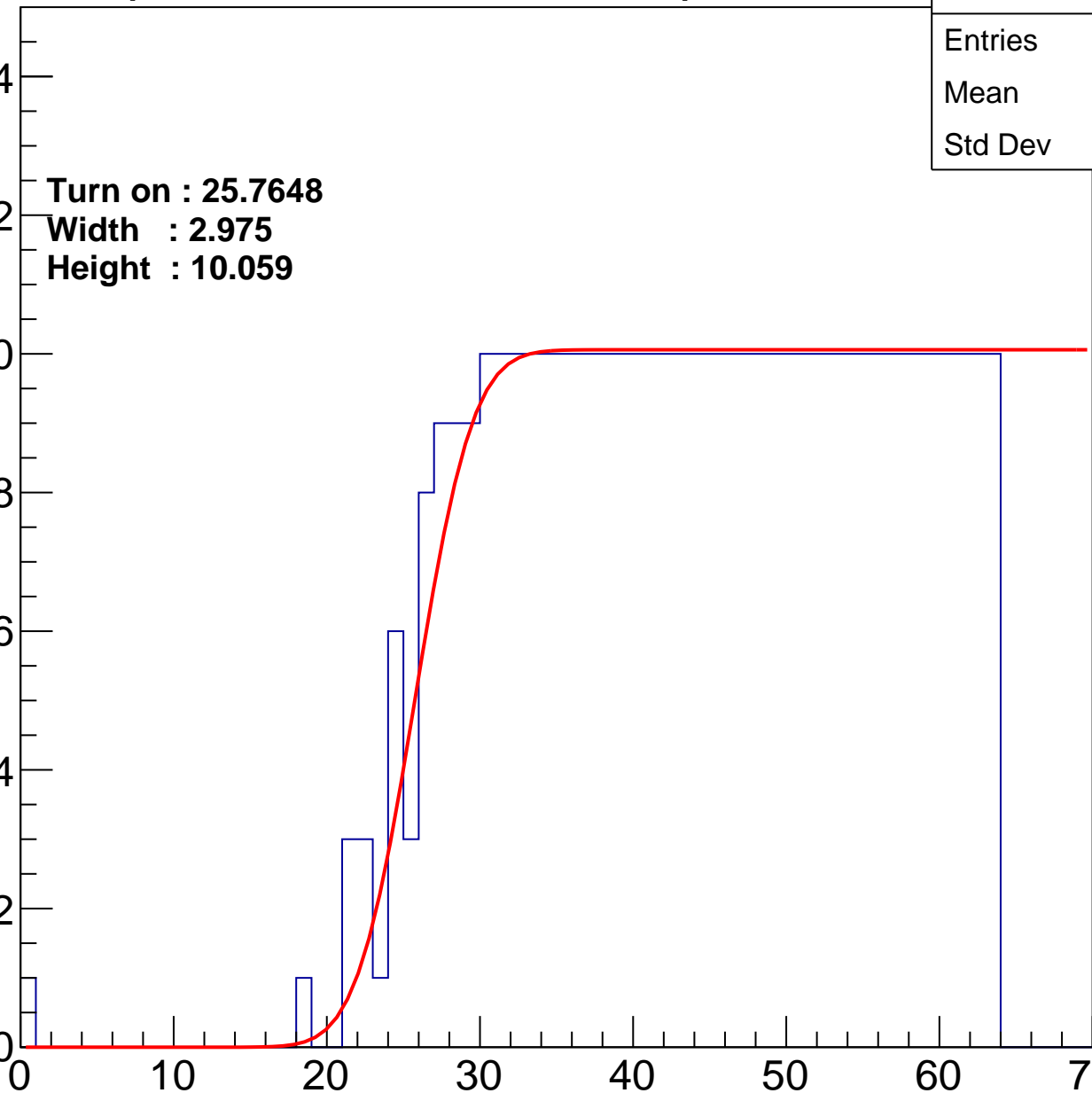
Width : 2.975

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch11

calib_packv5_042523_0143.root, FC#12, port B1

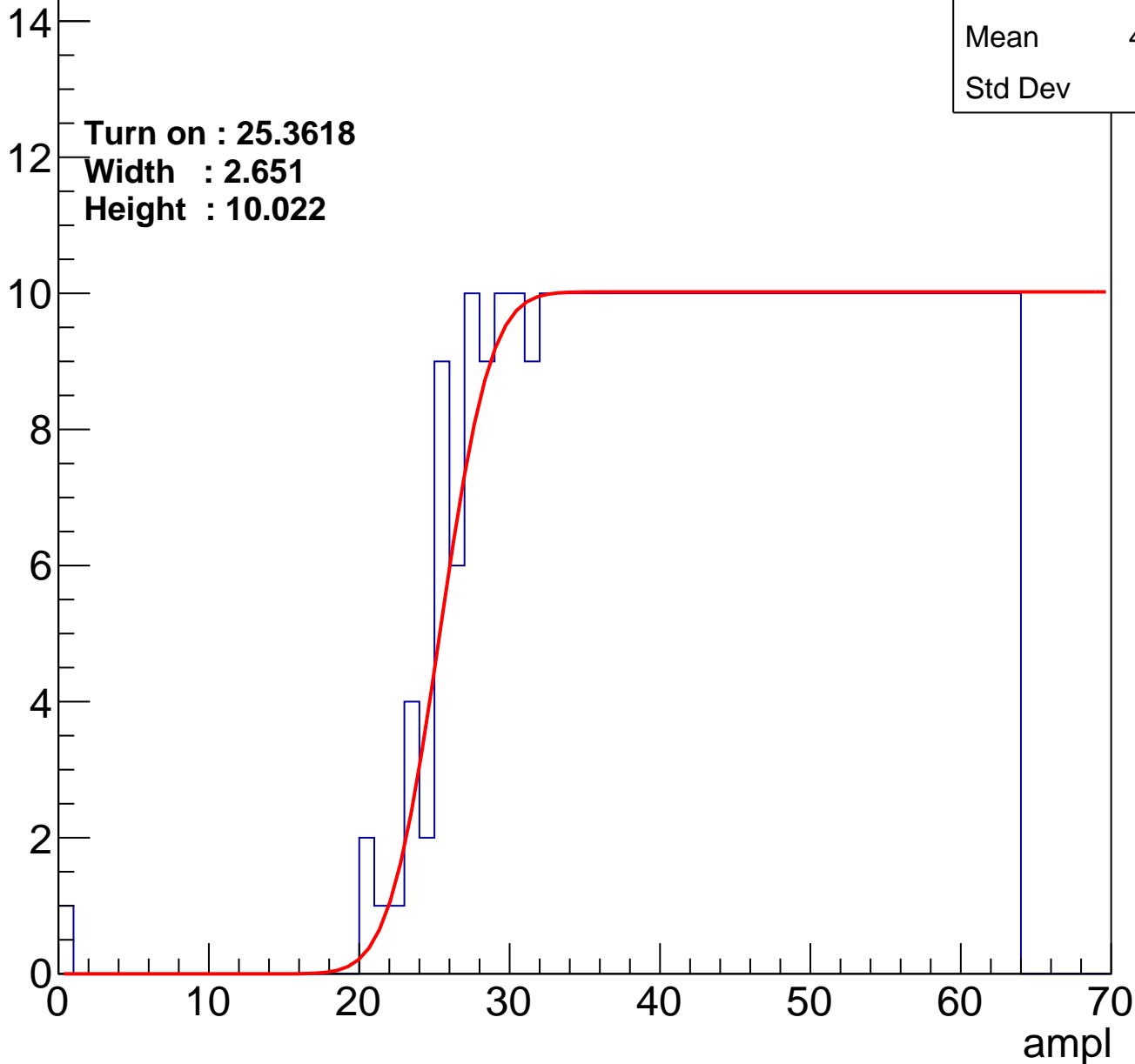
Entries	394
Mean	43.64
Std Dev	11.7

Turn on : 25.3618

Width : 2.651

Height : 10.022

Entry



B0L102S, U3-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.72
Std Dev	11.92

Turn on : 25.4890

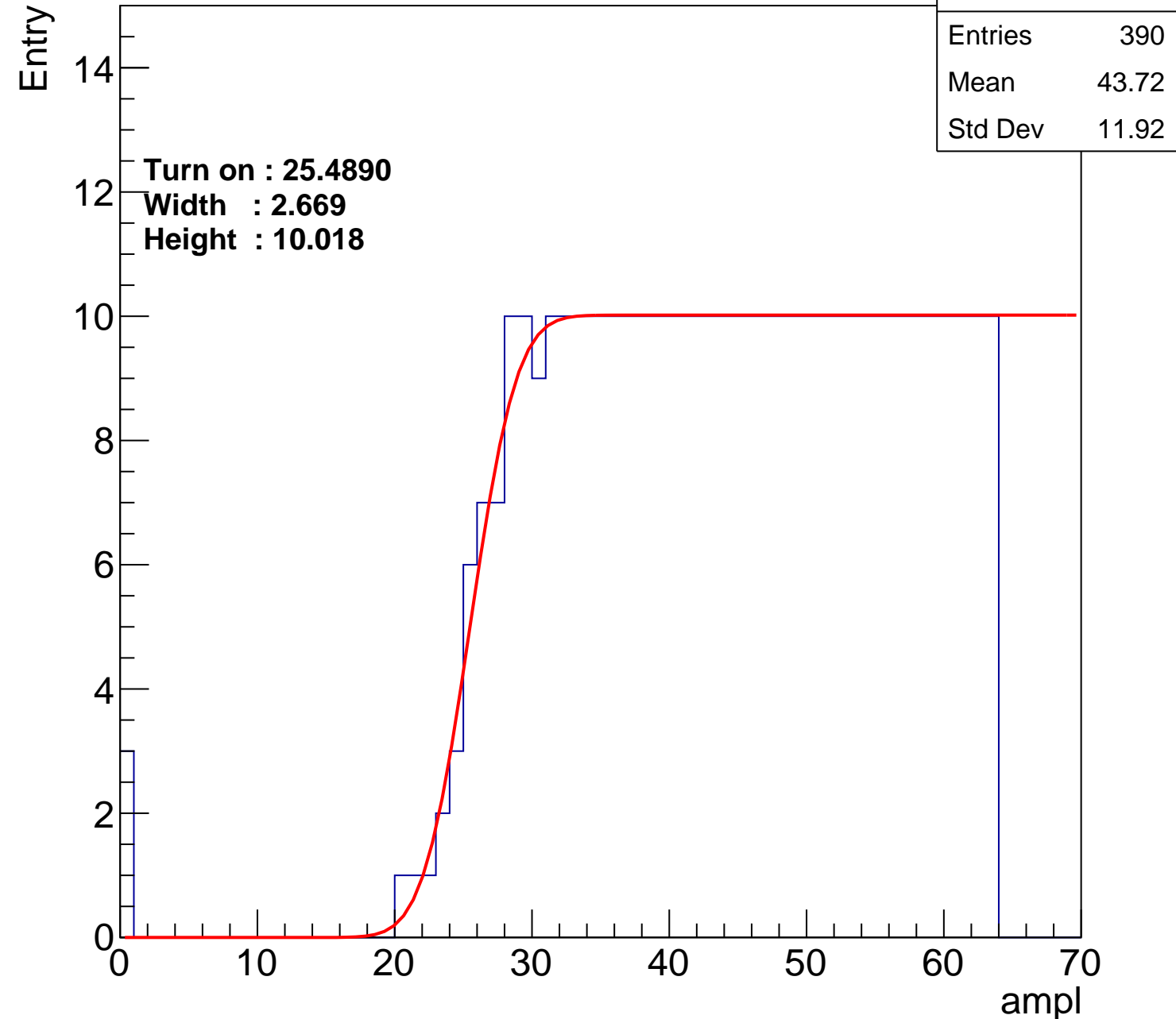
Width : 2.669

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 26.1528

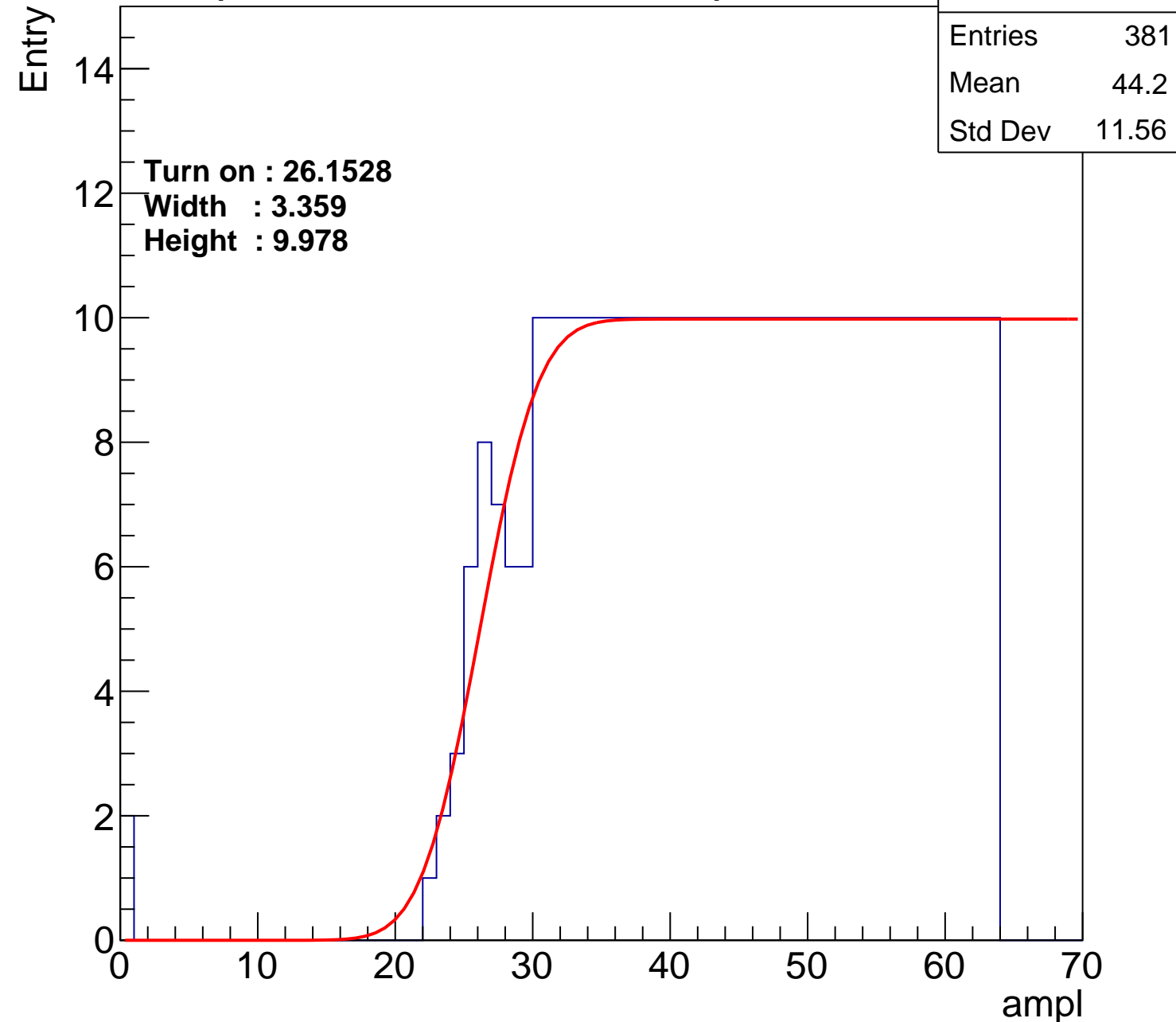
Width : 3.359

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	418
Mean	42.45
Std Dev	12.37

Turn on : 22.5486

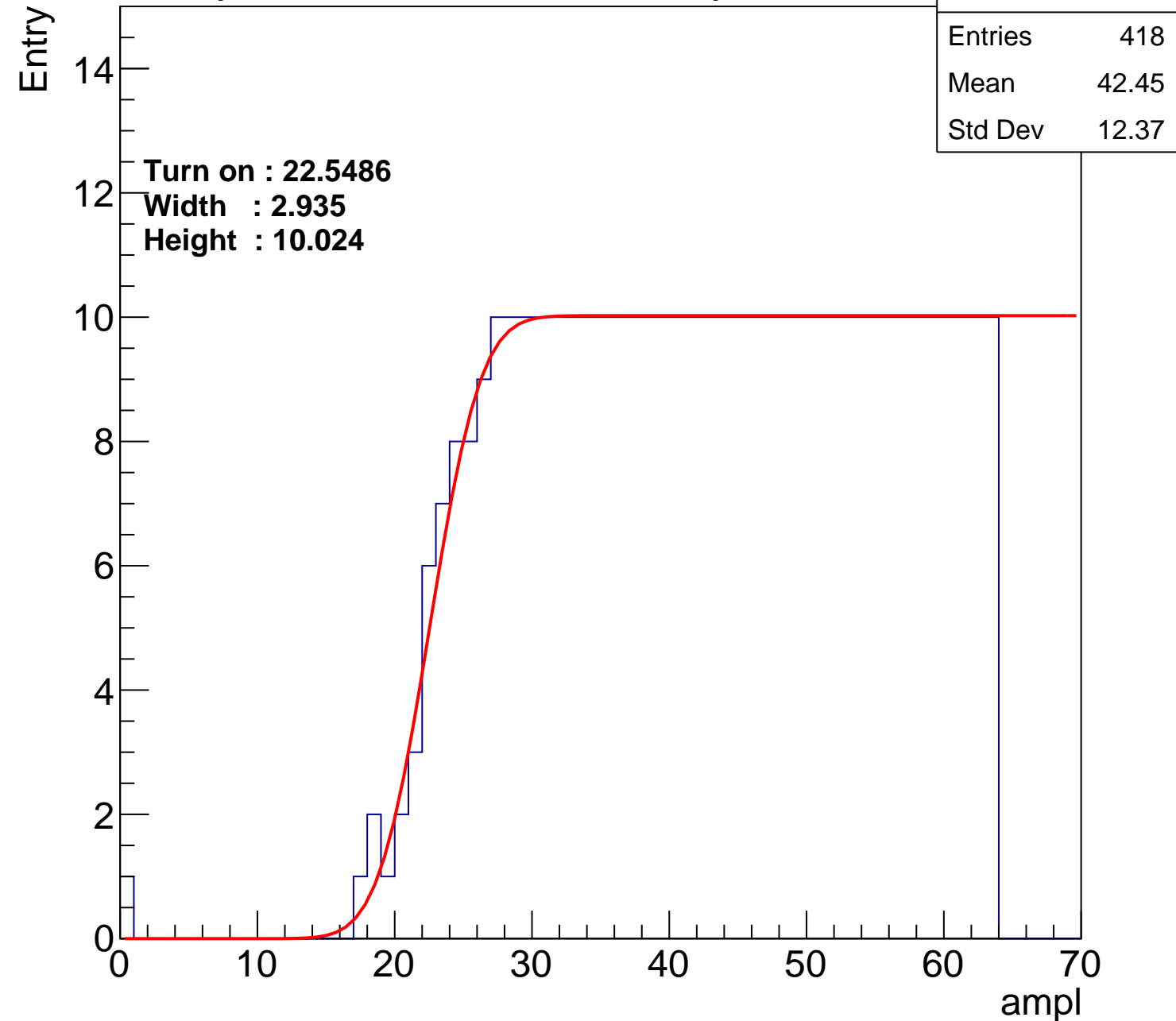
Width : 2.935

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch15

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.06
Std Dev	11.09

Turn on : 28.3021

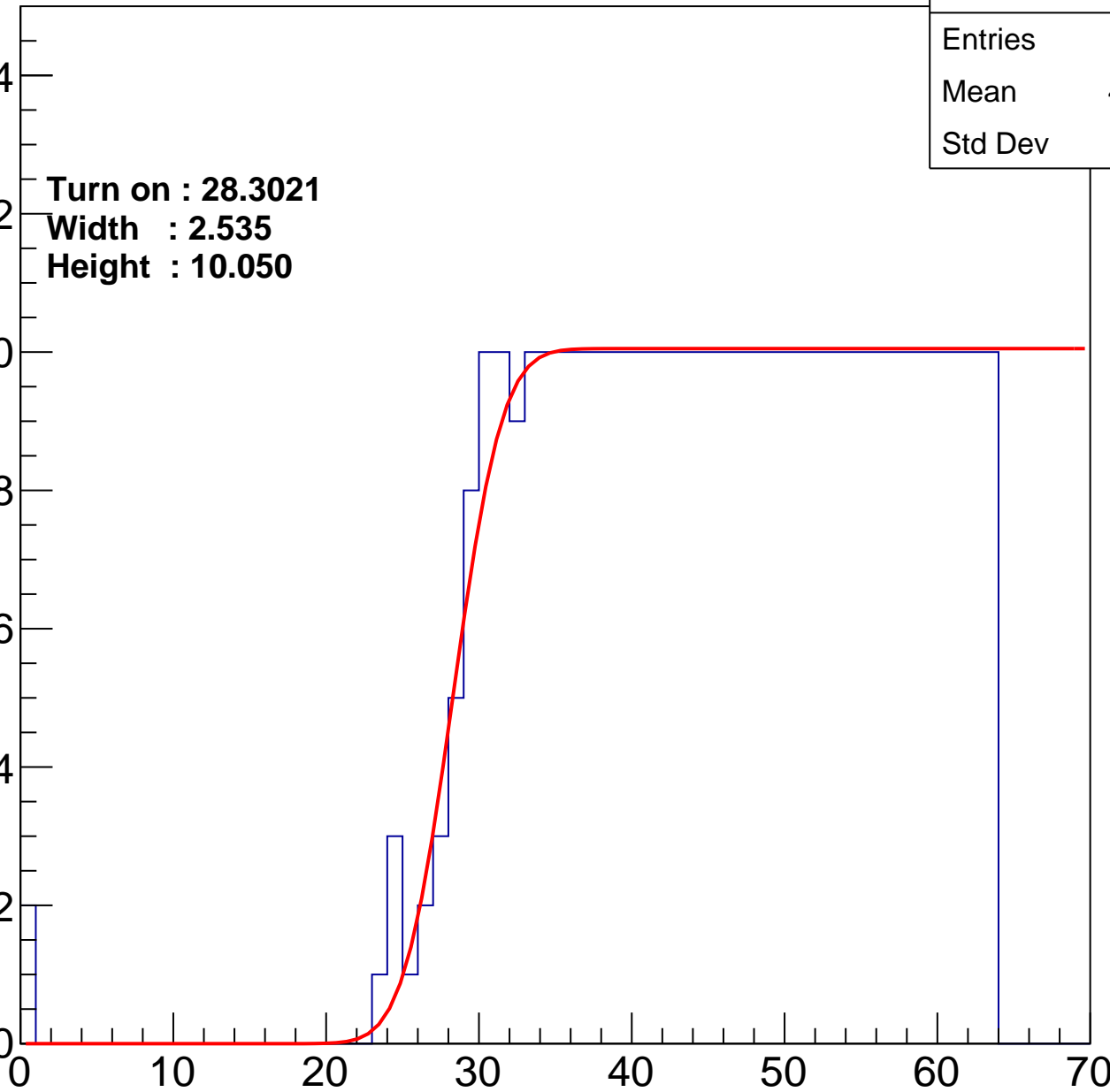
Width : 2.535

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.01
Std Dev	12.25

Turn on : 26.5914

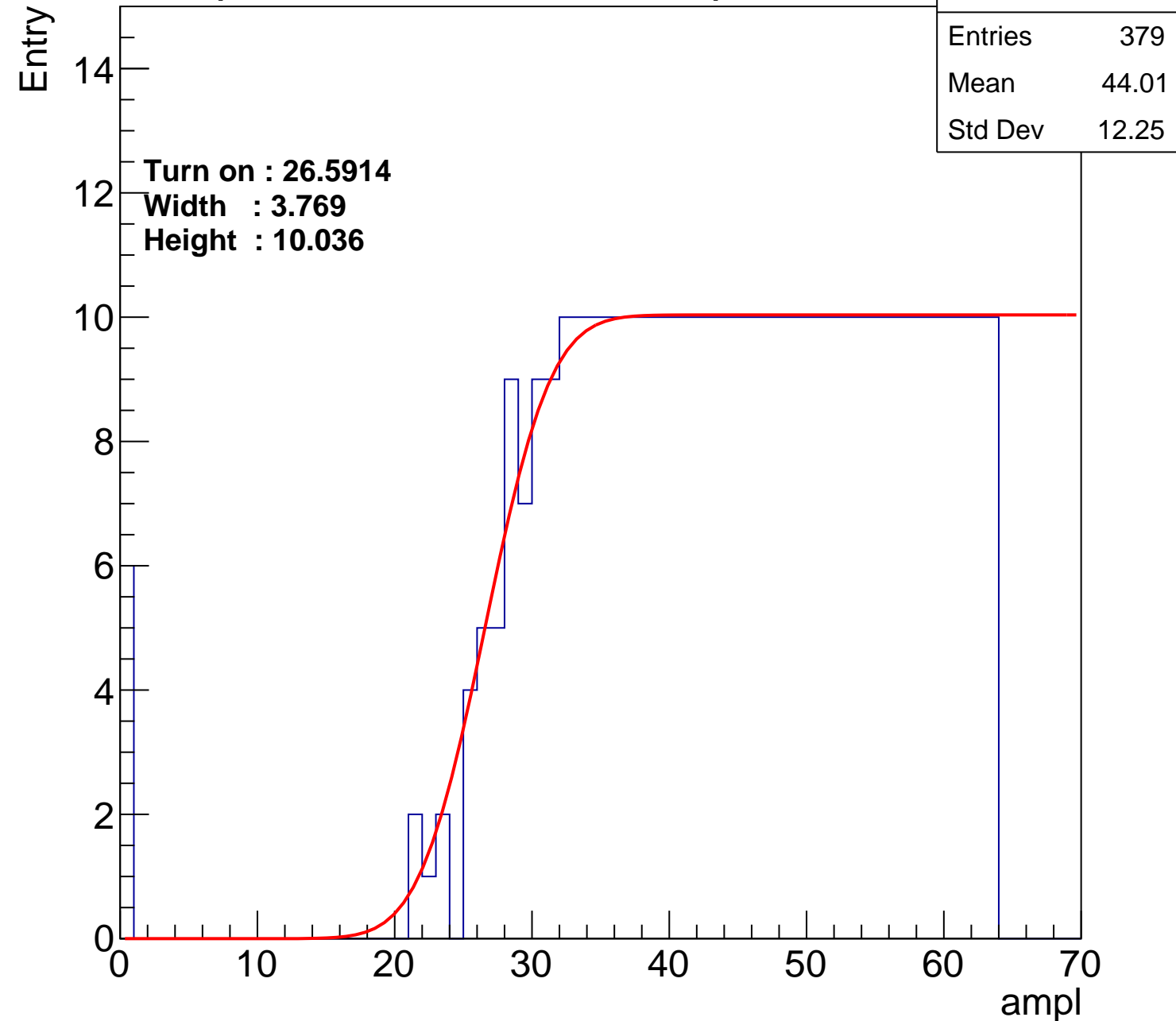
Width : 3.769

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.63
Std Dev	12

Turn on : 25.4001

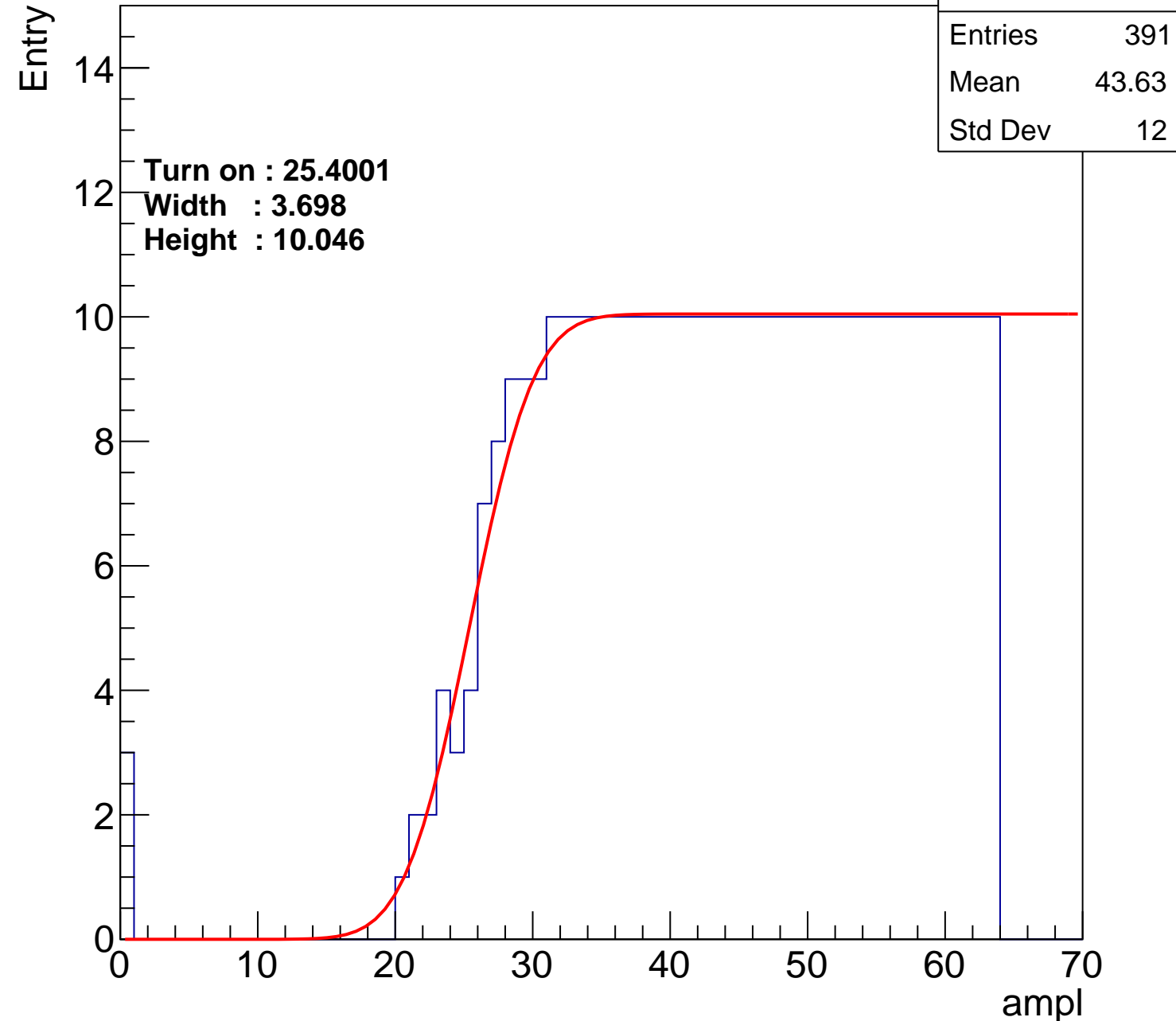
Width : 3.698

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.39
Std Dev	11.64

Turn on : 26.6419

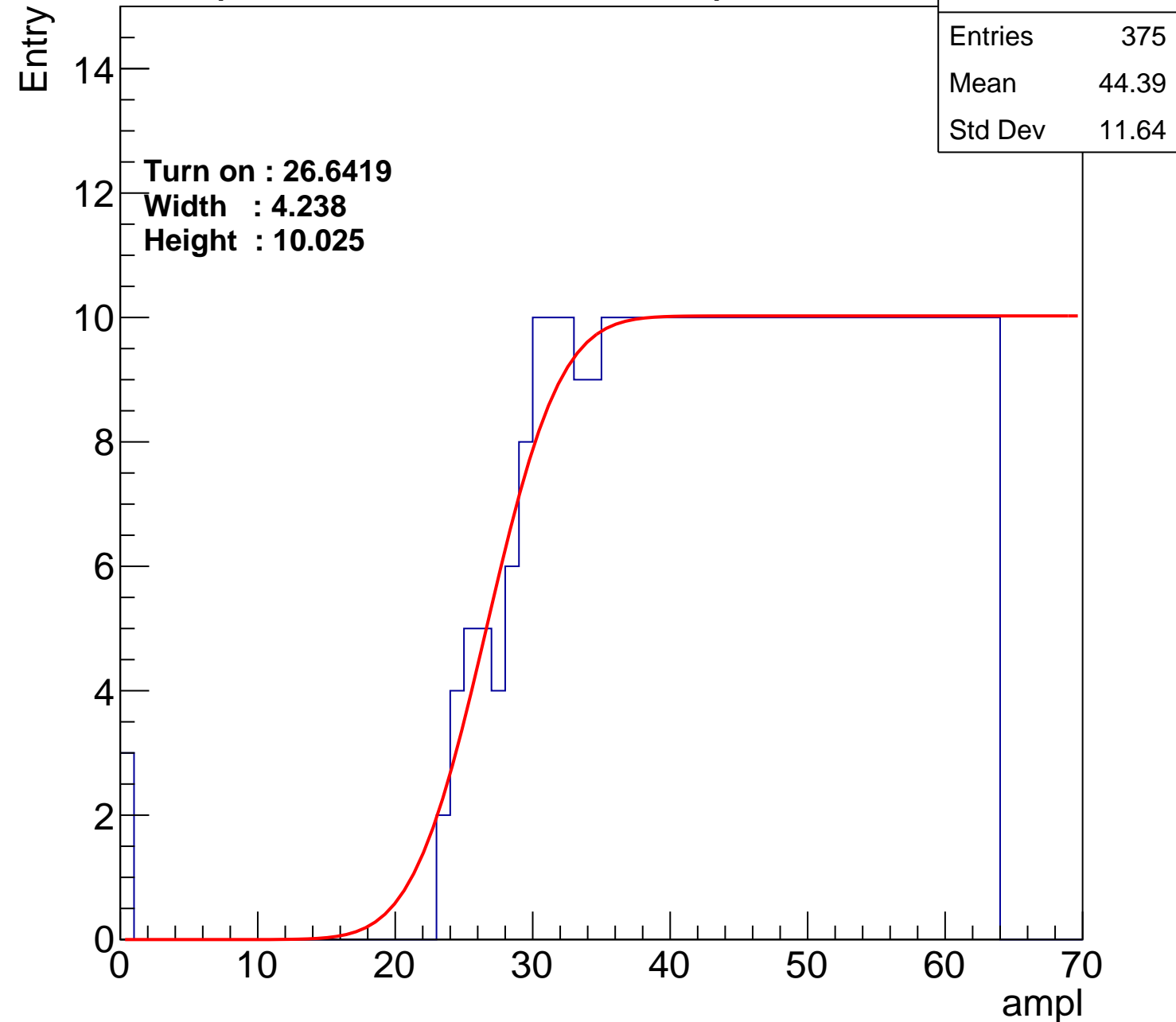
Width : 4.238

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.33
Std Dev	12.51

Turn on : 25.5796

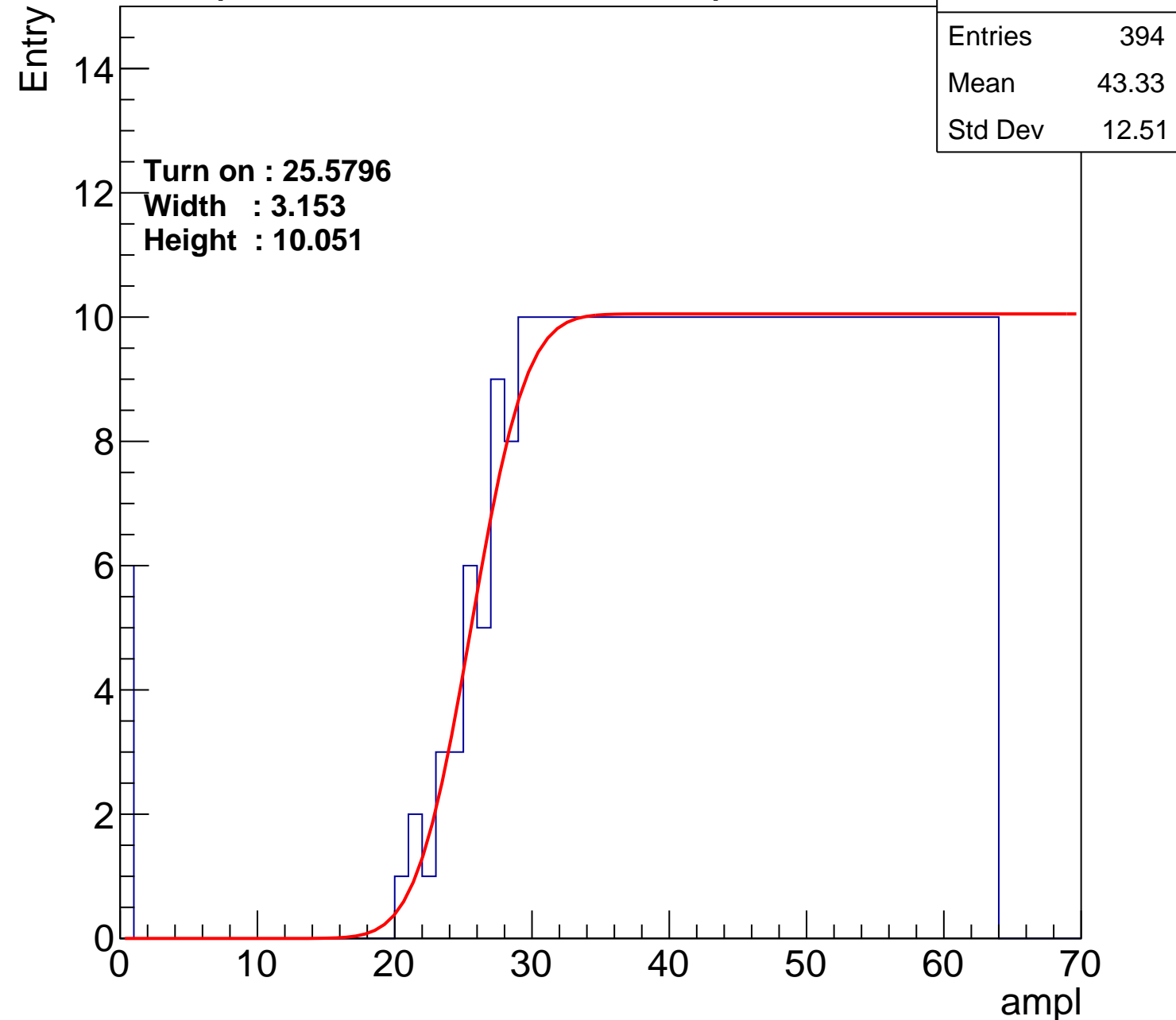
Width : 3.153

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch20

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.39
Std Dev	12.29

Turn on : 25.9545

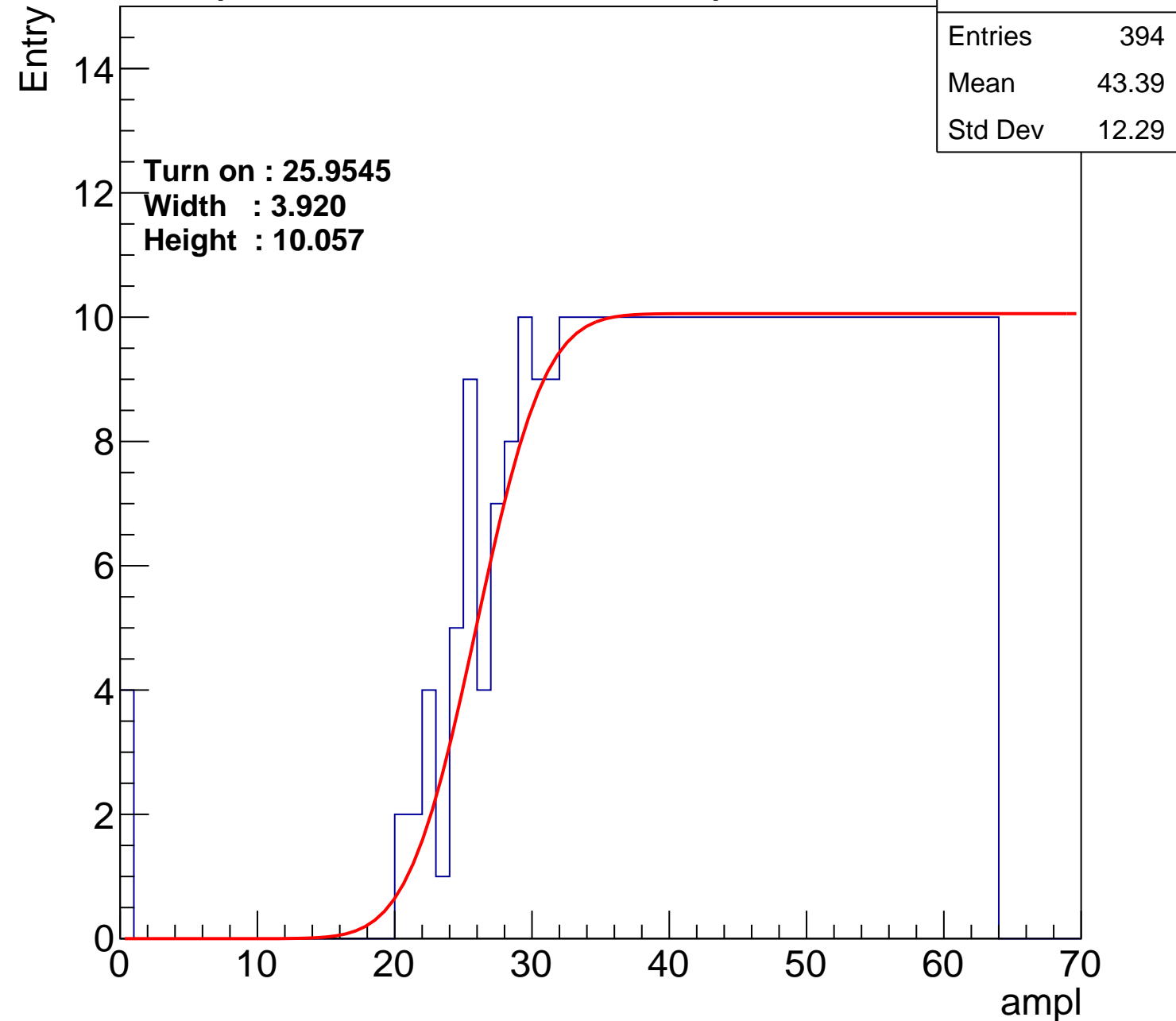
Width : 3.920

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.93
Std Dev	12.09

Turn on : 26.5462

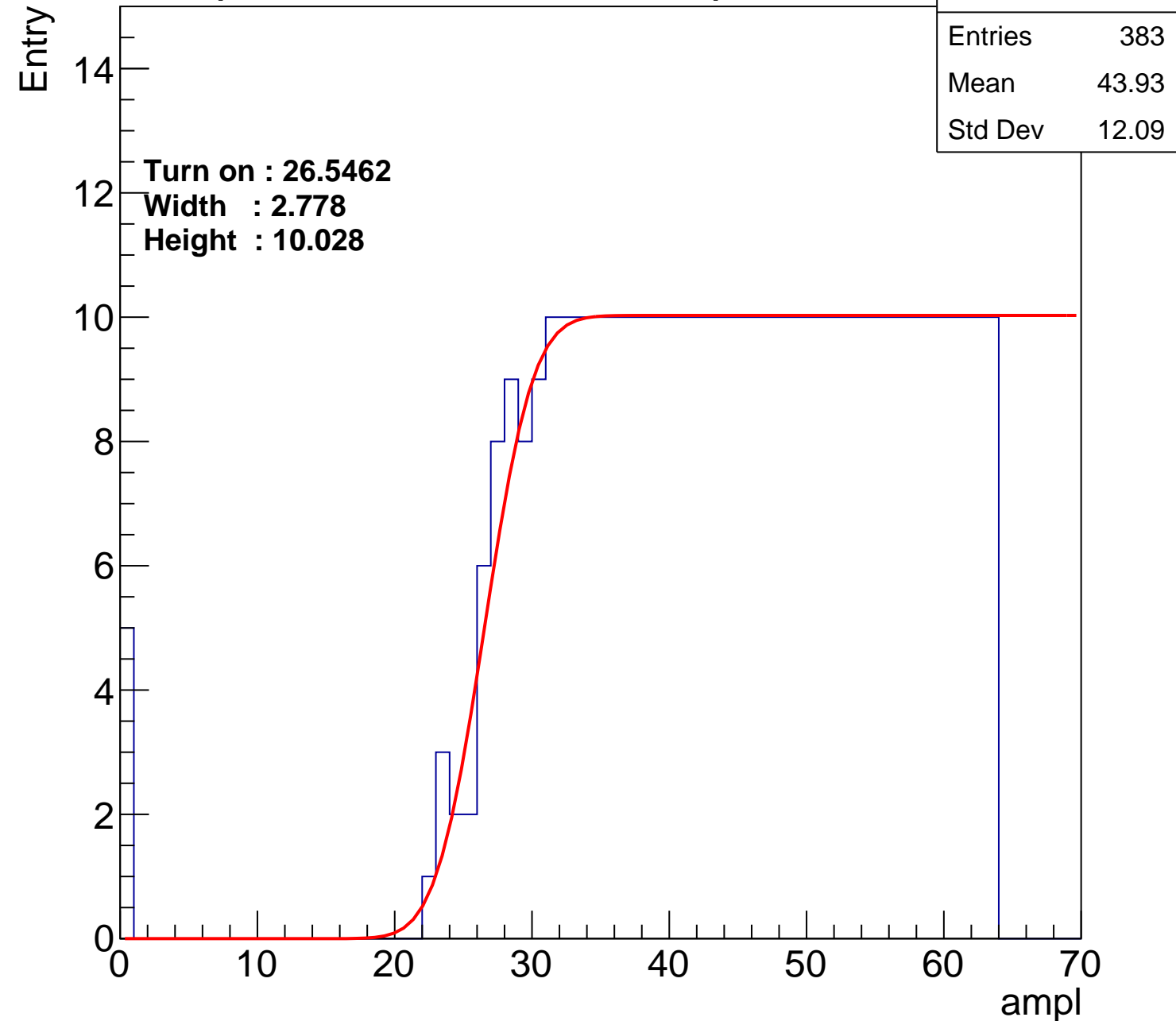
Width : 2.778

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.63
Std Dev	11.72

Turn on : 25.0752

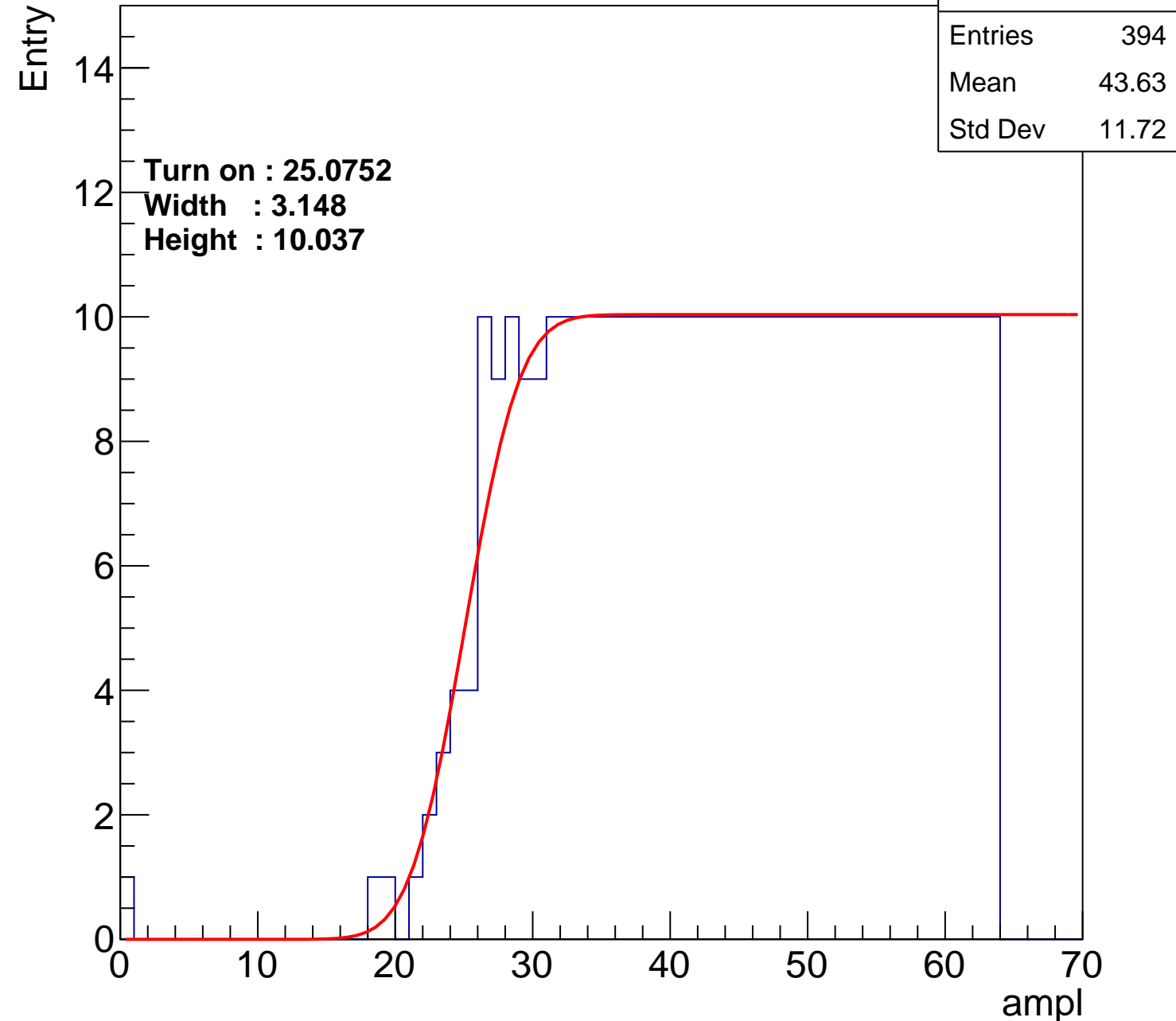
Width : 3.148

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.84
Std Dev	12.41

Turn on : 26.4337

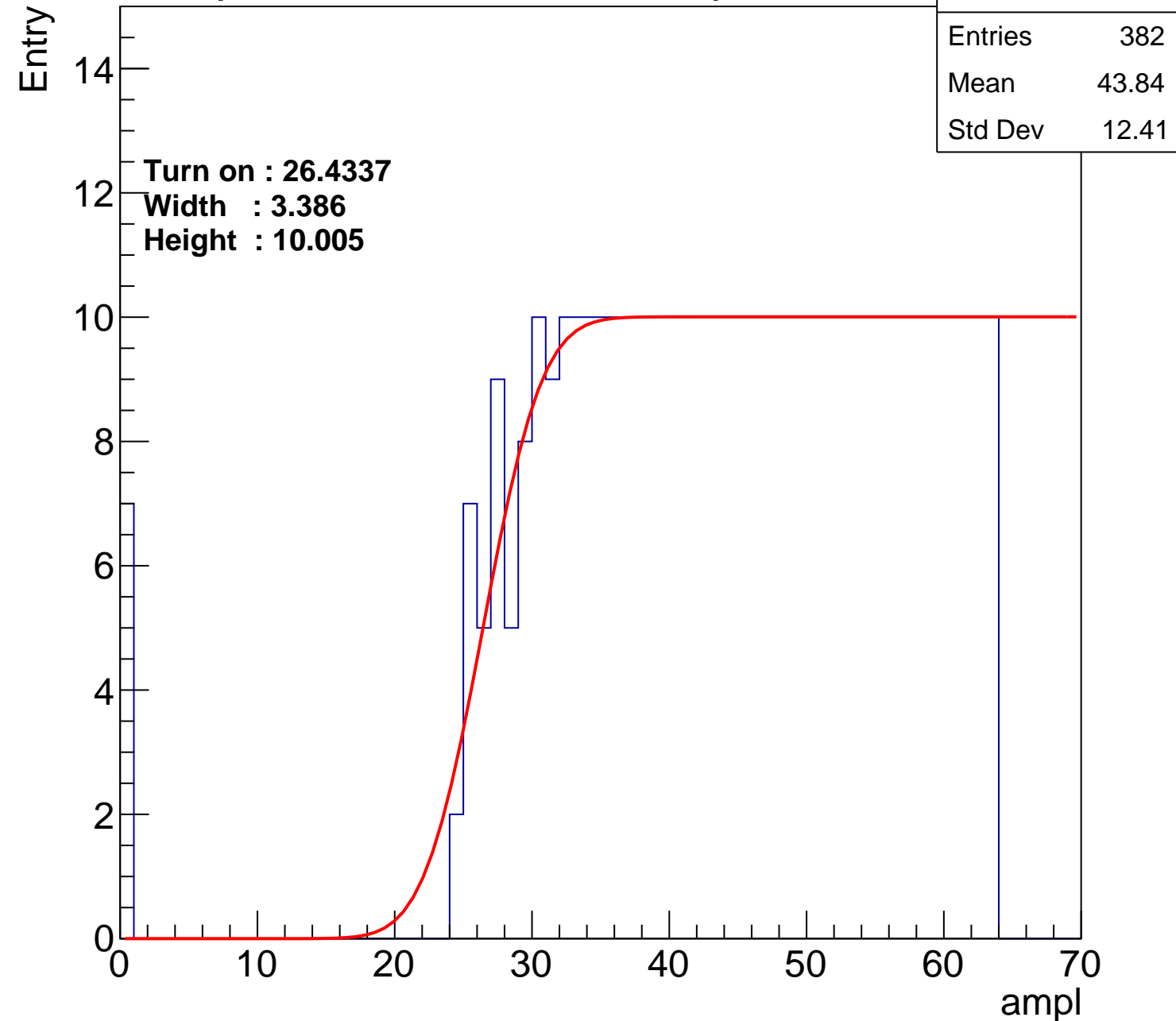
Width : 3.386

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch24

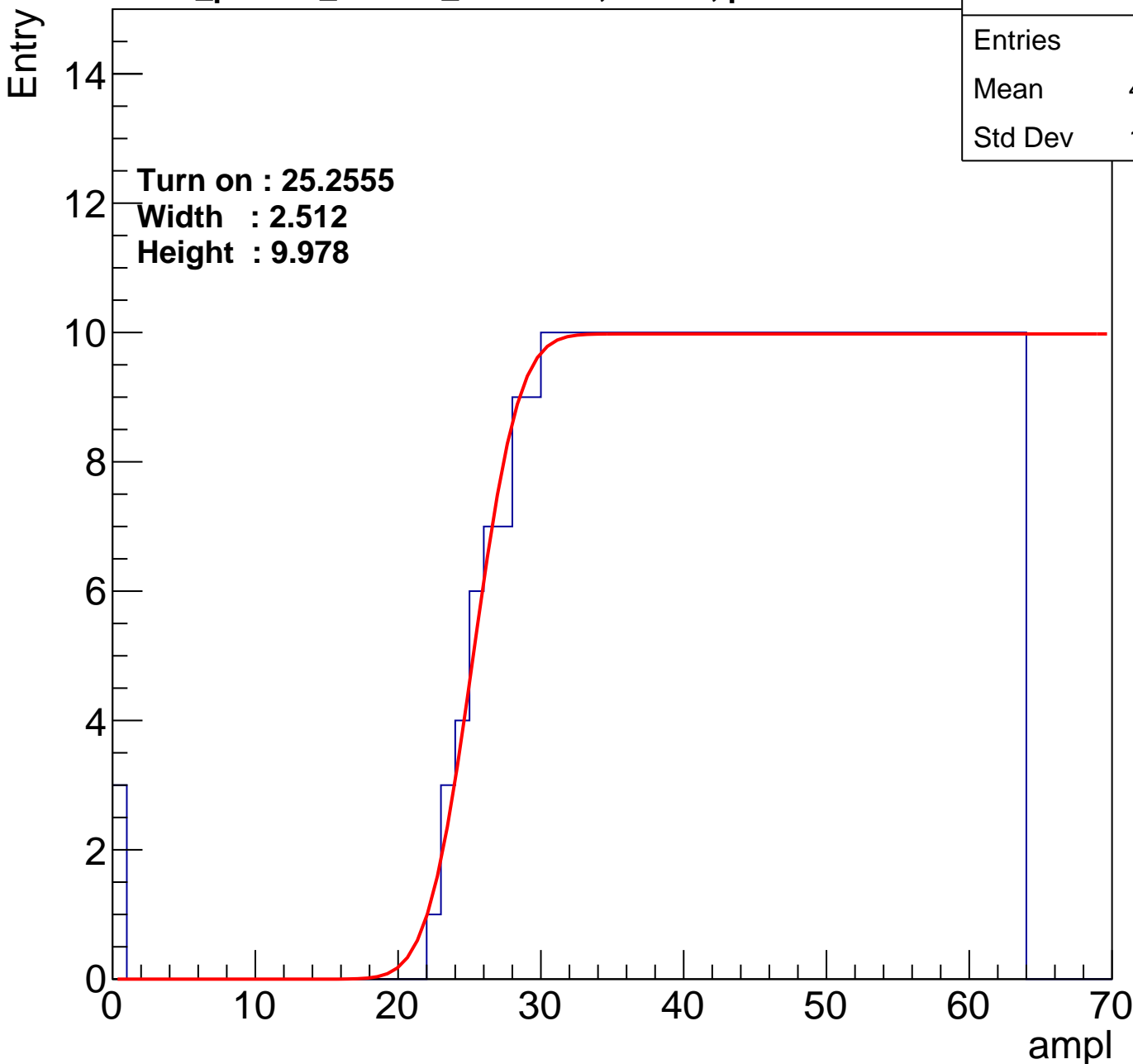
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 25.2555

Width : 2.512

Height : 9.978

Entries	389
Mean	43.78
Std Dev	11.87



B0L102S, U3-ch25

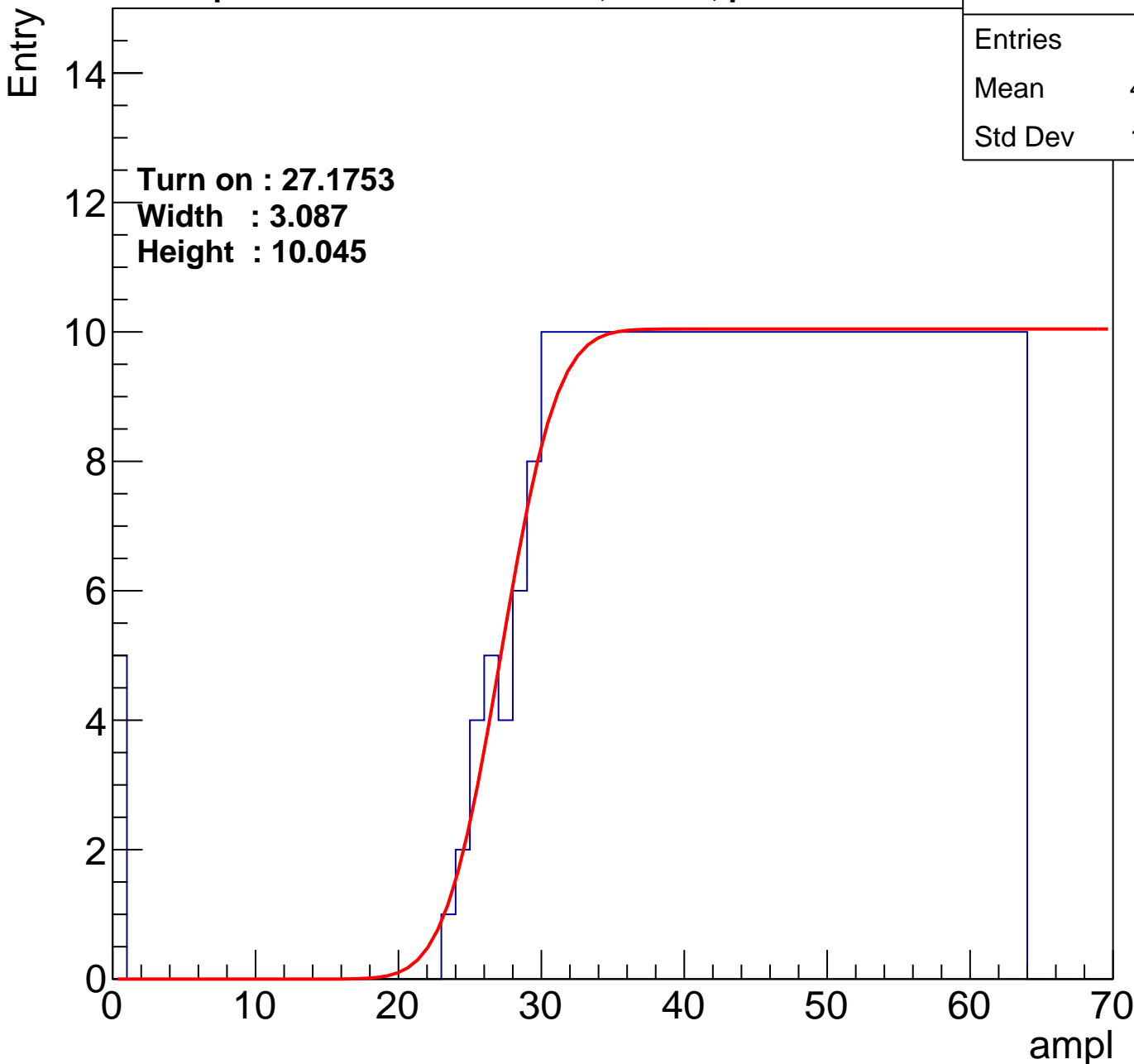
calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.32
Std Dev	11.92

Turn on : 27.1753

Width : 3.087

Height : 10.045



B0L102S, U3-ch26

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.68
Std Dev	11.98

Turn on : 25.6233

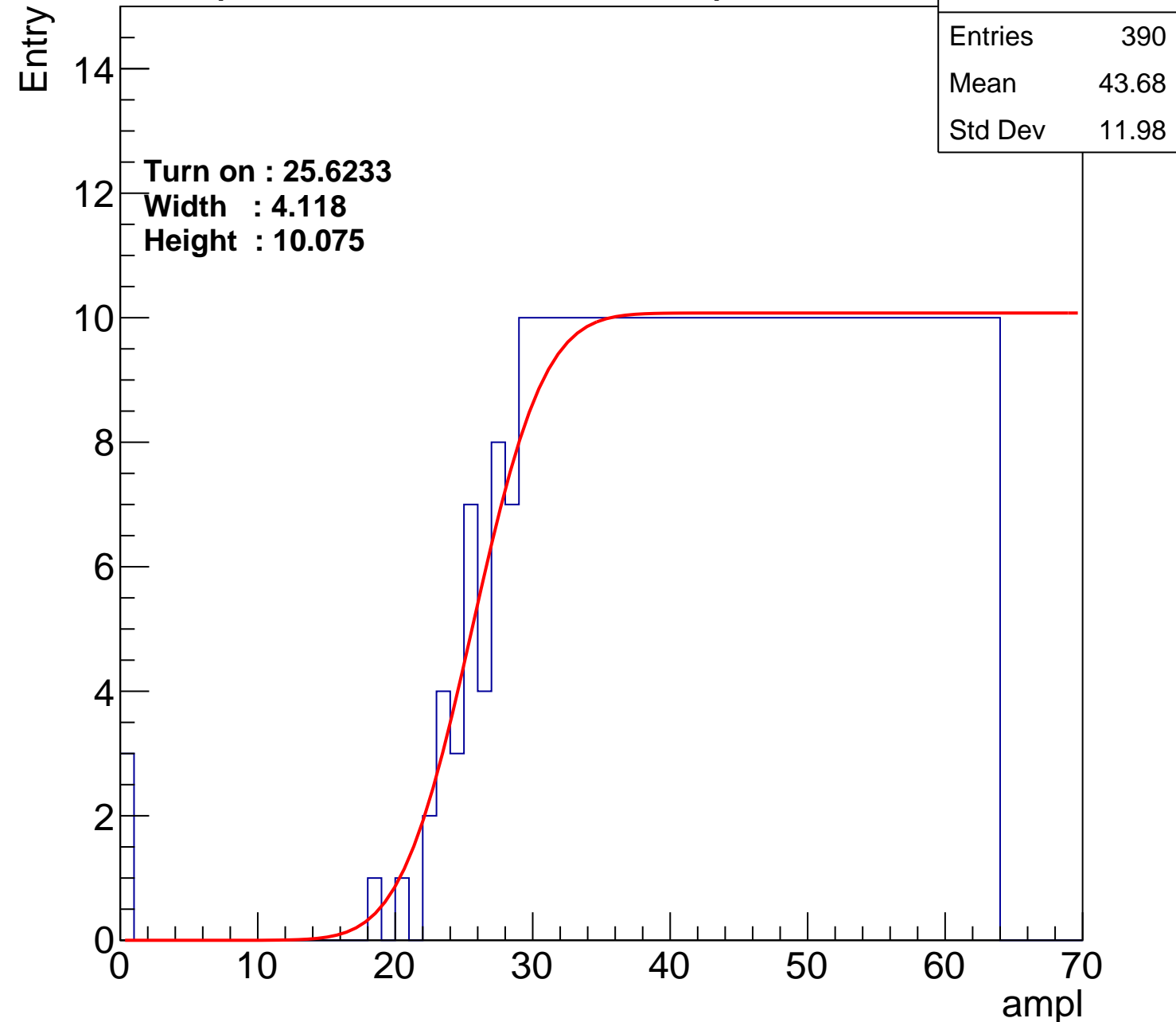
Width : 4.118

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 27.9129

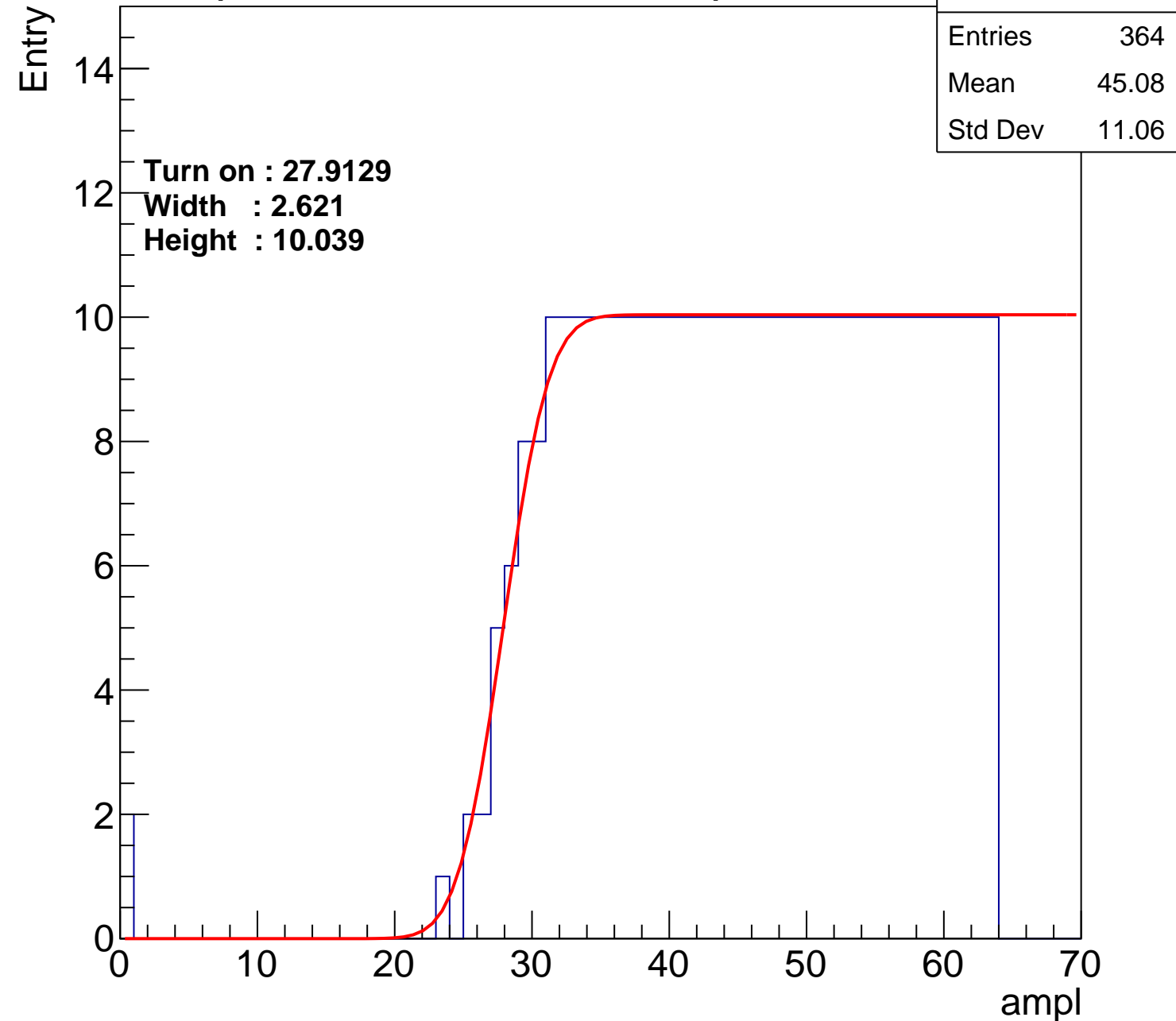
Width : 2.621

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch28

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.61
Std Dev	11.86

Turn on : 24.8454

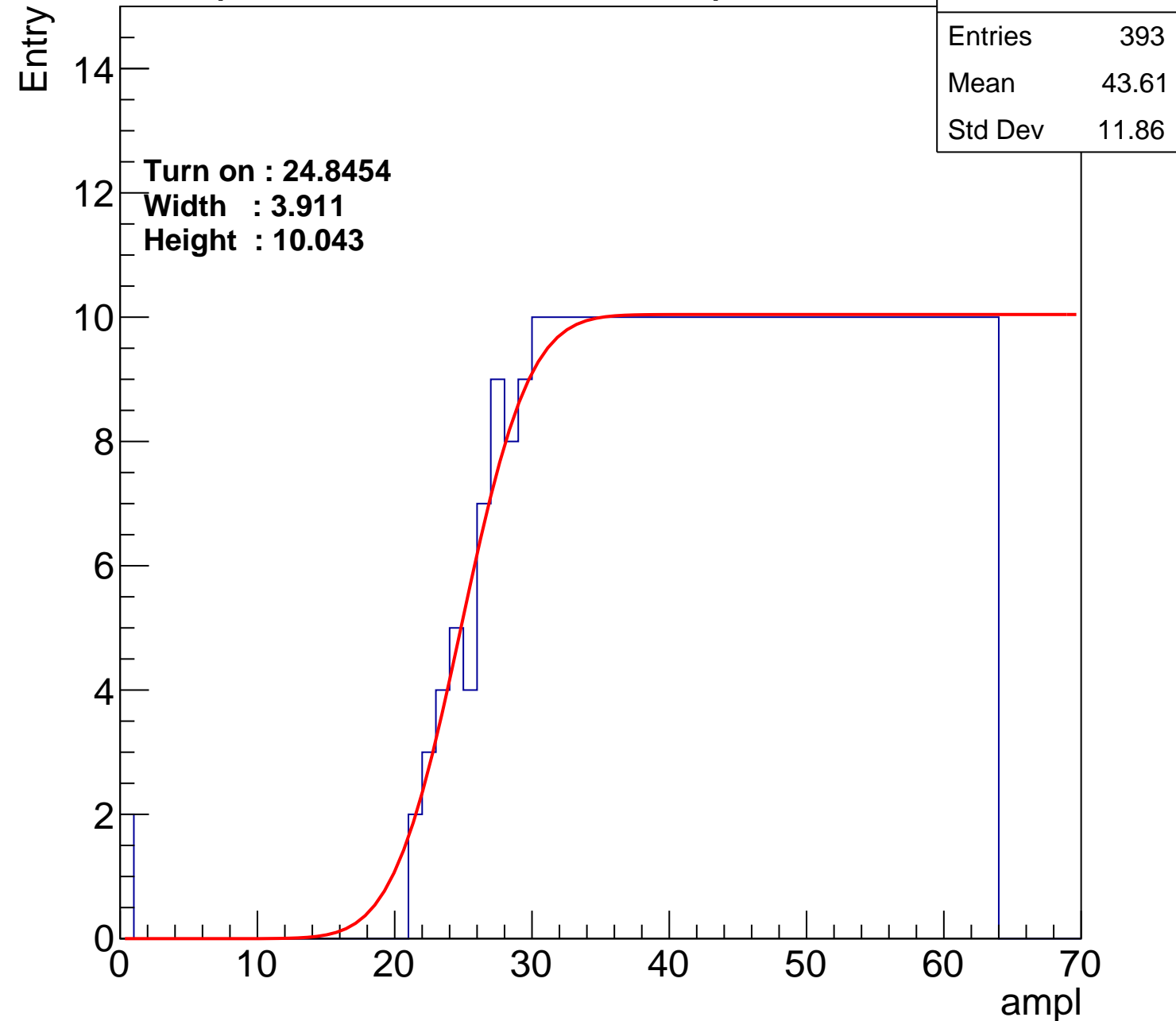
Width : 3.911

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 26.4639

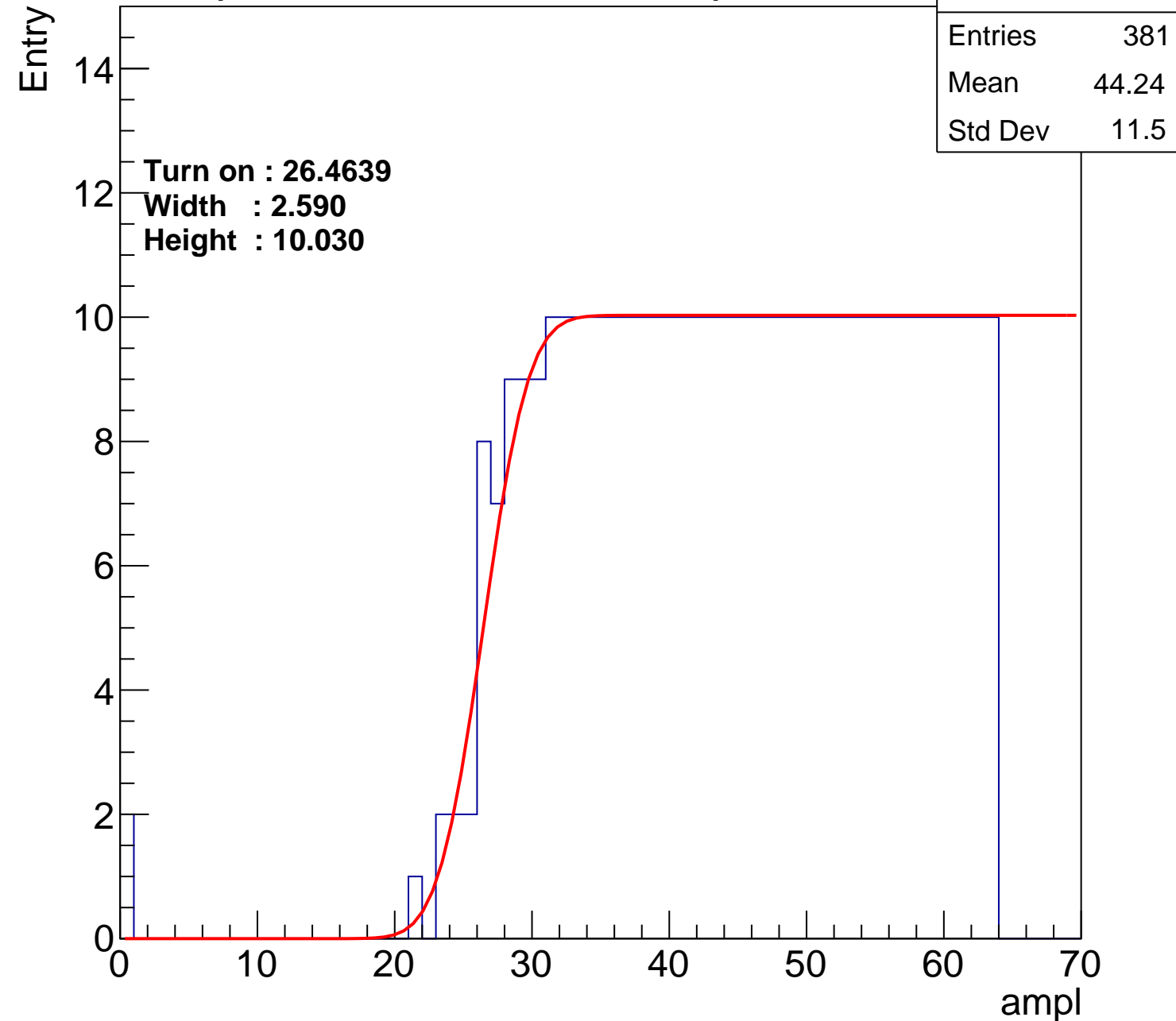
Width : 2.590

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.81
Std Dev	11.13

Turn on : 27.4021

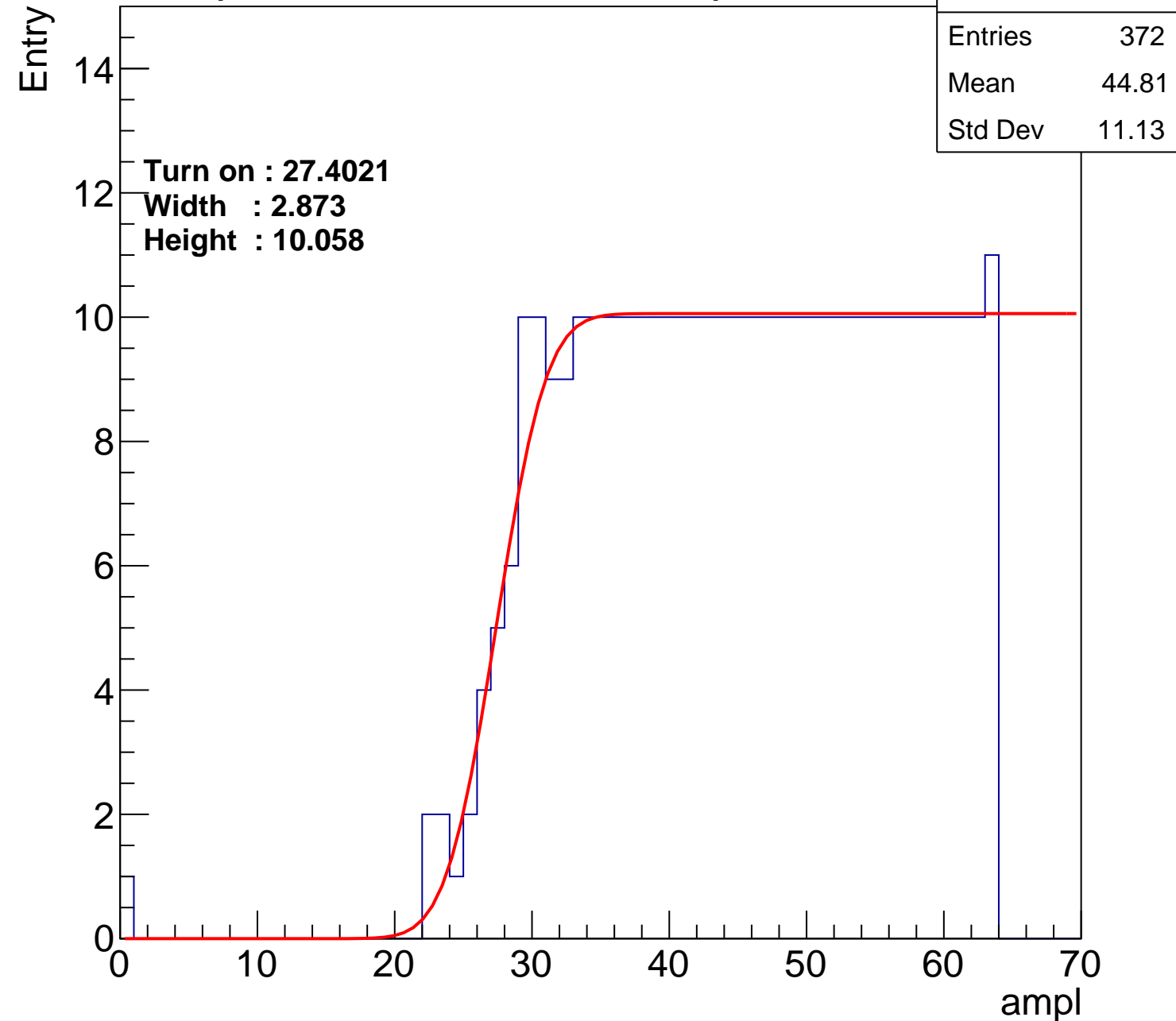
Width : 2.873

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.78
Std Dev	12

Turn on : 25.8099

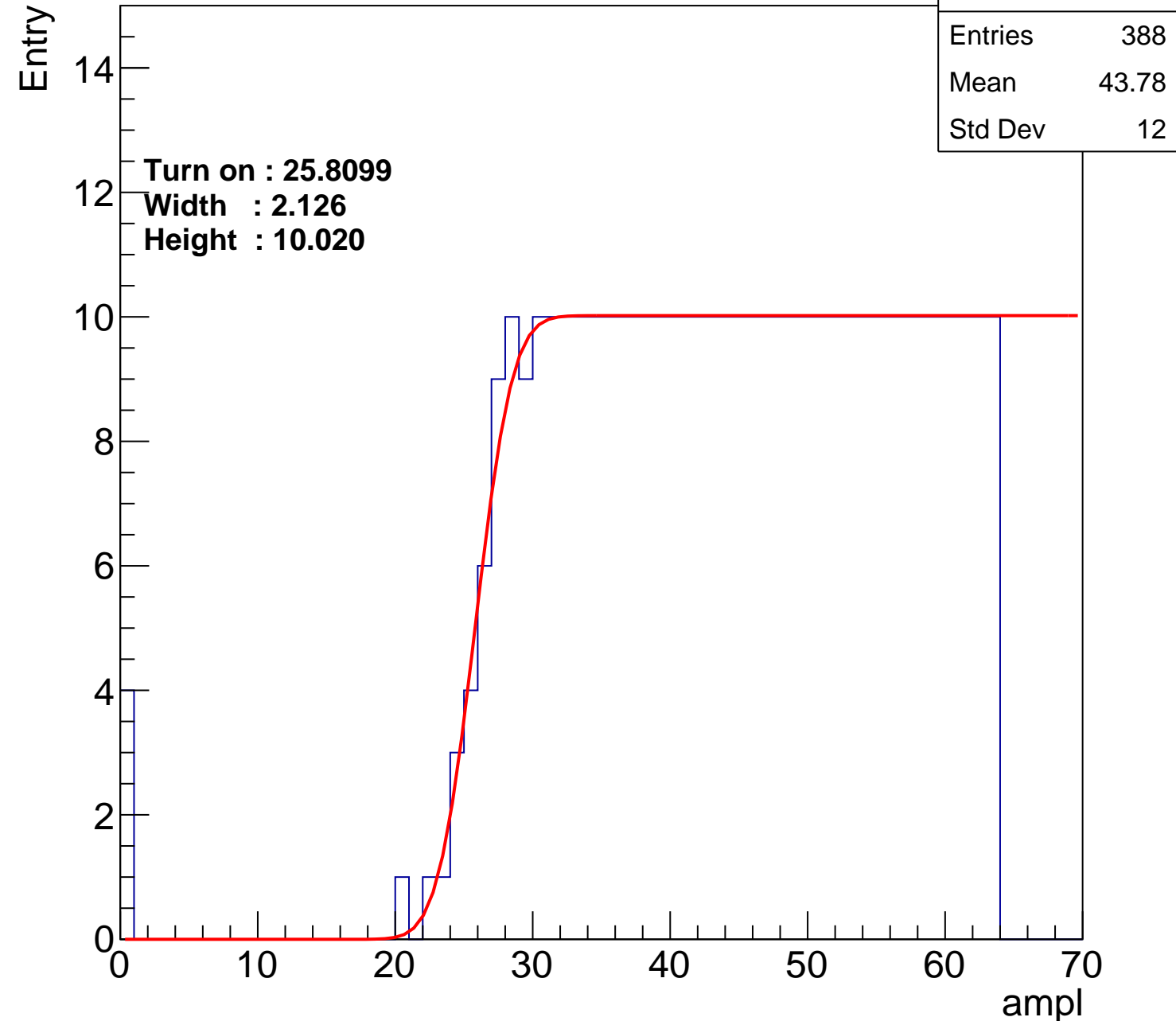
Width : 2.126

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch32

calib_packv5_042523_0143.root, FC#12, port B1

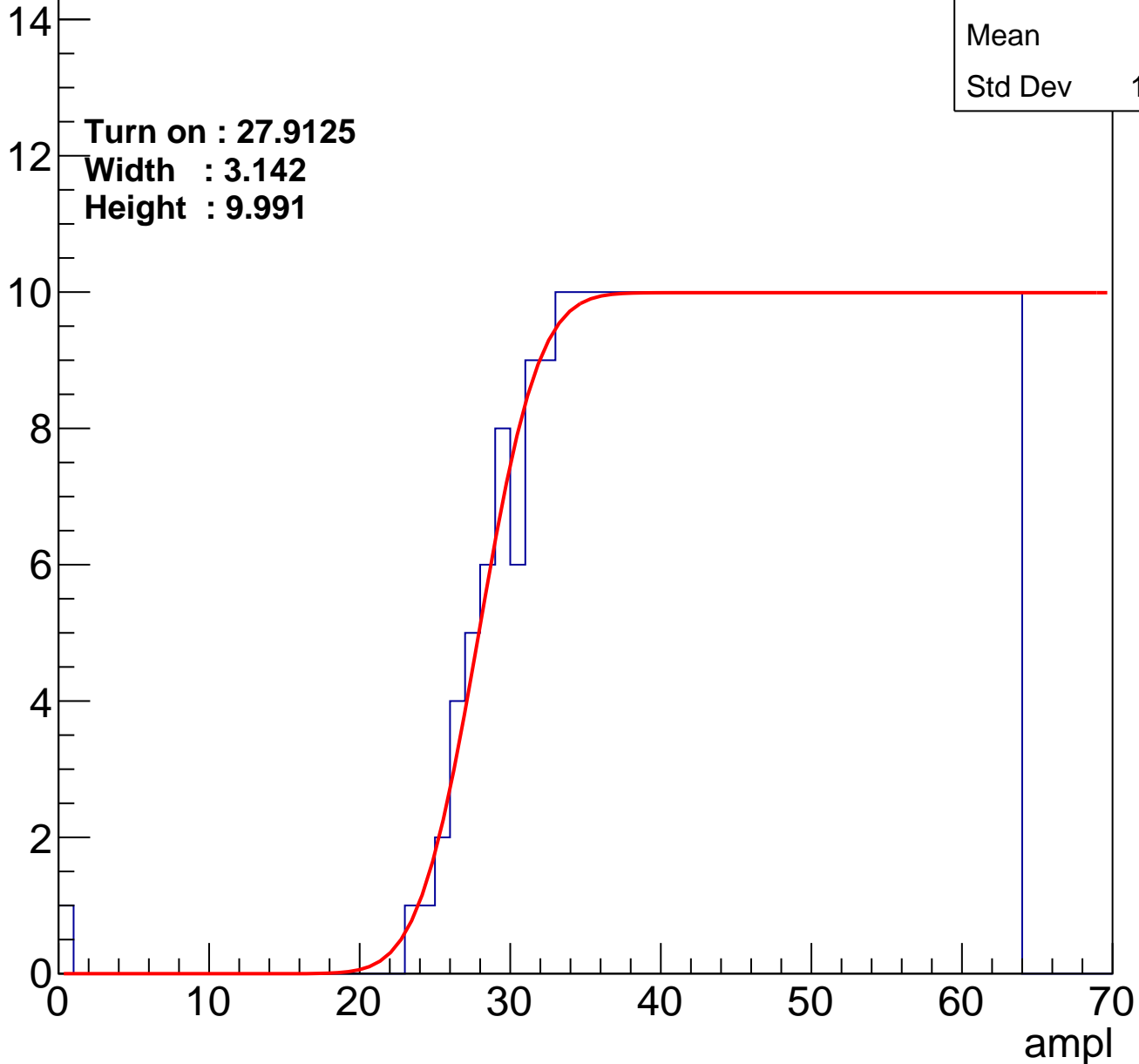
Entries	362
Mean	45.2
Std Dev	10.87

Turn on : 27.9125

Width : 3.142

Height : 9.991

Entry



B0L102S, U3-ch33

calib_packv5_042523_0143.root, FC#12, port B1

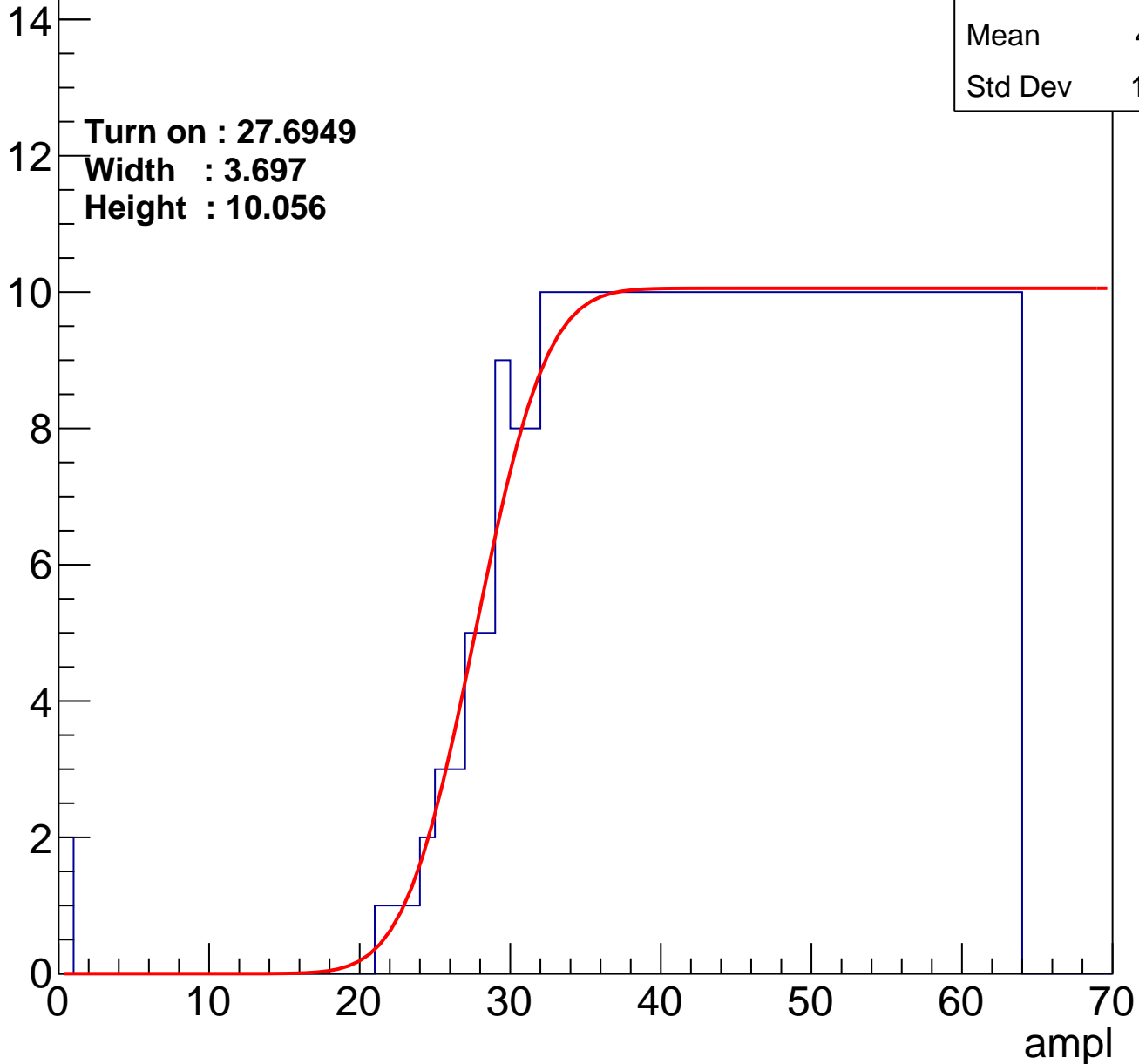
Entries	368
Mean	44.81
Std Dev	11.28

Turn on : 27.6949

Width : 3.697

Height : 10.056

Entry



B0L102S, U3-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.59
Std Dev	12.01

Turn on : 26.4136

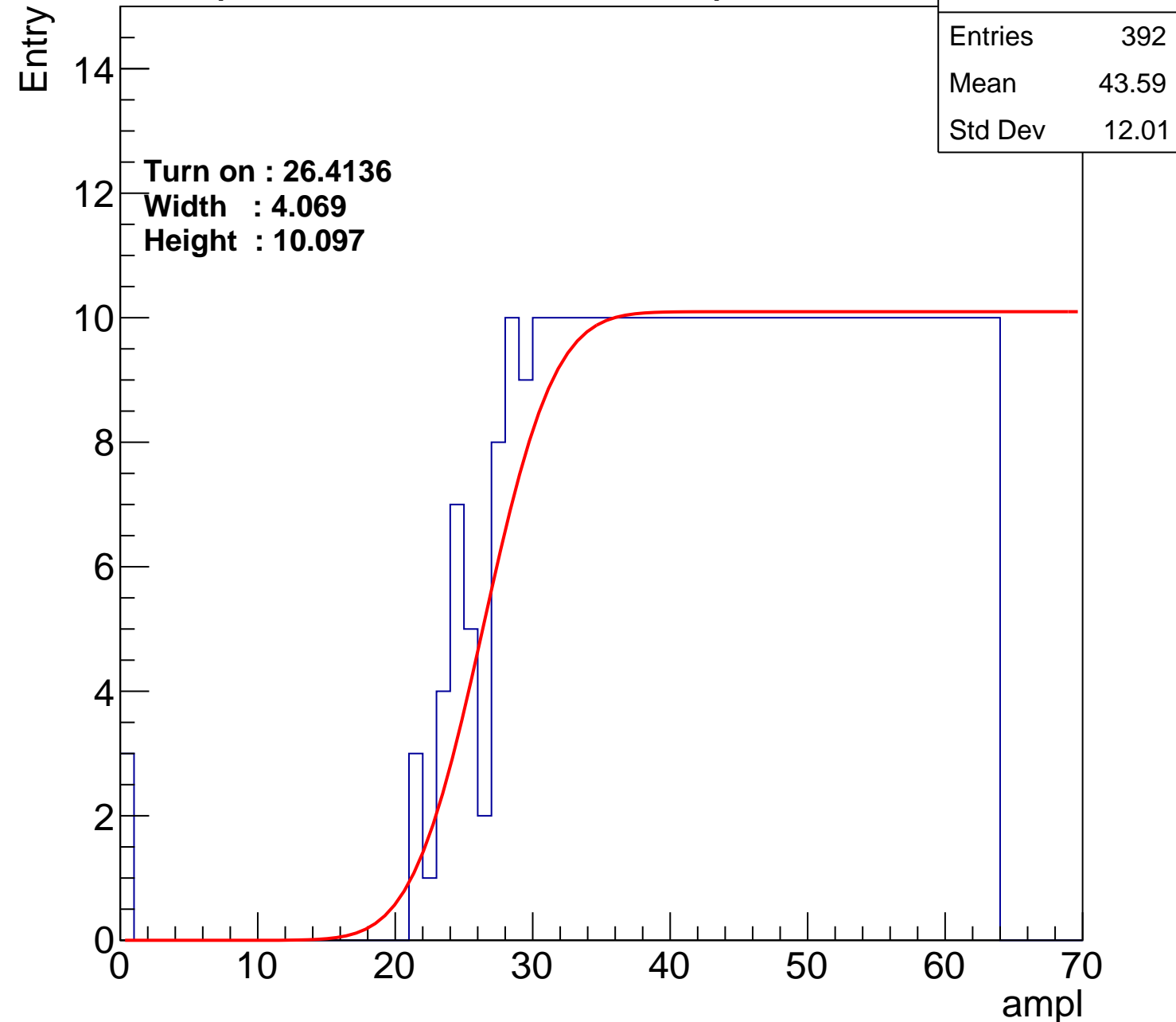
Width : 4.069

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.68
Std Dev	12.04

Turn on : 25.5908

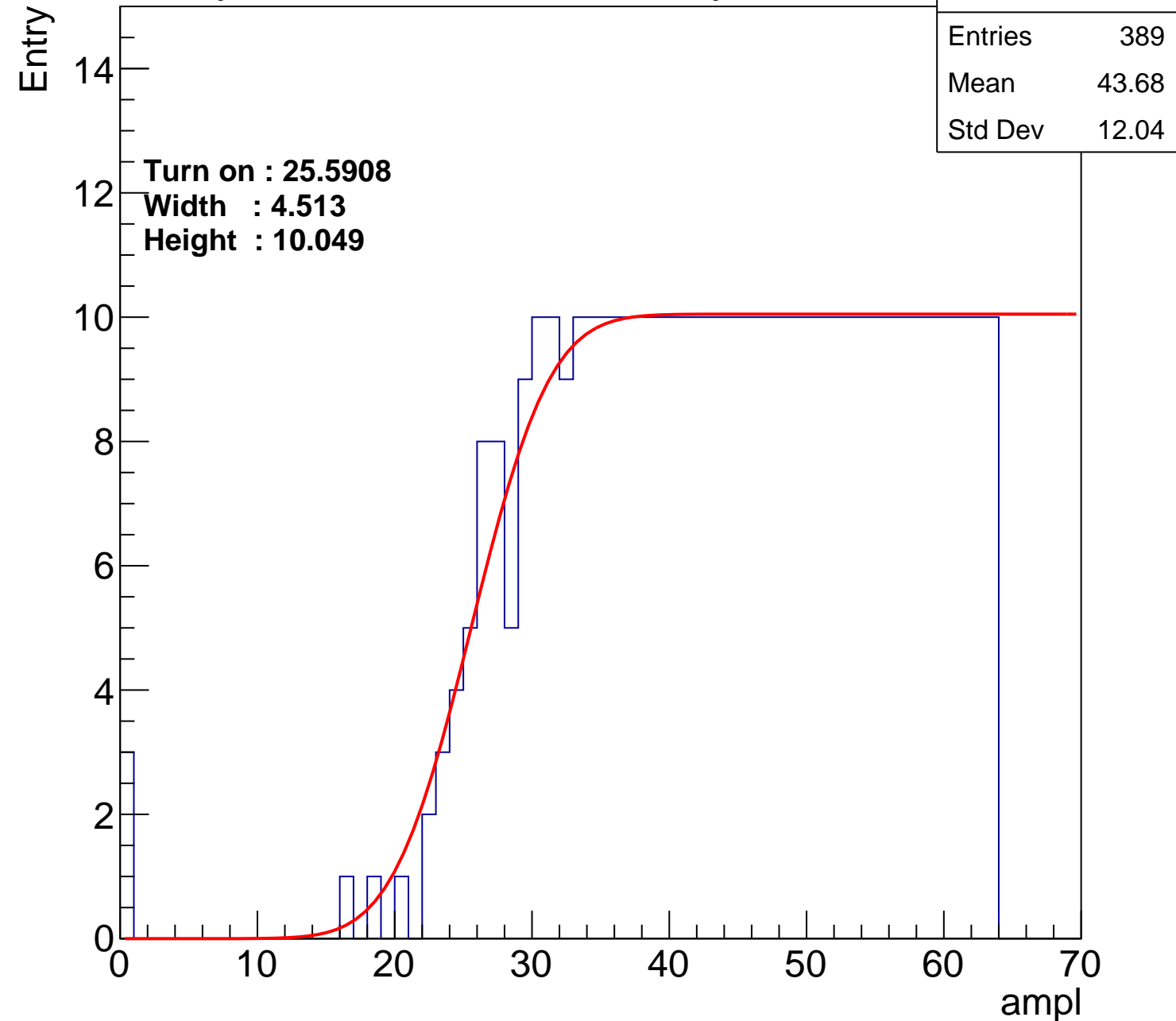
Width : 4.513

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch36

calib_packv5_042523_0143.root, FC#12, port B1

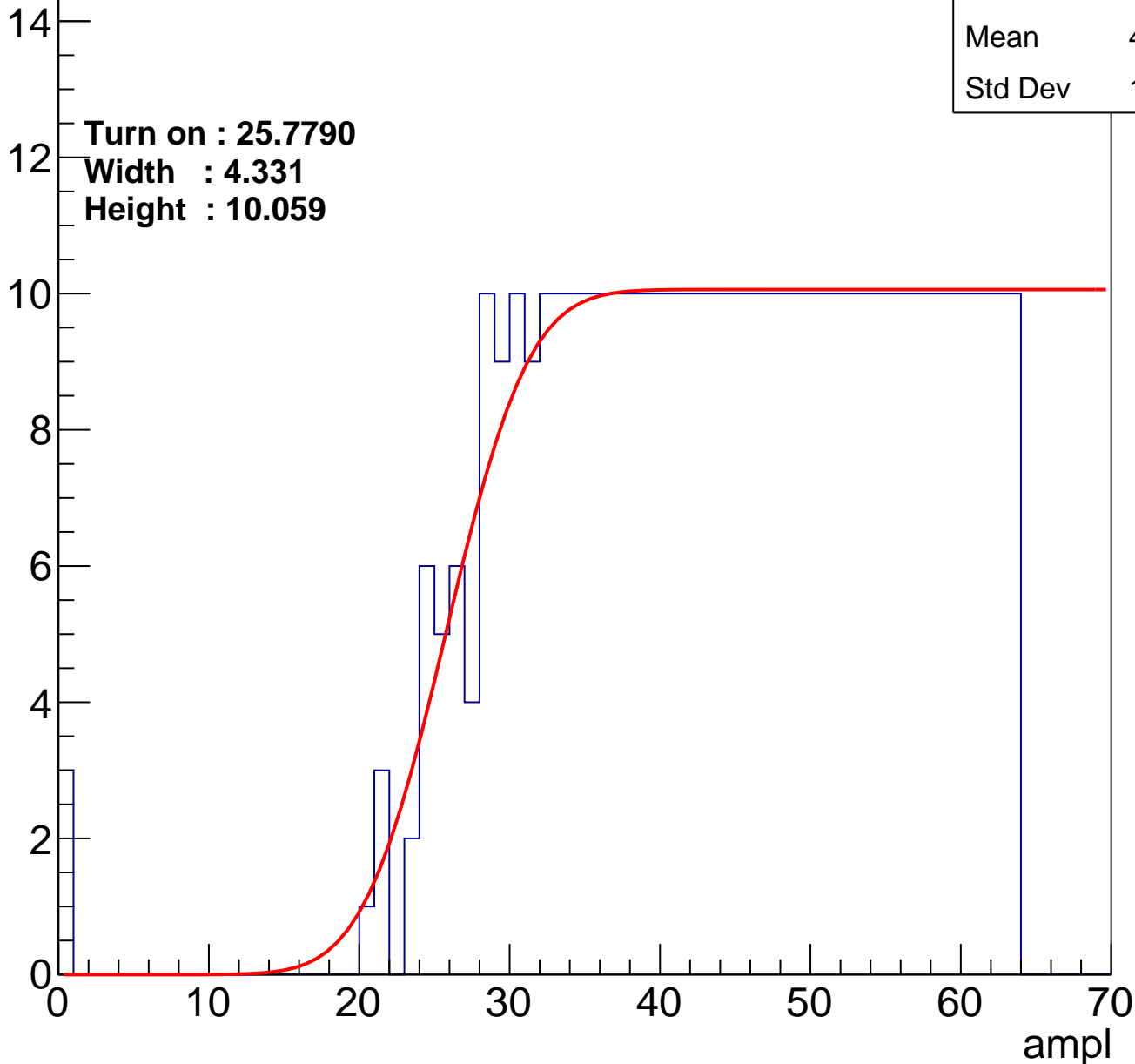
Entries	388
Mean	43.77
Std Dev	11.95

Turn on : 25.7790

Width : 4.331

Height : 10.059

Entry



B0L102S, U3-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.68
Std Dev	12.34

Turn on : 26.2581

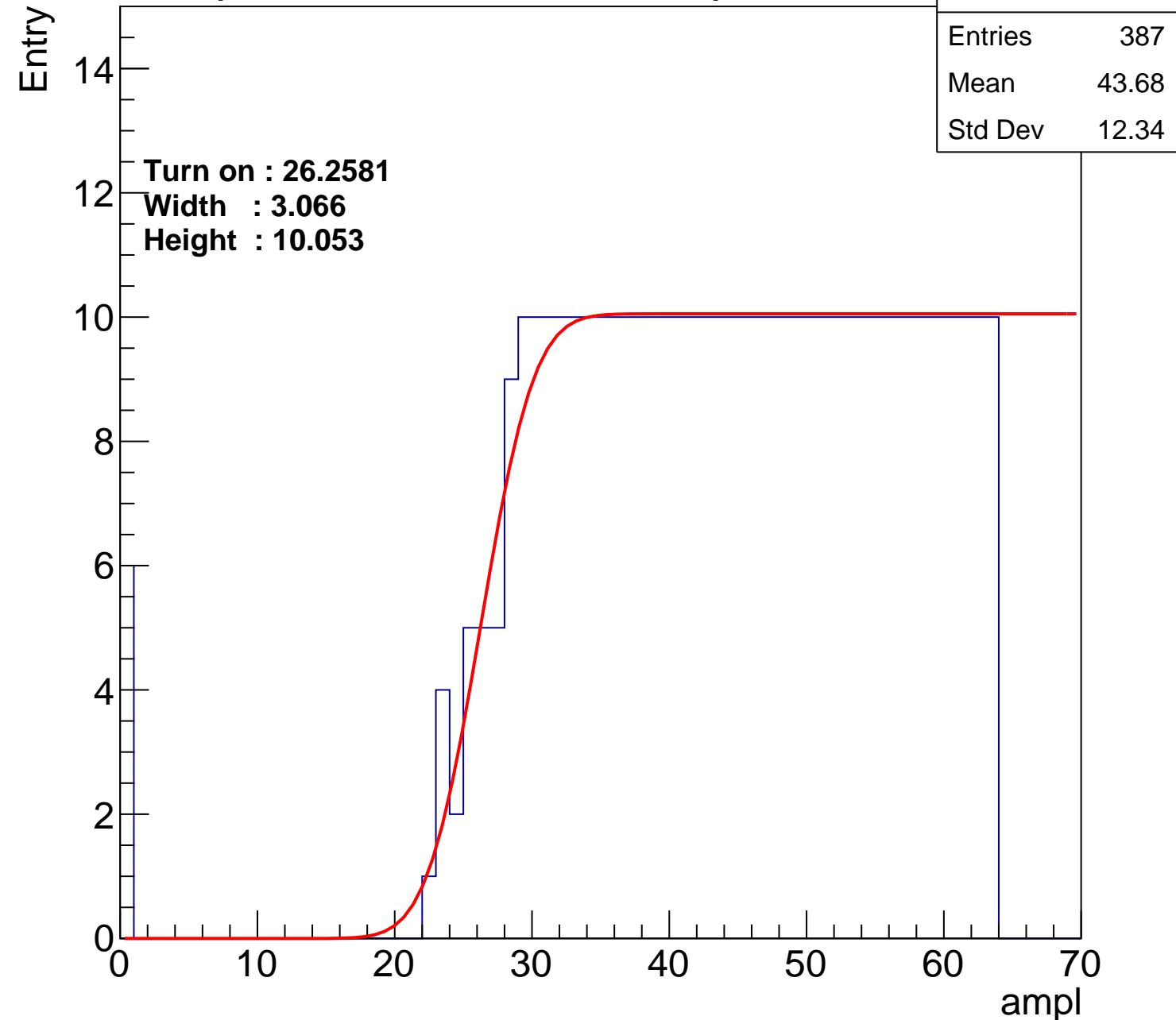
Width : 3.066

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.26
Std Dev	12.29

Turn on : 24.9119

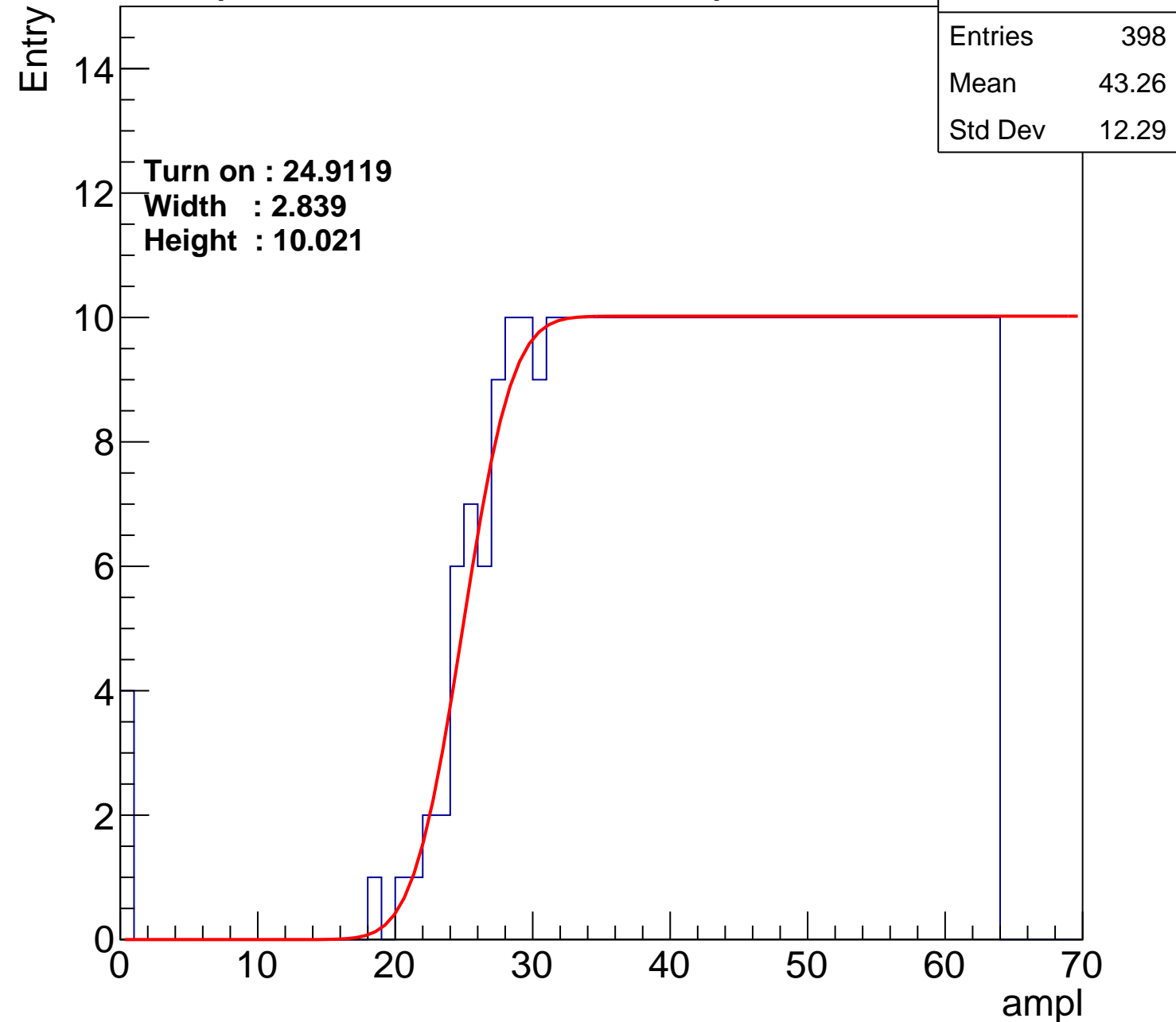
Width : 2.839

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch39

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.98
Std Dev	12.07

Turn on : 26.0464

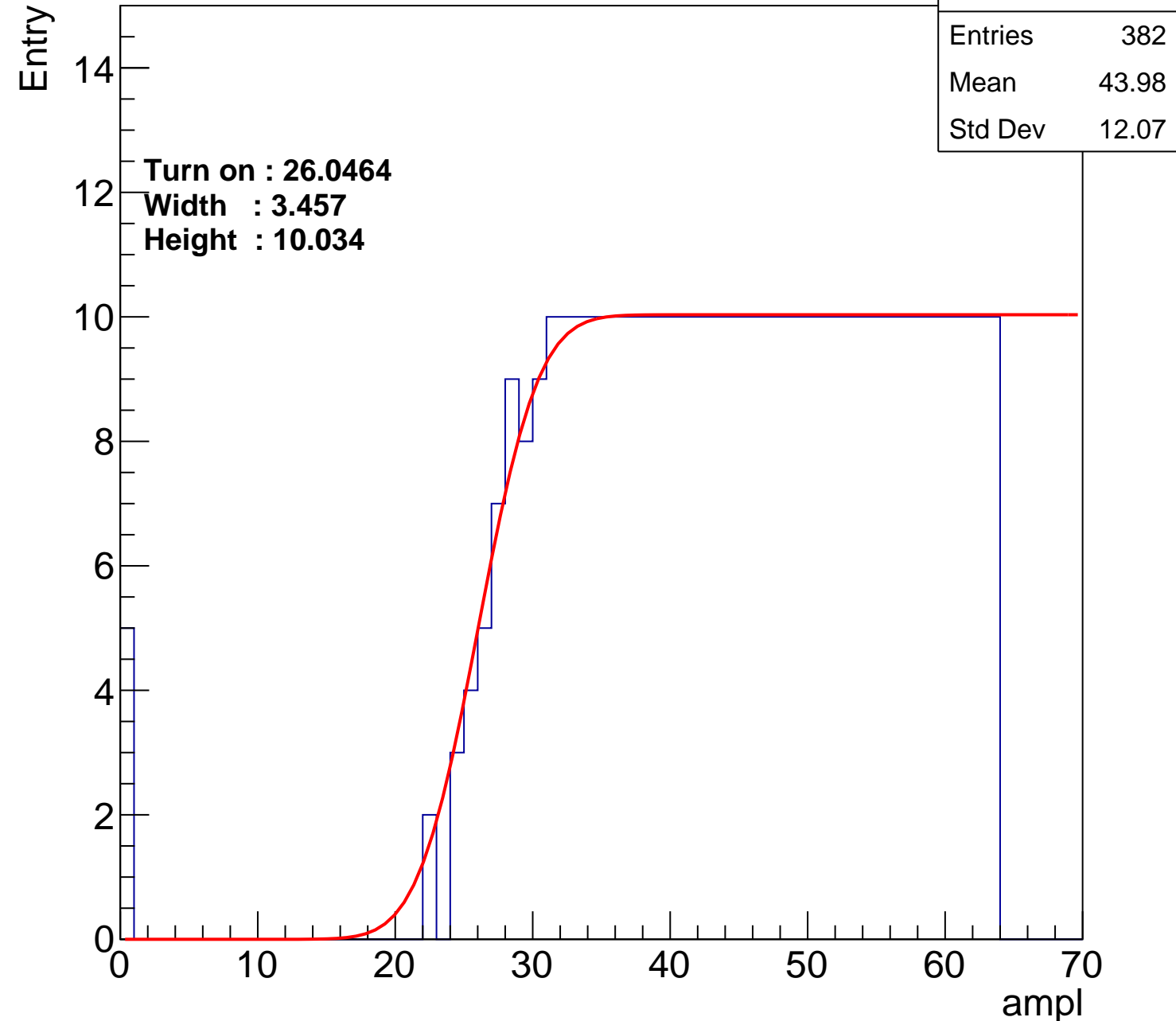
Width : 3.457

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.6
Std Dev	11.88

Turn on : 25.0770

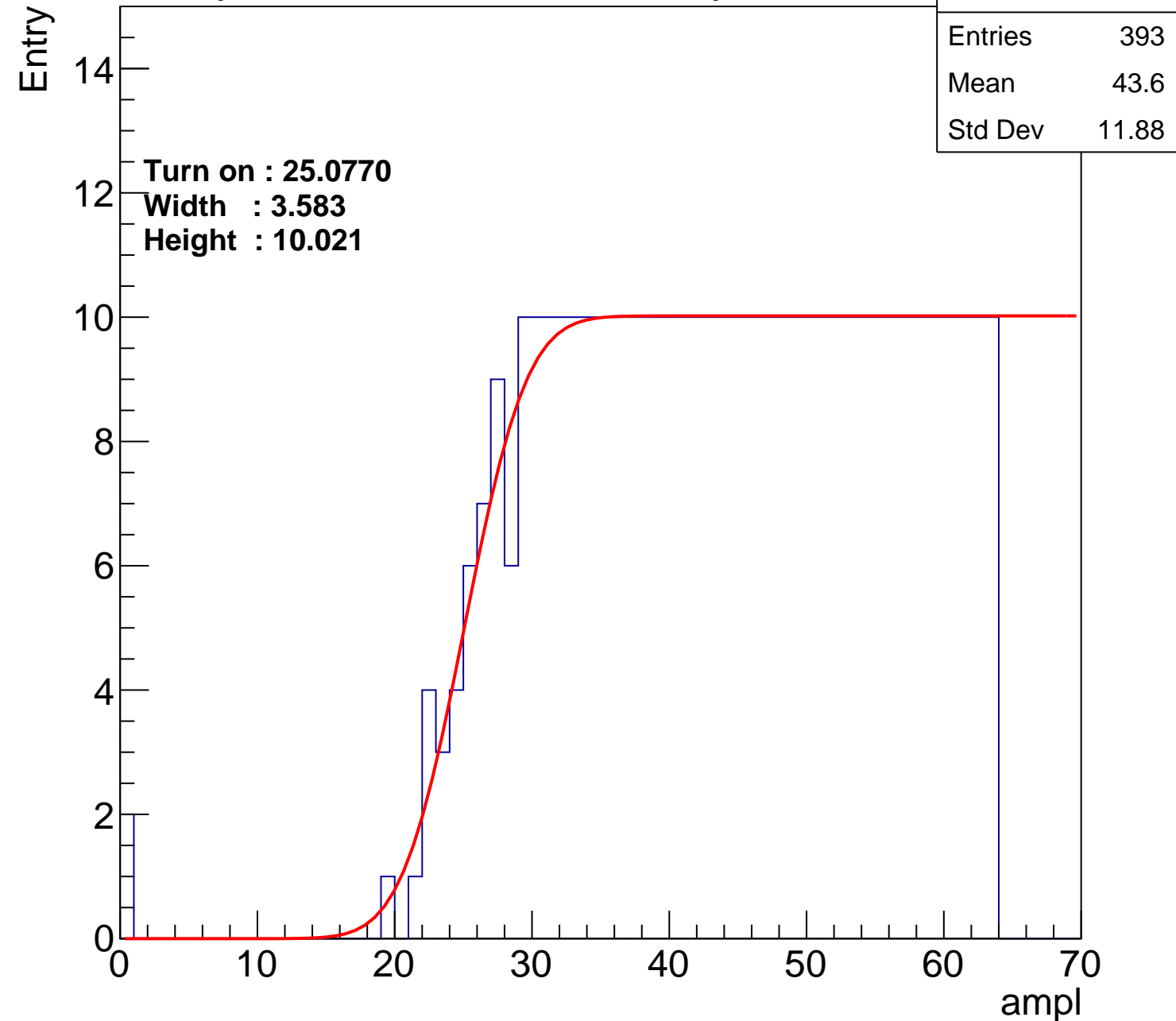
Width : 3.583

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch41

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.04
Std Dev	11.65

Turn on : 25.7957

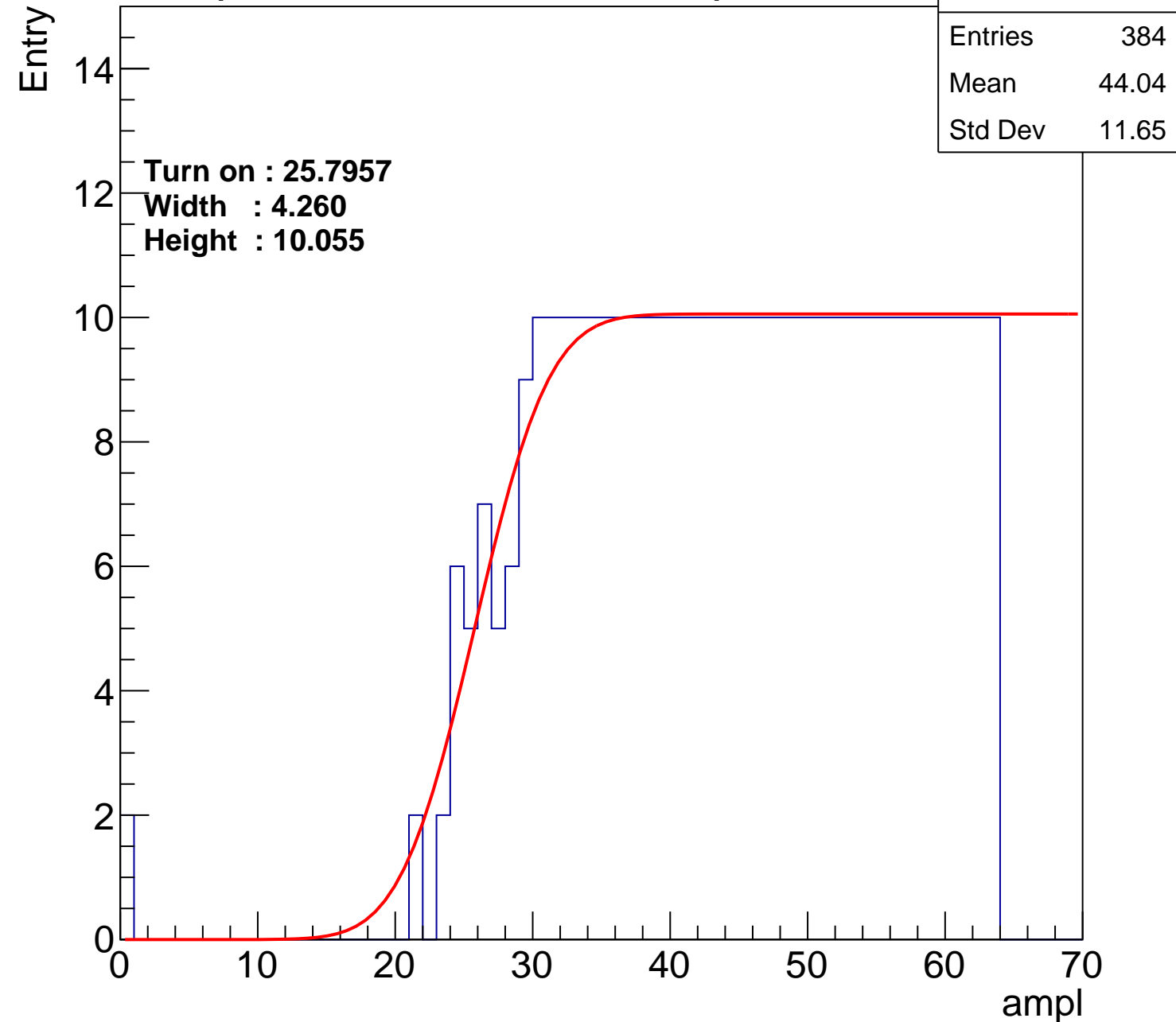
Width : 4.260

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch42

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.31
Std Dev	10.8

Turn on : 28.4184

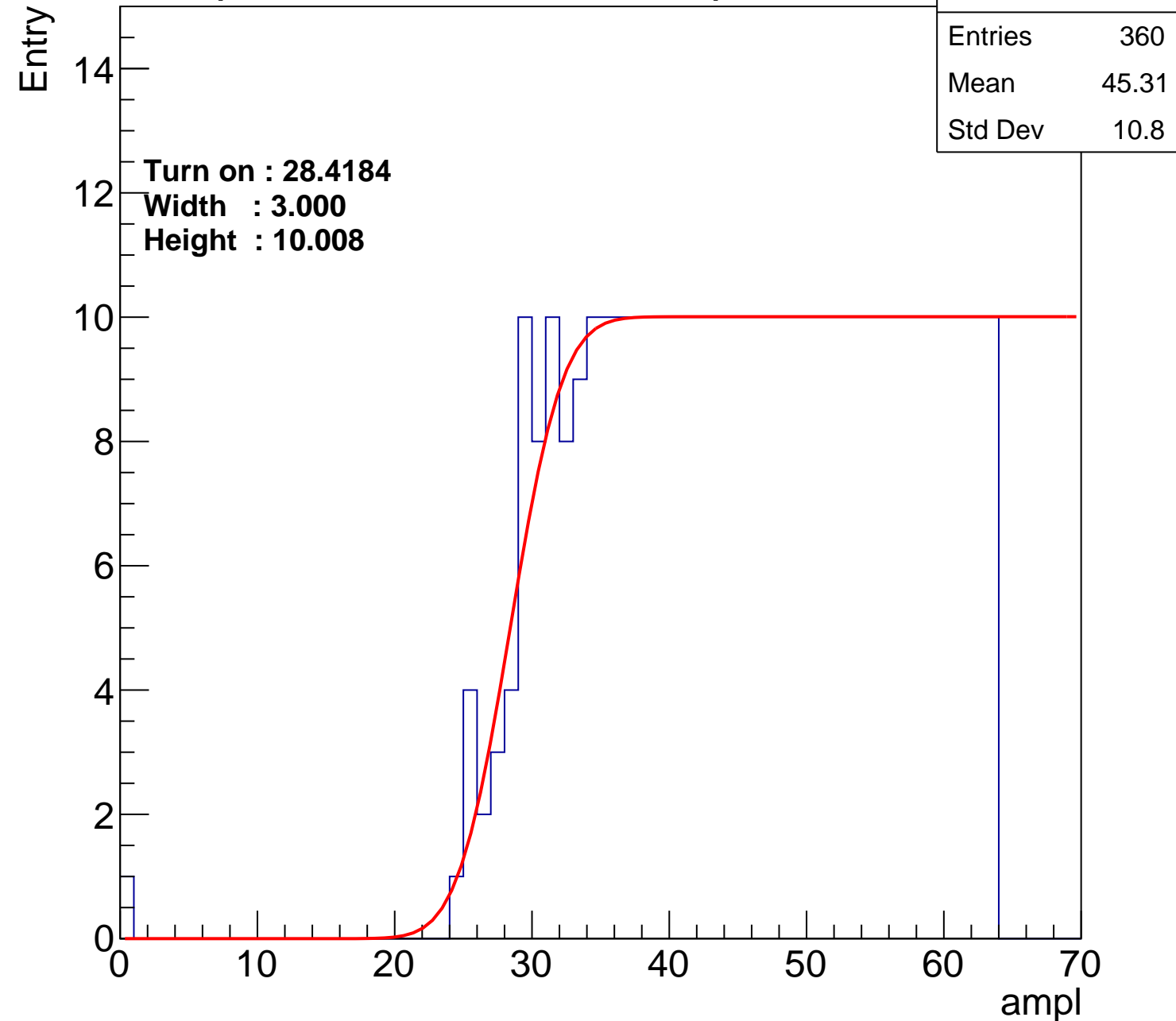
Width : 3.000

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.93
Std Dev	11

Turn on : 27.5820

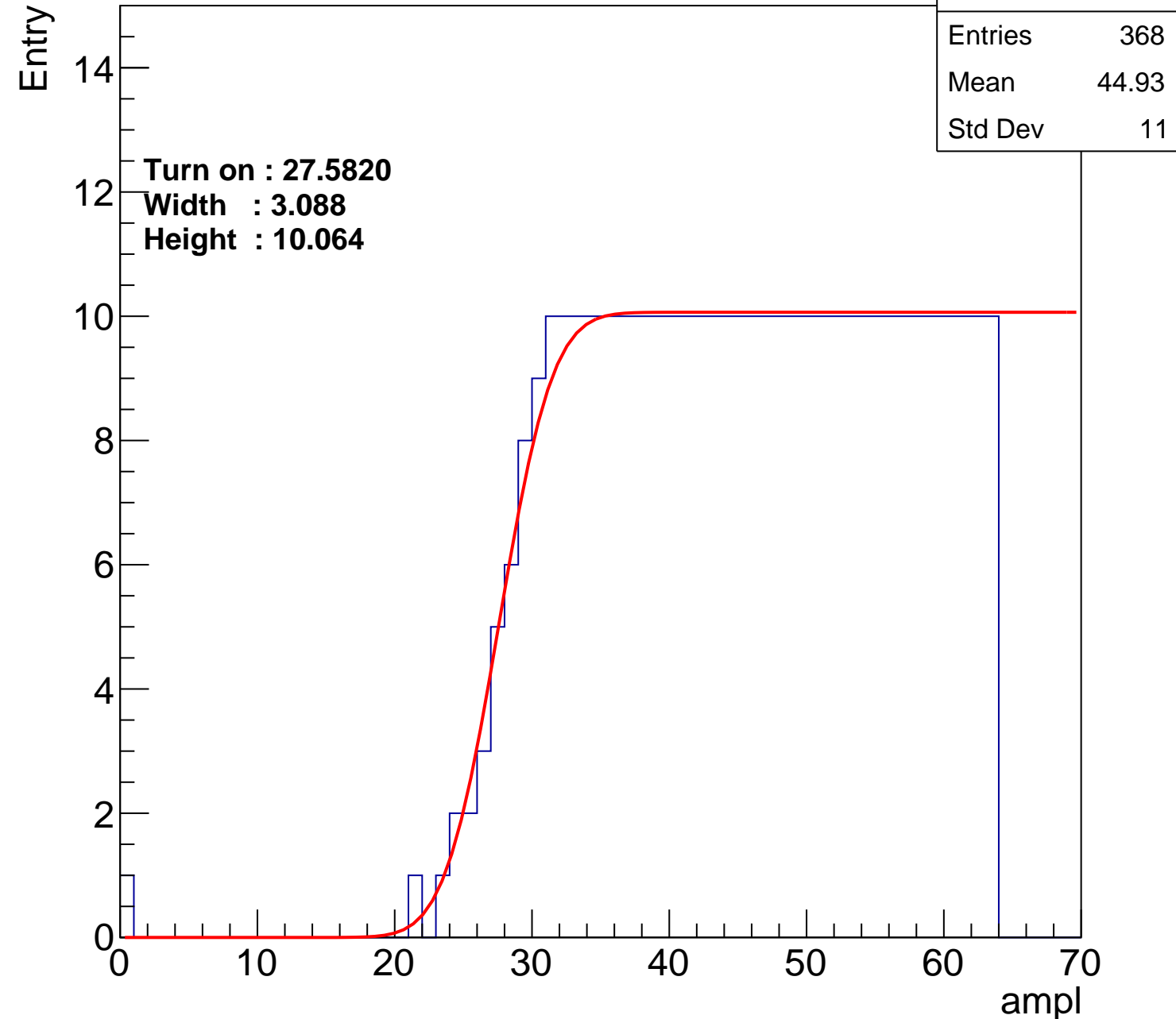
Width : 3.088

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch44

calib_packv5_042523_0143.root, FC#12, port B1

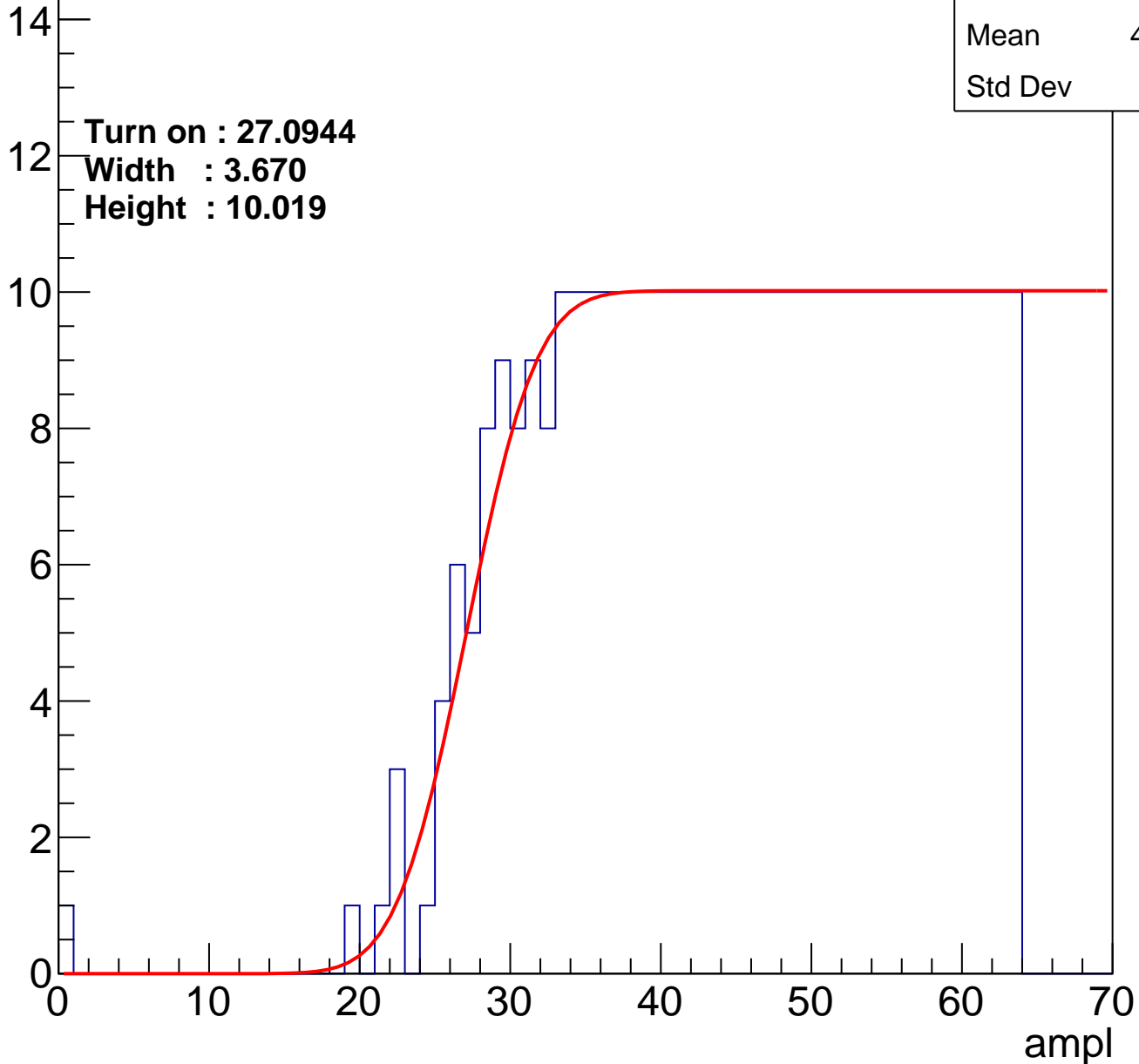
Entries	374
Mean	44.55
Std Dev	11.3

Turn on : 27.0944

Width : 3.670

Height : 10.019

Entry



B0L102S, U3-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.25
Std Dev	12.35

Turn on : 25.0518

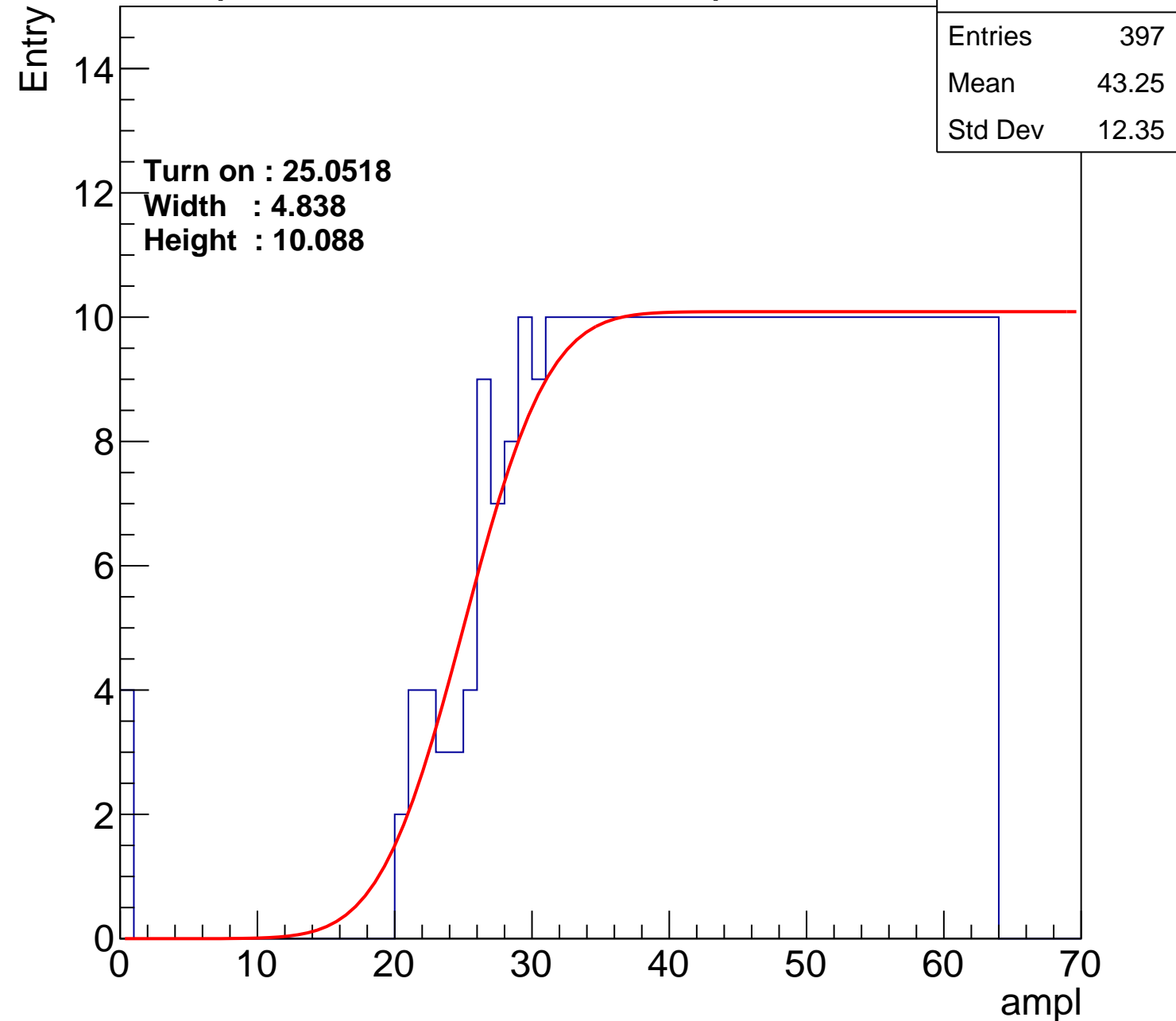
Width : 4.838

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch46

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.09
Std Dev	12.29

Turn on : 24.4374

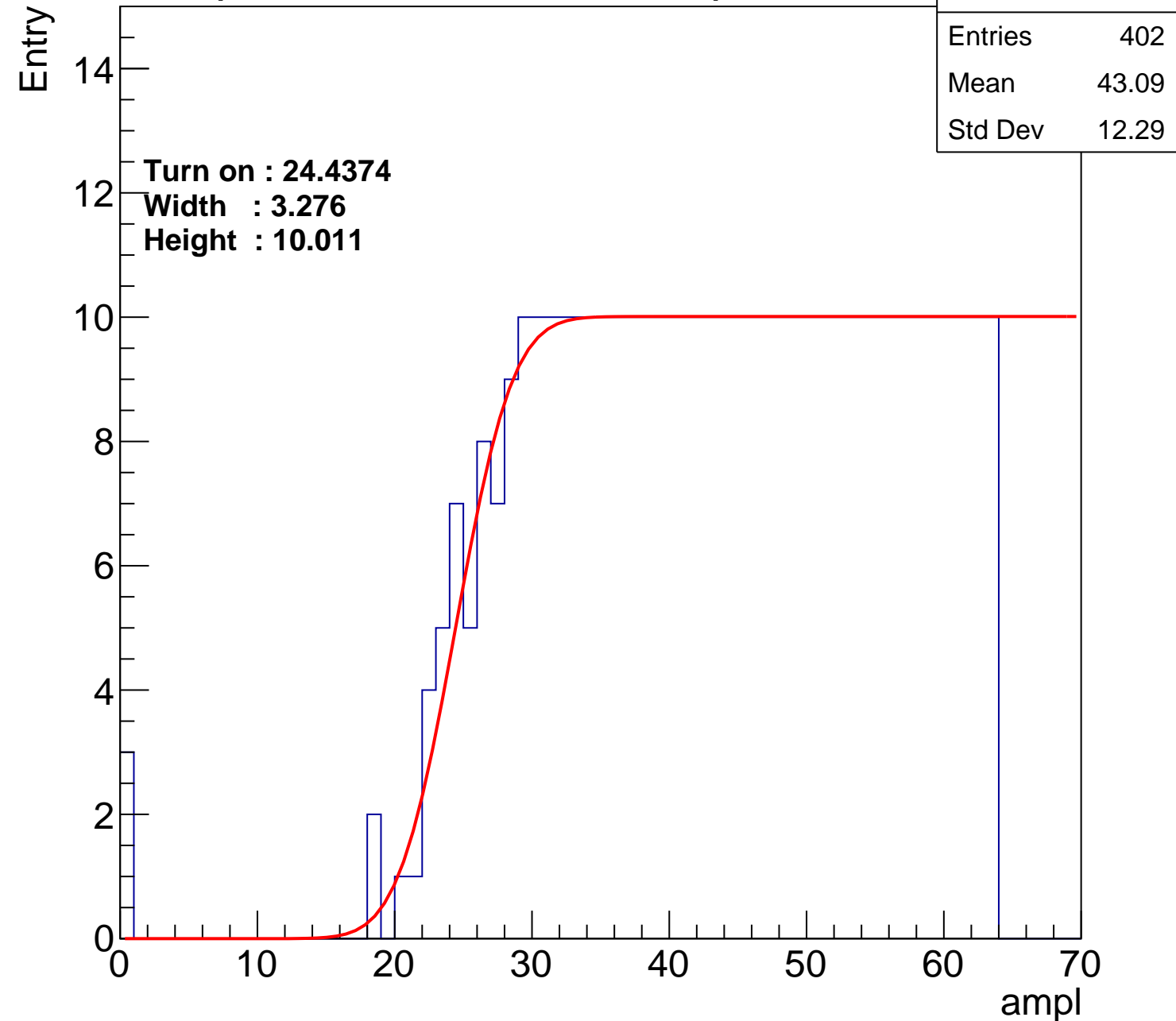
Width : 3.276

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch47

calib_packv5_042523_0143.root, FC#12, port B1

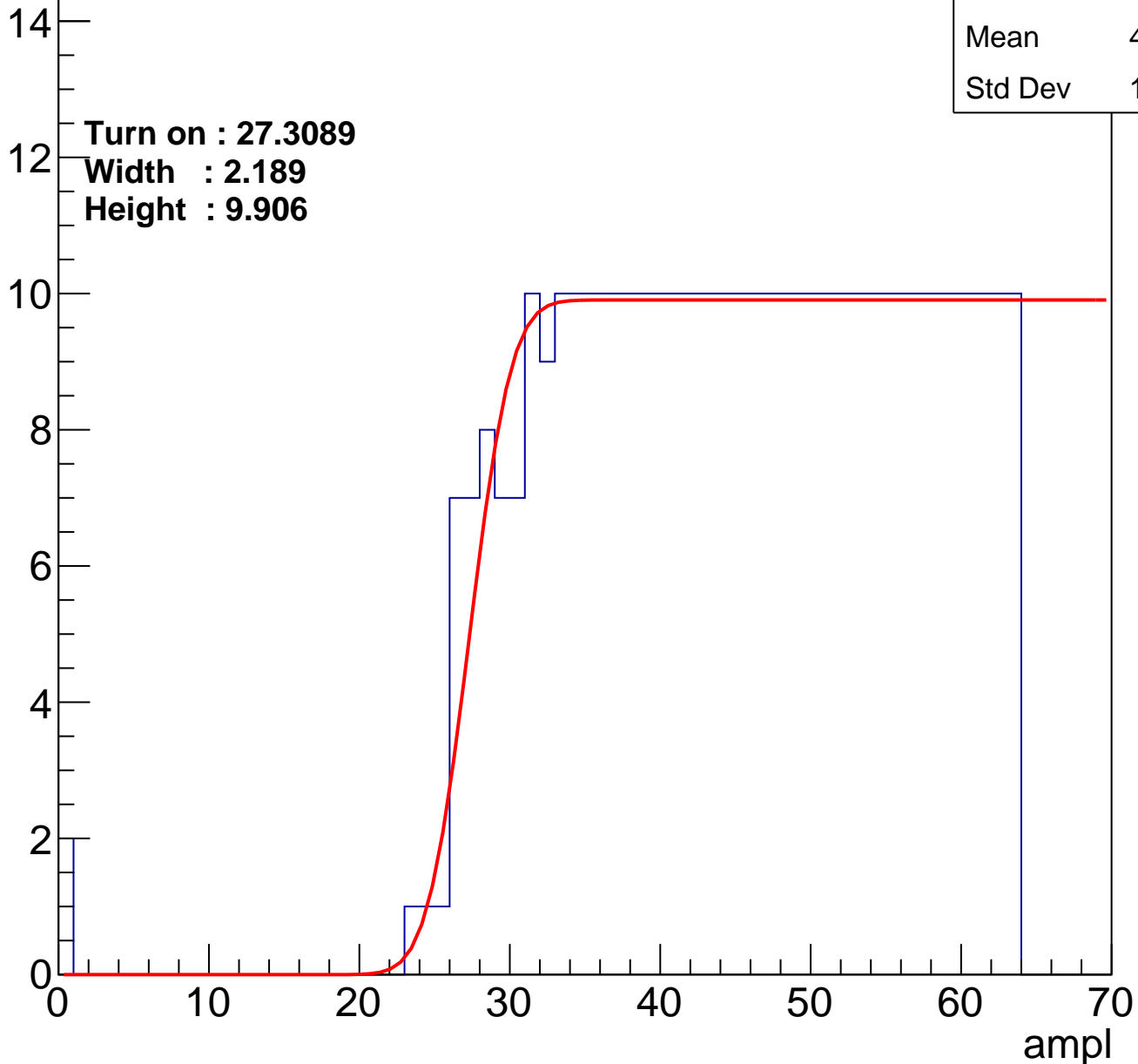
Entry

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.3089

Width : 2.189

Height : 9.906



B0L102S, U3-ch48

calib_packv5_042523_0143.root, FC#12, port B1

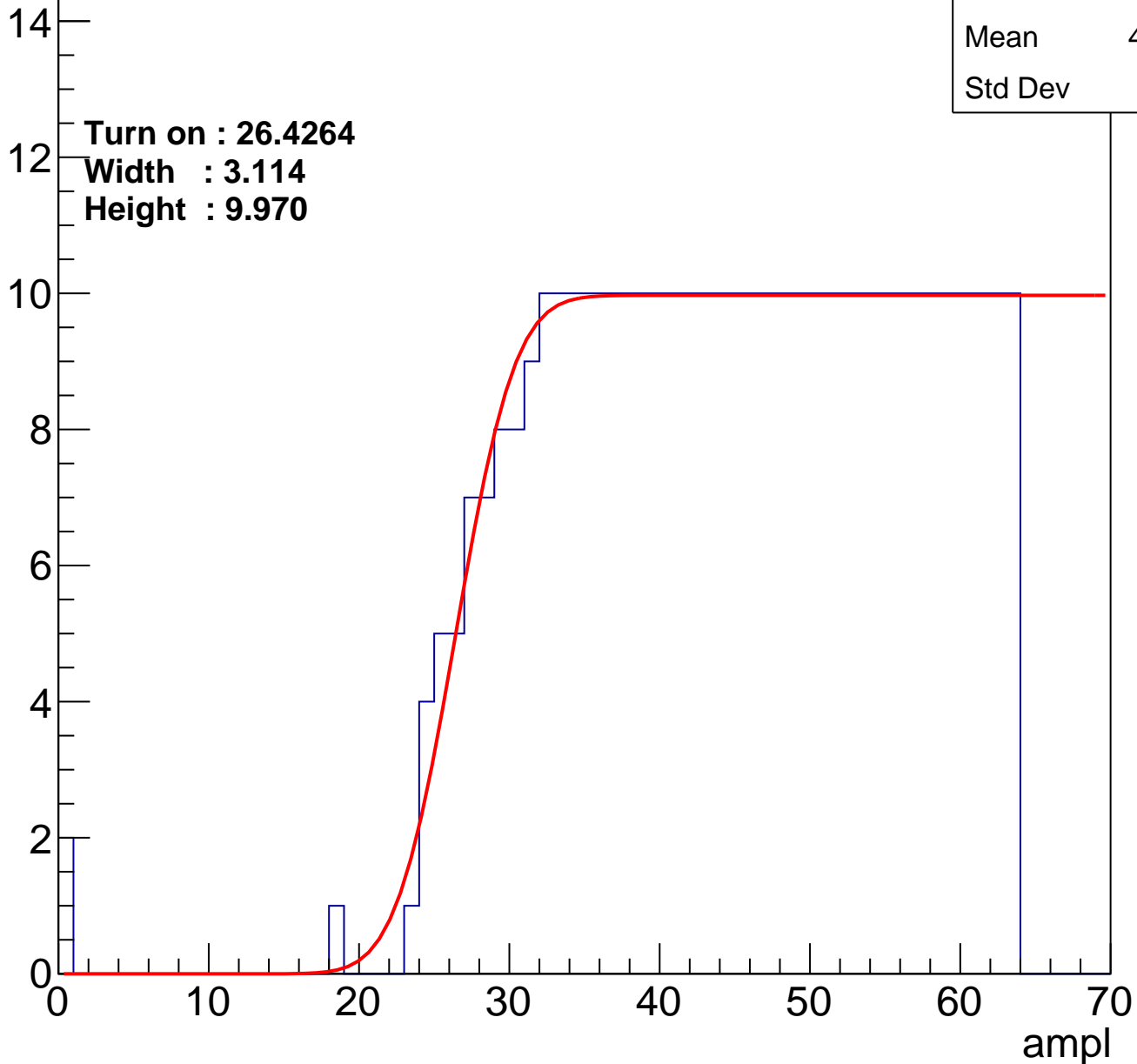
Entries	377
Mean	44.37
Std Dev	11.5

Turn on : 26.4264

Width : 3.114

Height : 9.970

Entry



B0L102S, U3-ch49

calib_packv5_042523_0143.root, FC#12, port B1

Entries	353
Mean	45.42
Std Dev	11.29

Turn on : 28.9398

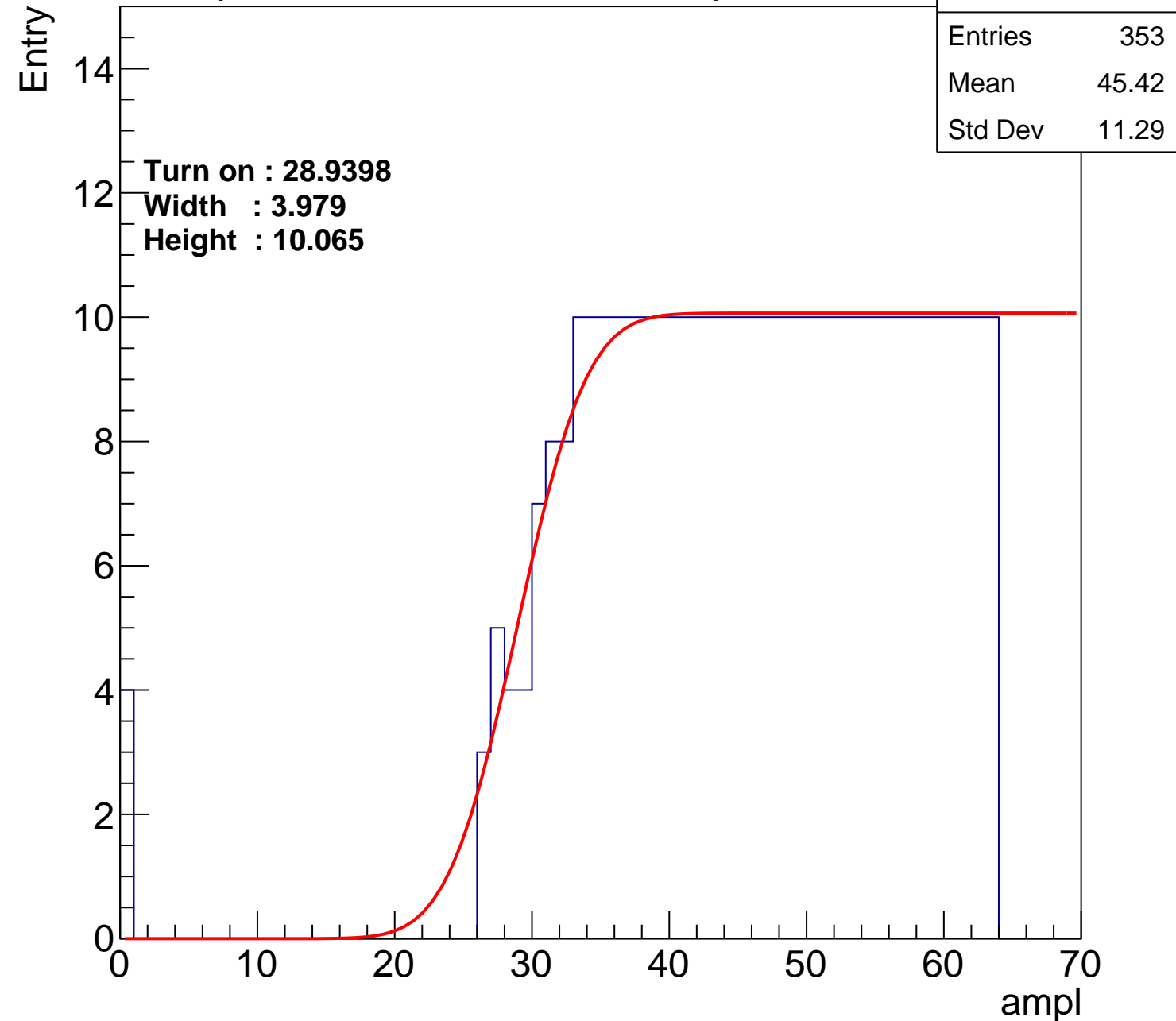
Width : 3.979

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch50

calib_packv5_042523_0143.root, FC#12, port B1

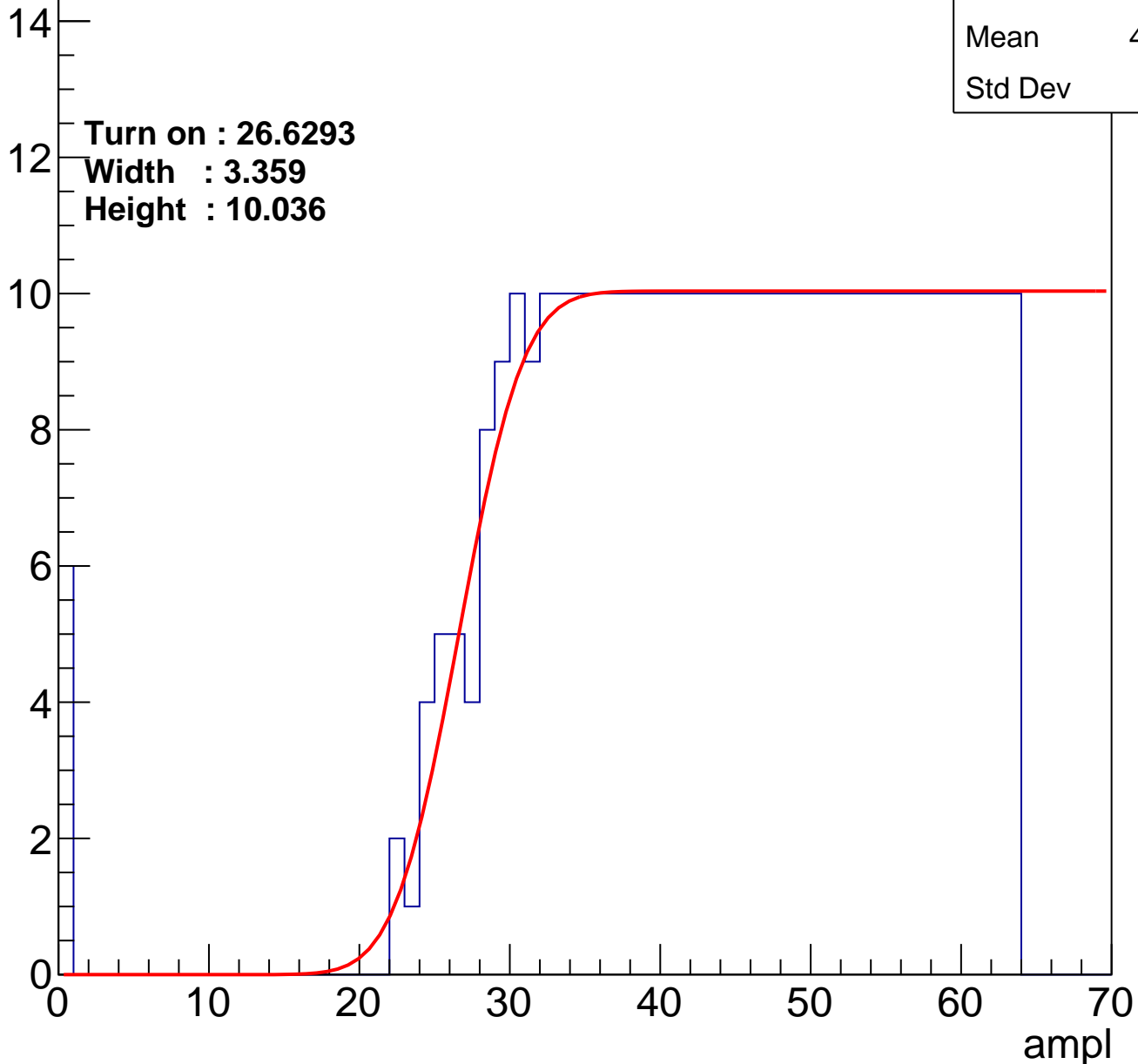
Entries	383
Mean	43.84
Std Dev	12.3

Turn on : 26.6293

Width : 3.359

Height : 10.036

Entry



B0L102S, U3-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.22
Std Dev	12.3

Turn on : 24.6487

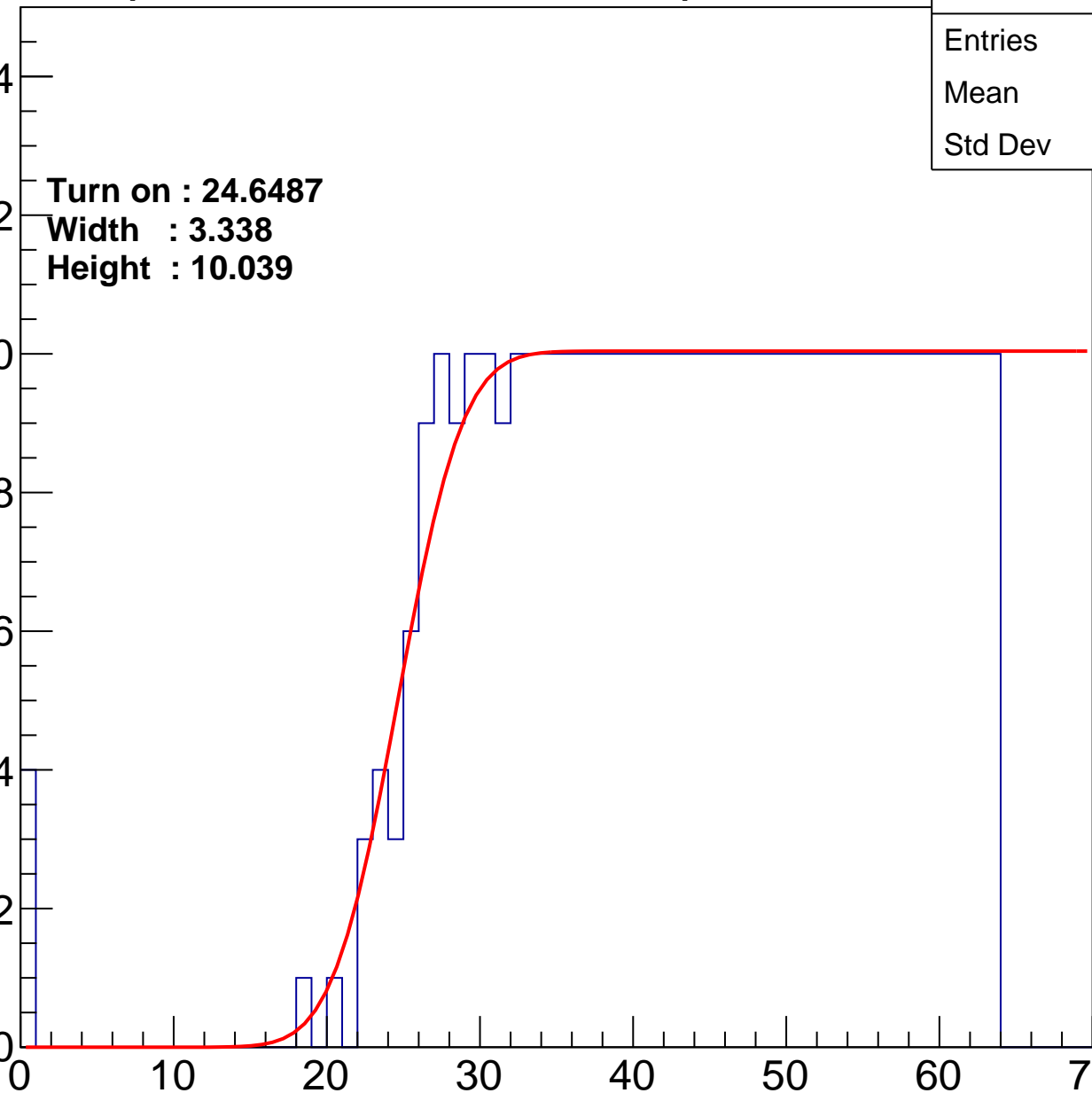
Width : 3.338

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.41
Std Dev	11.5

Turn on : 27.4555

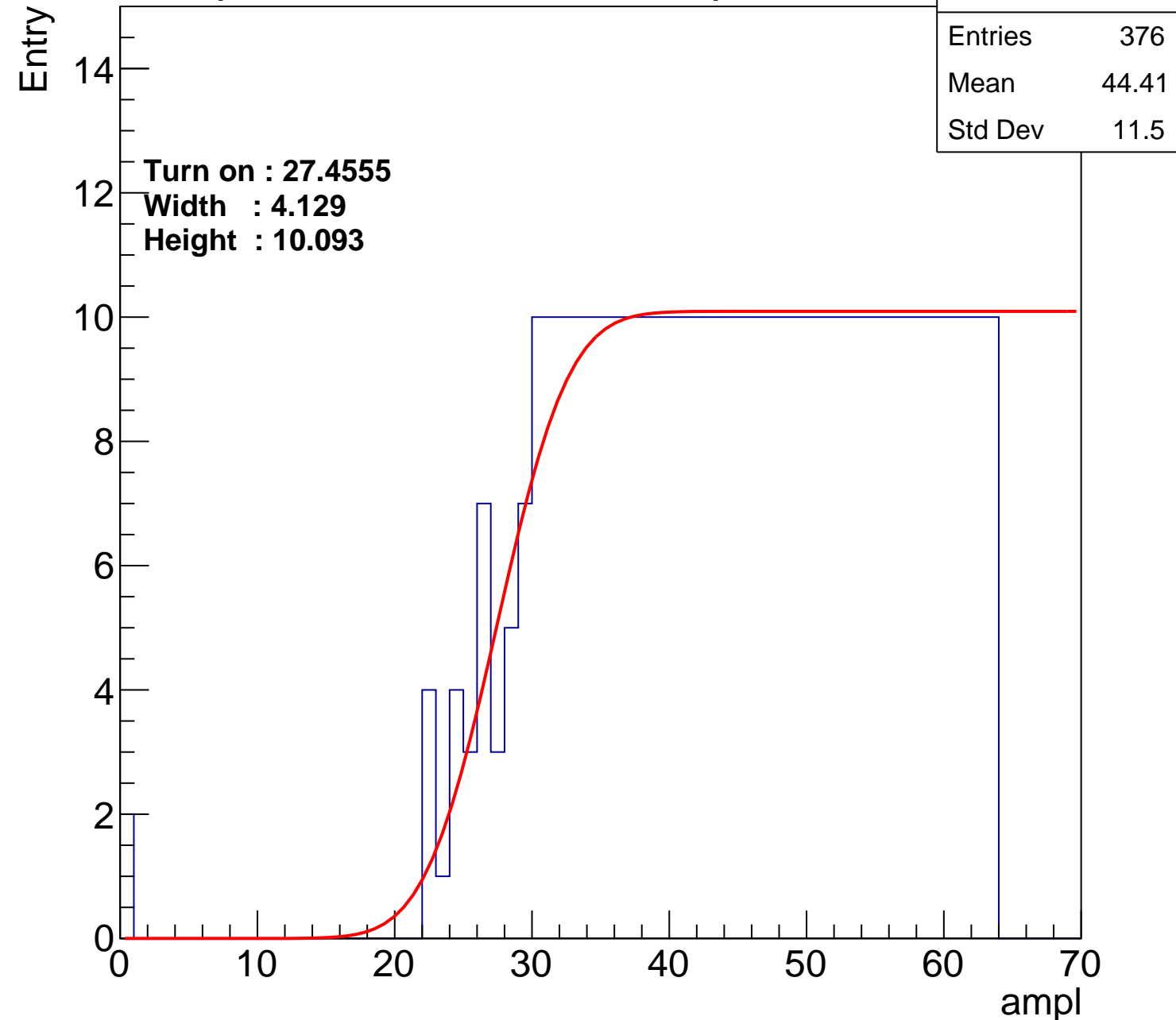
Width : 4.129

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch53

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.43
Std Dev	12.62

Turn on : 25.7877

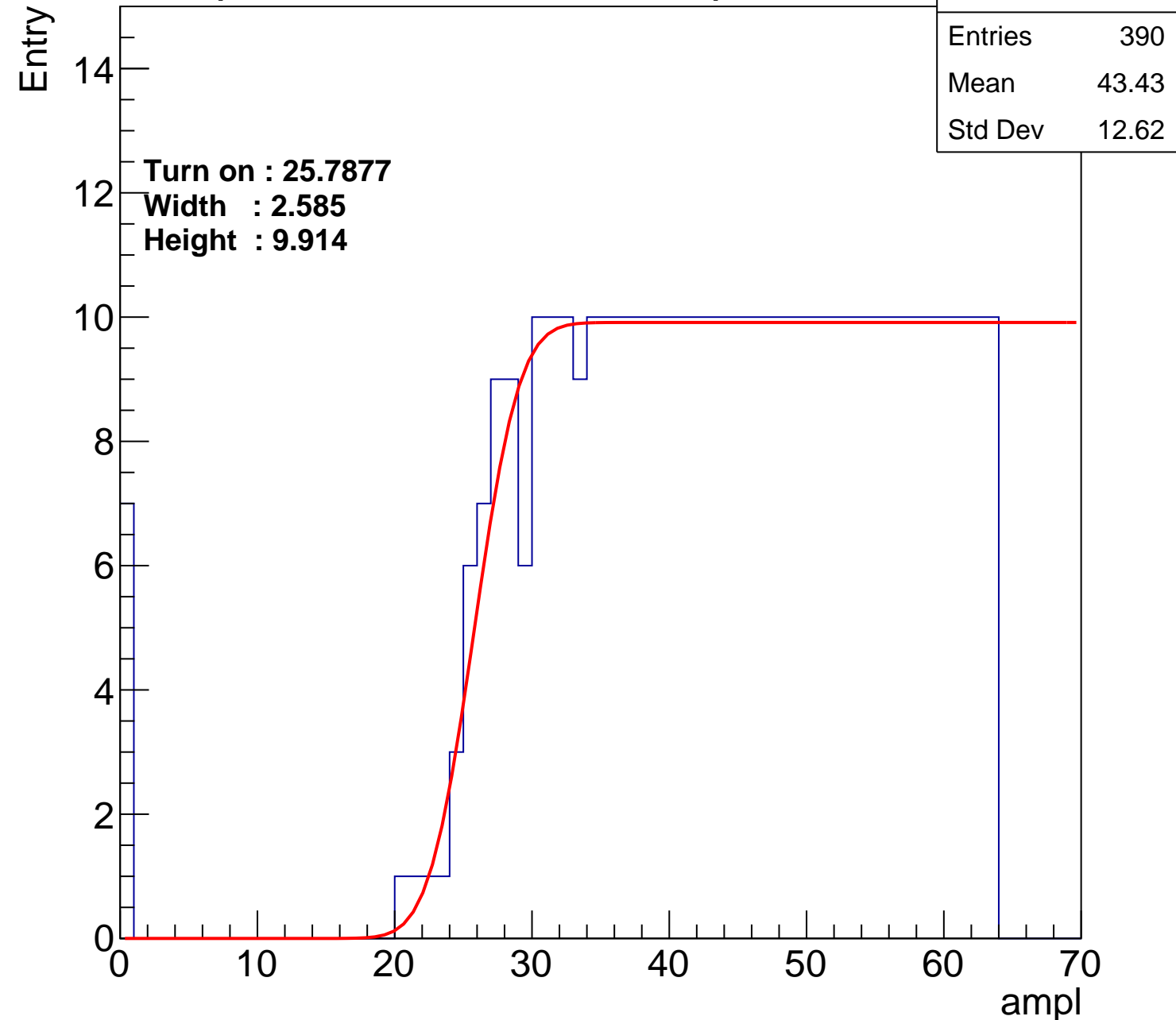
Width : 2.585

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch54

calib_packv5_042523_0143.root, FC#12, port B1

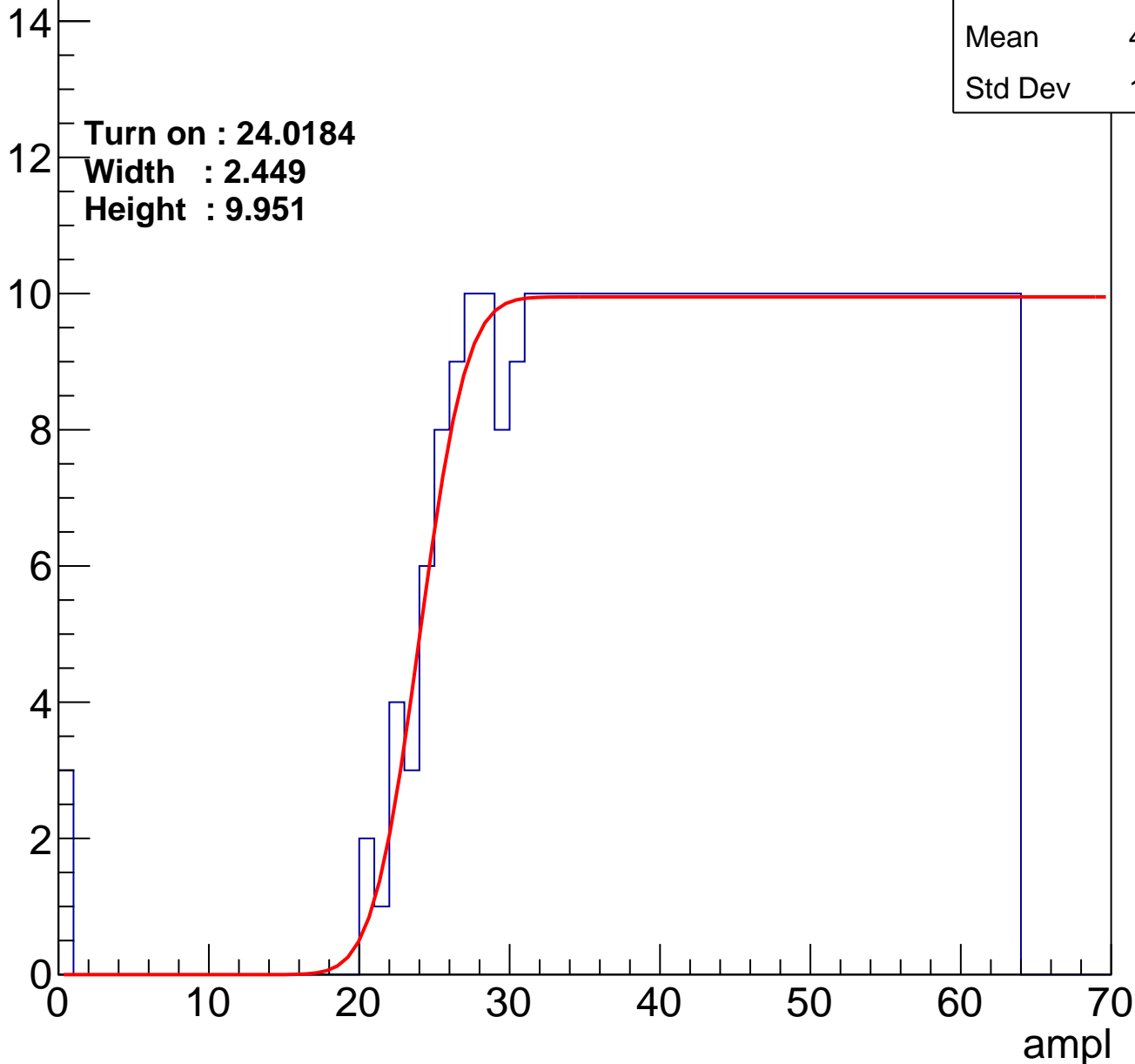
Entry

Entries	403
Mean	43.07
Std Dev	12.25

Turn on : 24.0184

Width : 2.449

Height : 9.951



B0L102S, U3-ch55

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.09
Std Dev	11.29

Turn on : 28.6124

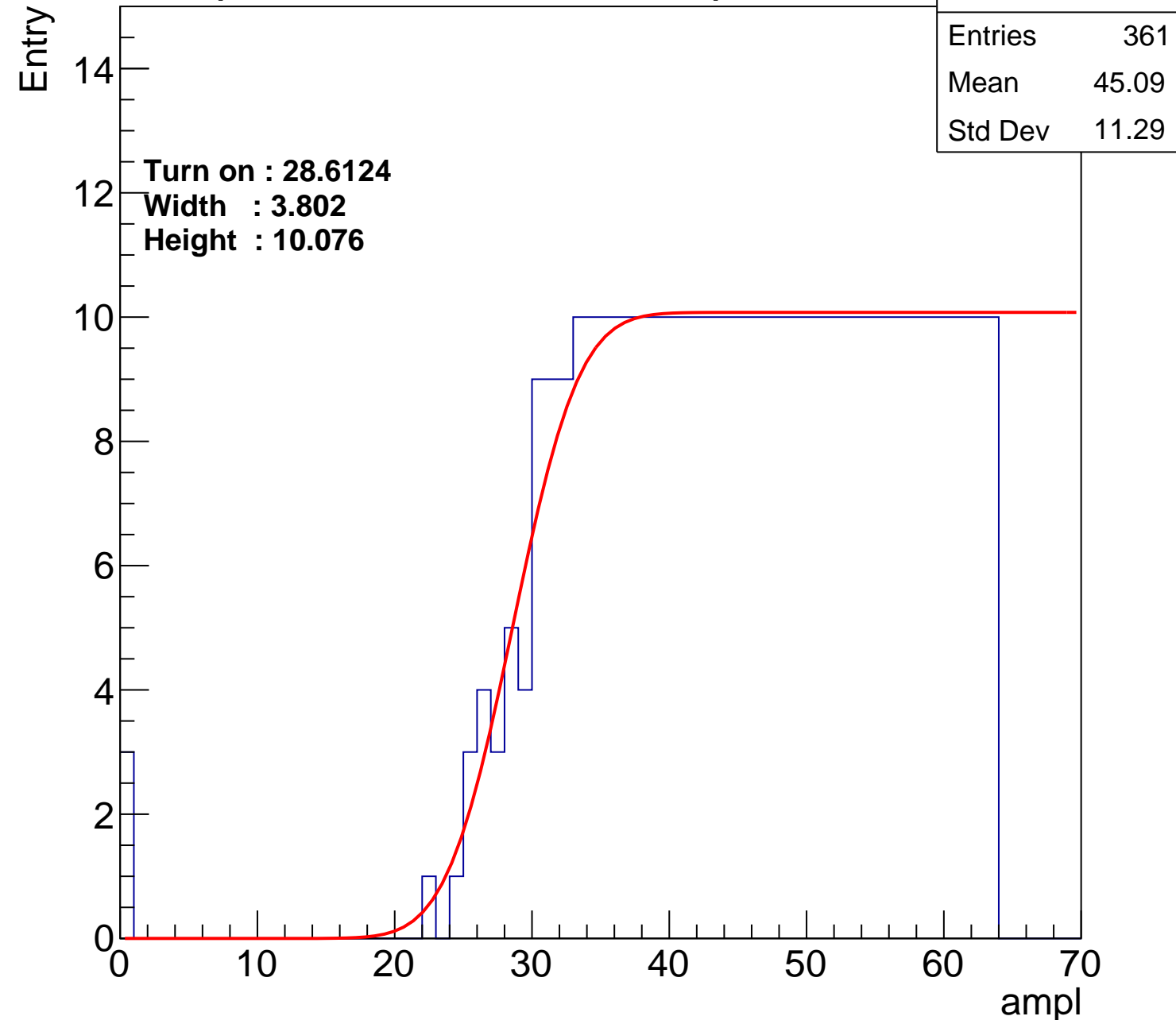
Width : 3.802

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.71
Std Dev	11.97

Turn on : 25.5049

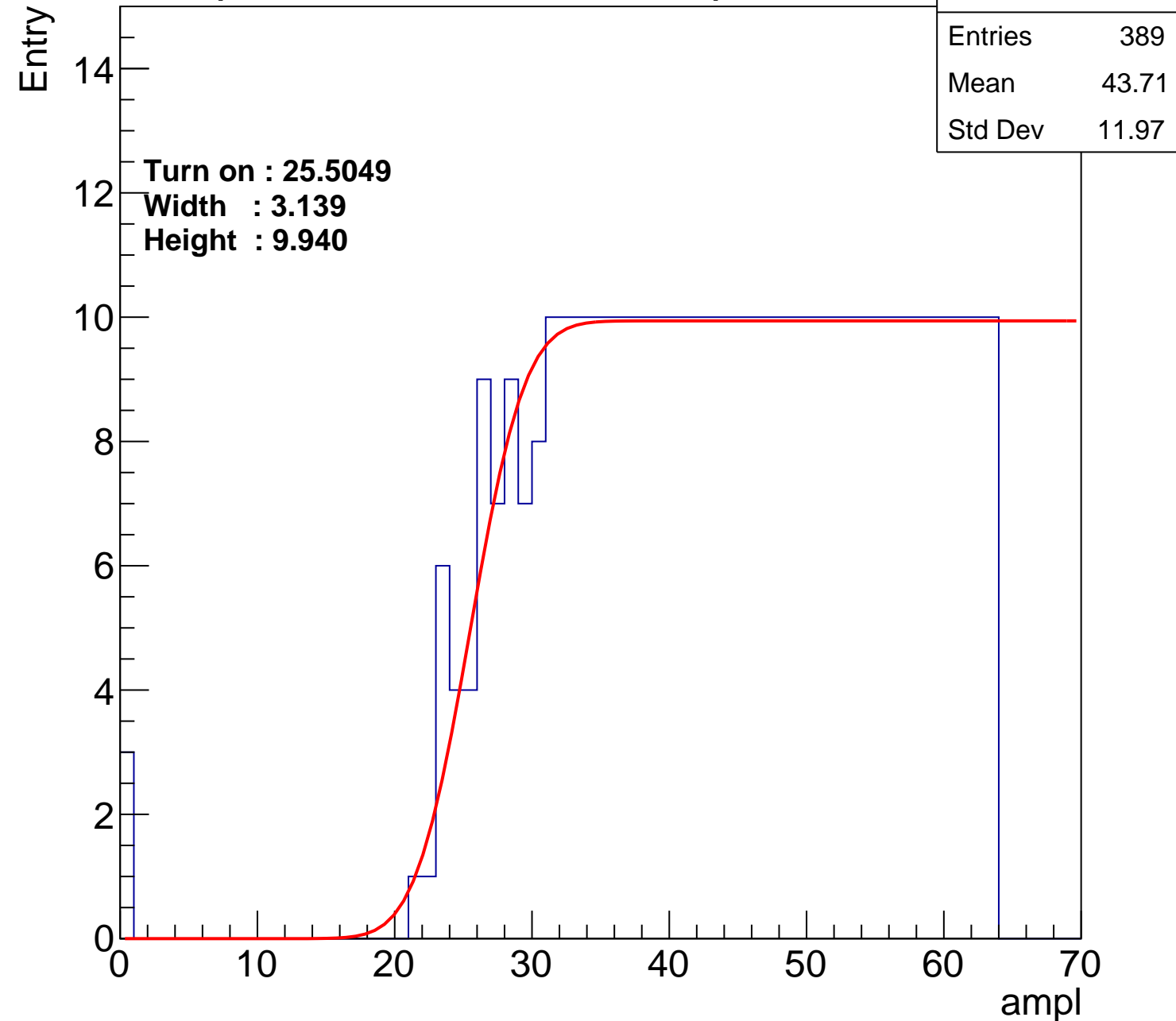
Width : 3.139

Height : 9.940

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.83
Std Dev	11

Turn on : 27.4180

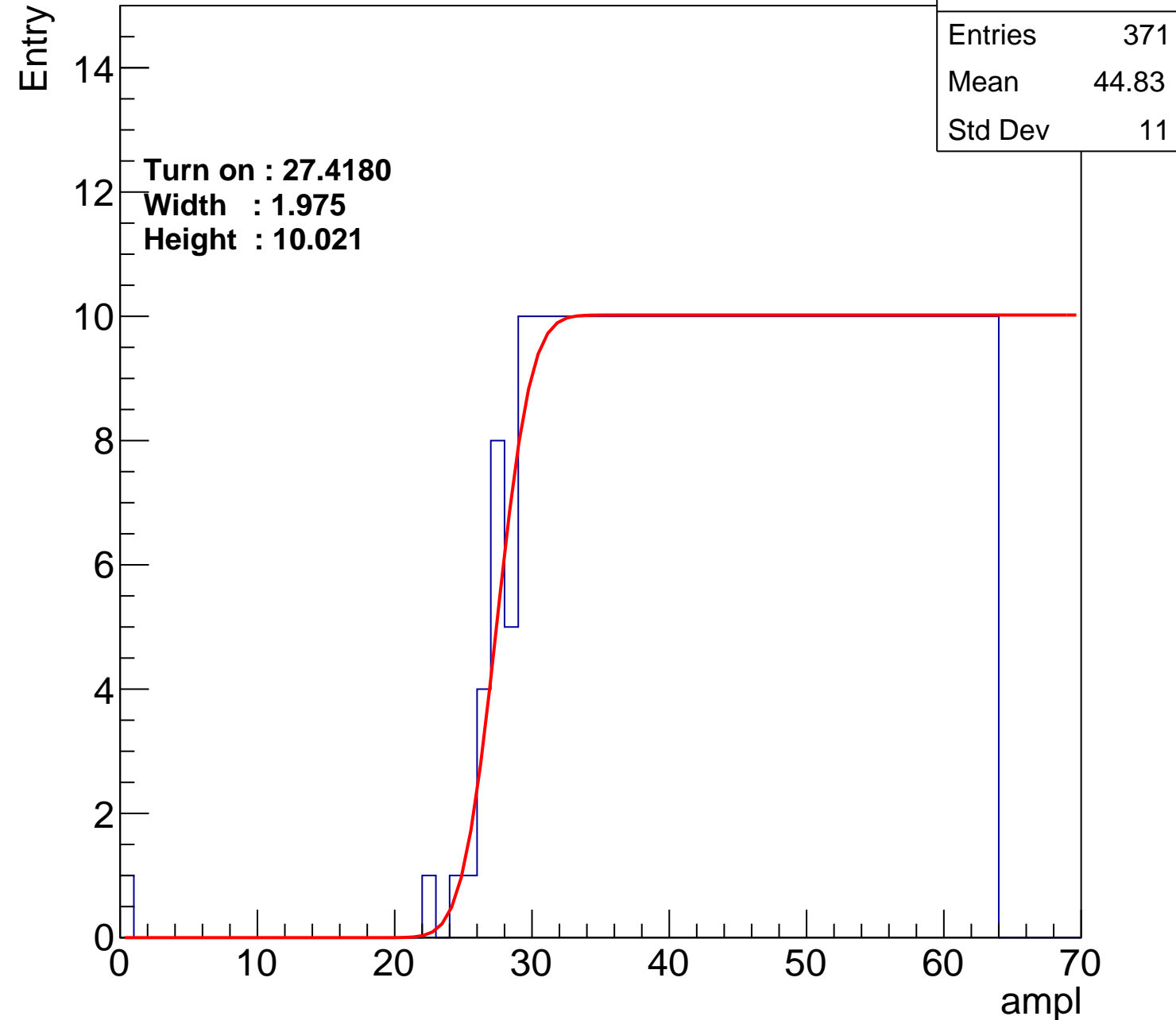
Width : 1.975

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch58

calib_packv5_042523_0143.root, FC#12, port B1

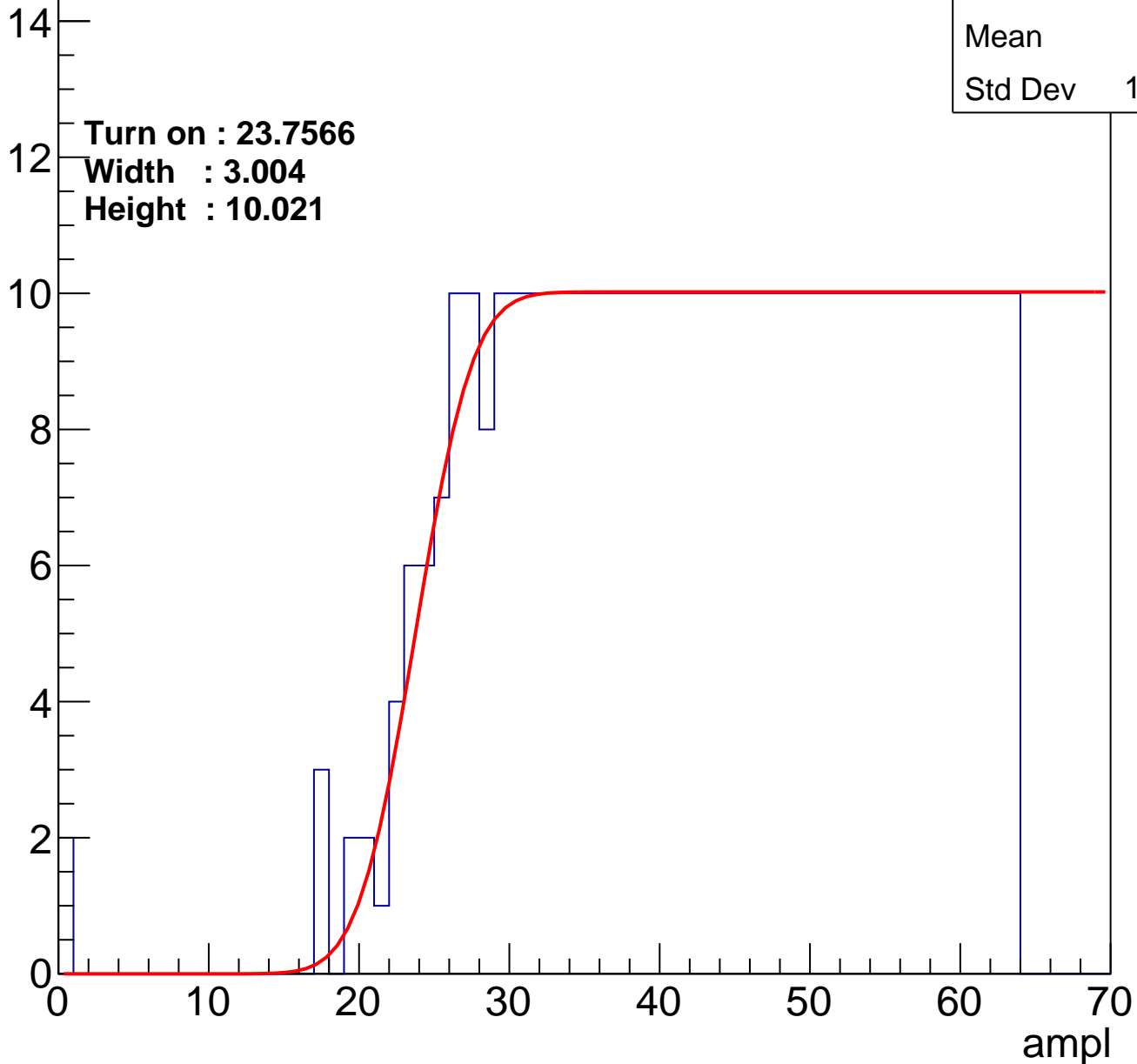
Entries	411
Mean	42.7
Std Dev	12.39

Turn on : 23.7566

Width : 3.004

Height : 10.021

Entry



B0L102S, U3-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.09
Std Dev	12.29

Turn on : 24.7749

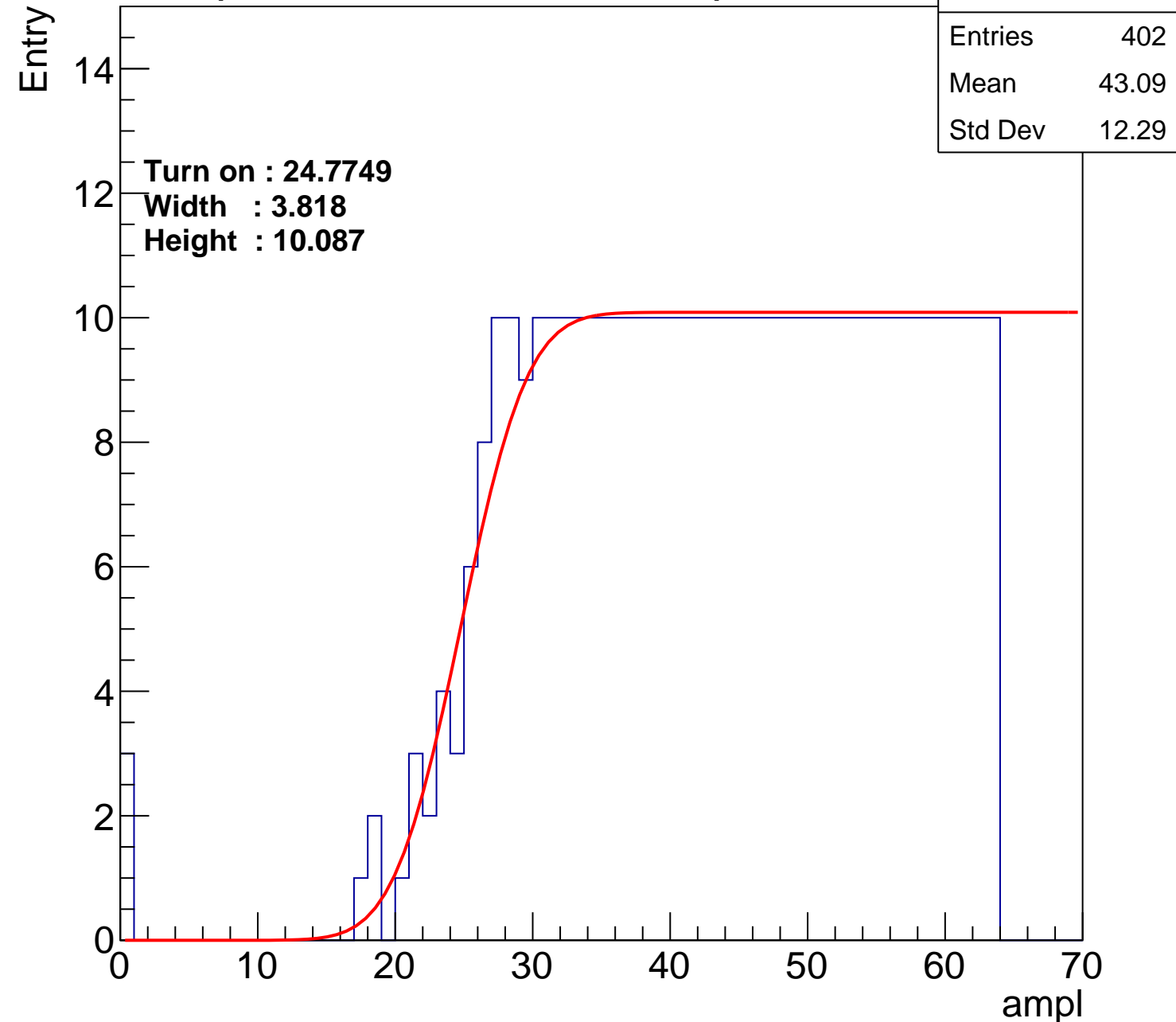
Width : 3.818

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.11
Std Dev	12.52

Turn on : 24.7520

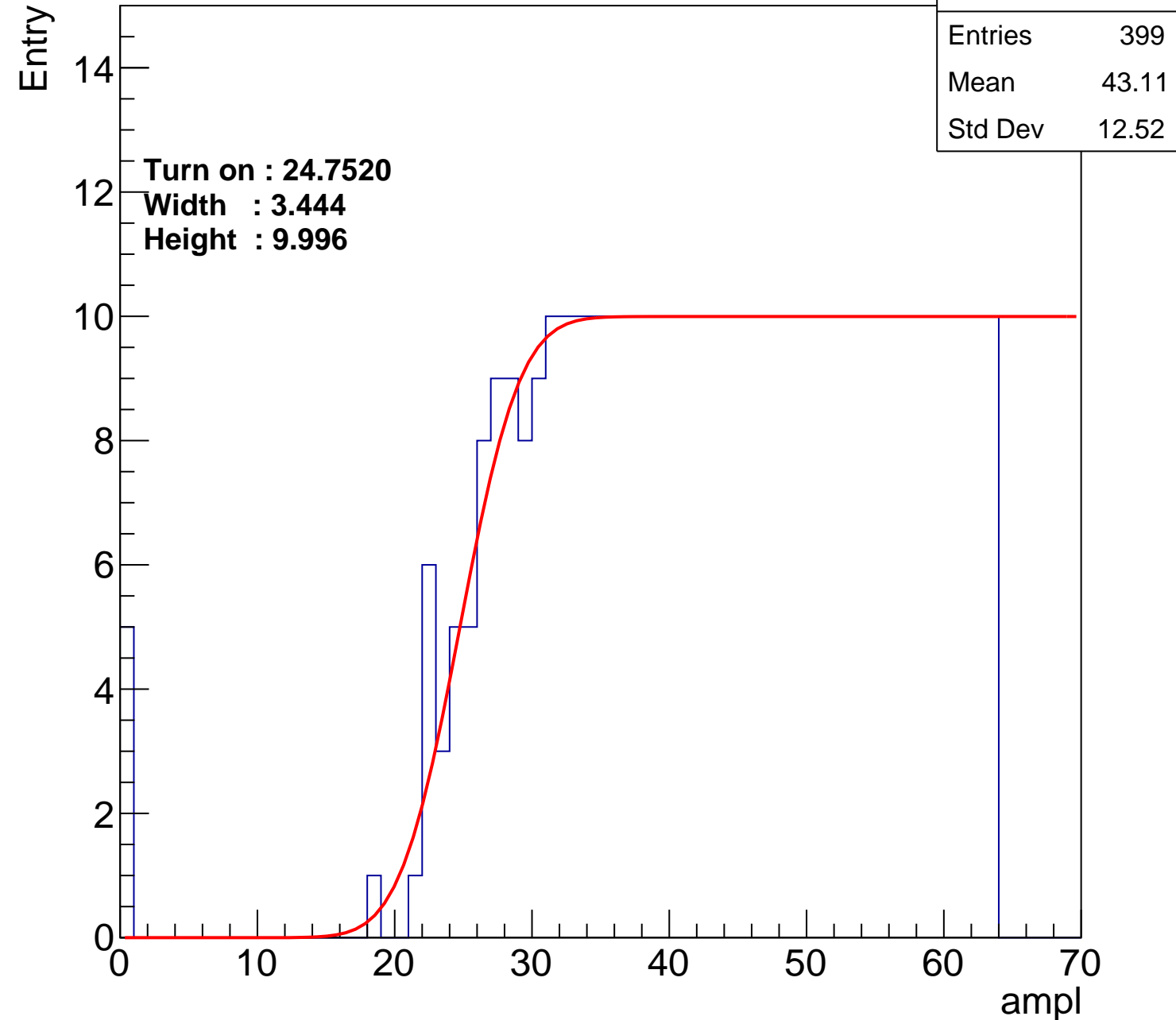
Width : 3.444

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch61

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.19
Std Dev	11.88

Turn on : 26.7936

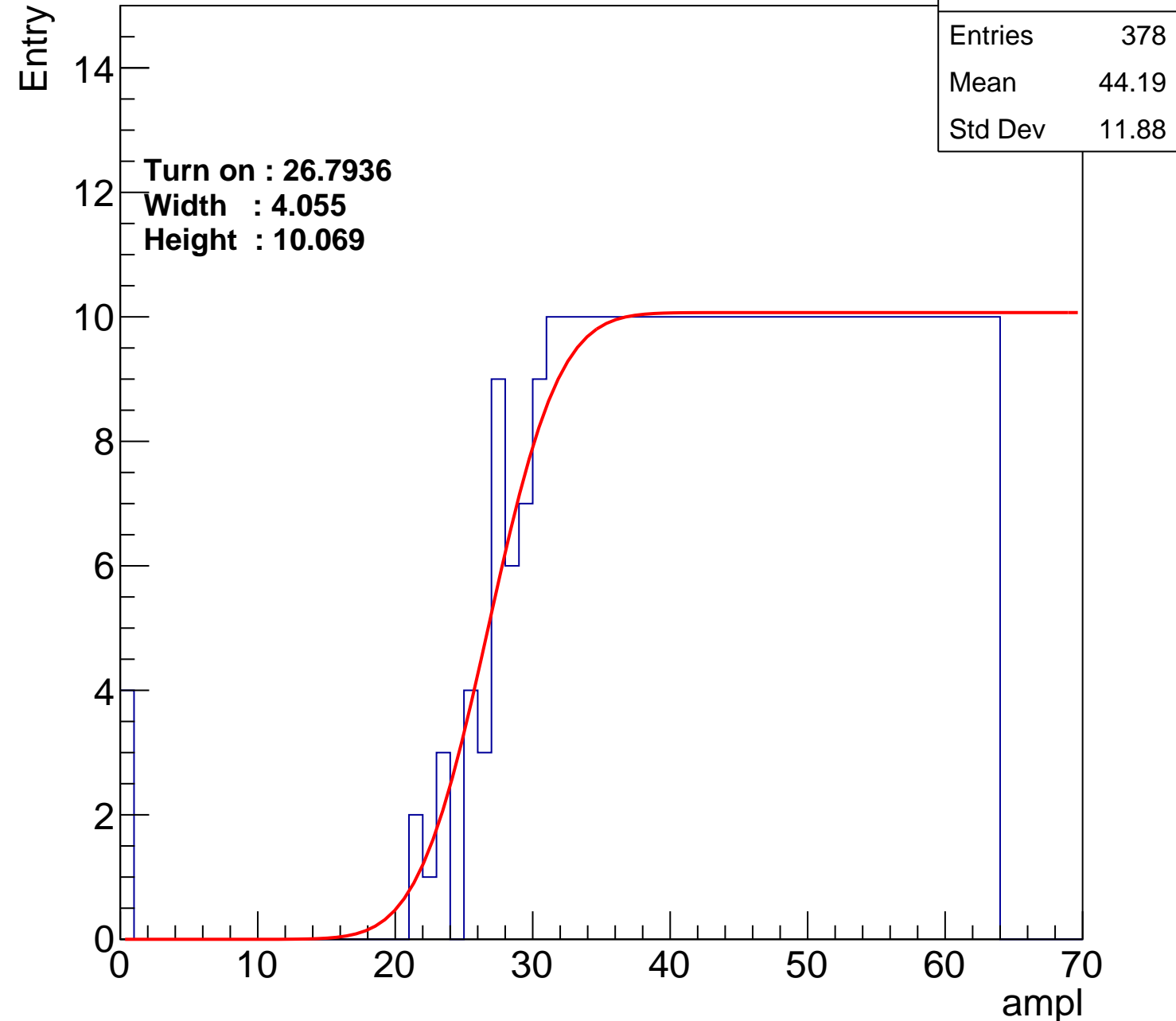
Width : 4.055

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entry

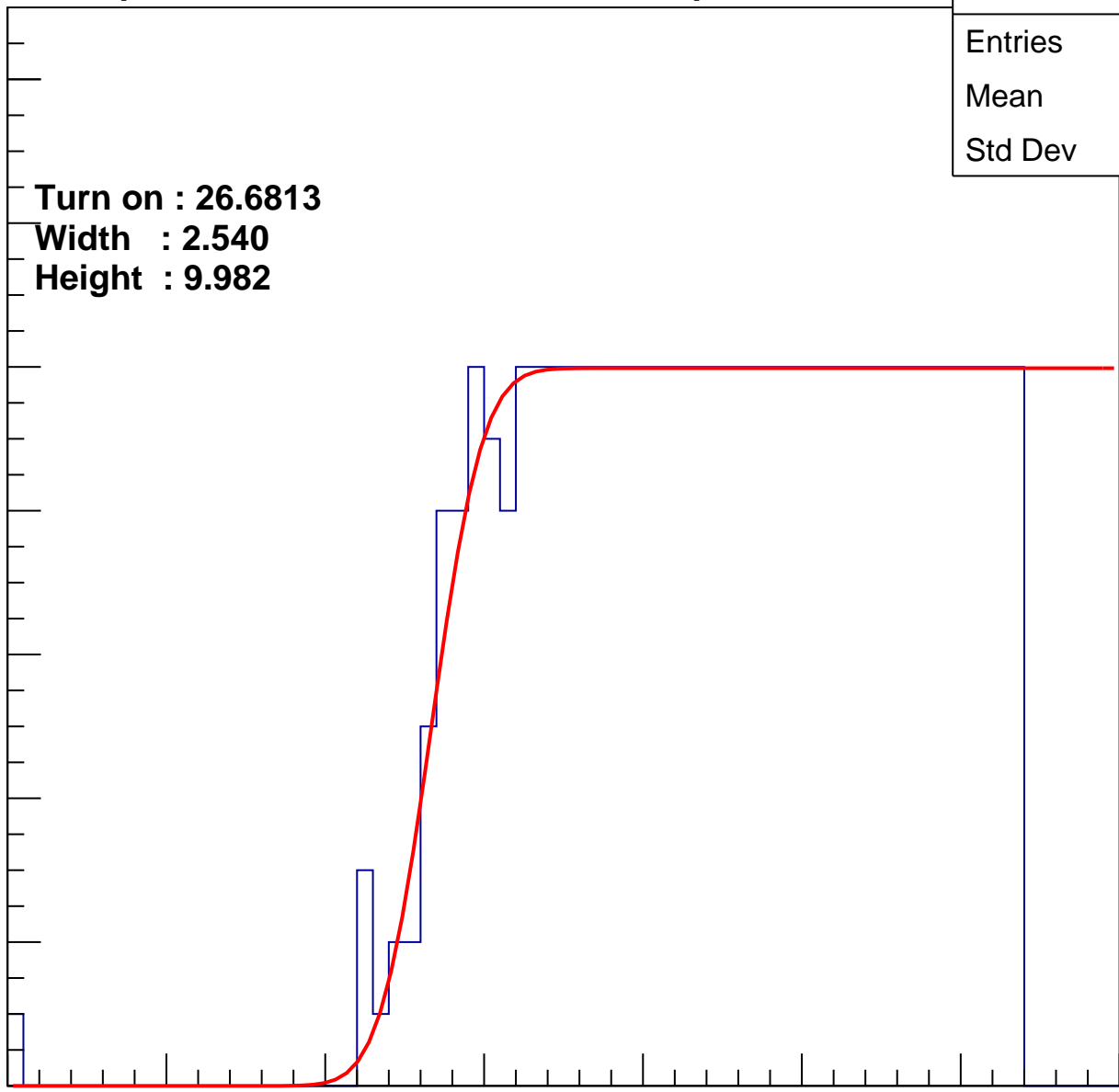
Entries	377
Mean	44.47
Std Dev	11.26

Turn on : 26.6813
Width : 2.540
Height : 9.982

14
12
10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L102S, U3-ch63

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.84
Std Dev	11.61

Turn on : 25.6627

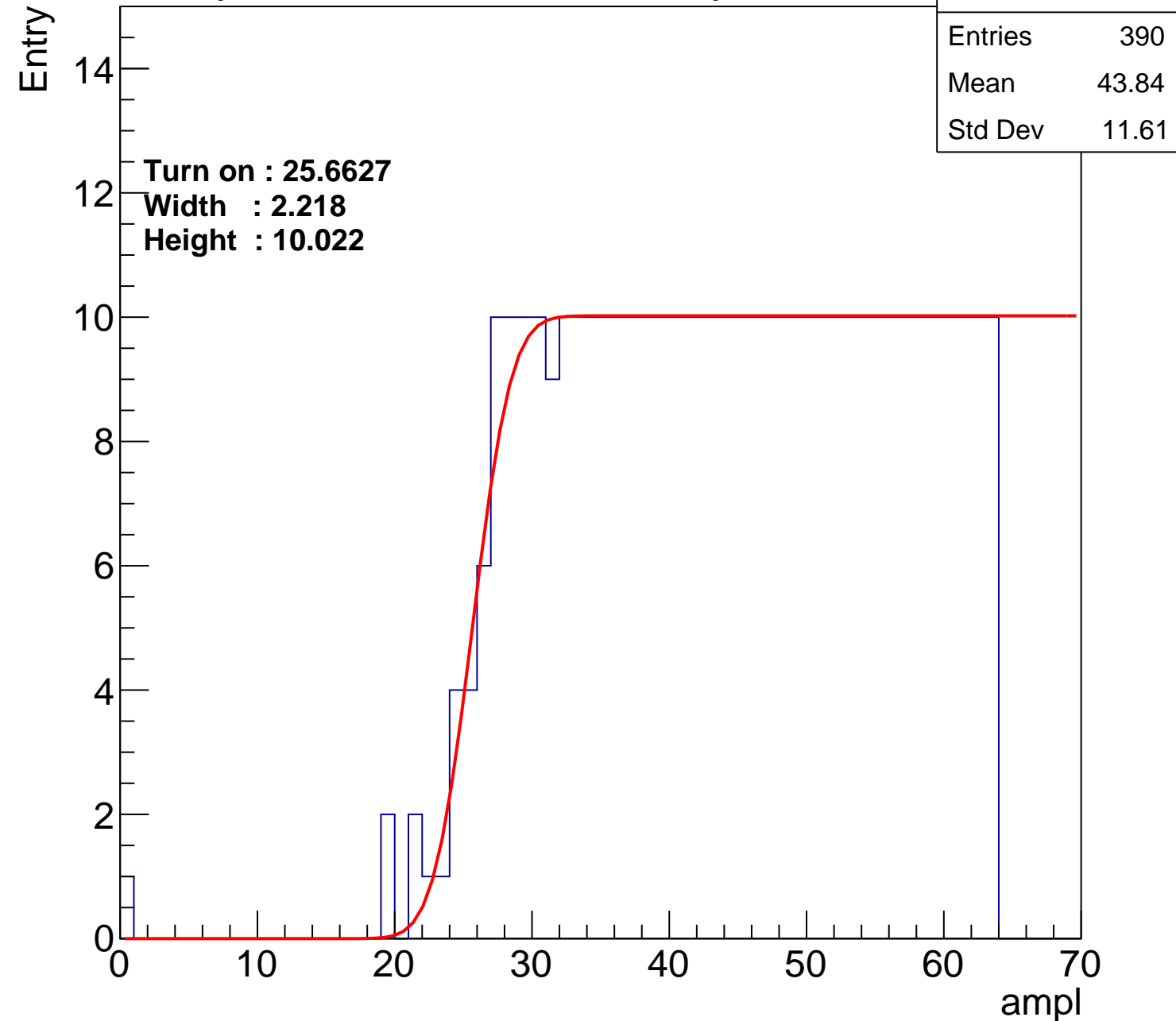
Width : 2.218

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.92
Std Dev	11.72

Turn on : 26.3362

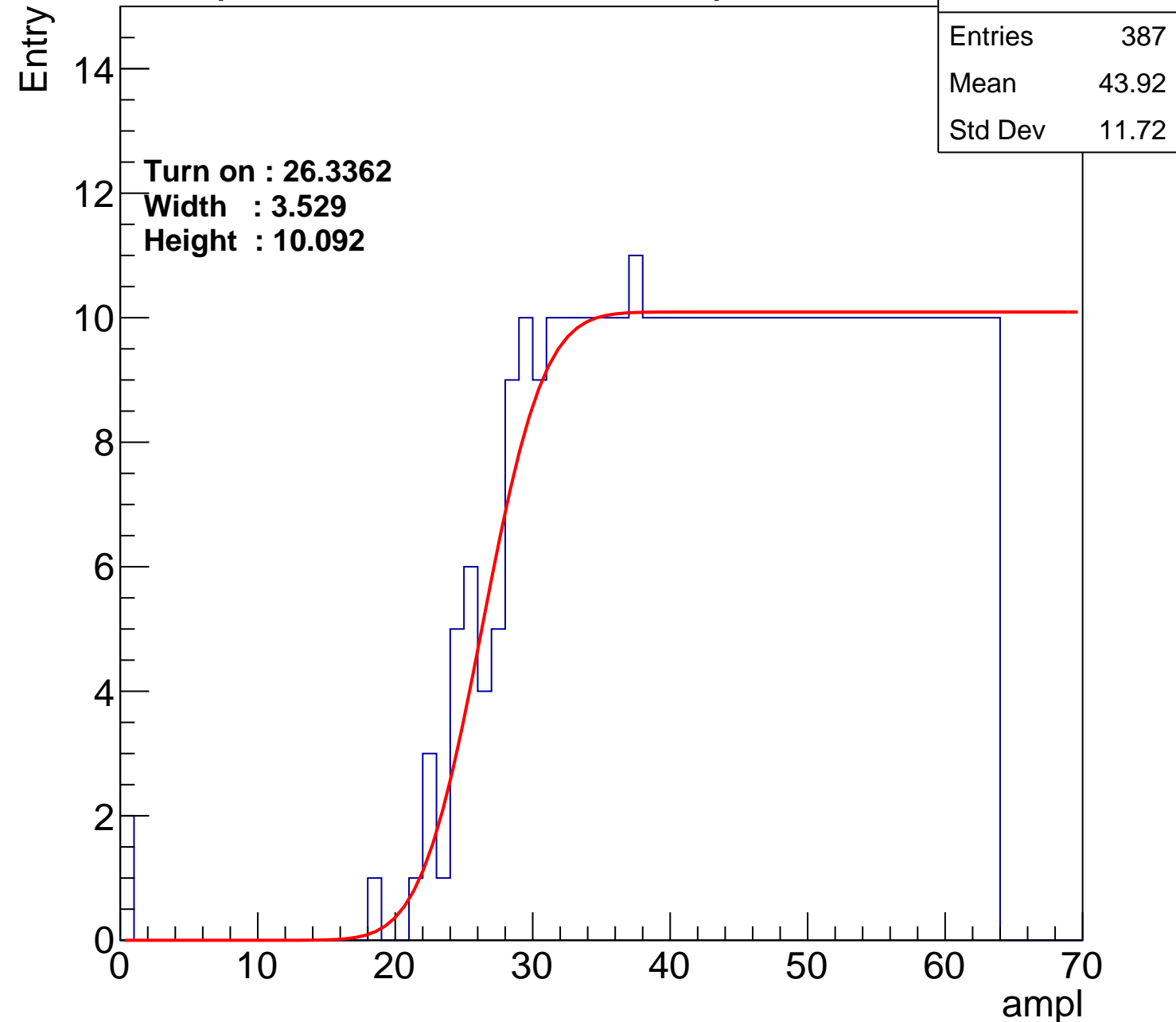
Width : 3.529

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch65

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.1
Std Dev	12.65

Turn on : 25.3025

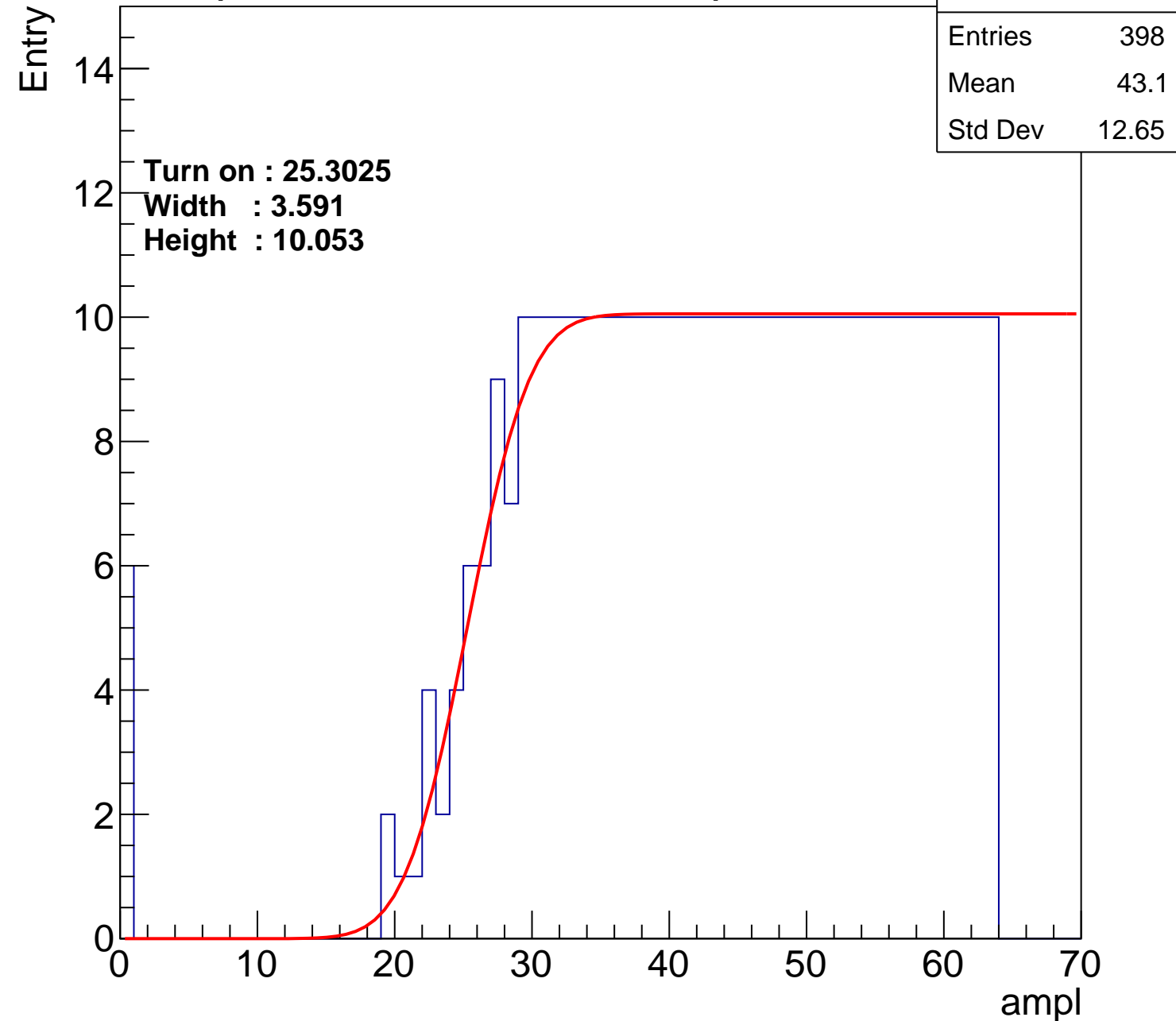
Width : 3.591

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.6
Std Dev	11.53

Turn on : 26.5919

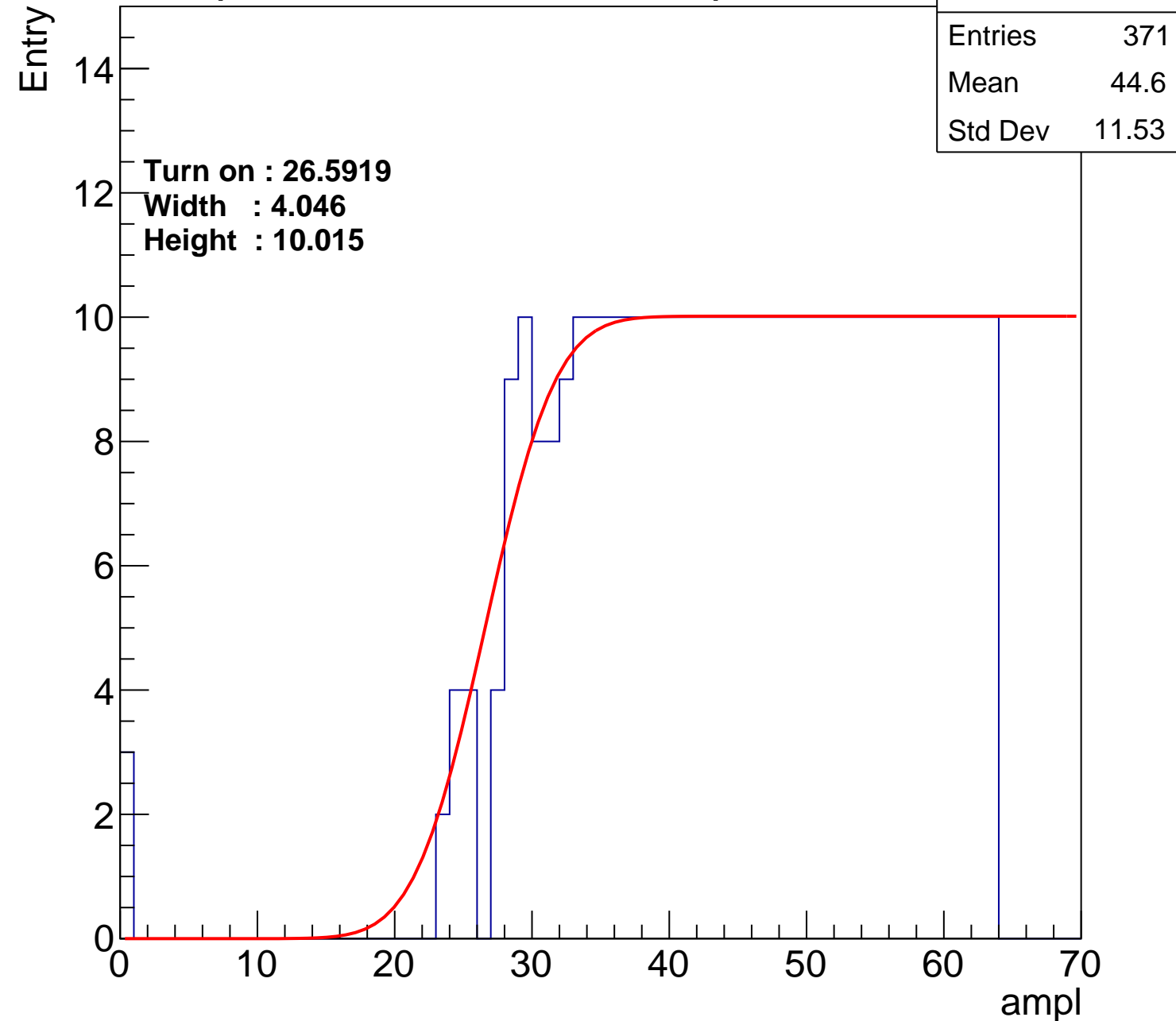
Width : 4.046

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.23
Std Dev	12.32

Turn on : 25.3693

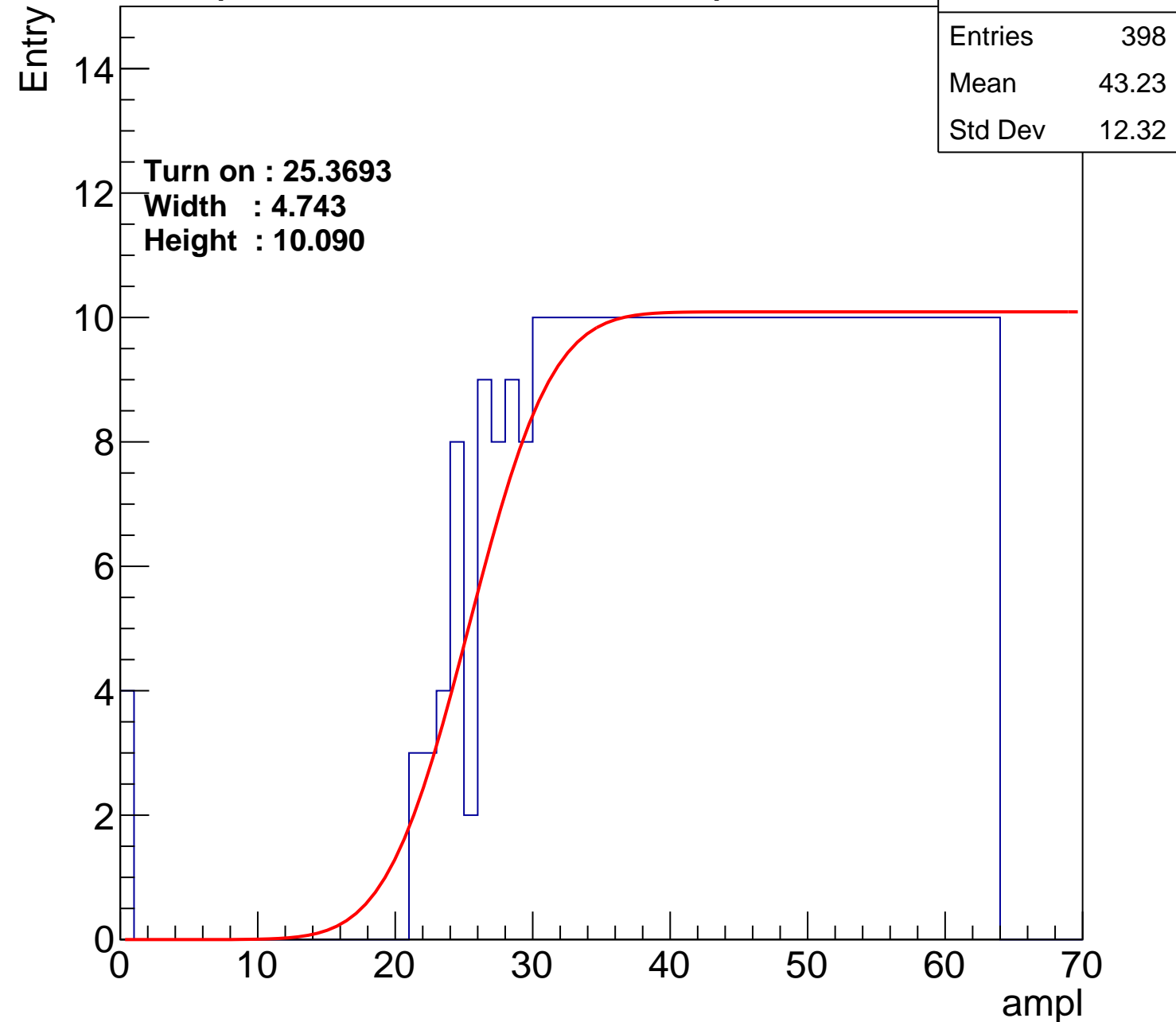
Width : 4.743

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.87
Std Dev	11.78

Turn on : 26.1166

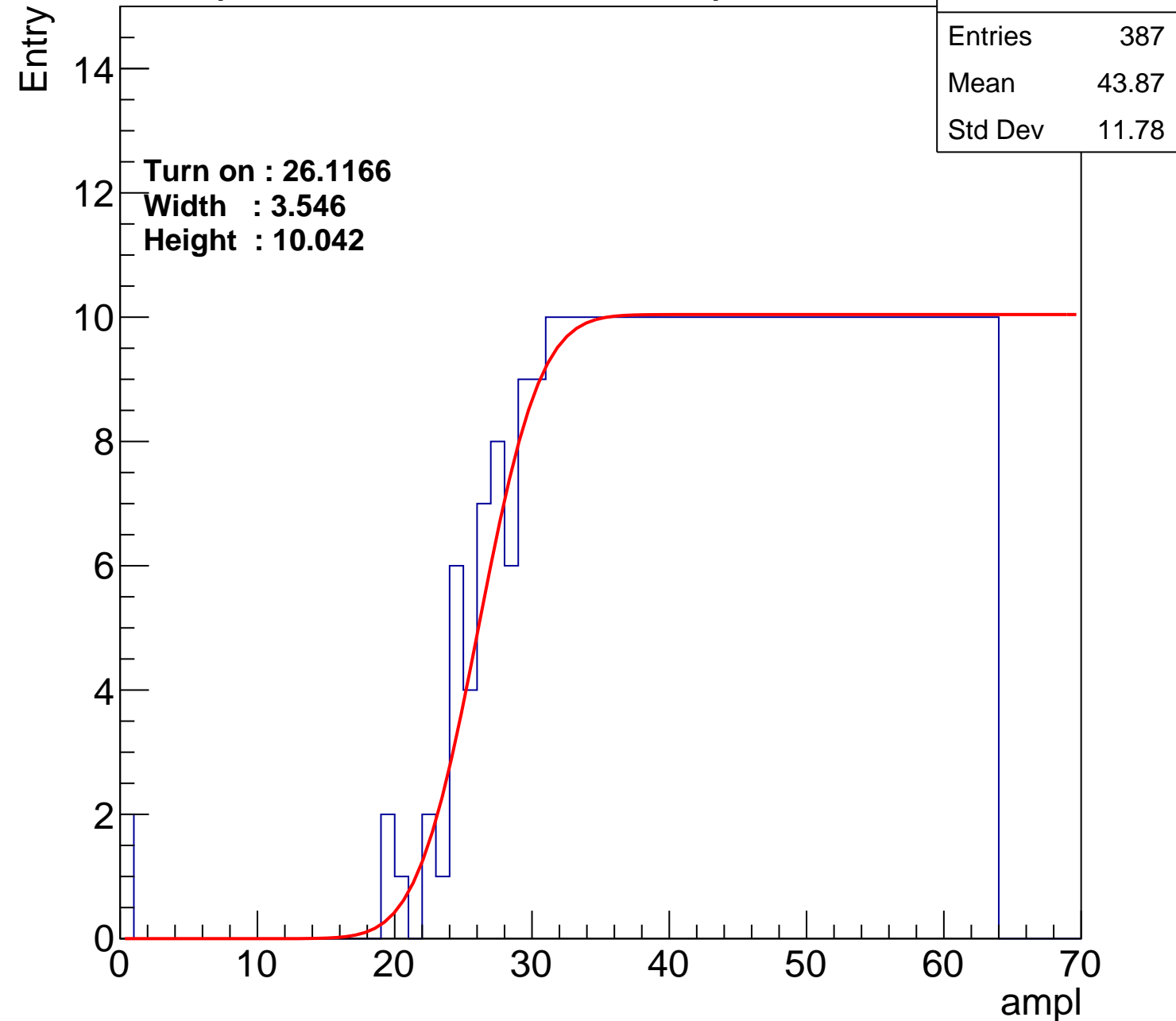
Width : 3.546

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.7
Std Dev	11.65

Turn on : 25.2704

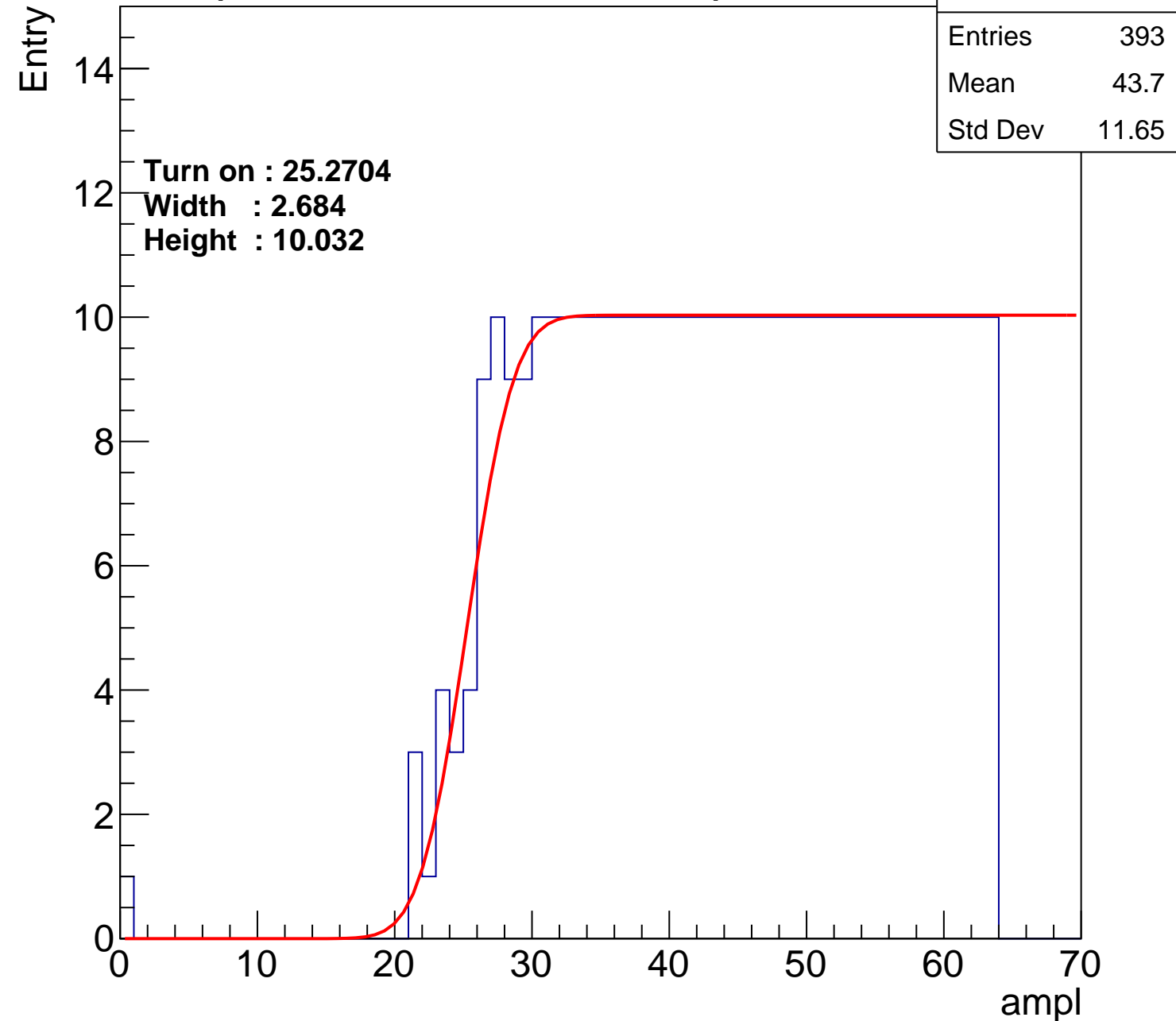
Width : 2.684

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	406
Mean	42.92
Std Dev	12.35

Turn on : 24.1999

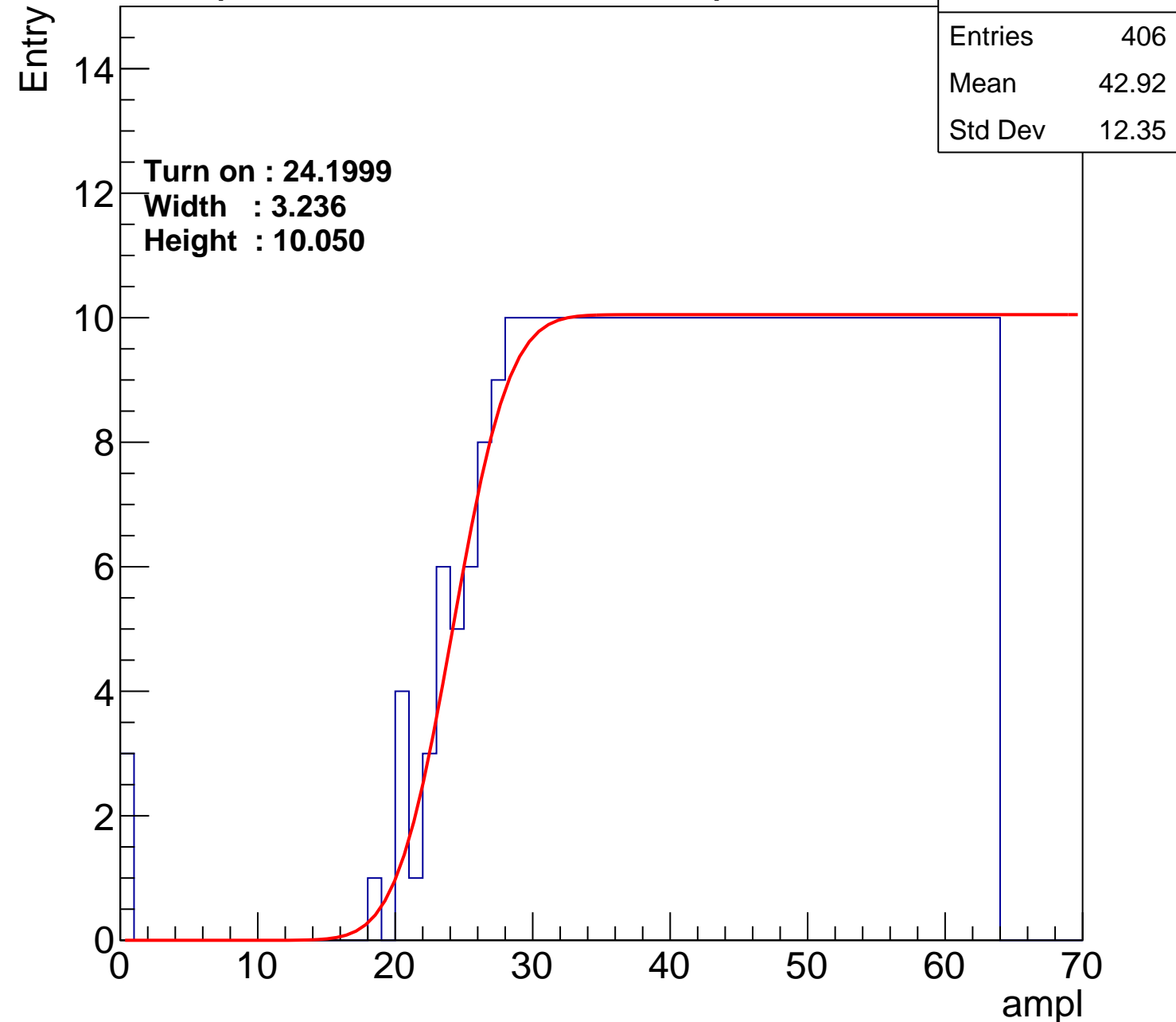
Width : 3.236

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.6108

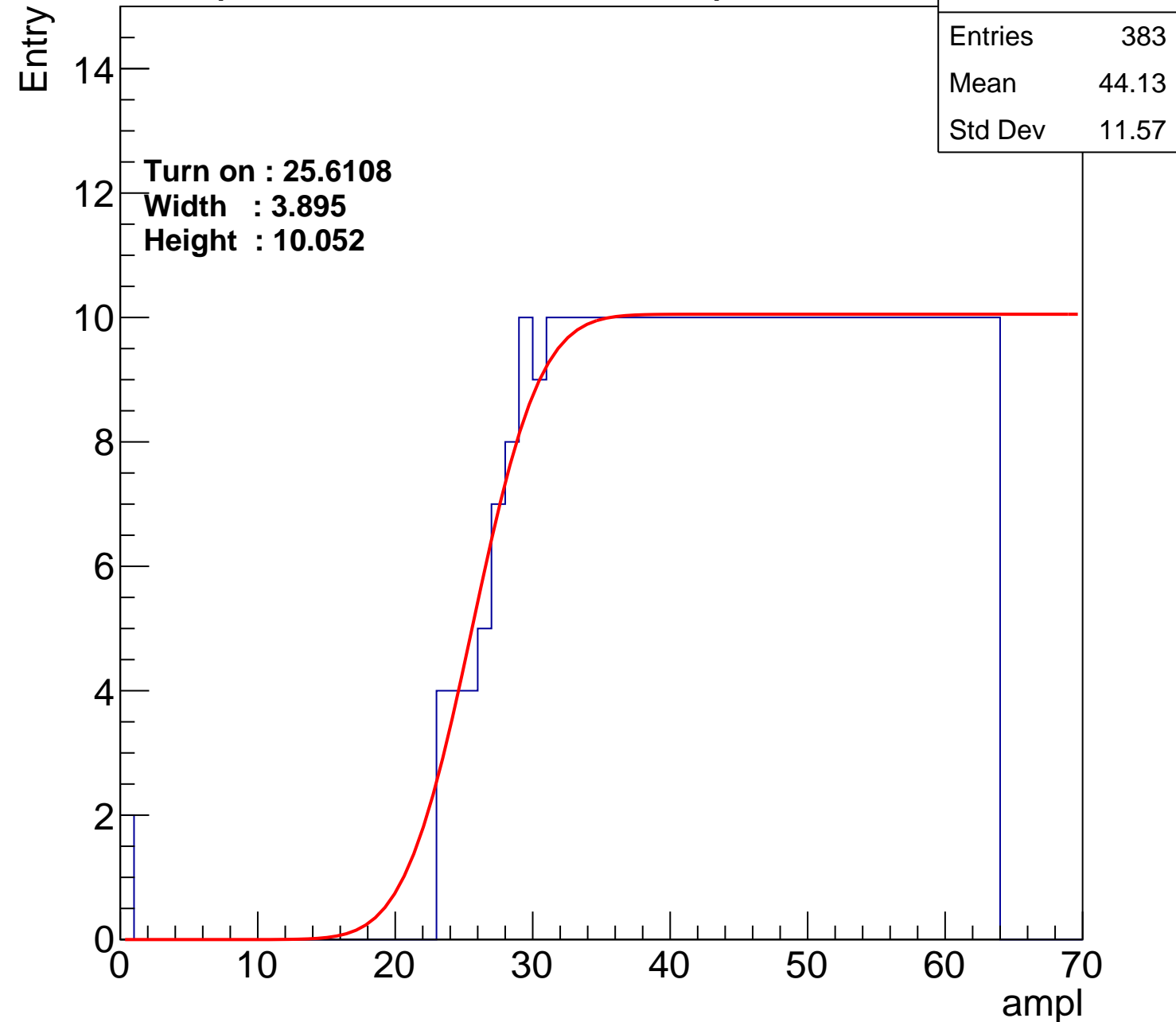
Width : 3.895

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.48
Std Dev	12.19

Turn on : 24.3469

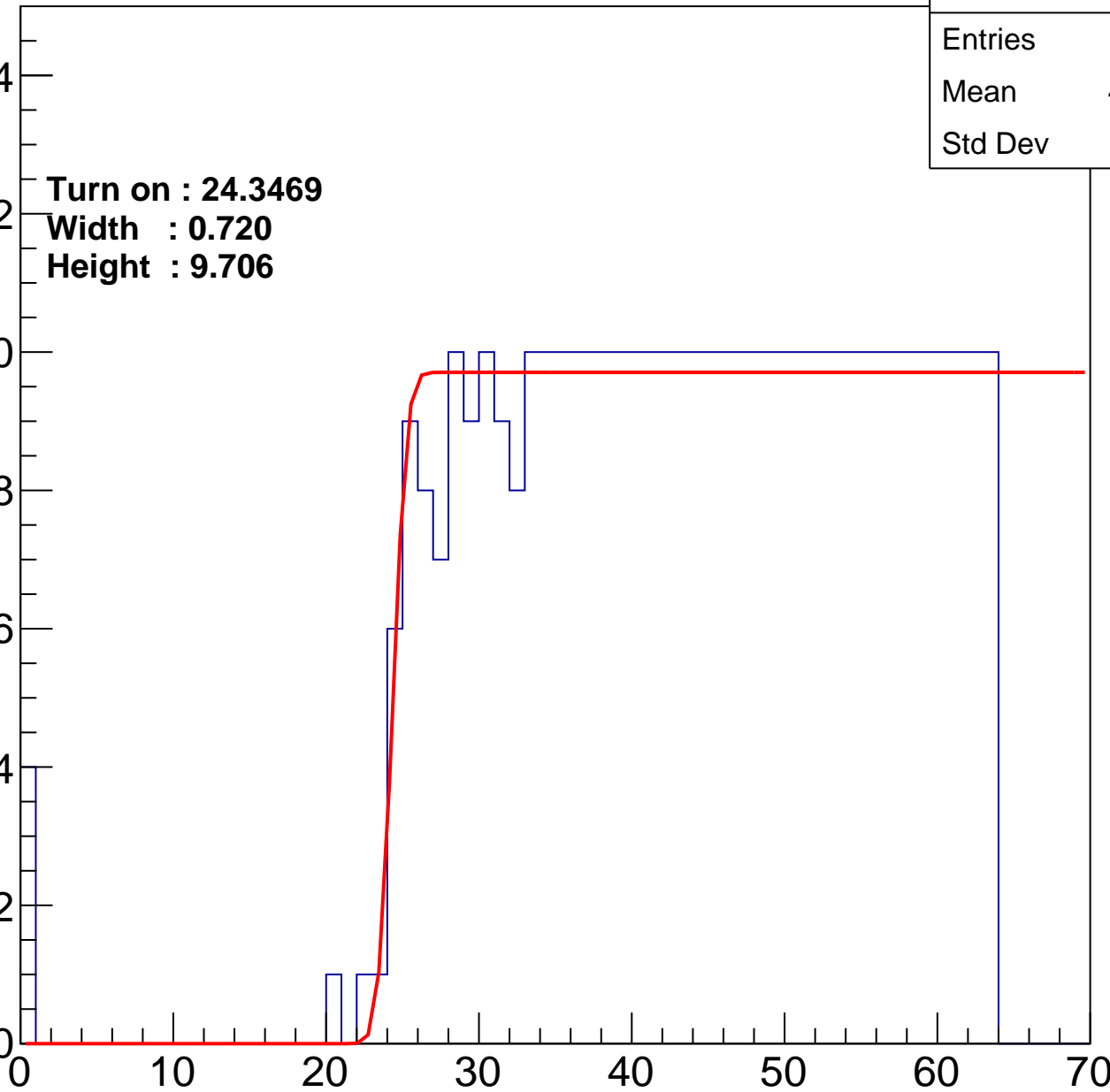
Width : 0.720

Height : 9.706

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.48
Std Dev	12.15

Turn on : 24.8514

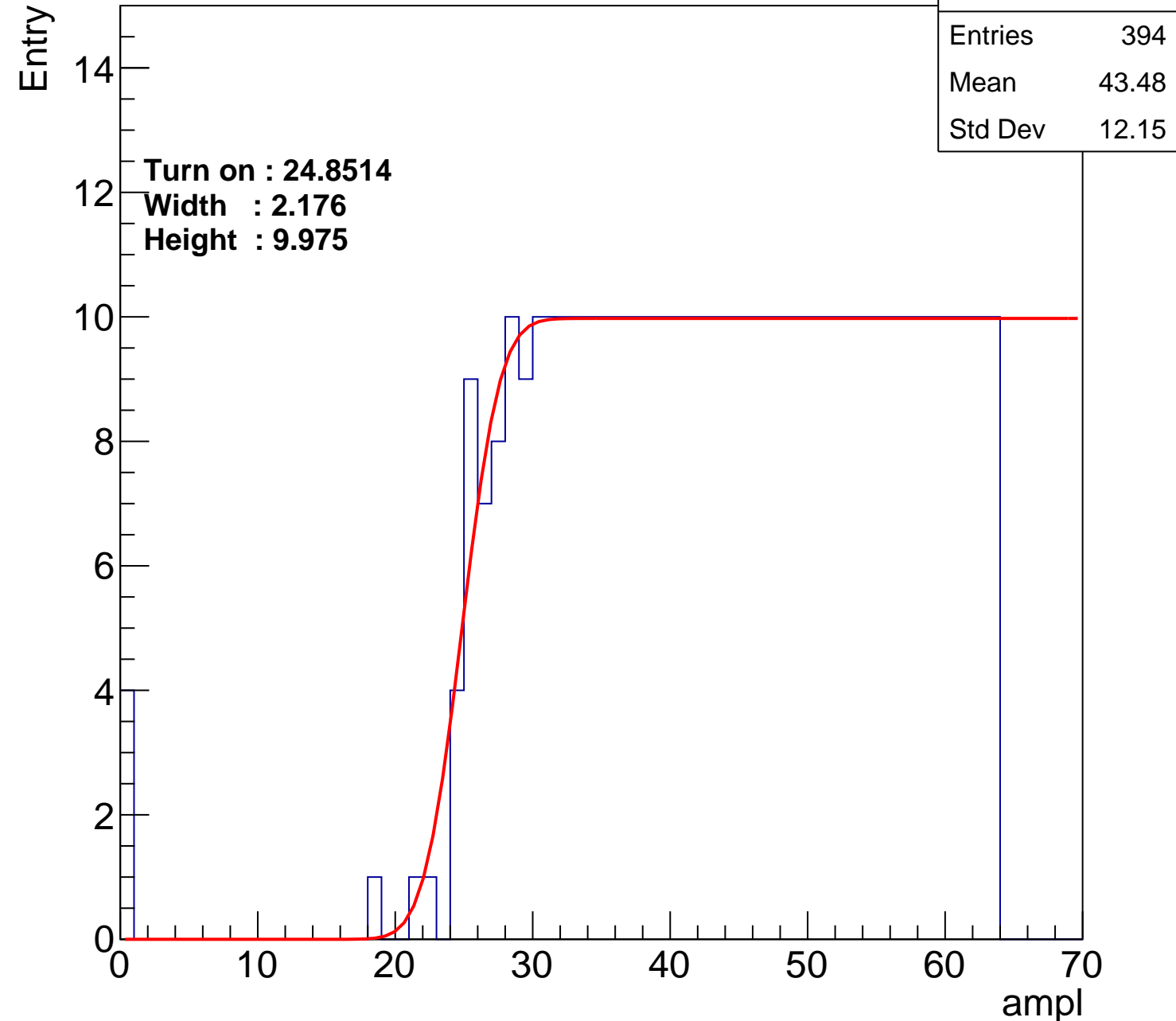
Width : 2.176

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.78
Std Dev	11.64

Turn on : 25.6049

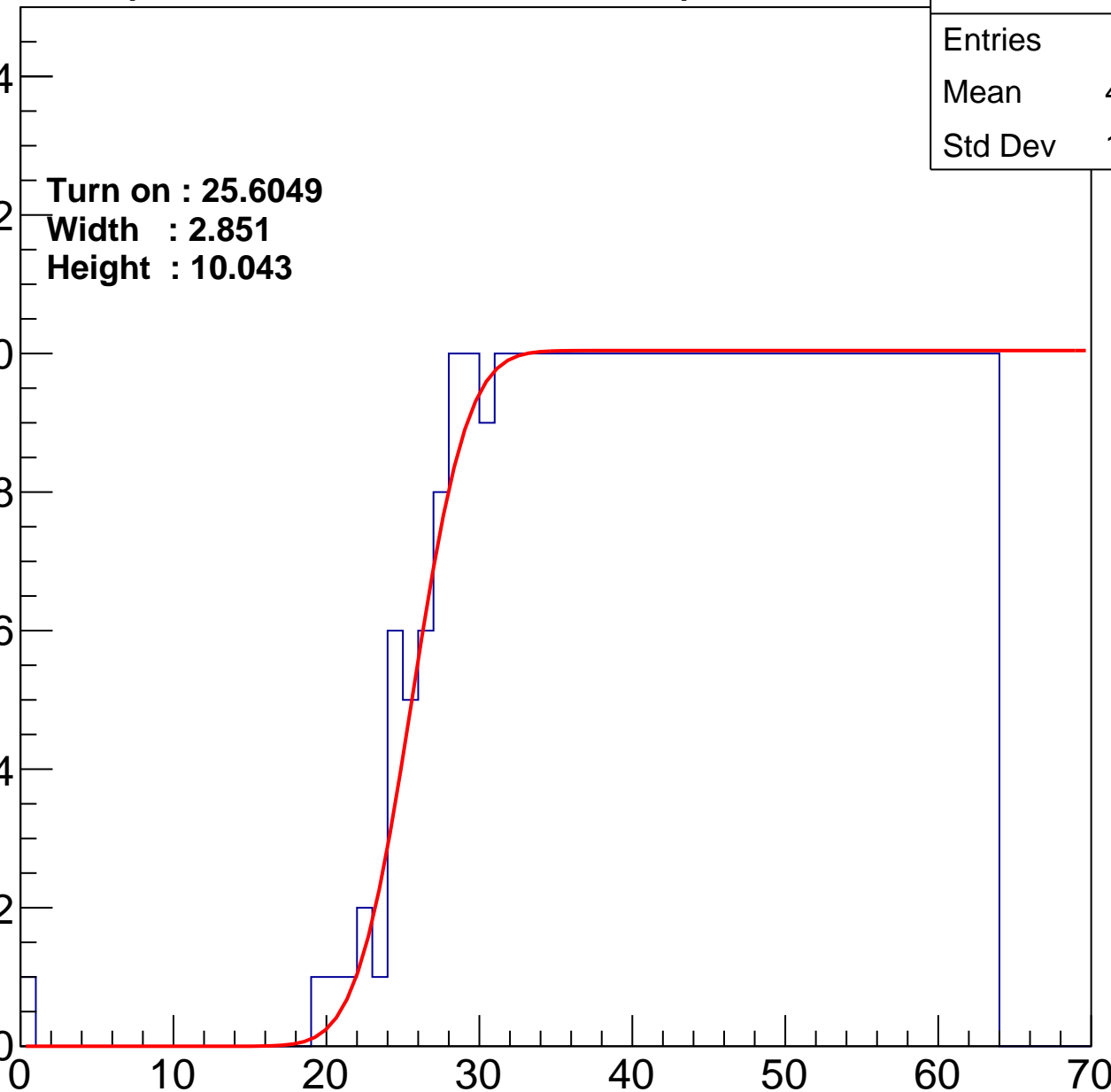
Width : 2.851

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 24.4723

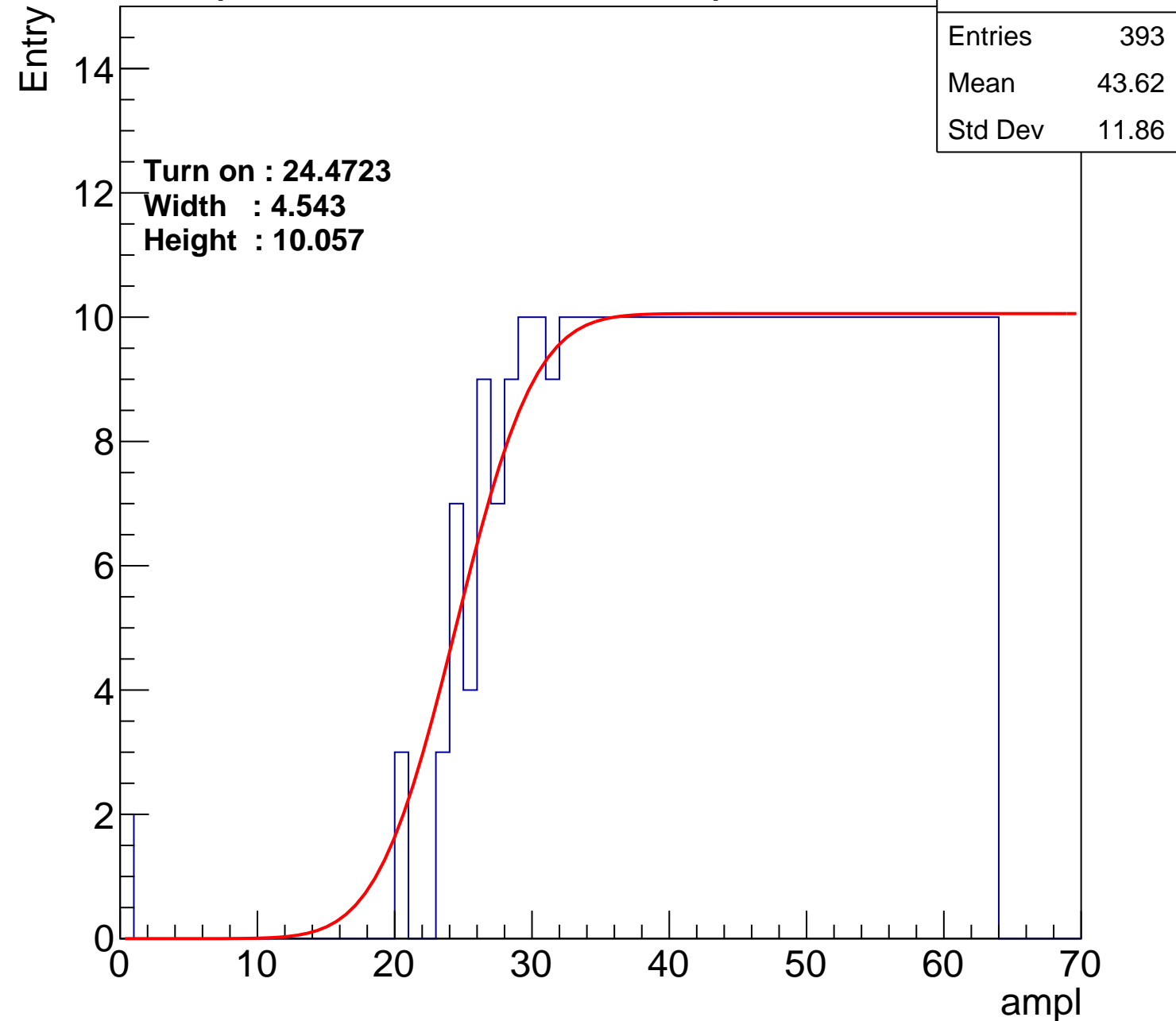
Width : 4.543

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.44
Std Dev	11.99

Turn on : 24.8343

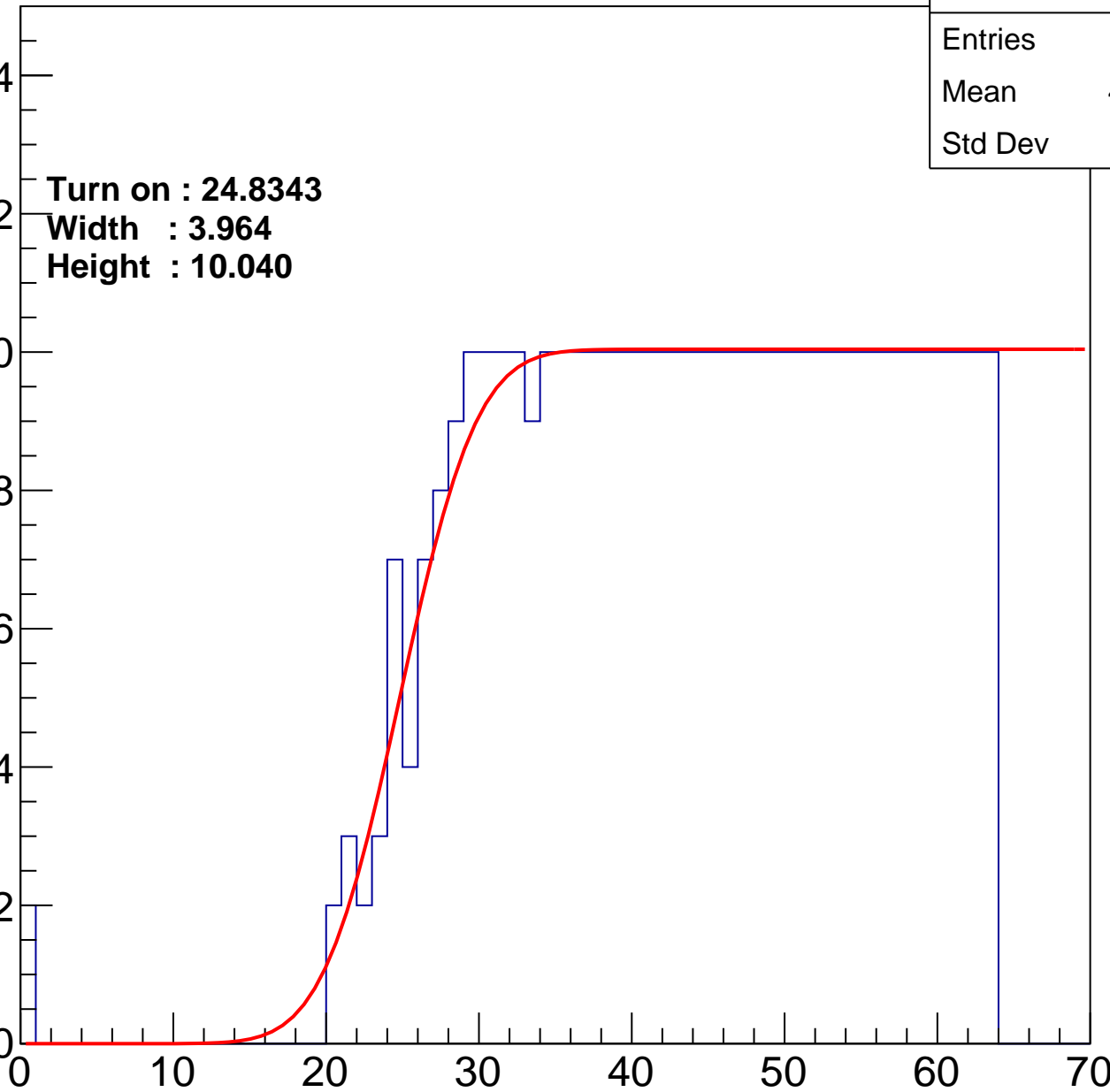
Width : 3.964

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch77

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.79
Std Dev	11.39

Turn on : 28.3857

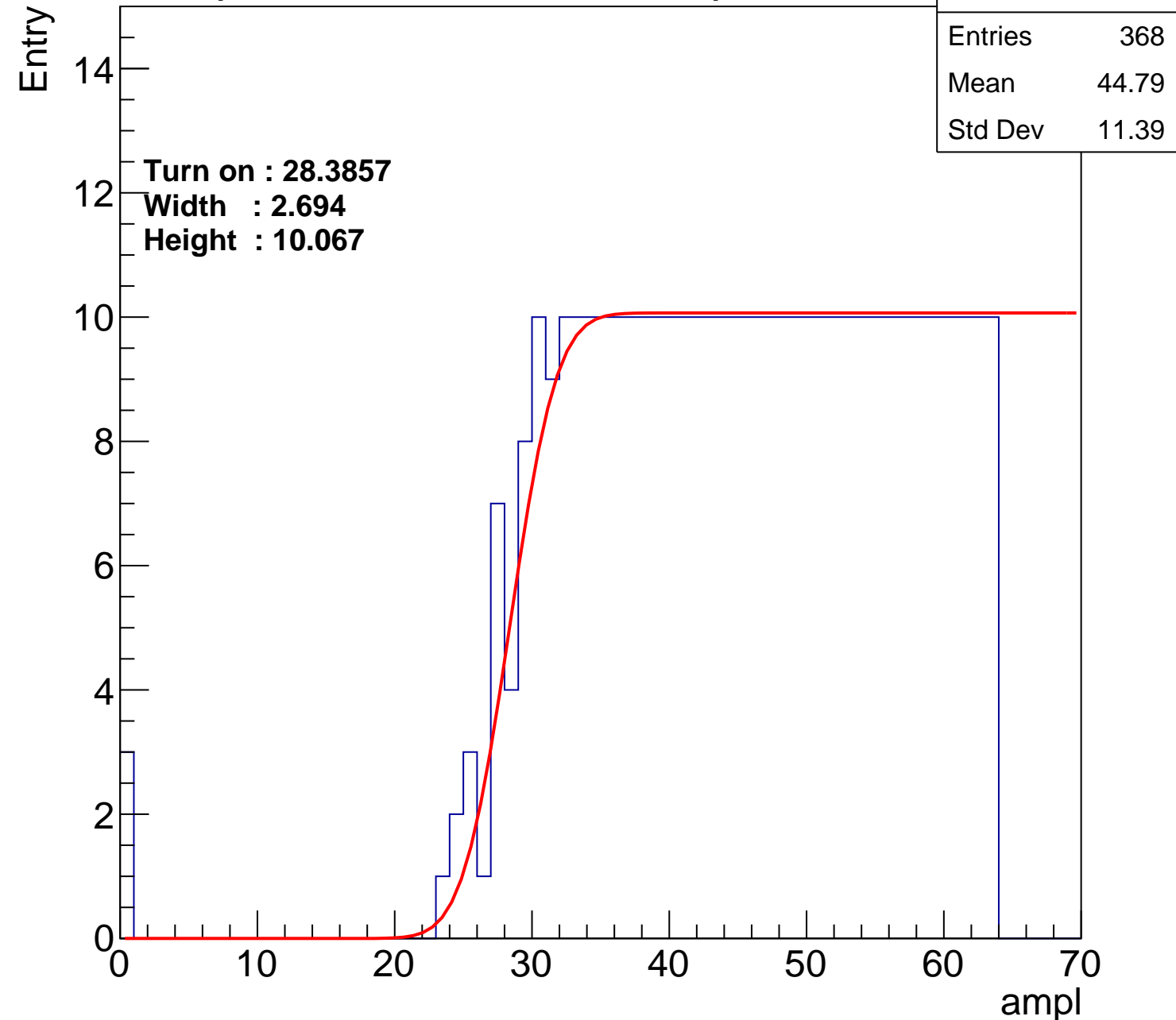
Width : 2.694

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch78

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.02
Std Dev	12.49

Turn on : 23.8831

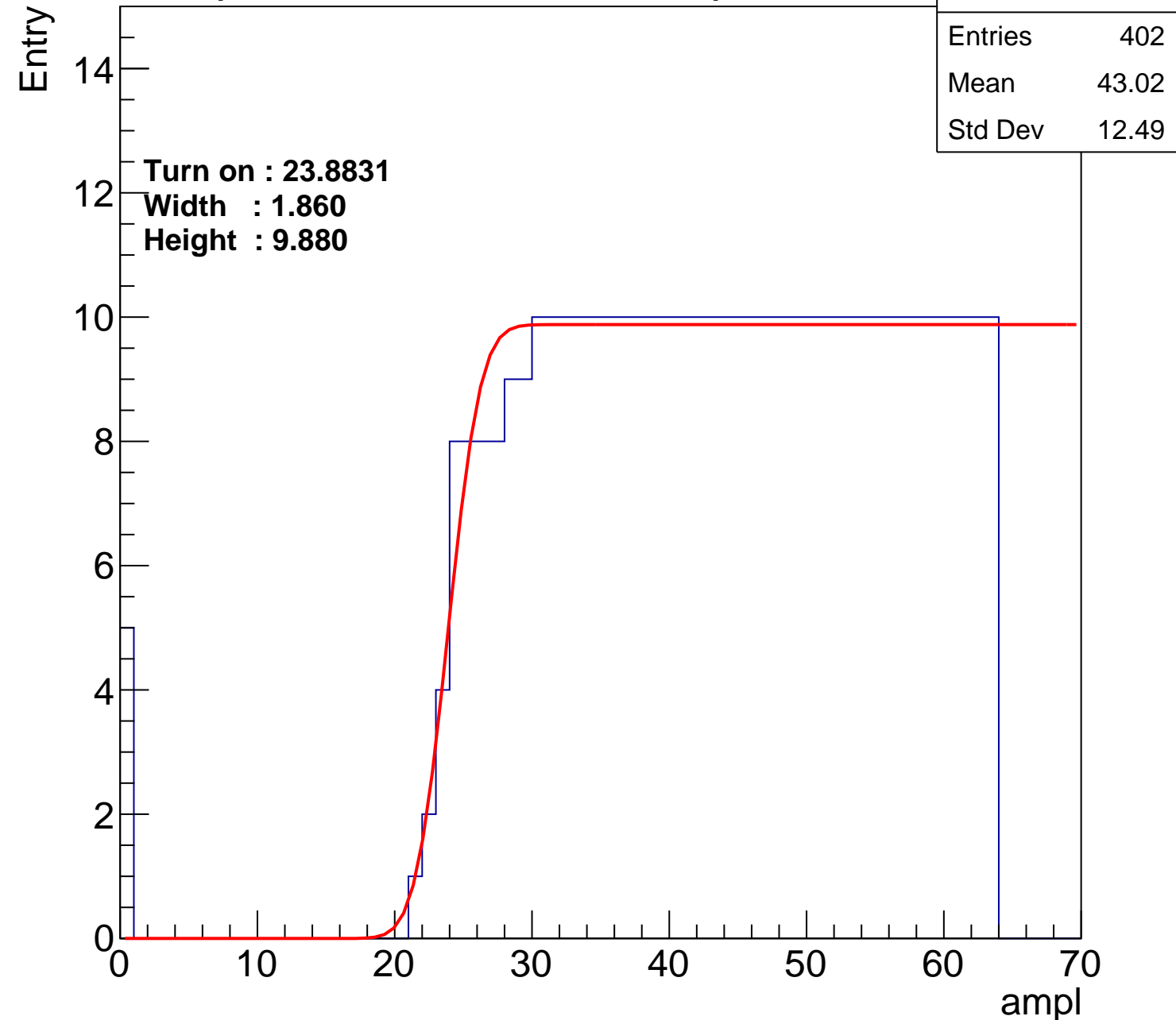
Width : 1.860

Height : 9.880

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.78
Std Dev	12.05

Turn on : 26.1185

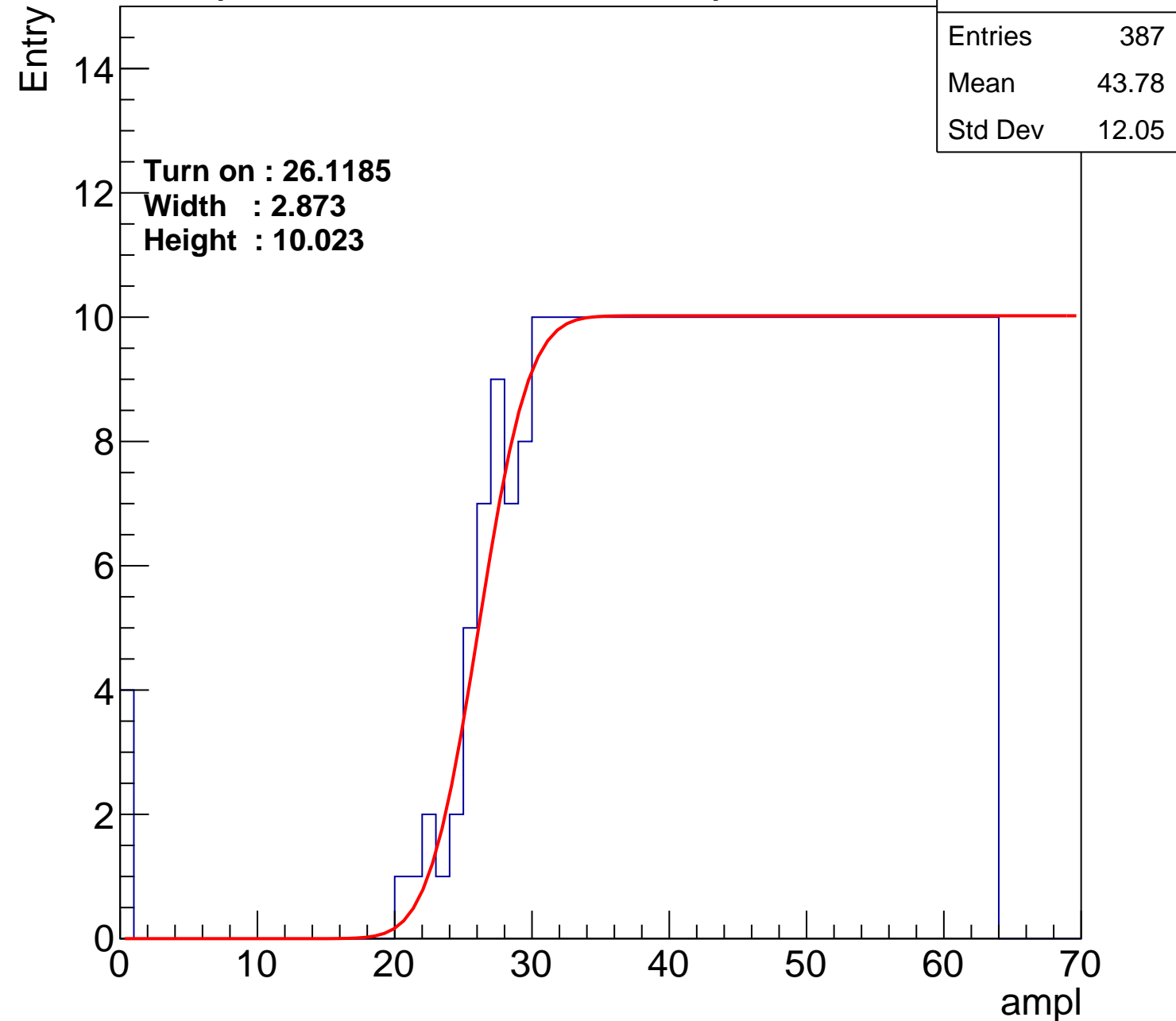
Width : 2.873

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.9
Std Dev	11.6

Turn on : 26.1603

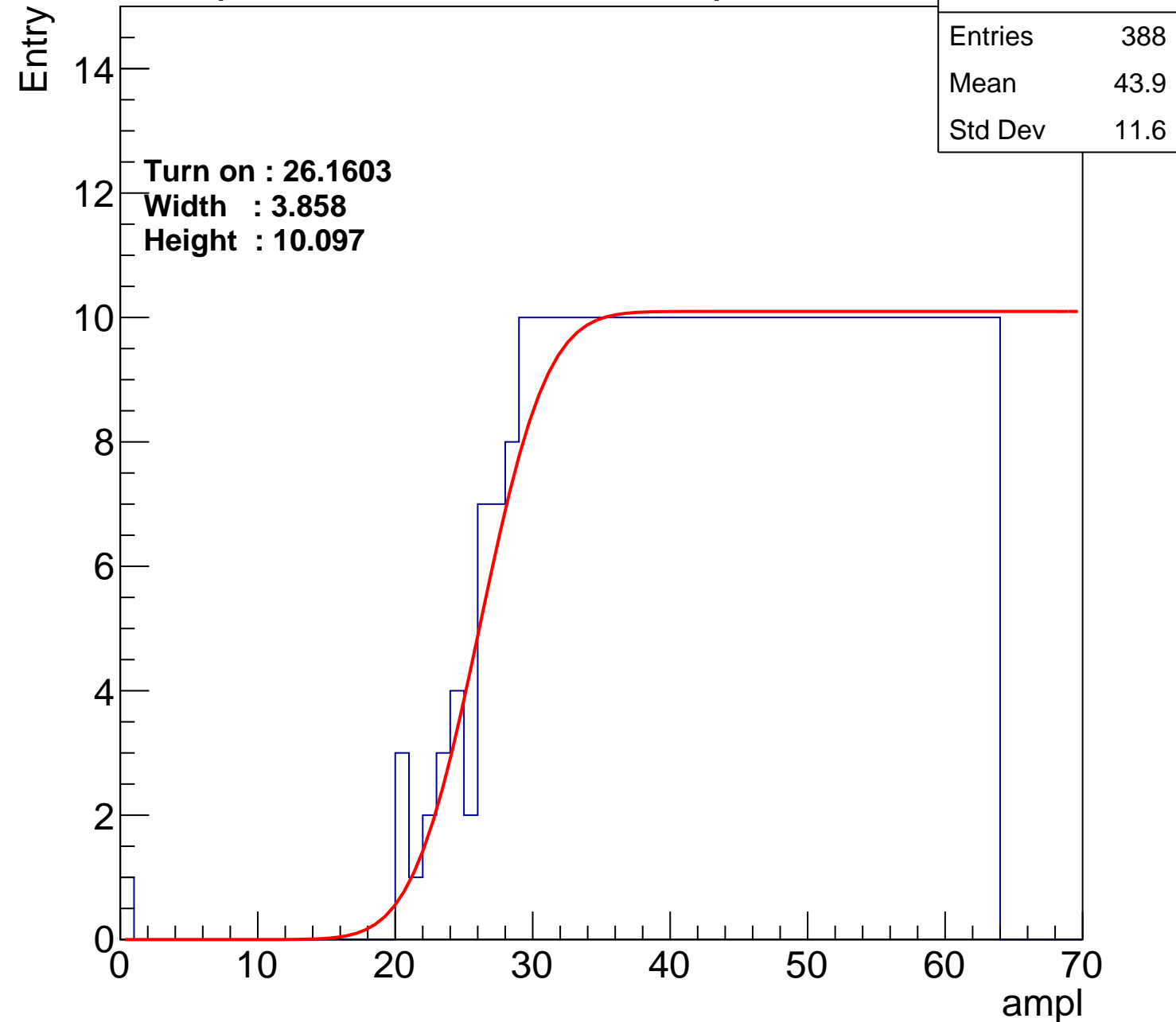
Width : 3.858

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch81

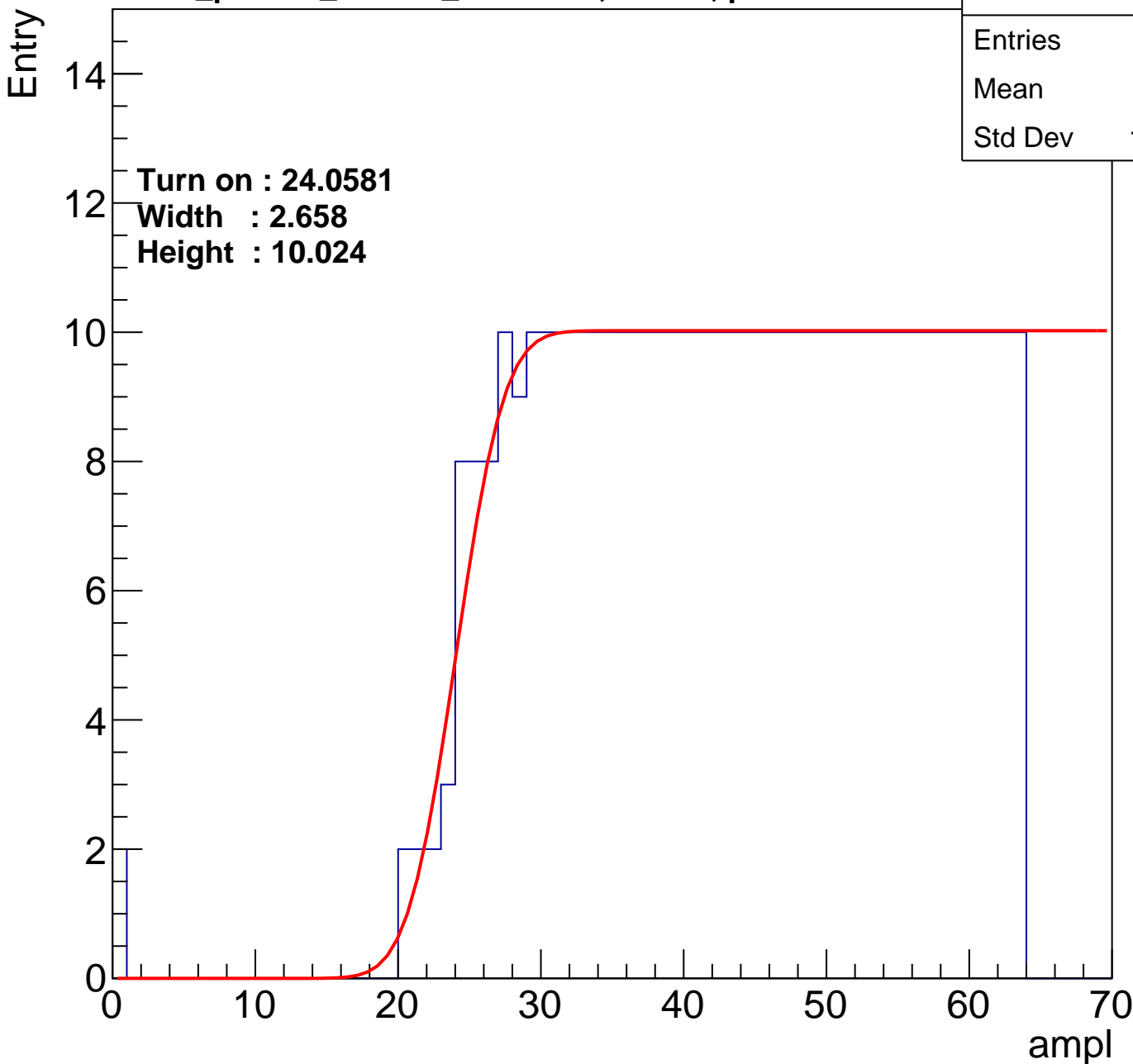
calib_packv5_042523_0143.root, FC#12, port B1

Entries	404
Mean	43.11
Std Dev	12.09

Turn on : 24.0581

Width : 2.658

Height : 10.024



B0L102S, U3-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.71
Std Dev	12.36

Turn on : 23.5602

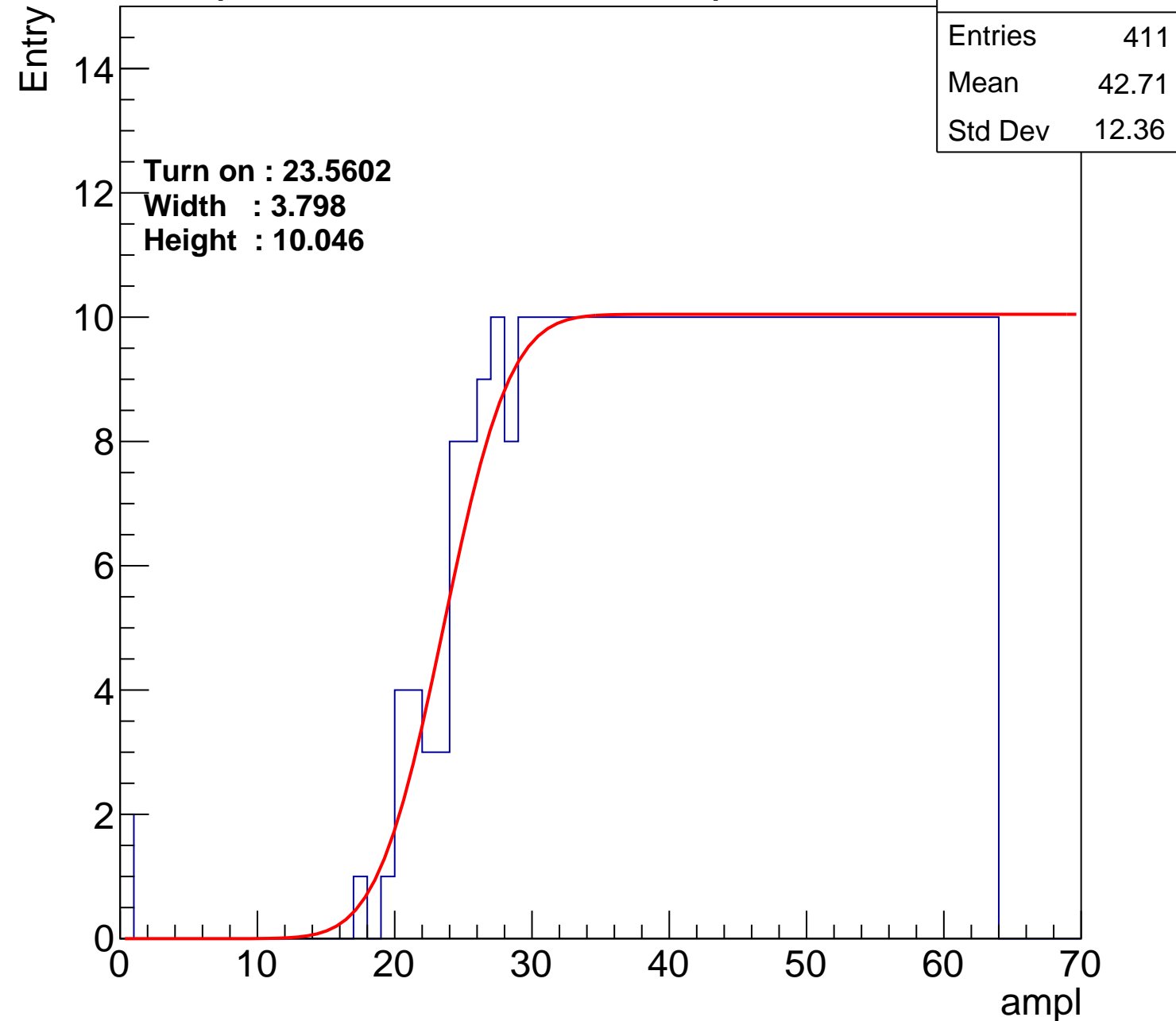
Width : 3.798

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.73
Std Dev	11.78

Turn on : 25.3153

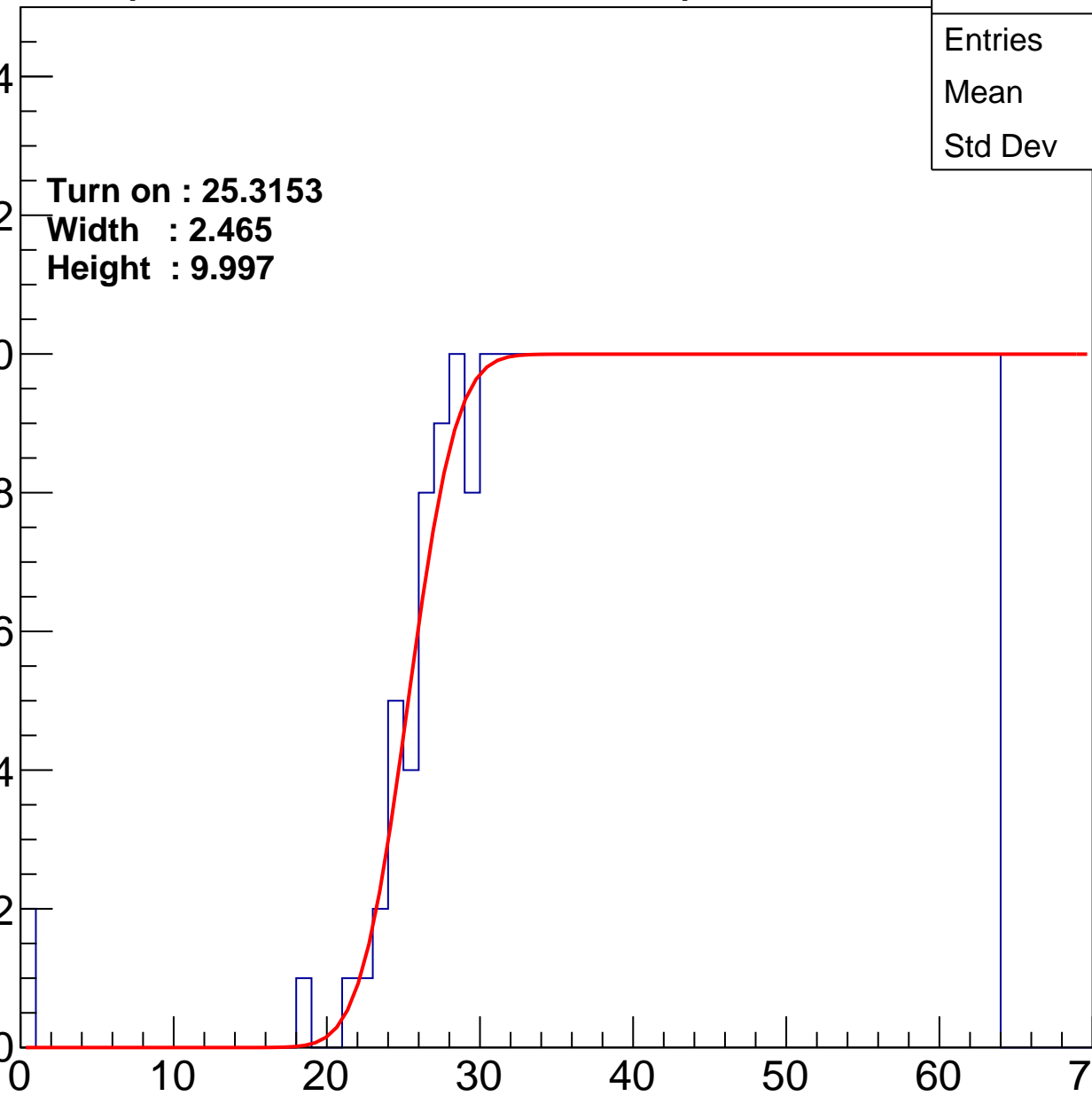
Width : 2.465

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.7
Std Dev	12.24

Turn on : 25.8309

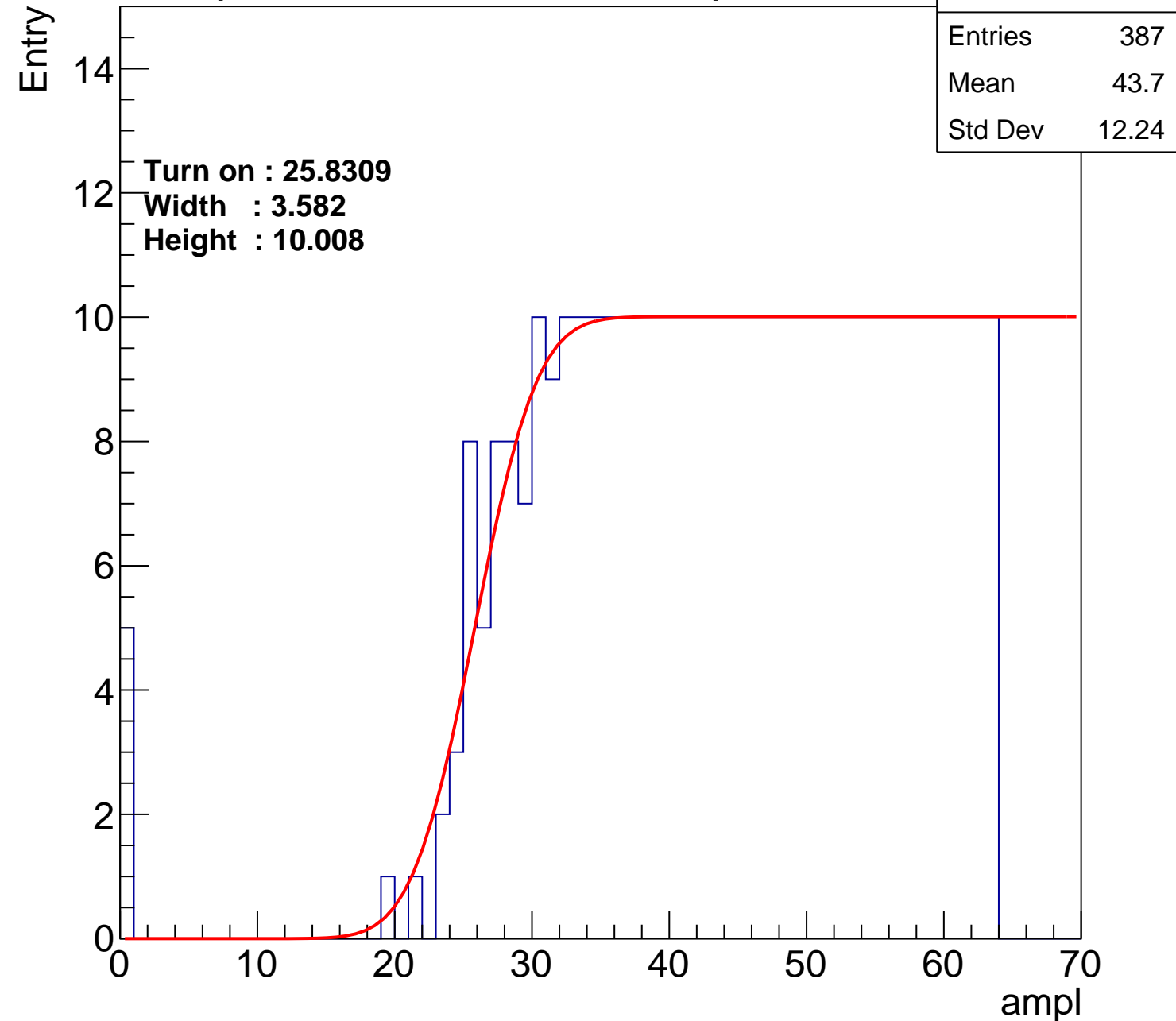
Width : 3.582

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.37
Std Dev	12.43

Turn on : 25.4463

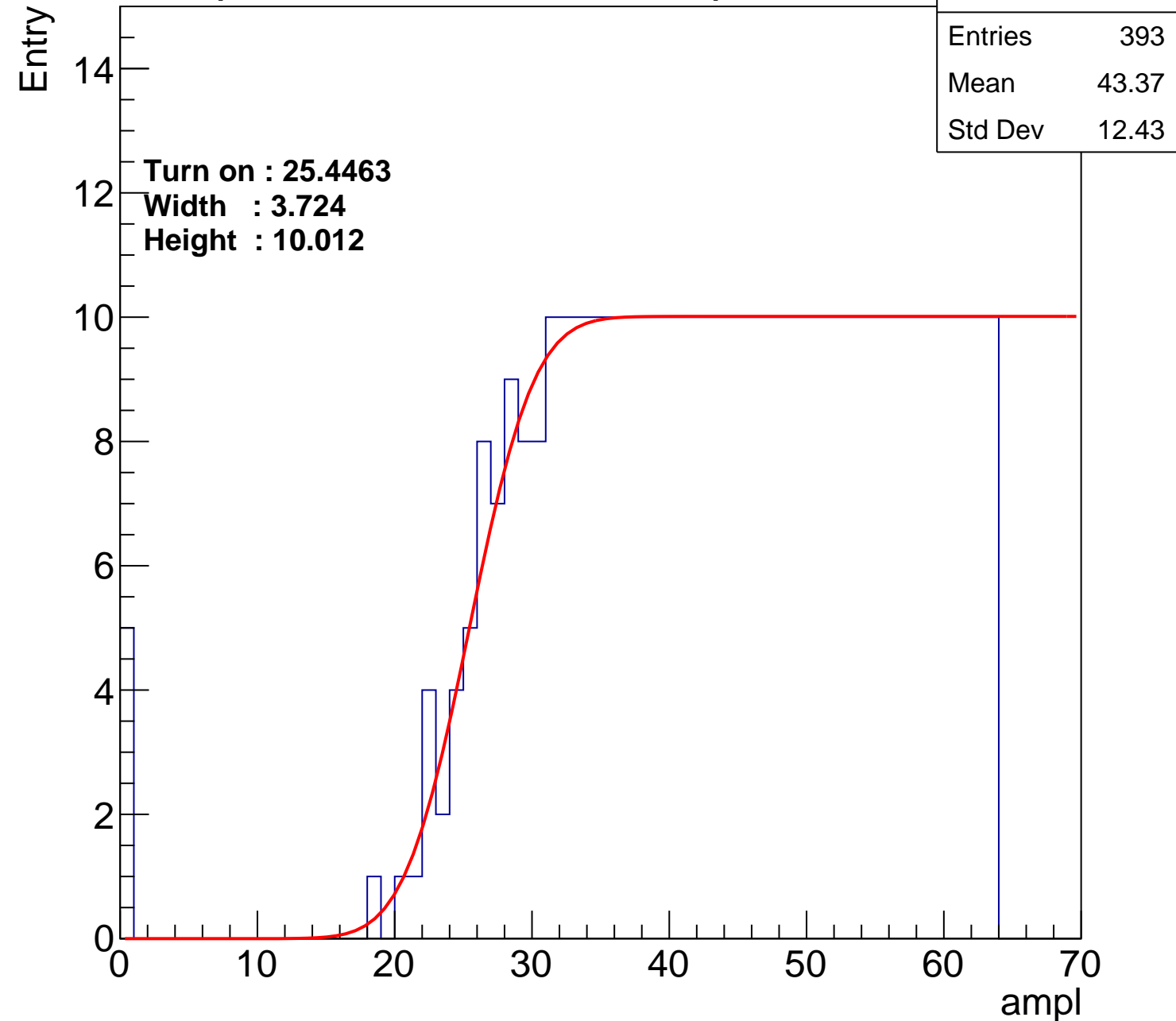
Width : 3.724

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch86

calib_packv5_042523_0143.root, FC#12, port B1

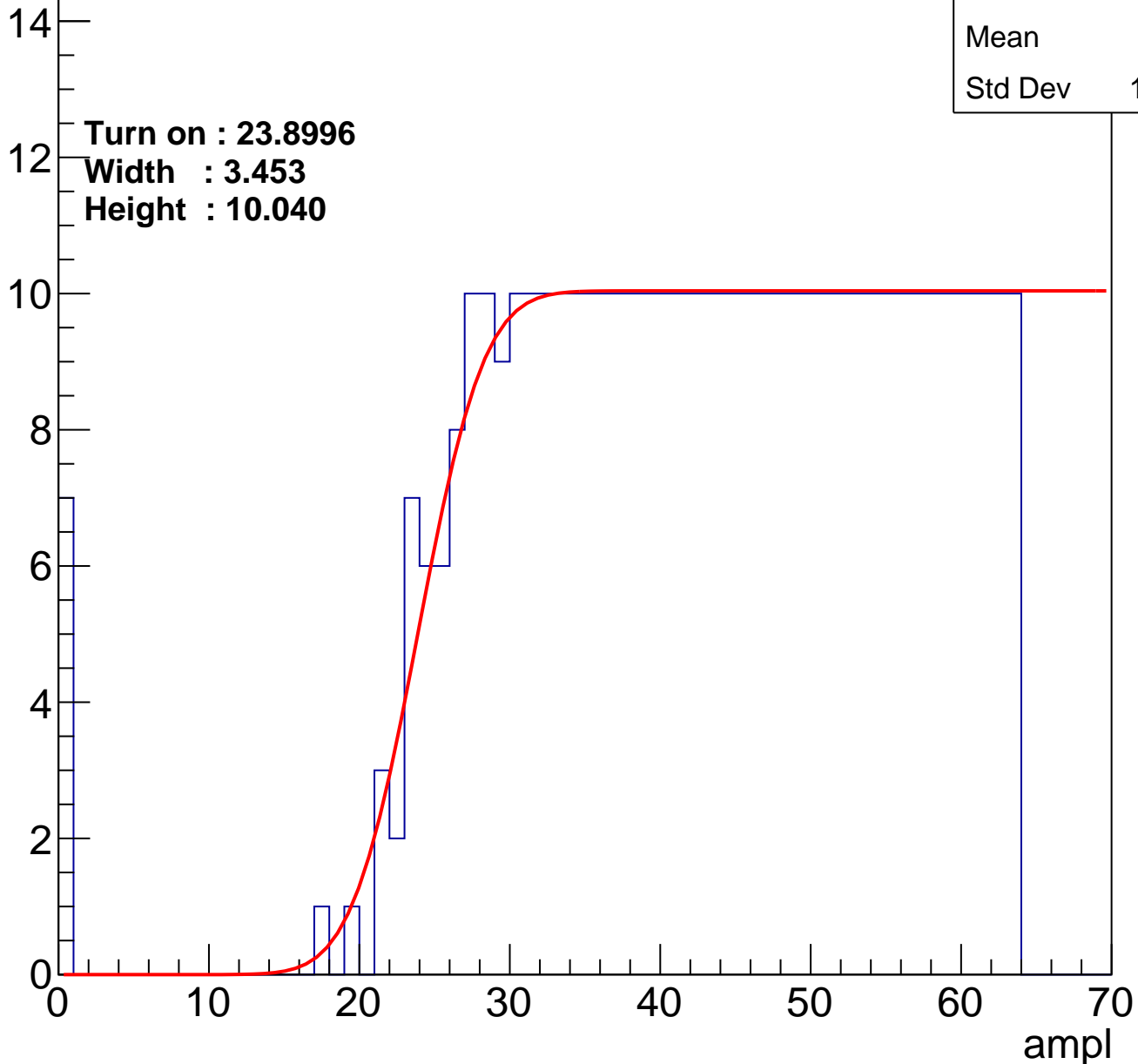
Entries	410
Mean	42.5
Std Dev	12.98

Turn on : 23.8996

Width : 3.453

Height : 10.040

Entry



B0L102S, U3-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.85
Std Dev	11.75

Turn on : 25.6232

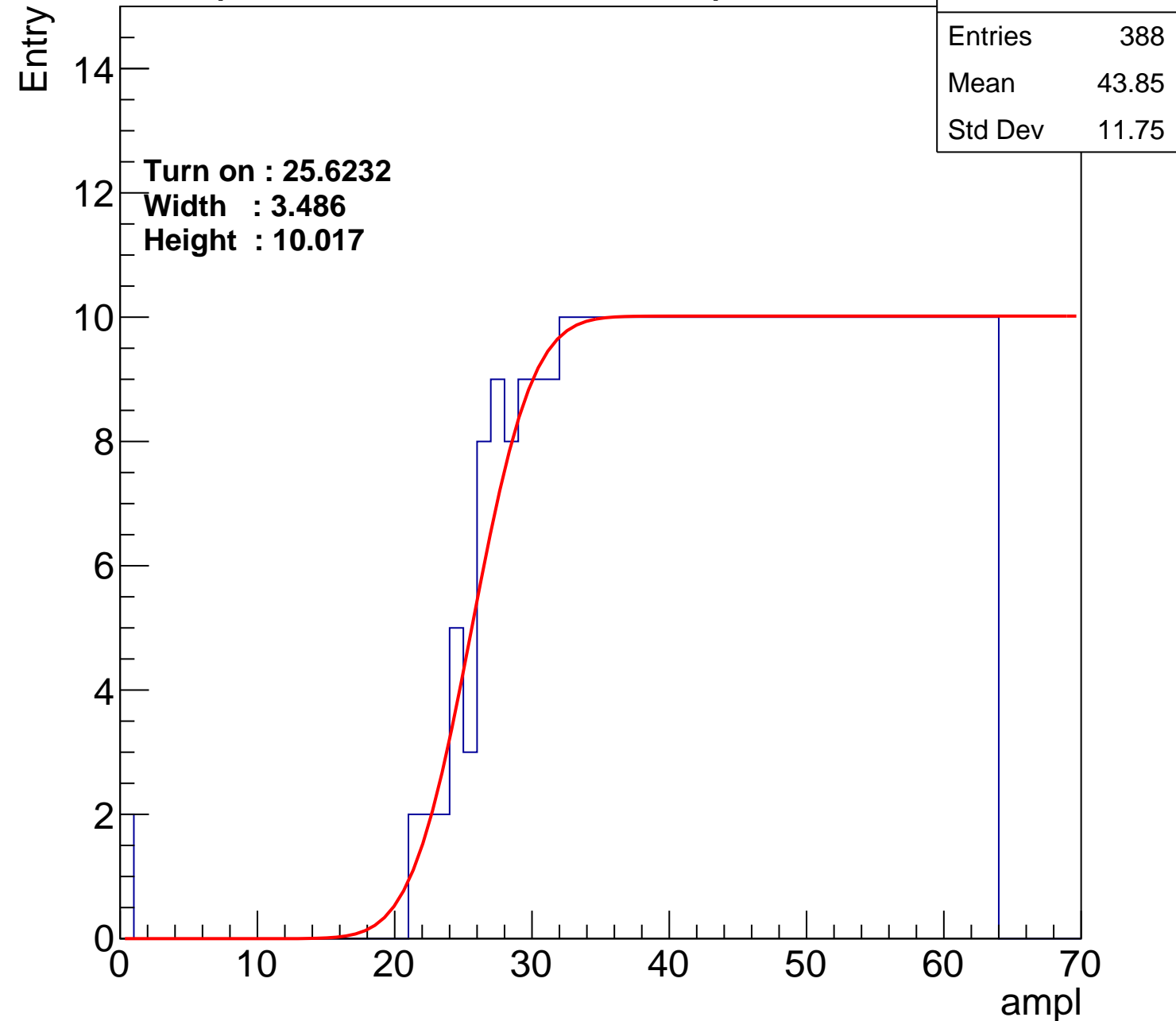
Width : 3.486

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch88

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.16
Std Dev	12.32

Turn on : 24.4916

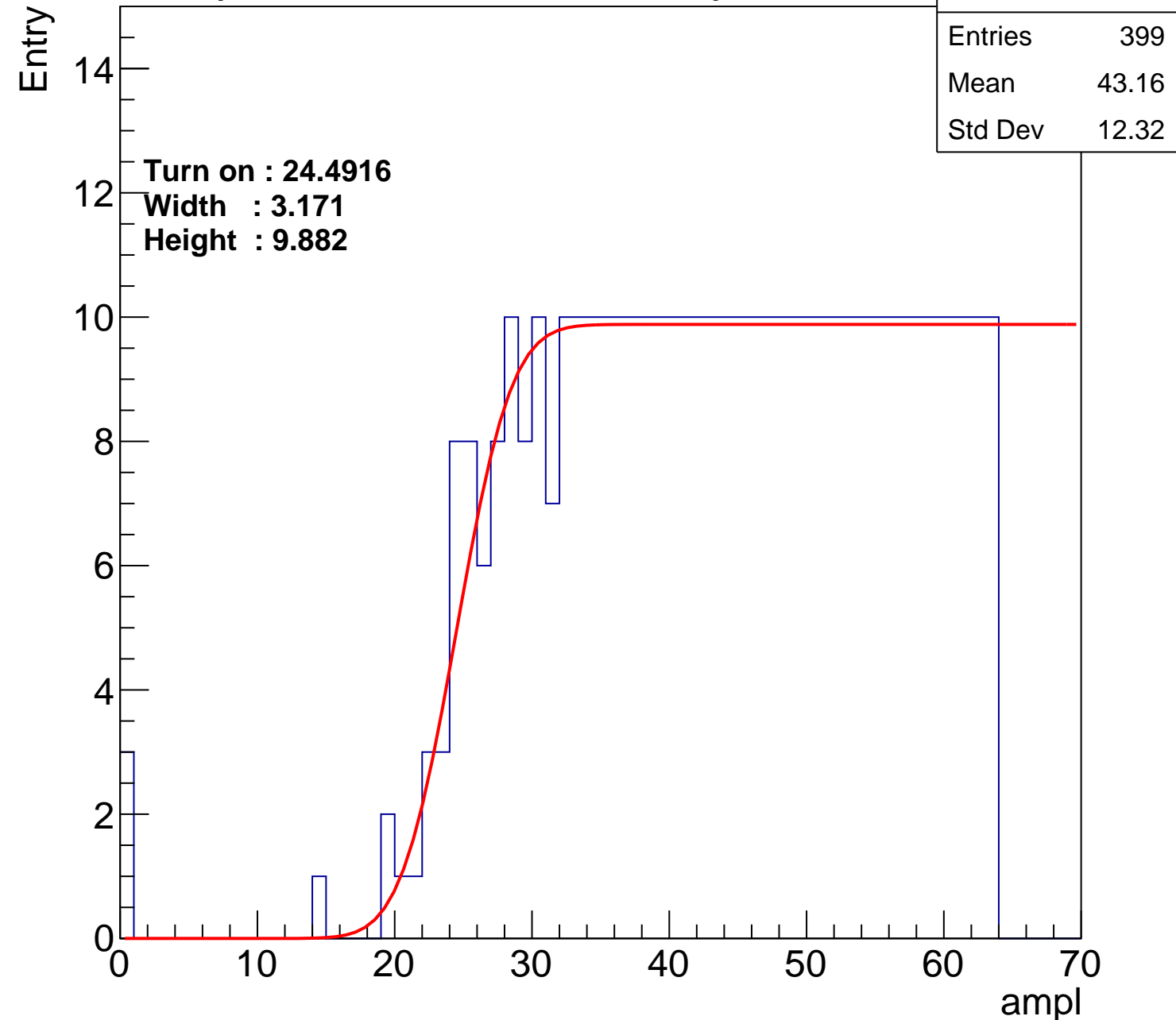
Width : 3.171

Height : 9.882

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.96
Std Dev	11.74

Turn on : 25.8319

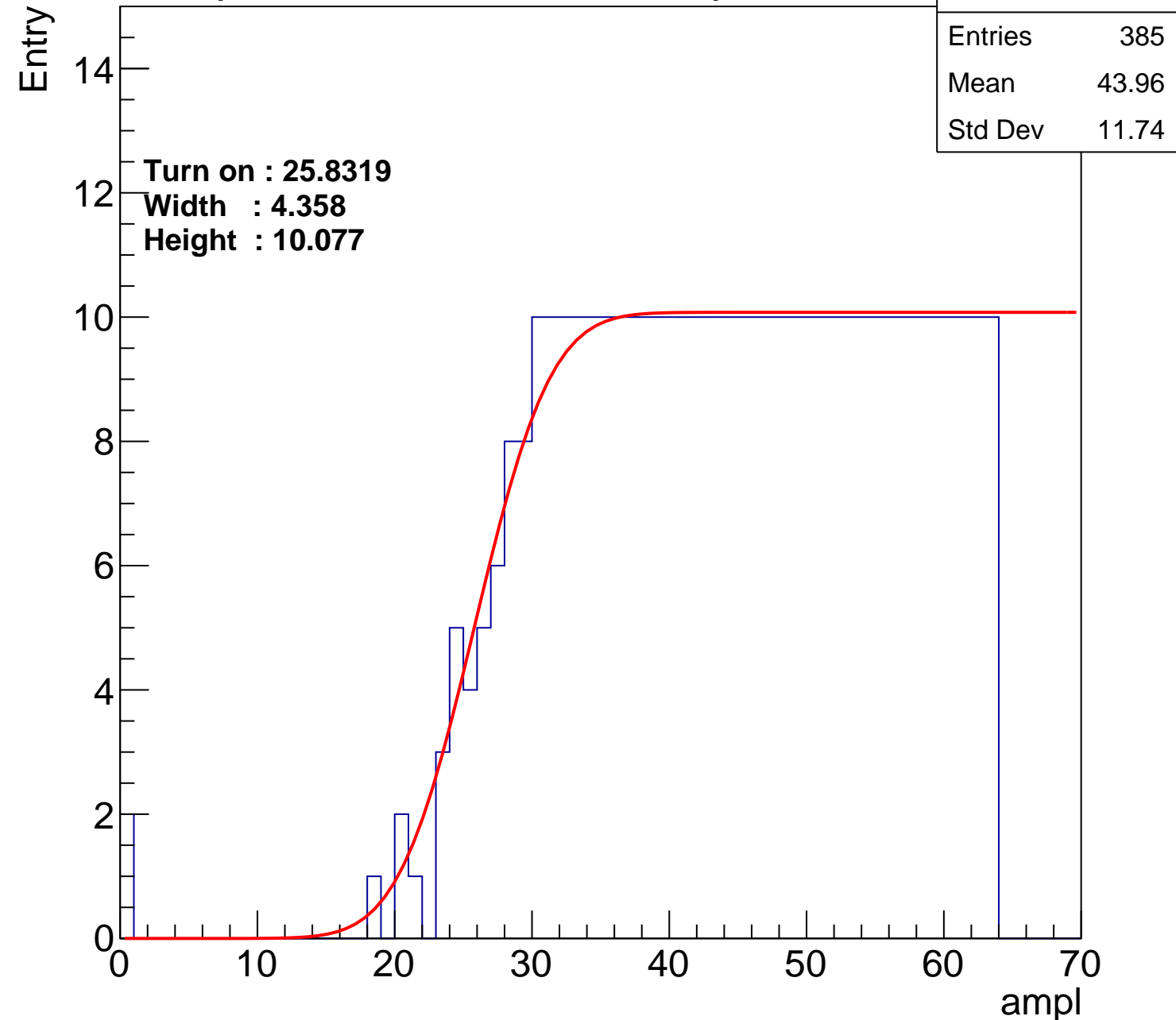
Width : 4.358

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.46
Std Dev	12.12

Turn on : 25.2851

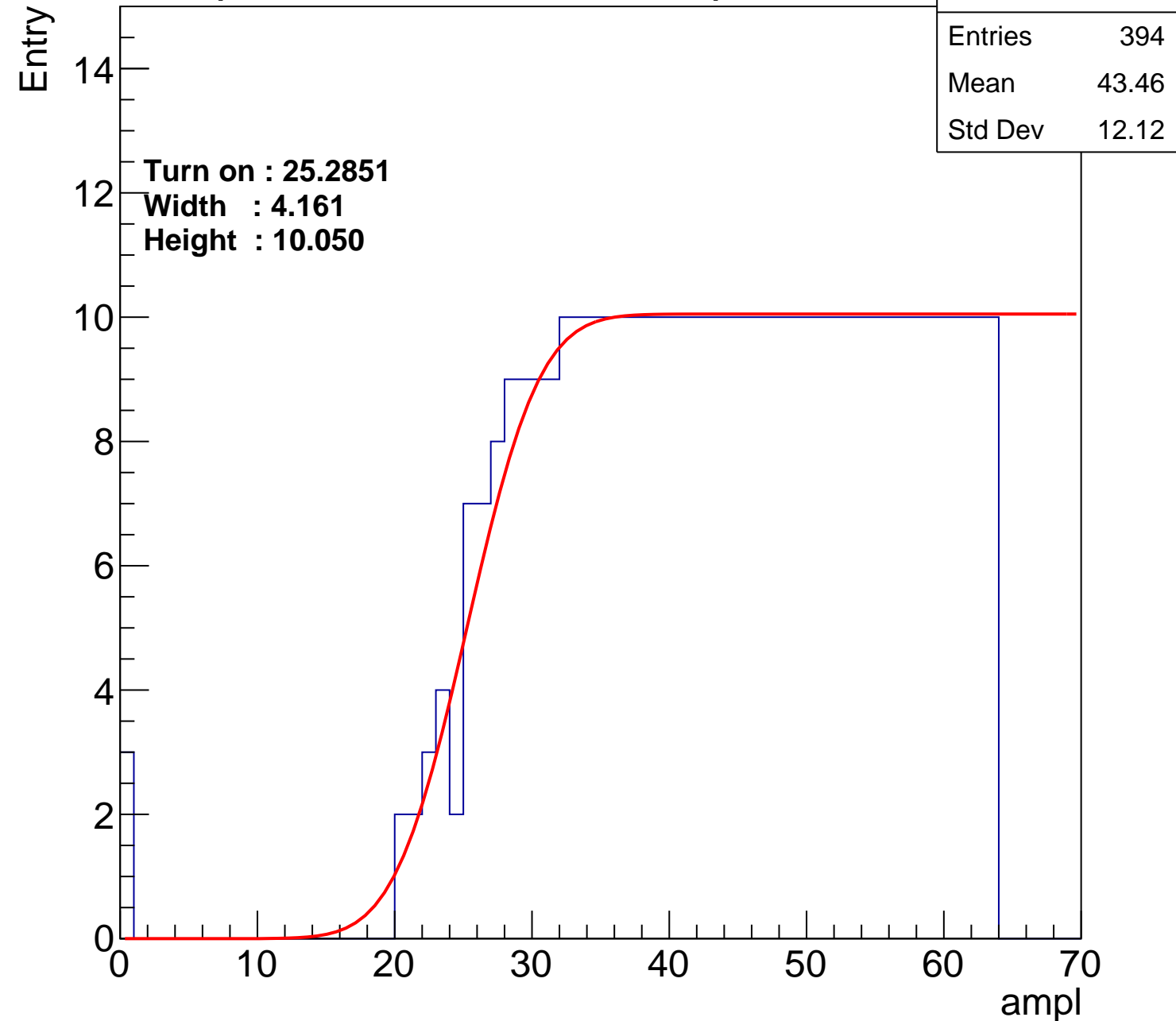
Width : 4.161

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.48
Std Dev	12.08

Turn on : 24.8973

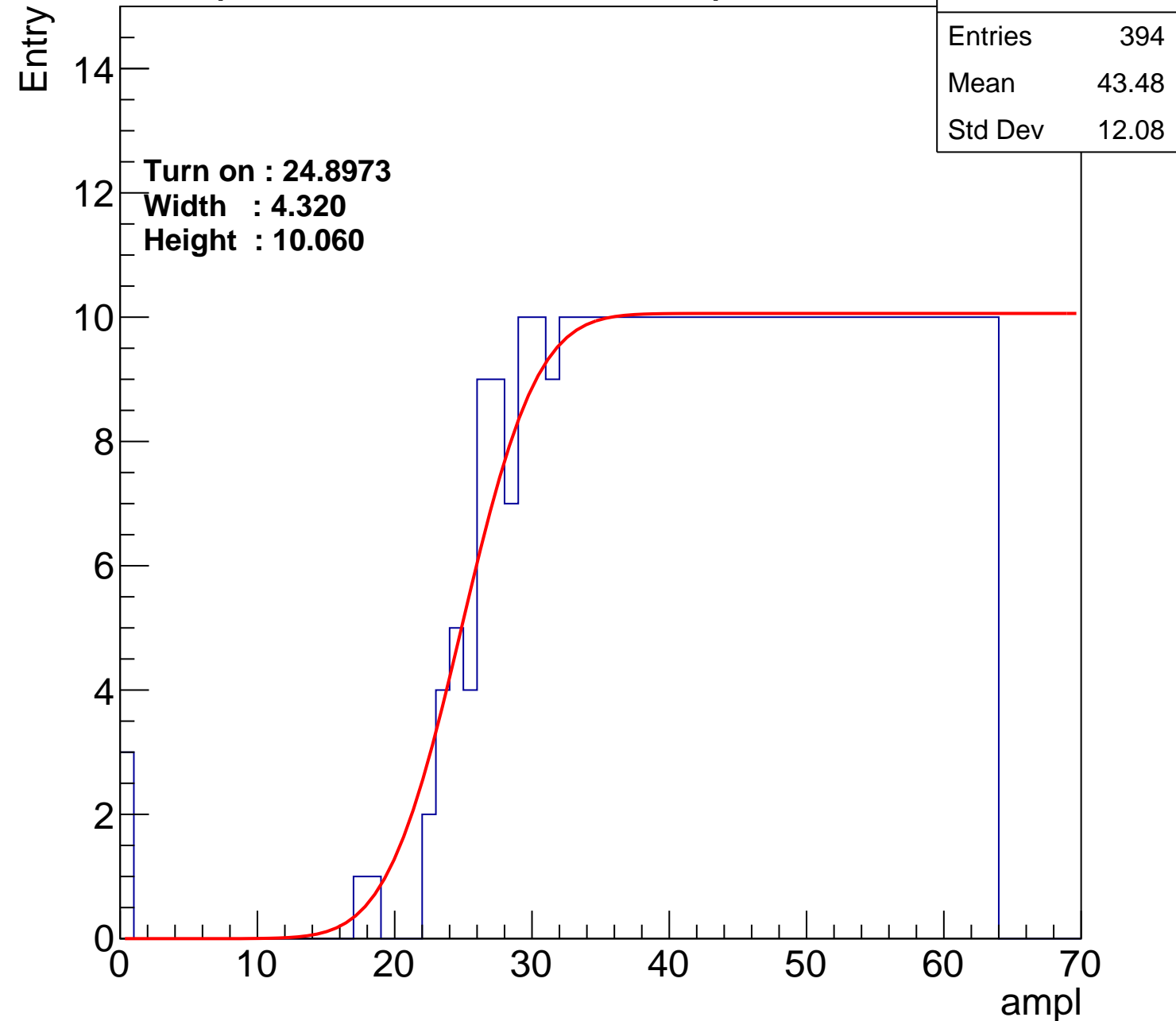
Width : 4.320

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.86
Std Dev	12.29

Turn on : 23.4454

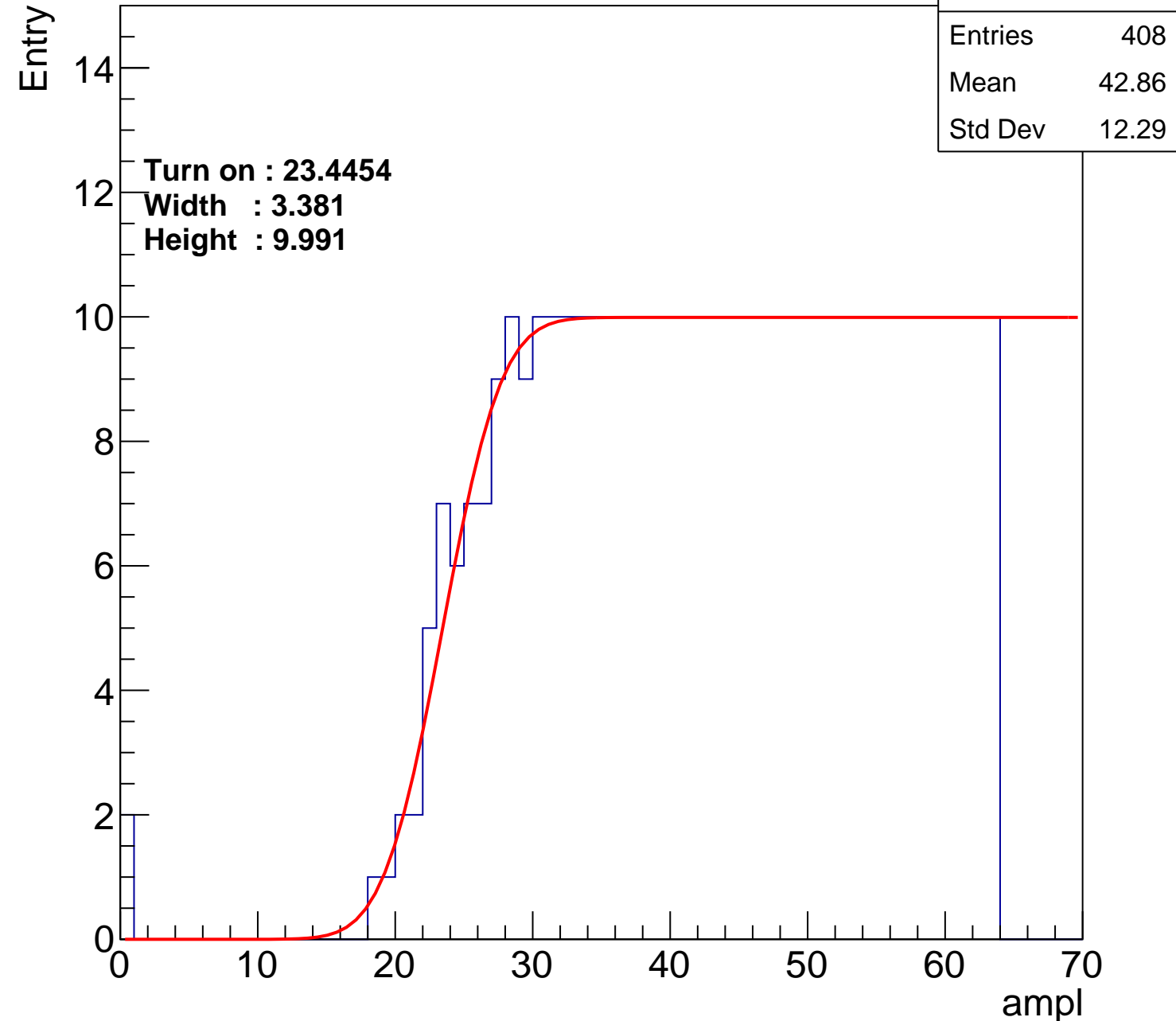
Width : 3.381

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch93

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 24.9053

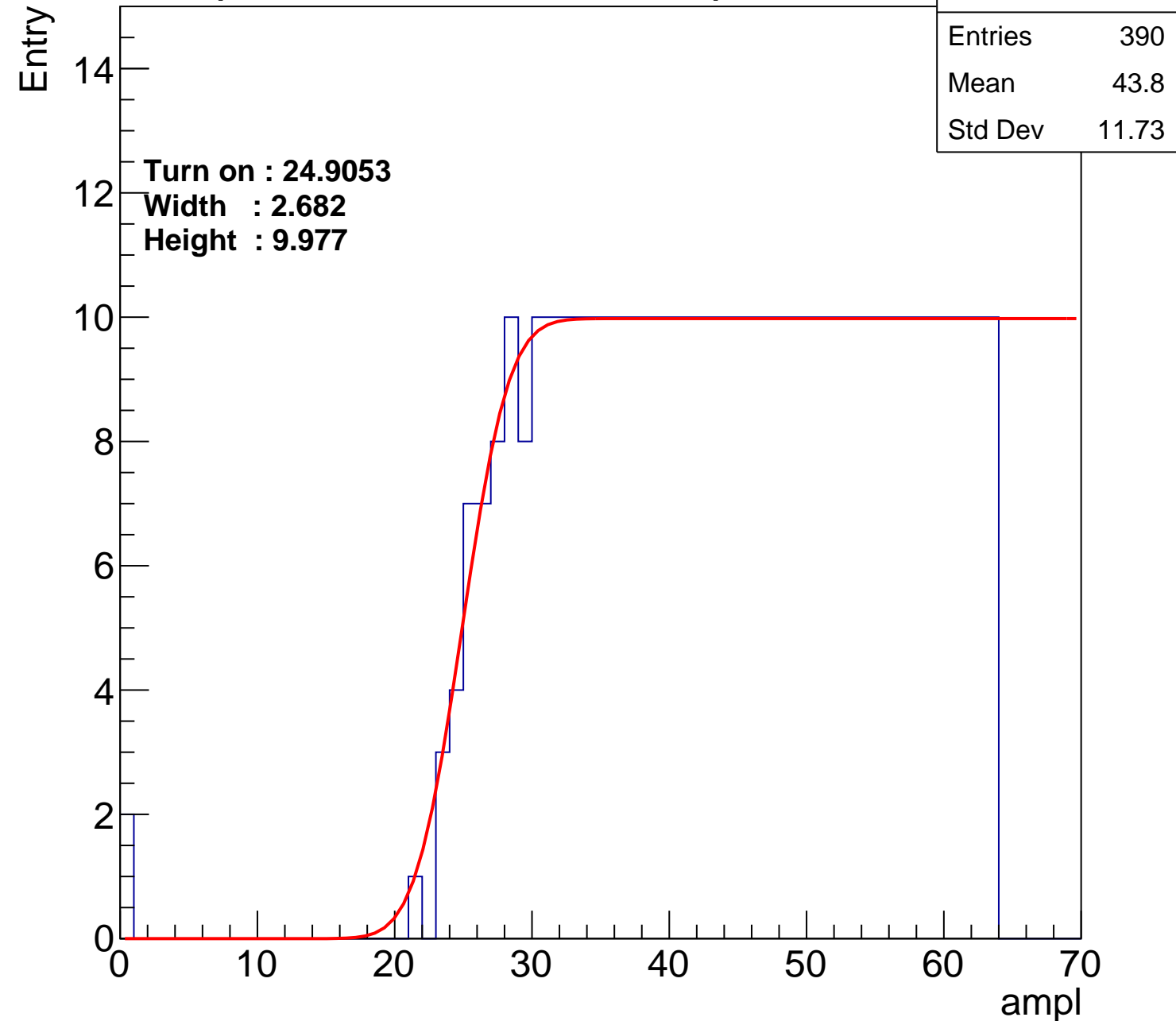
Width : 2.682

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.41
Std Dev	13.03

Turn on : 23.3289

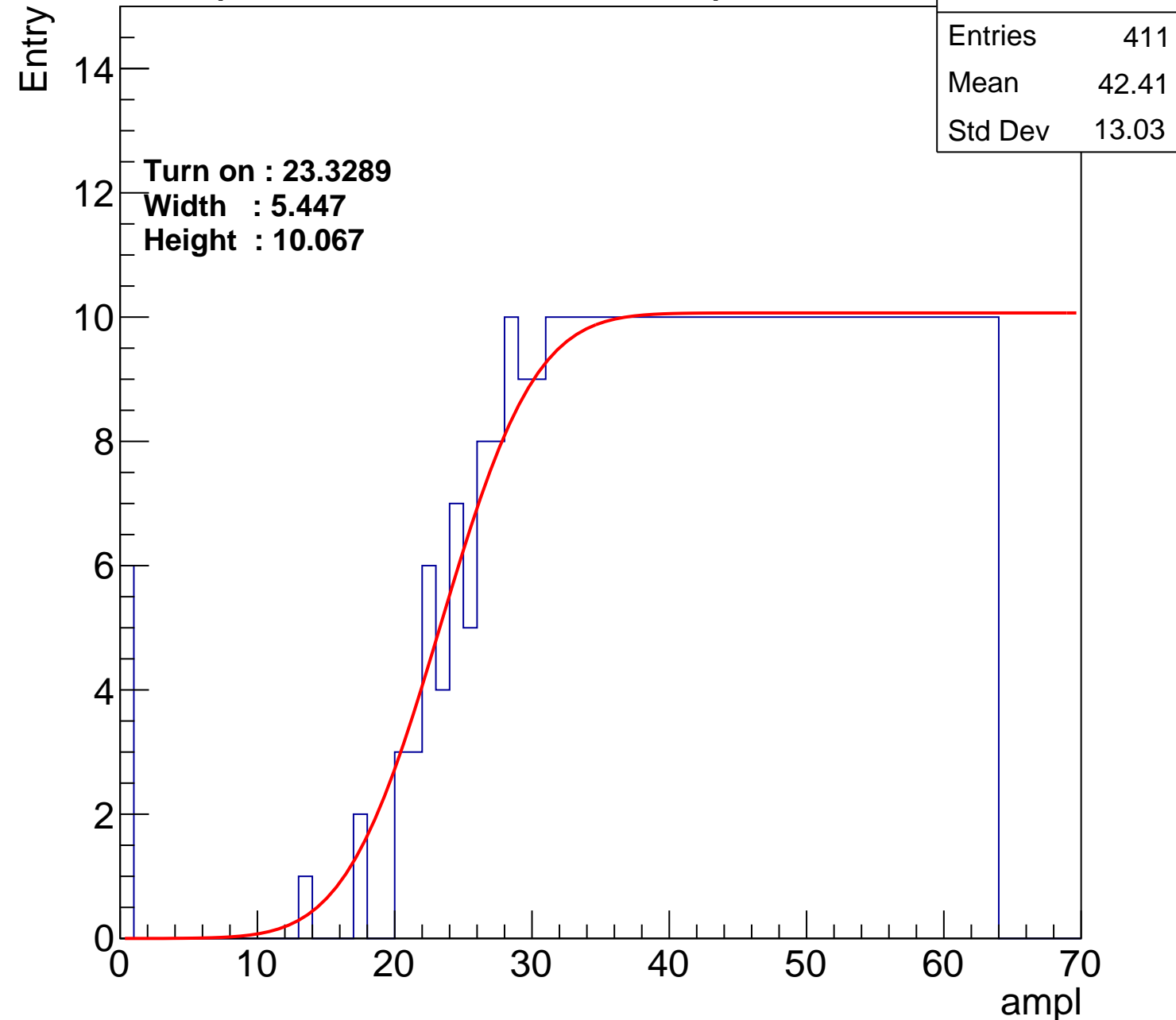
Width : 5.447

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.74
Std Dev	12.06

Turn on : 25.7120

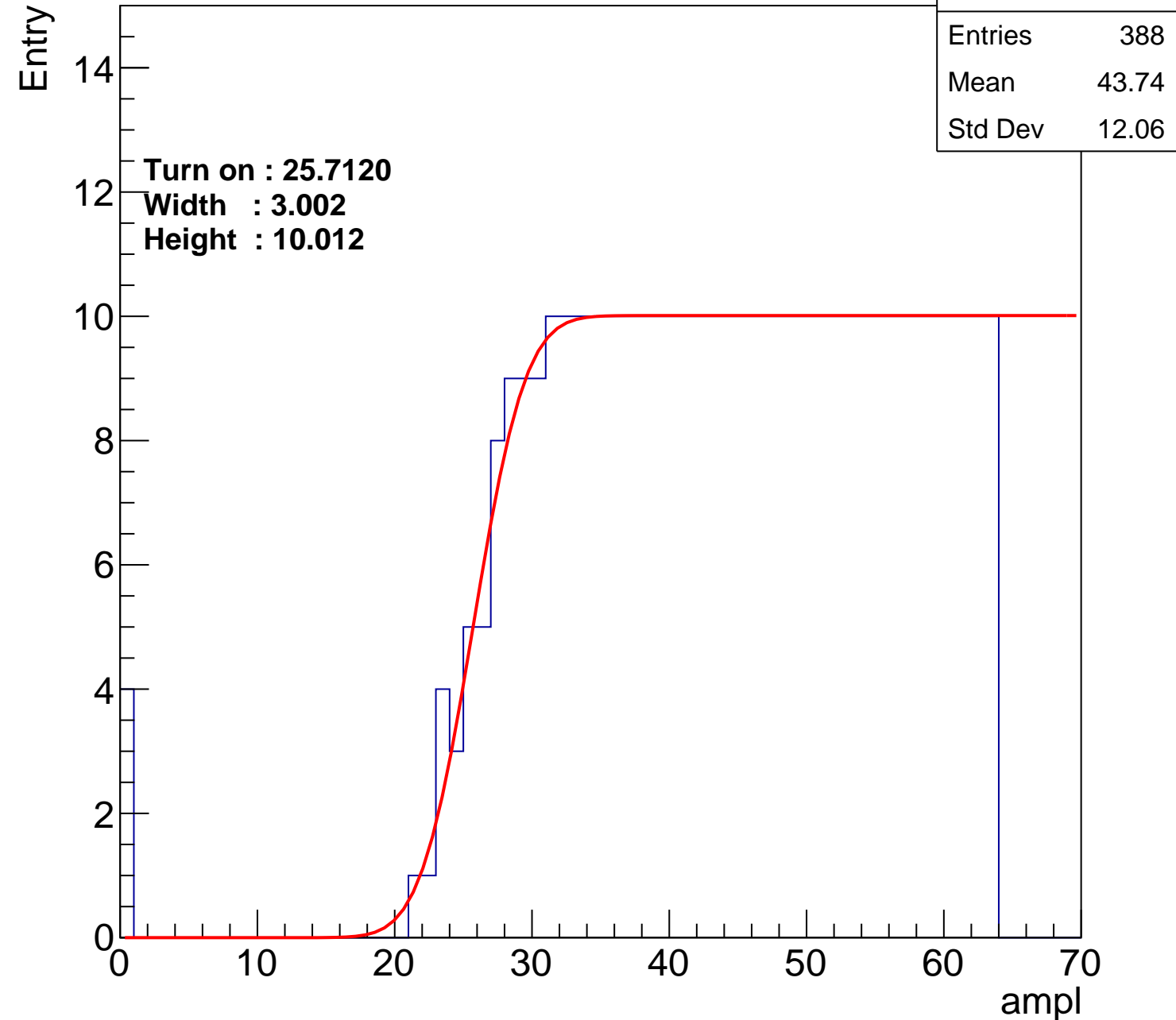
Width : 3.002

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.07
Std Dev	12.42

Turn on : 24.5934

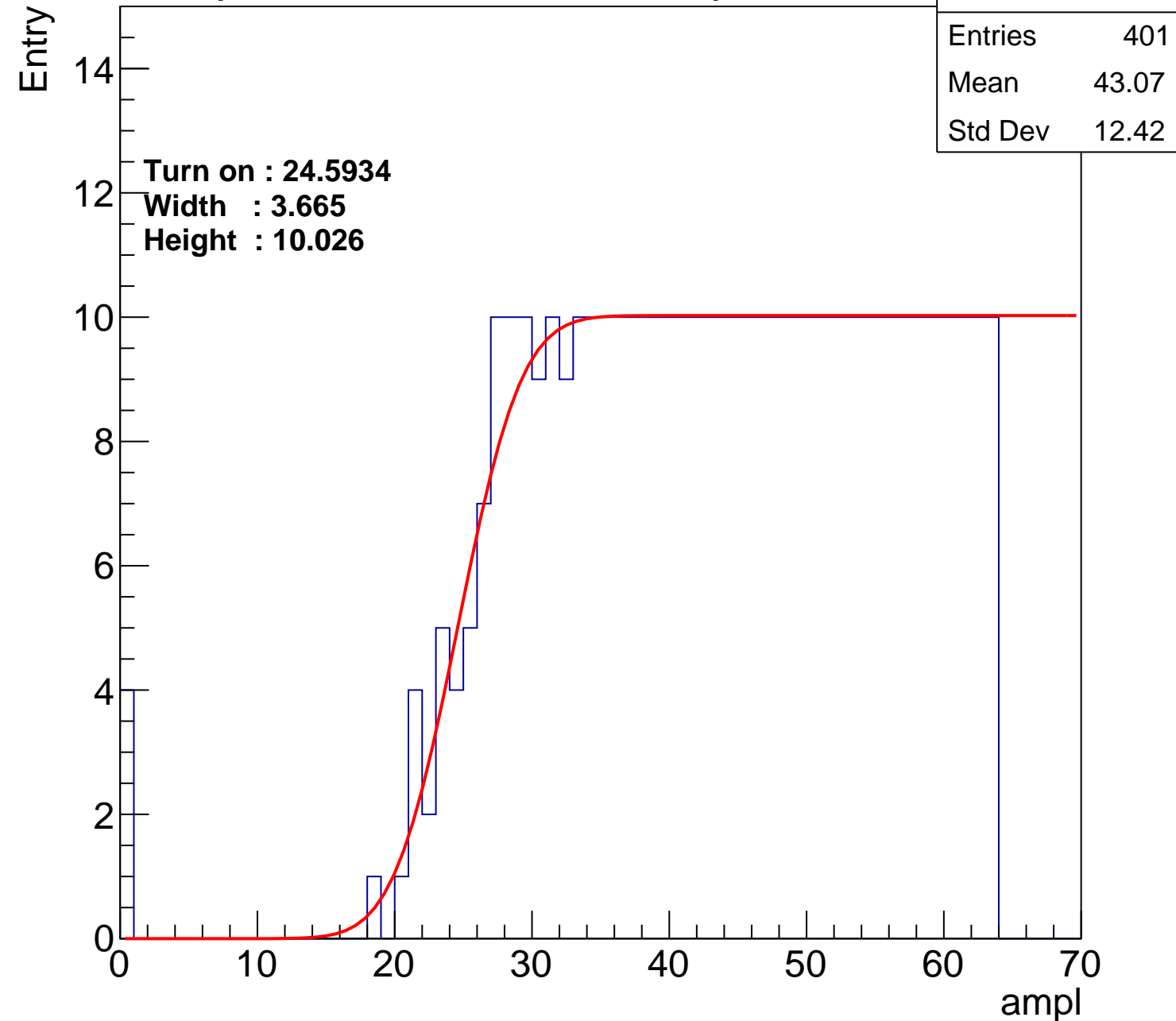
Width : 3.665

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	44.9
Std Dev	11.44

Turn on : 28.6007

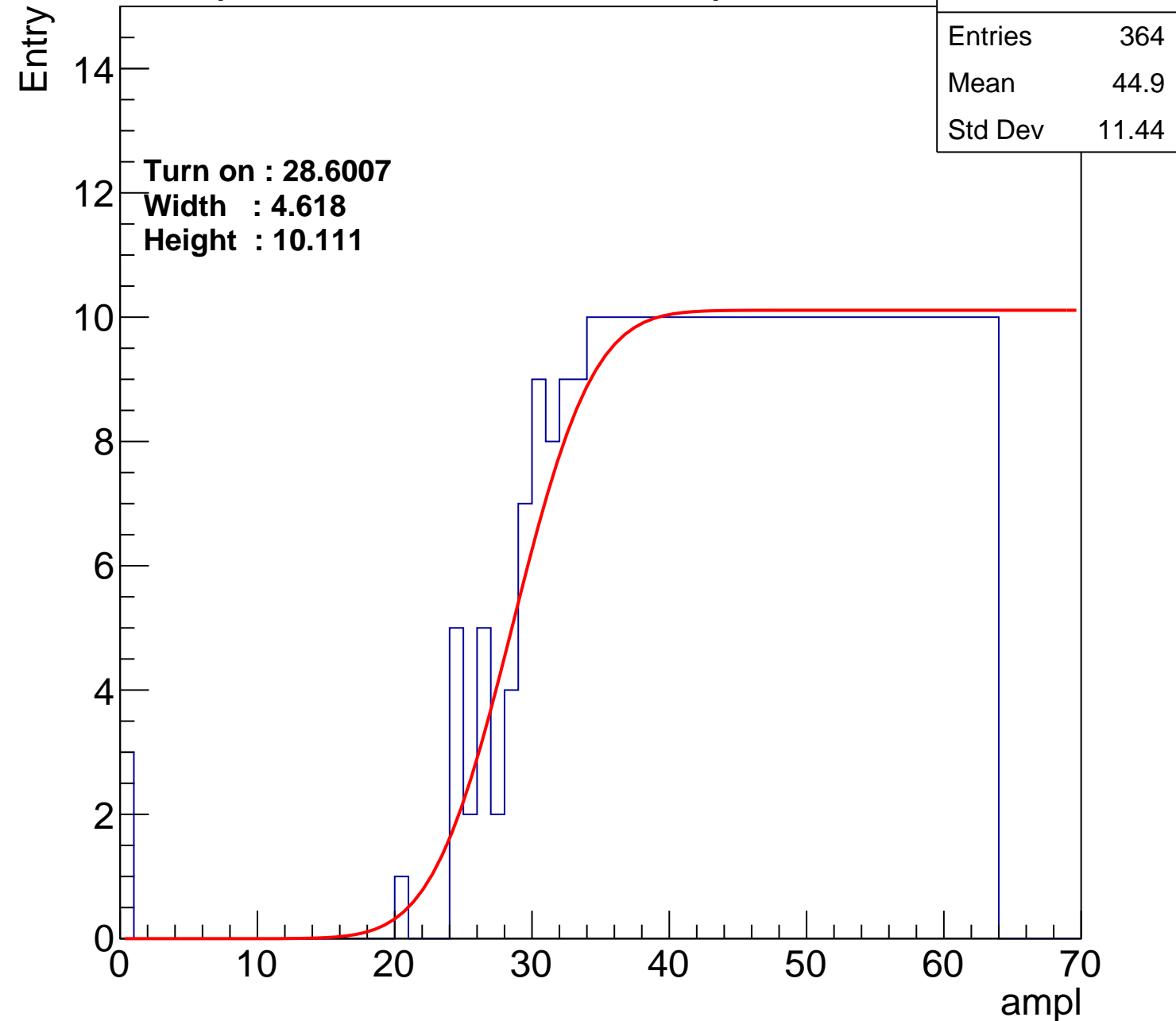
Width : 4.618

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	411
Mean	42.53
Std Dev	12.81

Turn on : 23.9348

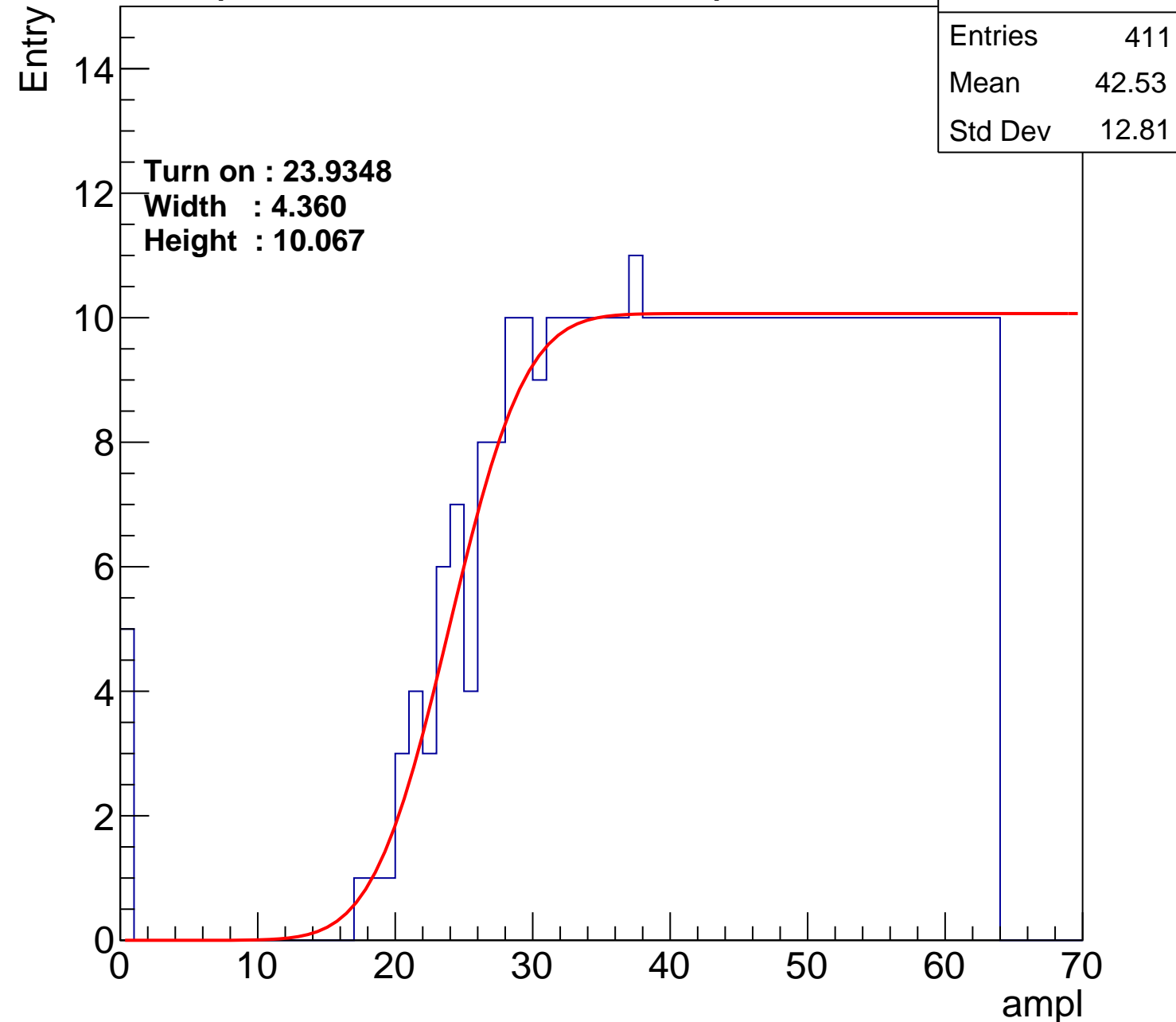
Width : 4.360

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch99

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.22
Std Dev	12.19

Turn on : 24.1393

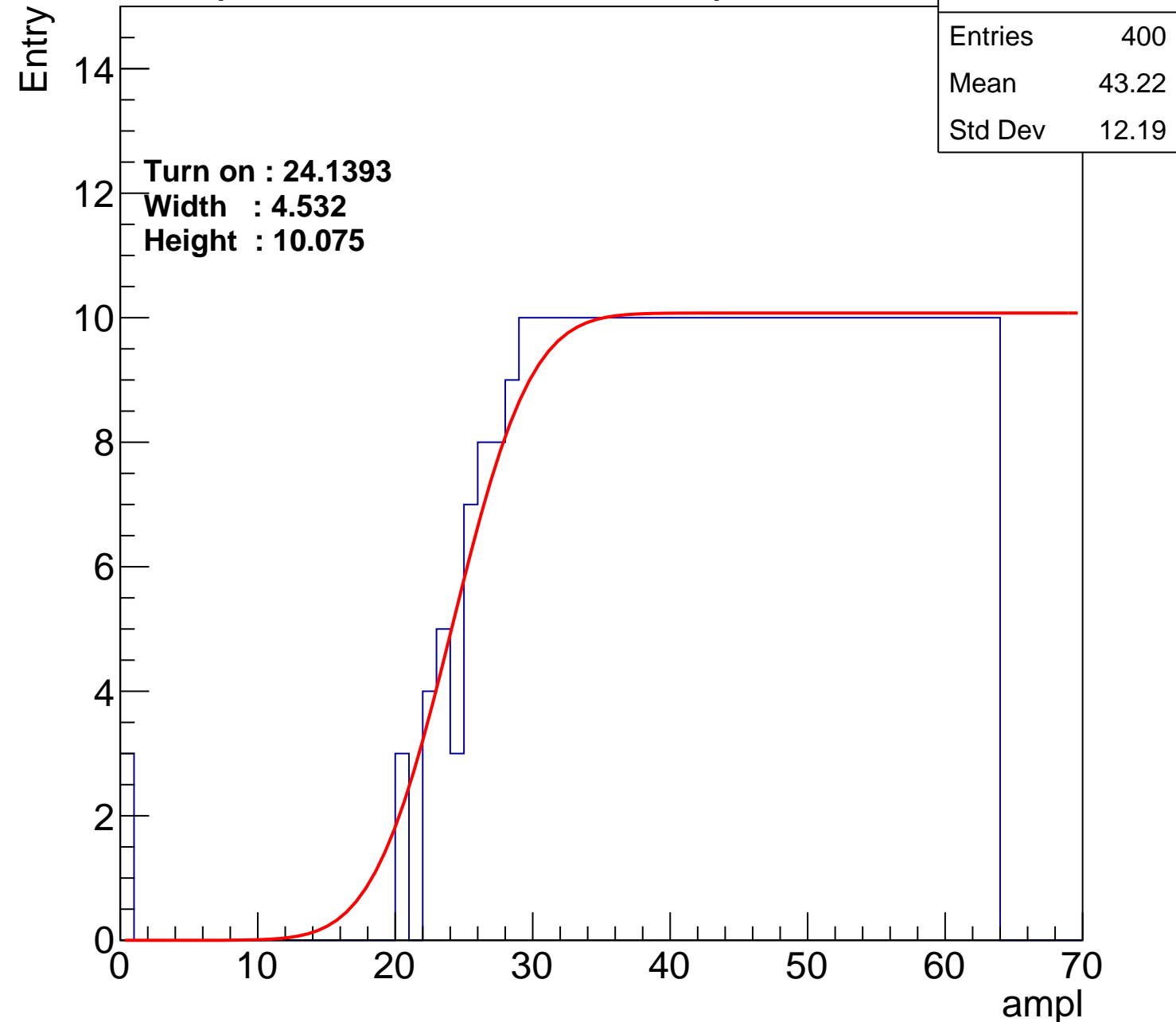
Width : 4.532

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	424
Mean	42.06
Std Dev	12.73

Turn on : 22.3469

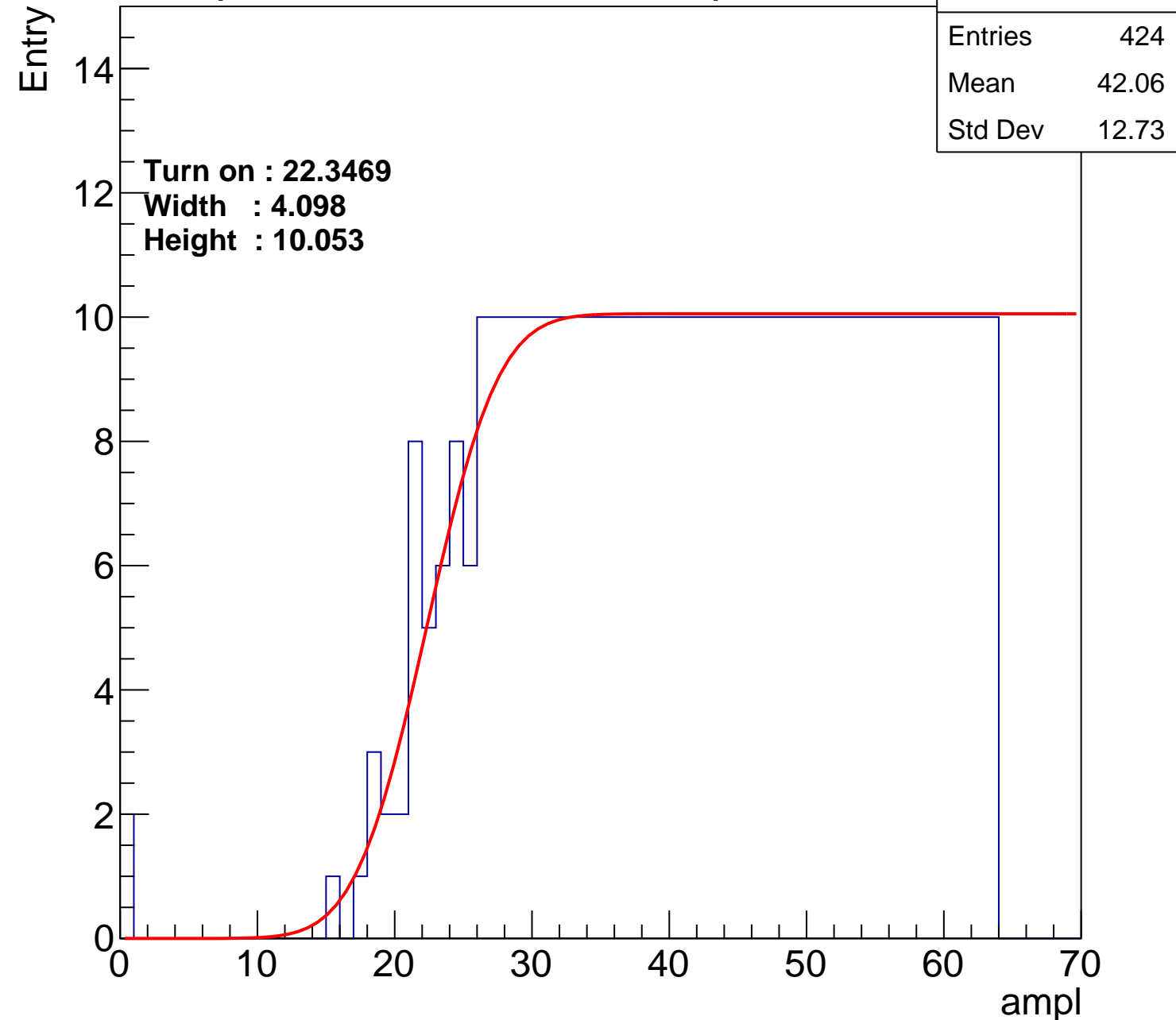
Width : 4.098

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.45
Std Dev	12.35

Turn on : 25.3746

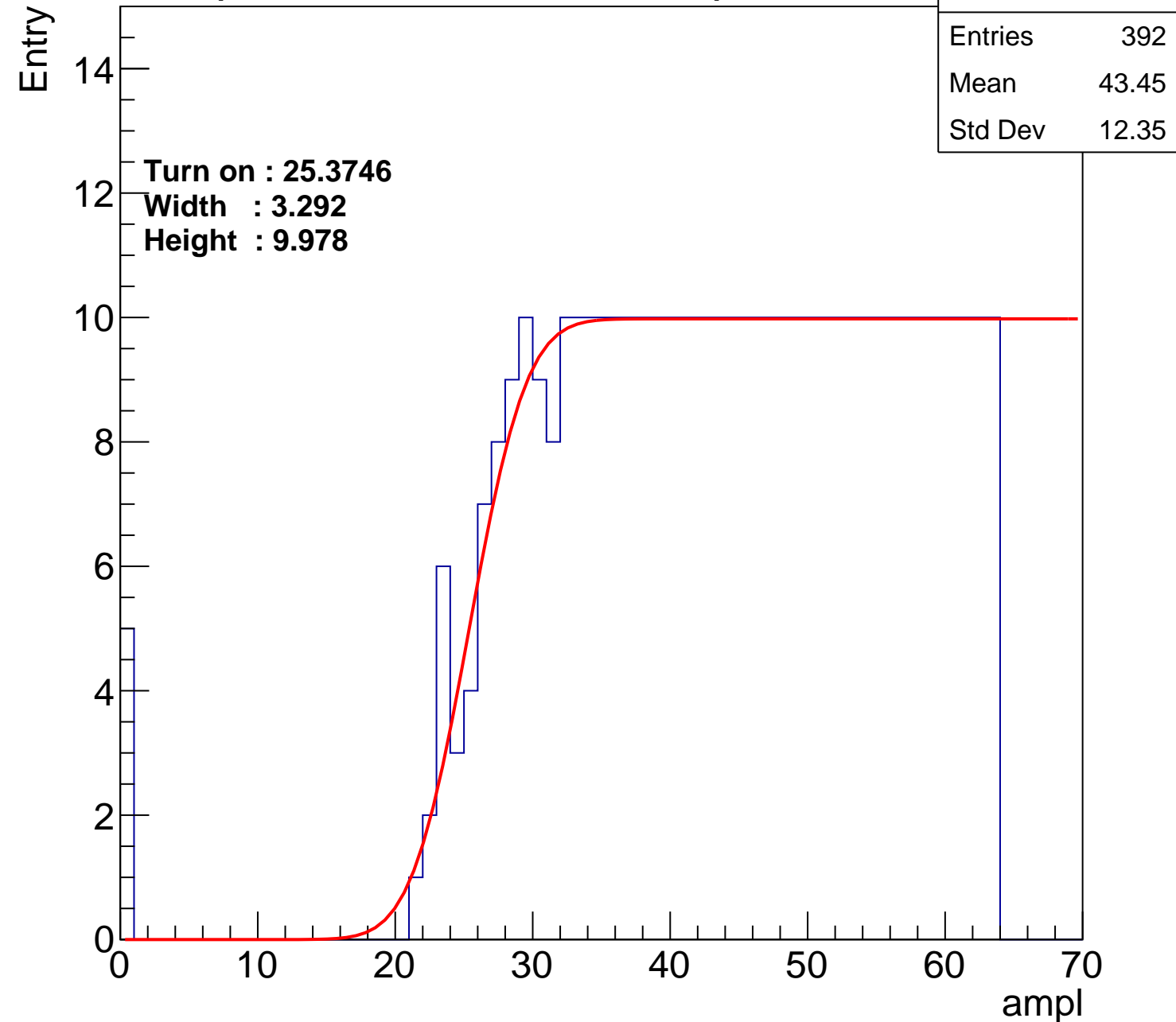
Width : 3.292

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch102

calib_packv5_042523_0143.root, FC#12, port B1

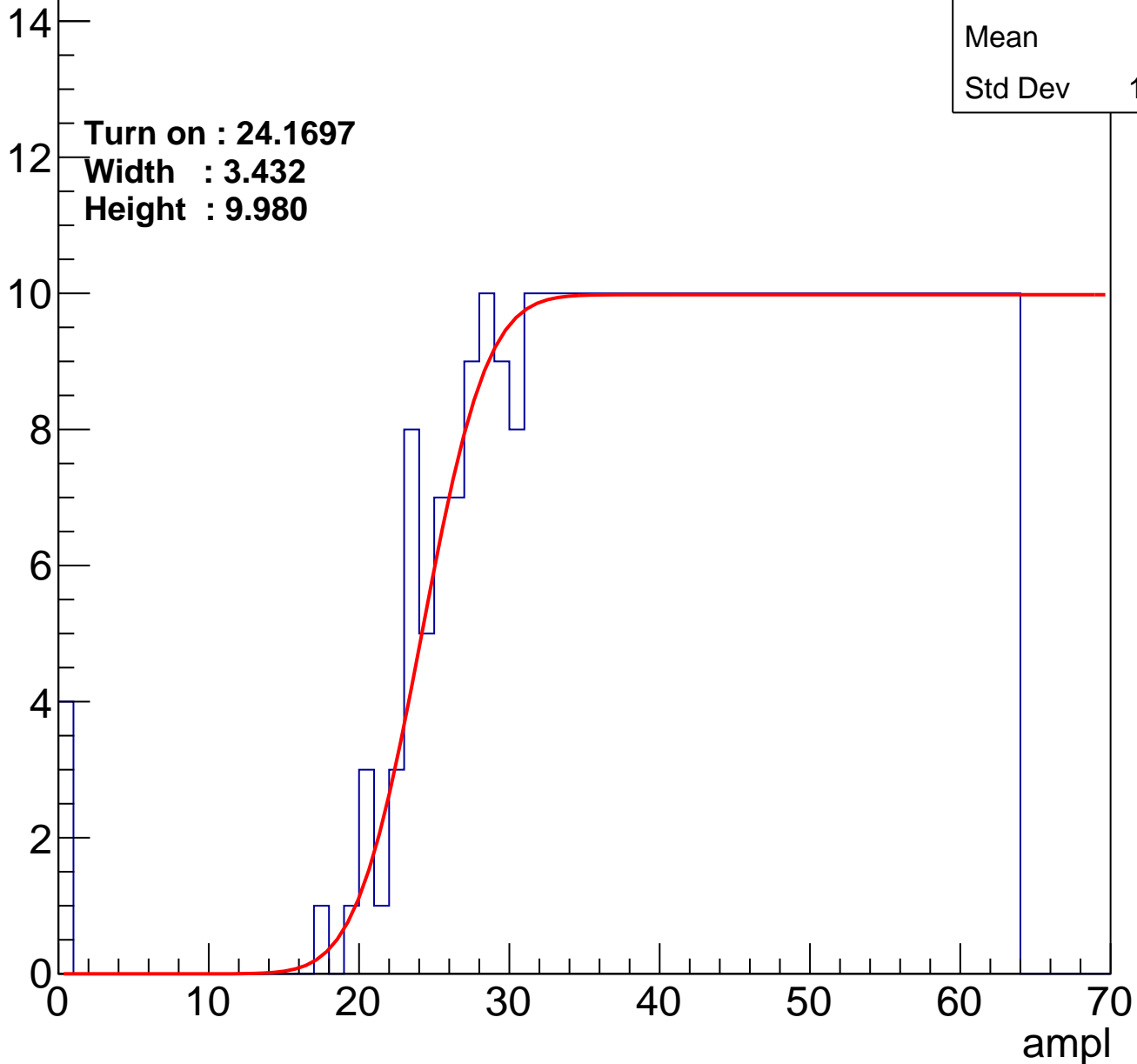
Entries	406
Mean	42.8
Std Dev	12.57

Turn on : 24.1697

Width : 3.432

Height : 9.980

Entry



B0L102S, U3-ch103

calib_packv5_042523_0143.root, FC#12, port B1

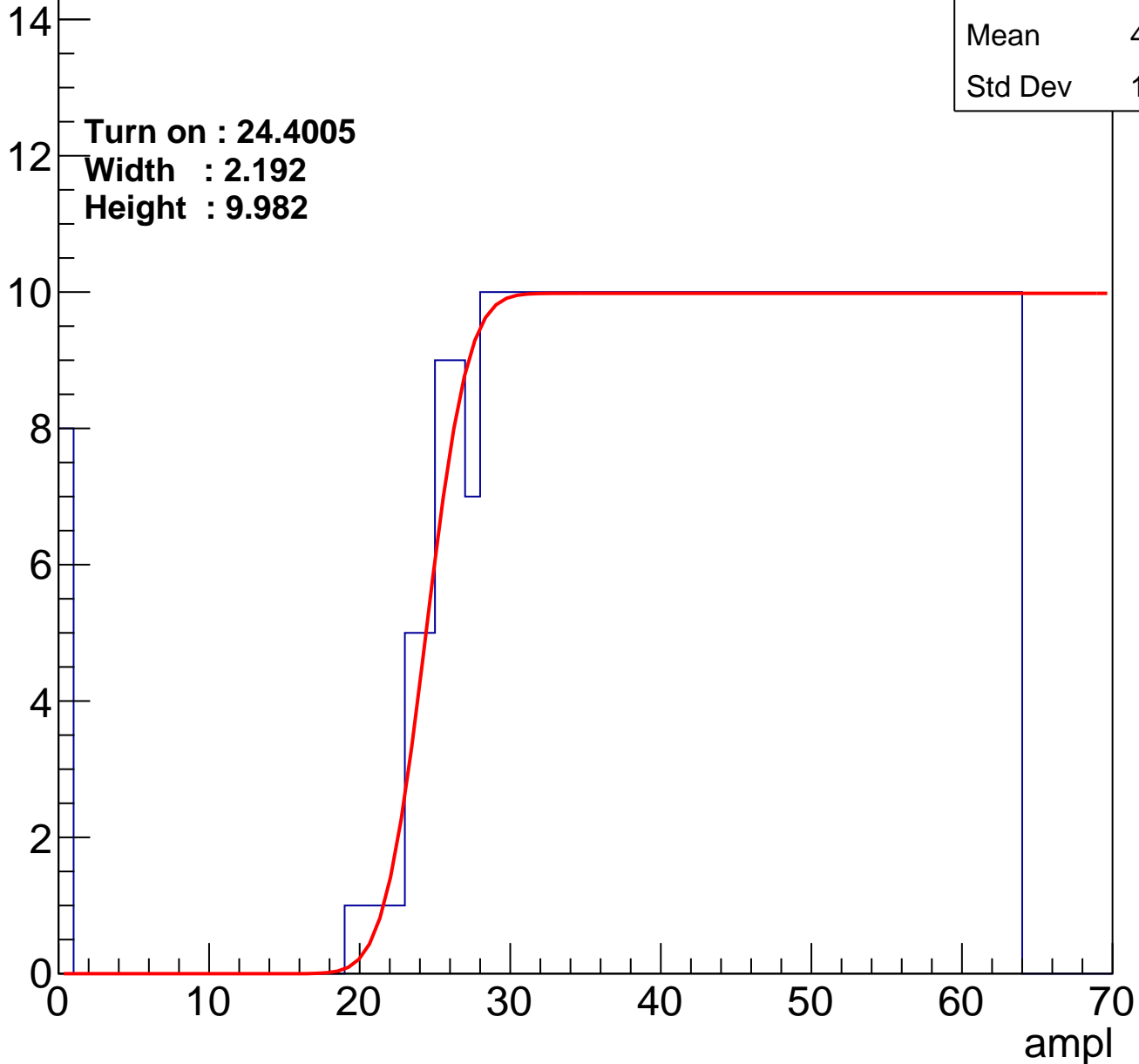
Entries	407
Mean	42.62
Std Dev	13.02

Turn on : 24.4005

Width : 2.192

Height : 9.982

Entry



B0L102S, U3-ch104

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.17
Std Dev	12.11

Turn on : 25.1308

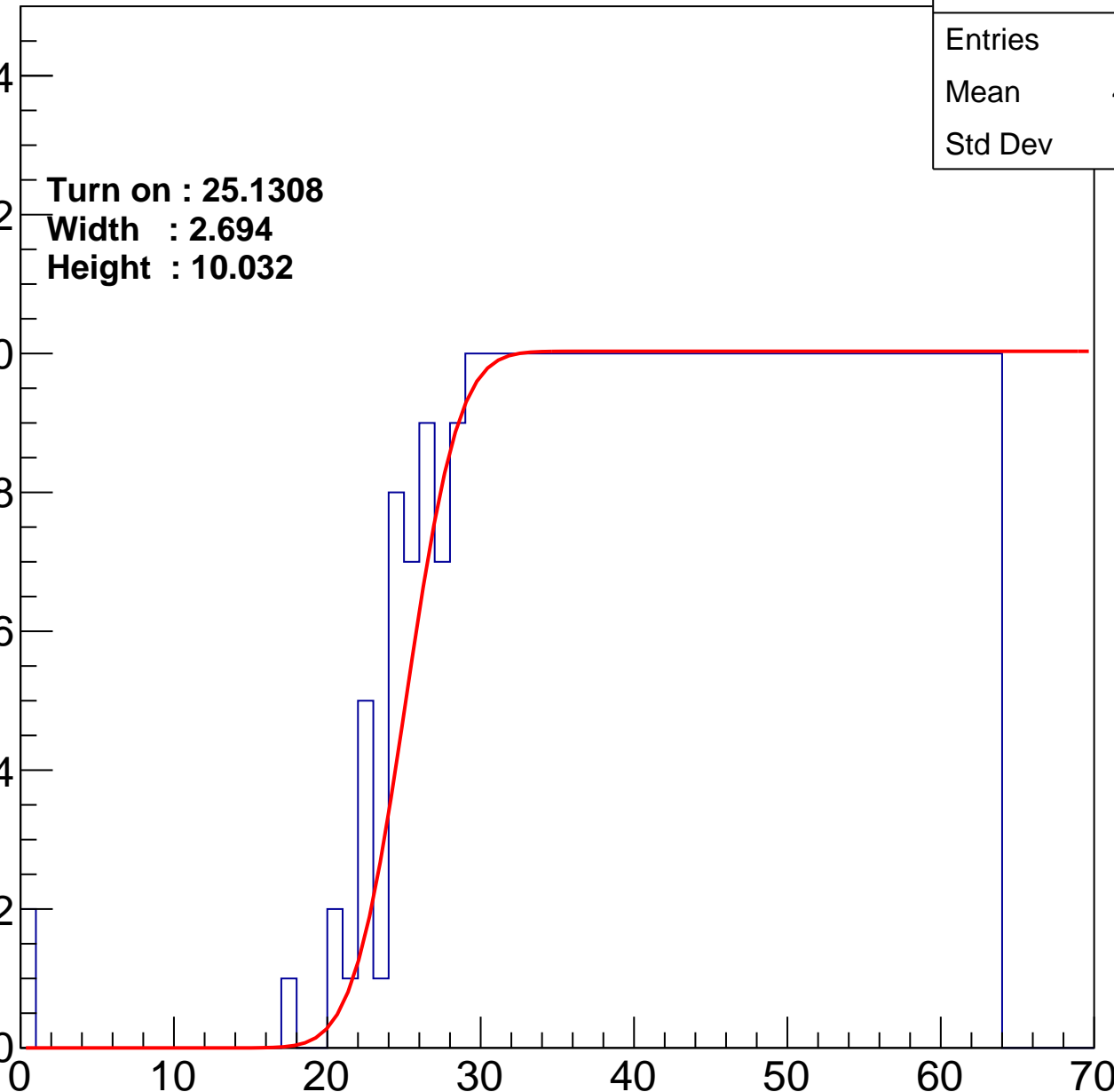
Width : 2.694

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch105

calib_packv5_042523_0143.root, FC#12, port B1

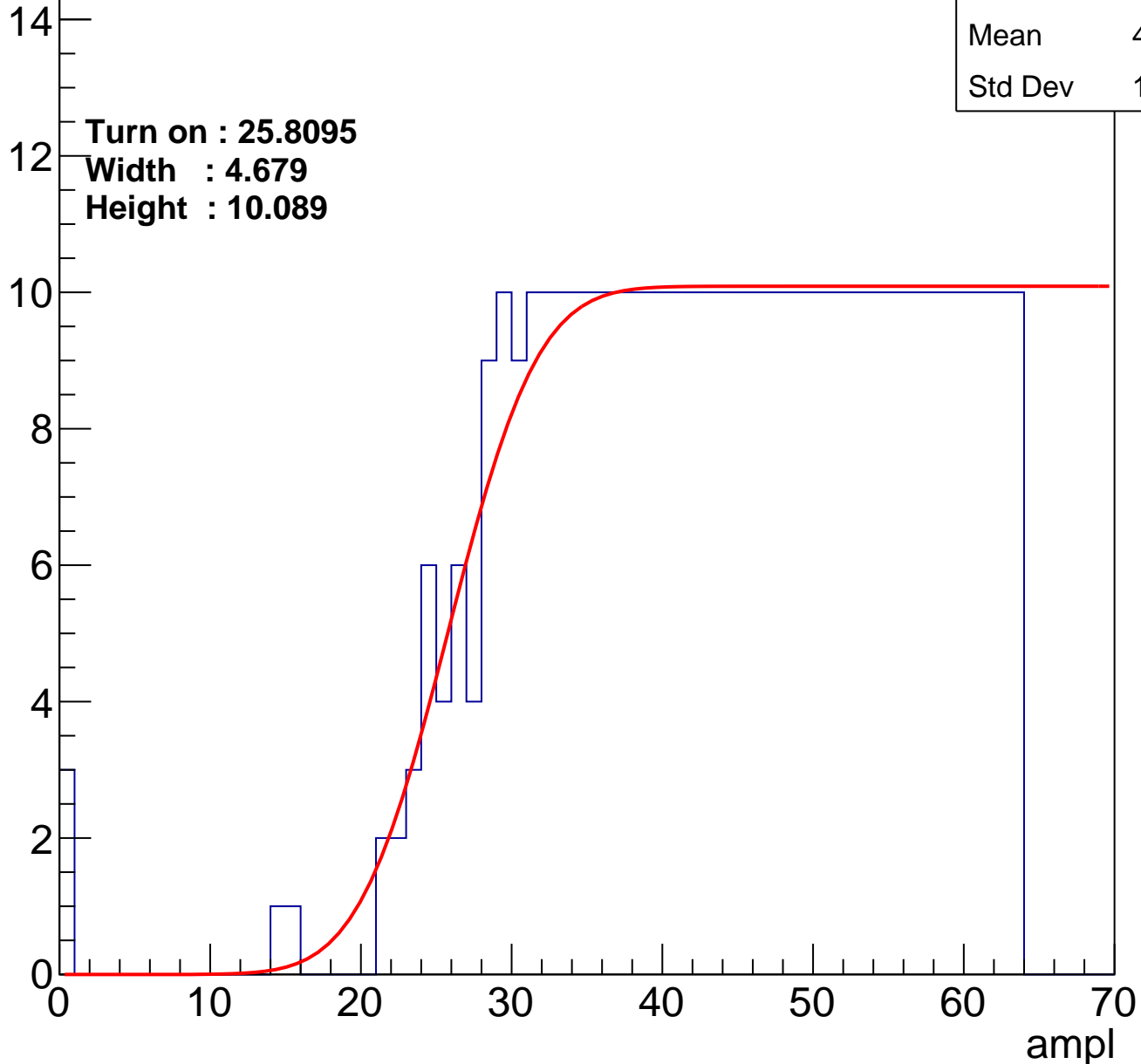
Entries	390
Mean	43.63
Std Dev	12.09

Turn on : 25.8095

Width : 4.679

Height : 10.089

Entry



B0L102S, U3-ch106

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.31
Std Dev	12.19

Turn on : 24.7090

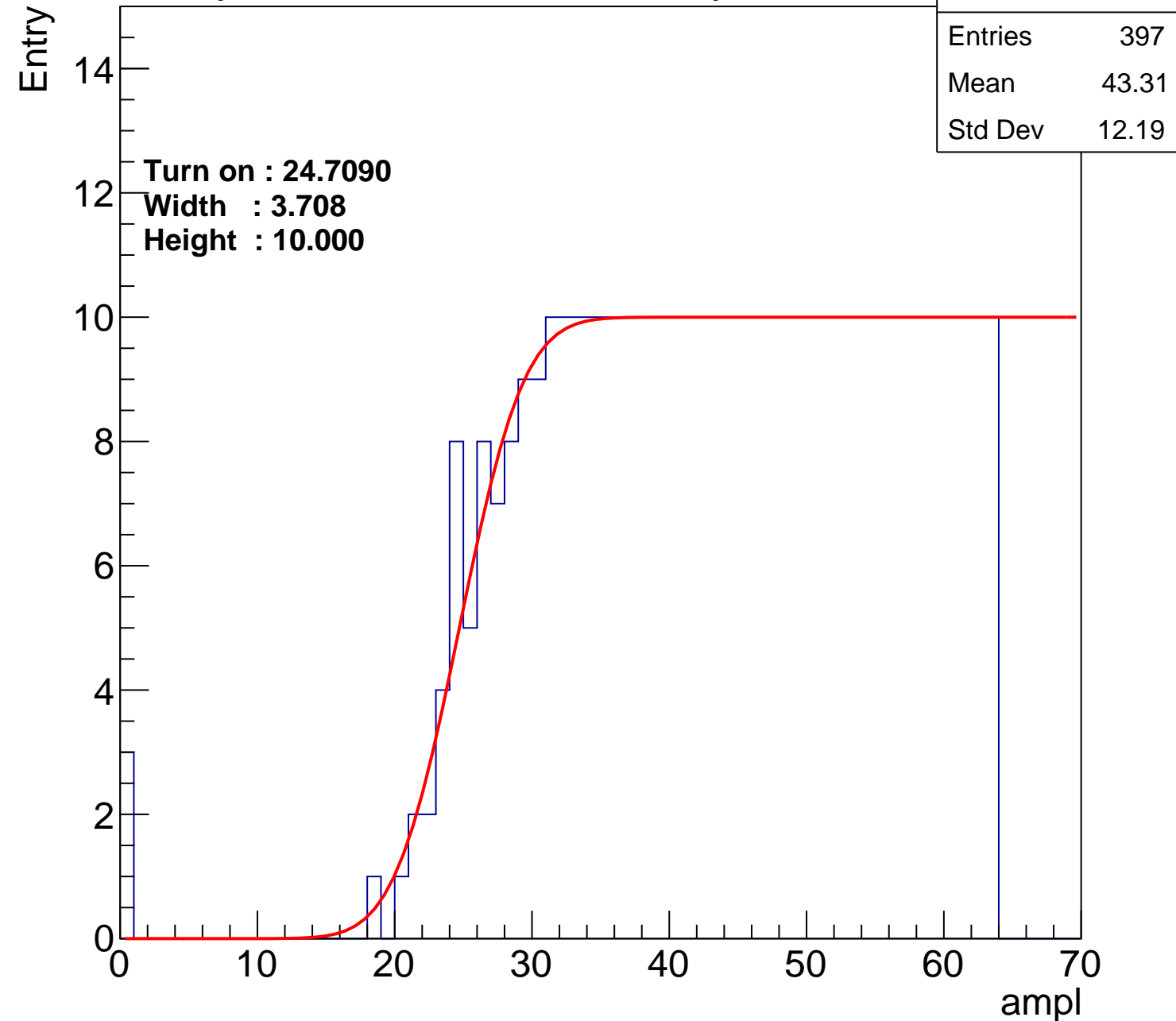
Width : 3.708

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch107

calib_packv5_042523_0143.root, FC#12, port B1

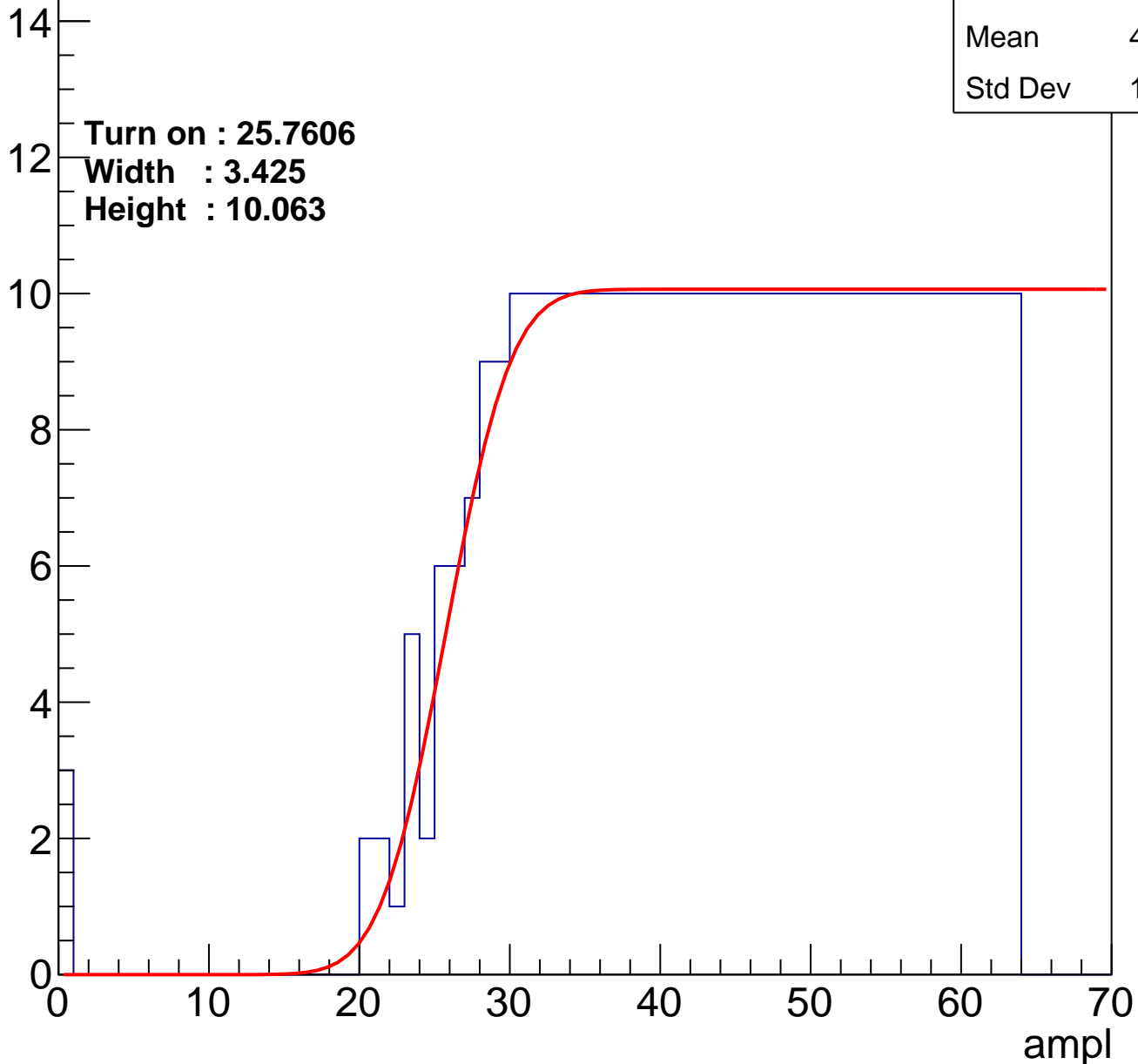
Entries	392
Mean	43.58
Std Dev	12.03

Turn on : 25.7606

Width : 3.425

Height : 10.063

Entry



B0L102S, U3-ch108

calib_packv5_042523_0143.root, FC#12, port B1

Entries	415
Mean	42.44
Std Dev	12.62

Turn on : 23.3487

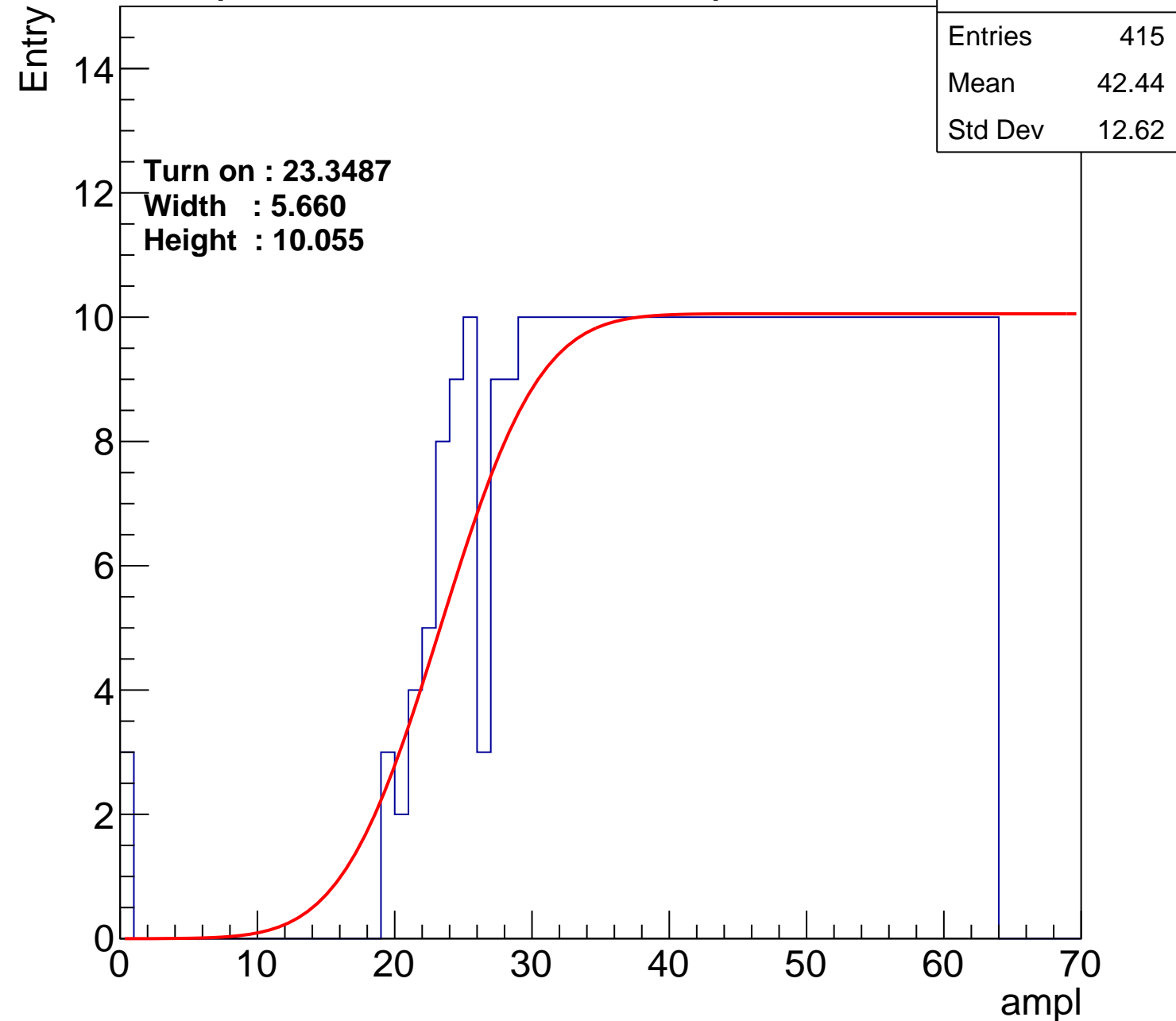
Width : 5.660

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch109

calib_packv5_042523_0143.root, FC#12, port B1

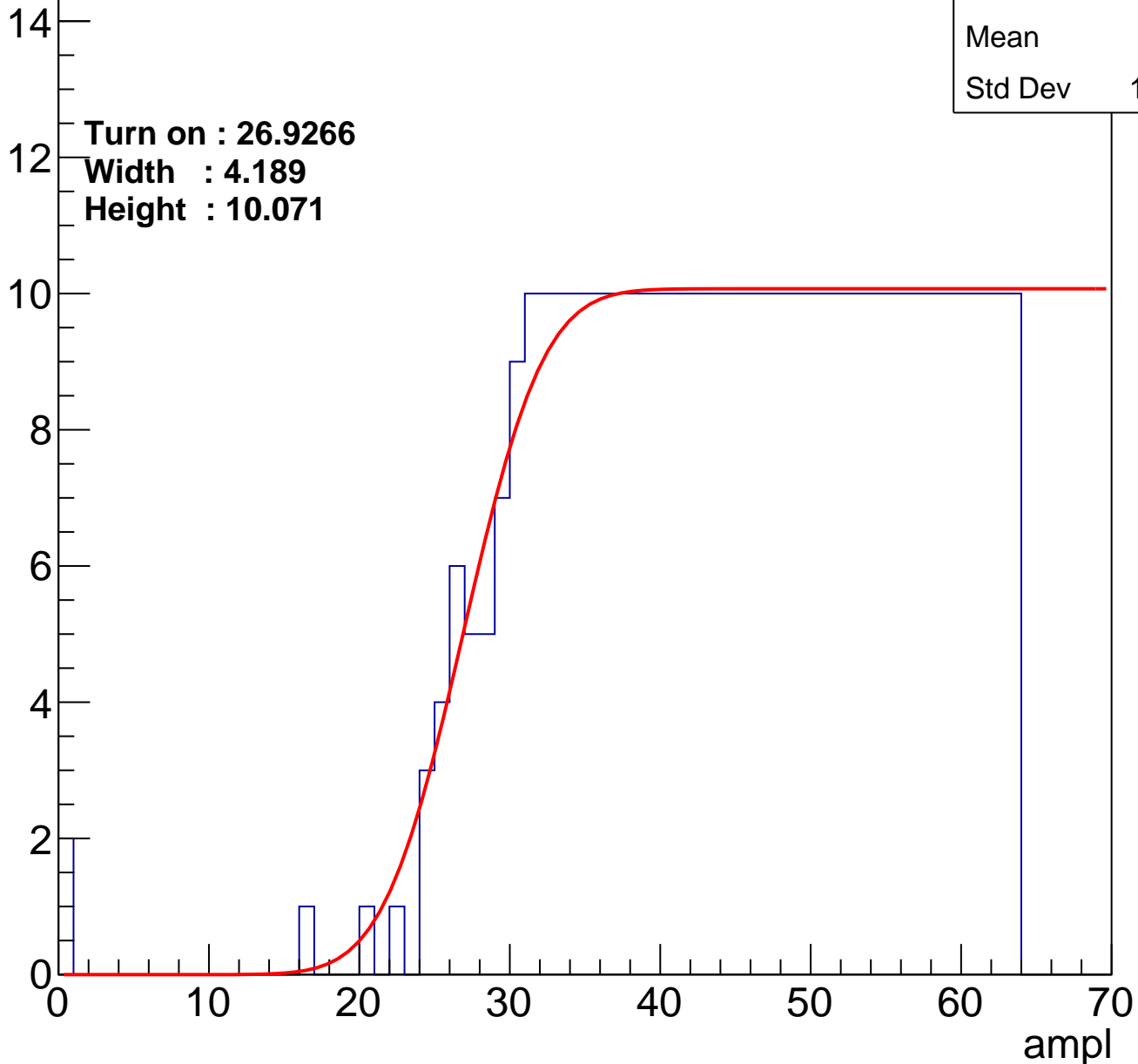
Entries	374
Mean	44.5
Std Dev	11.47

Turn on : 26.9266

Width : 4.189

Height : 10.071

Entry



B0L102S, U3-ch110

calib_packv5_042523_0143.root, FC#12, port B1

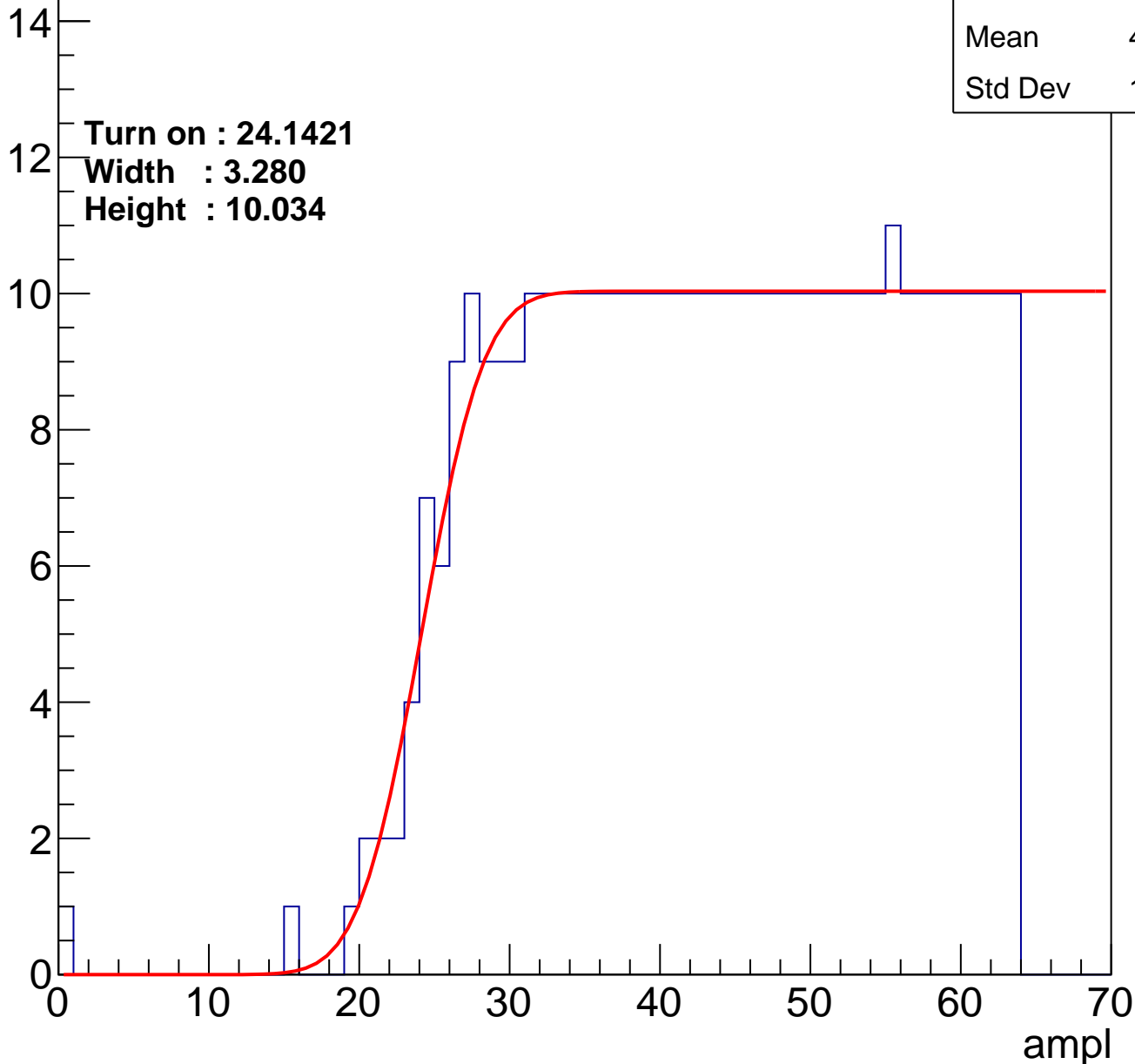
Entries	403
Mean	43.23
Std Dev	11.99

Turn on : 24.1421

Width : 3.280

Height : 10.034

Entry



B0L102S, U3-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.42
Std Dev	12.07

Turn on : 24.5332

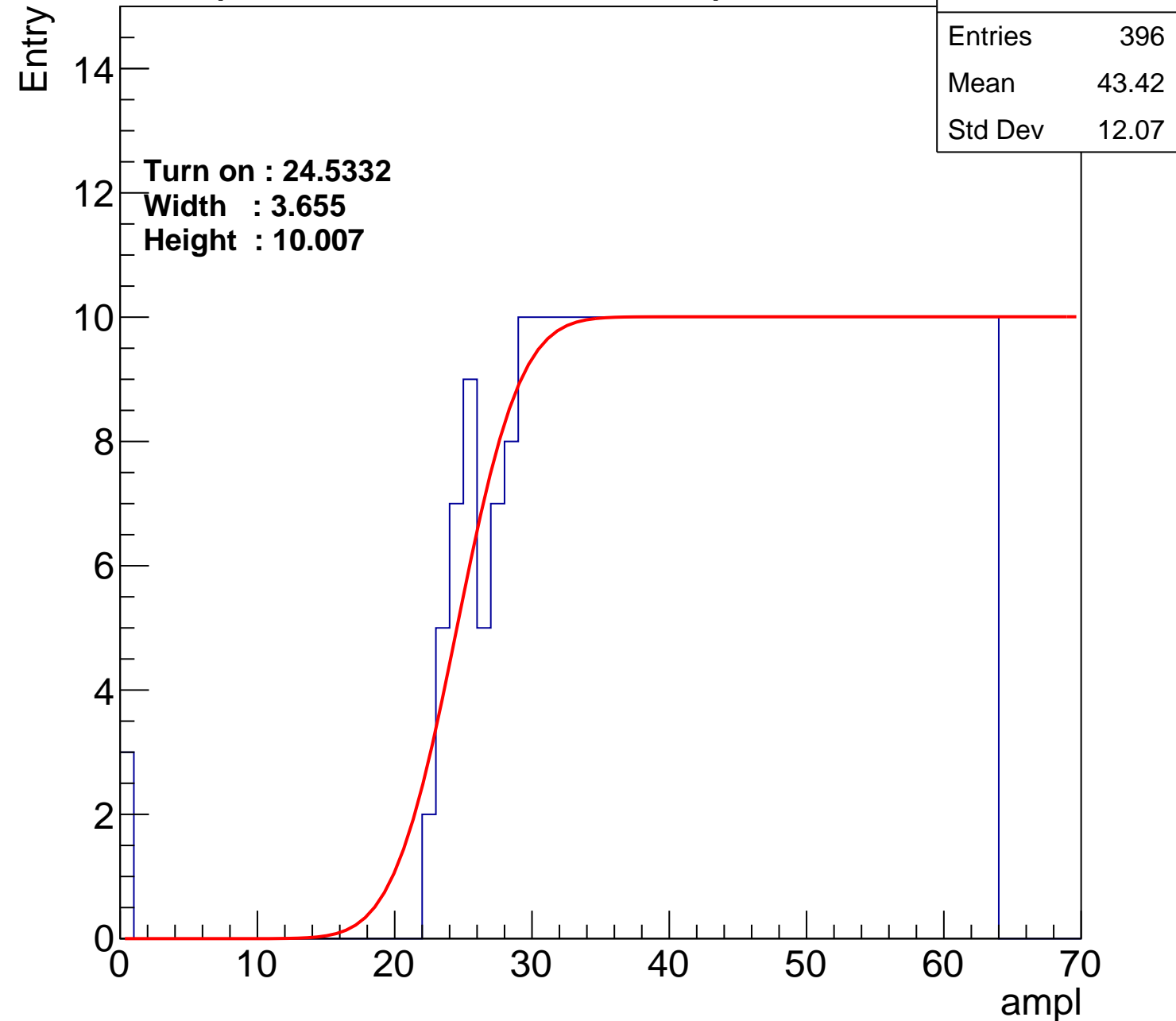
Width : 3.655

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	410
Mean	42.81
Std Dev	12.2

Turn on : 23.2323

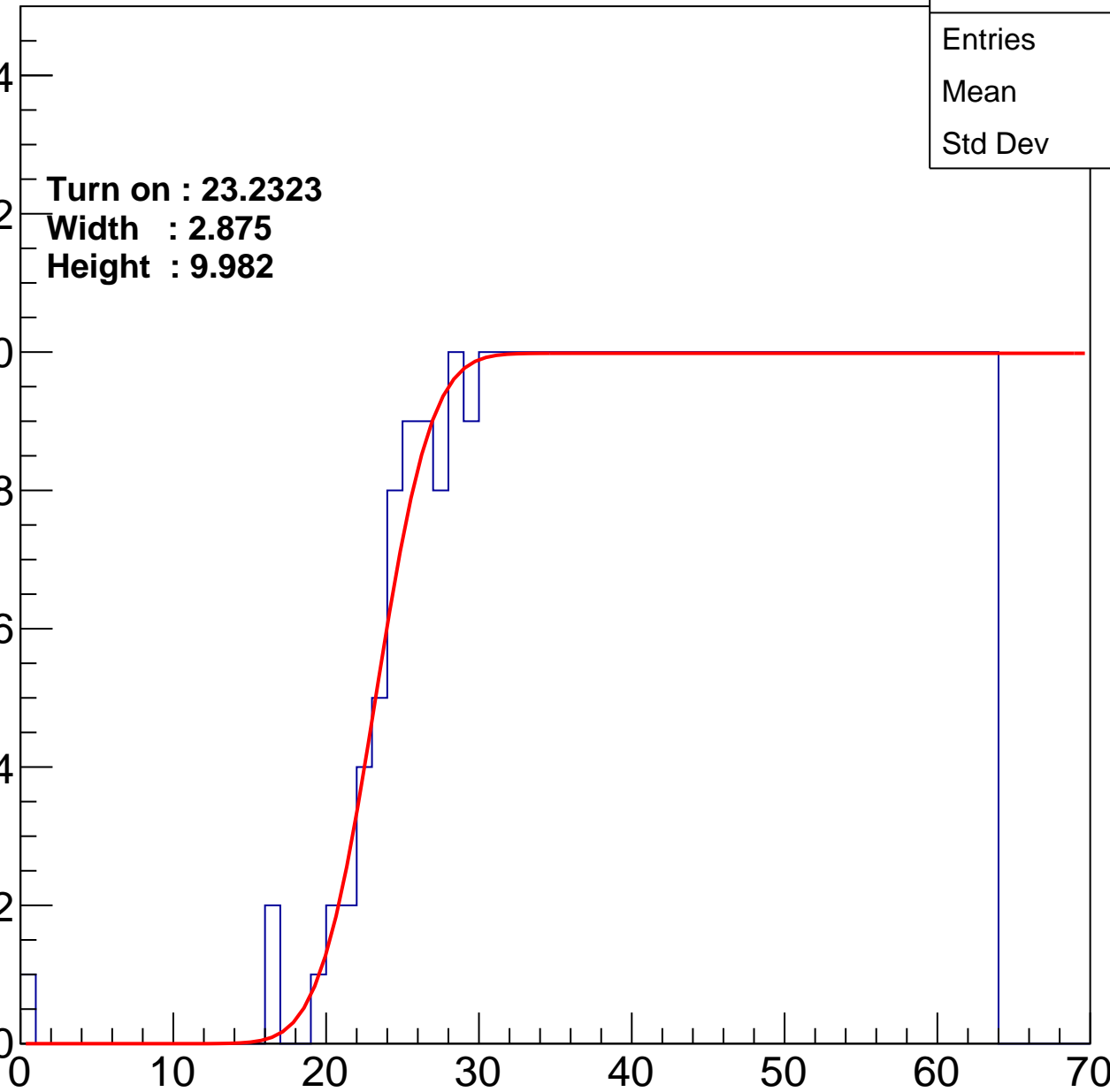
Width : 2.875

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch113

calib_packv5_042523_0143.root, FC#12, port B1

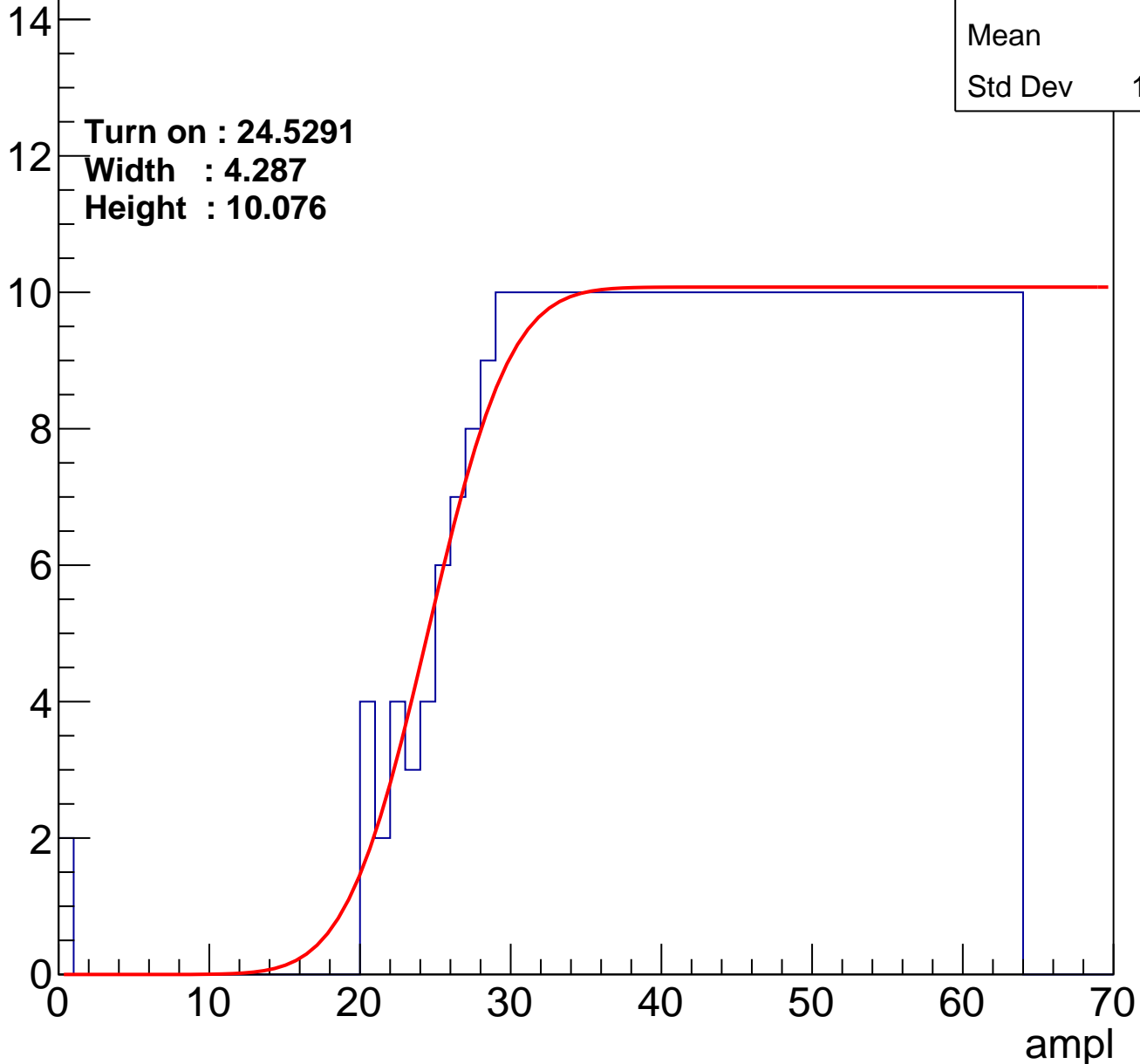
Entries	399
Mean	43.3
Std Dev	12.06

Turn on : 24.5291

Width : 4.287

Height : 10.076

Entry



B0L102S, U3-ch114

calib_packv5_042523_0143.root, FC#12, port B1

Entries	399
Mean	43.16
Std Dev	12.4

Turn on : 25.0241

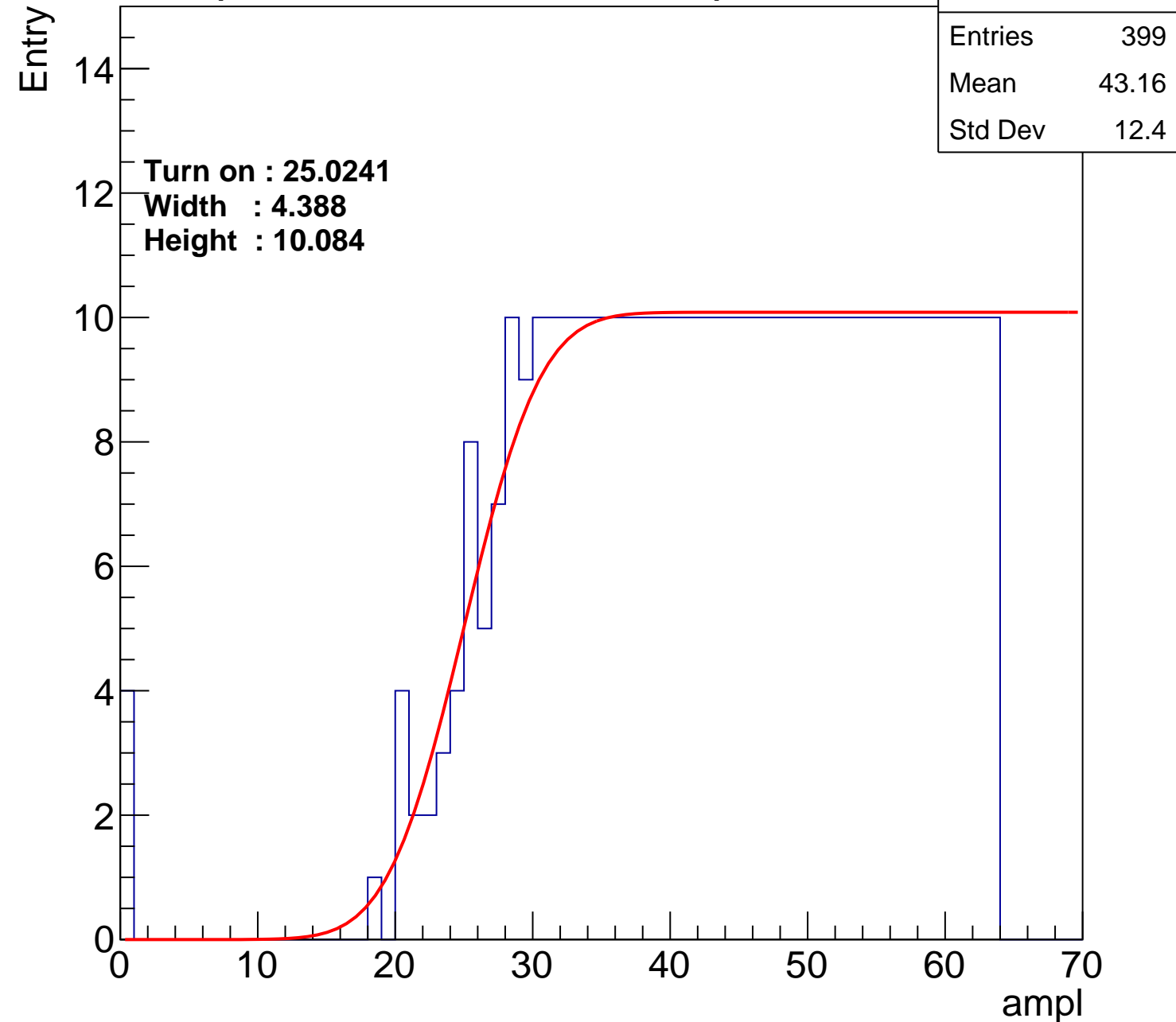
Width : 4.388

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.67
Std Dev	11.99

Turn on : 25.7059

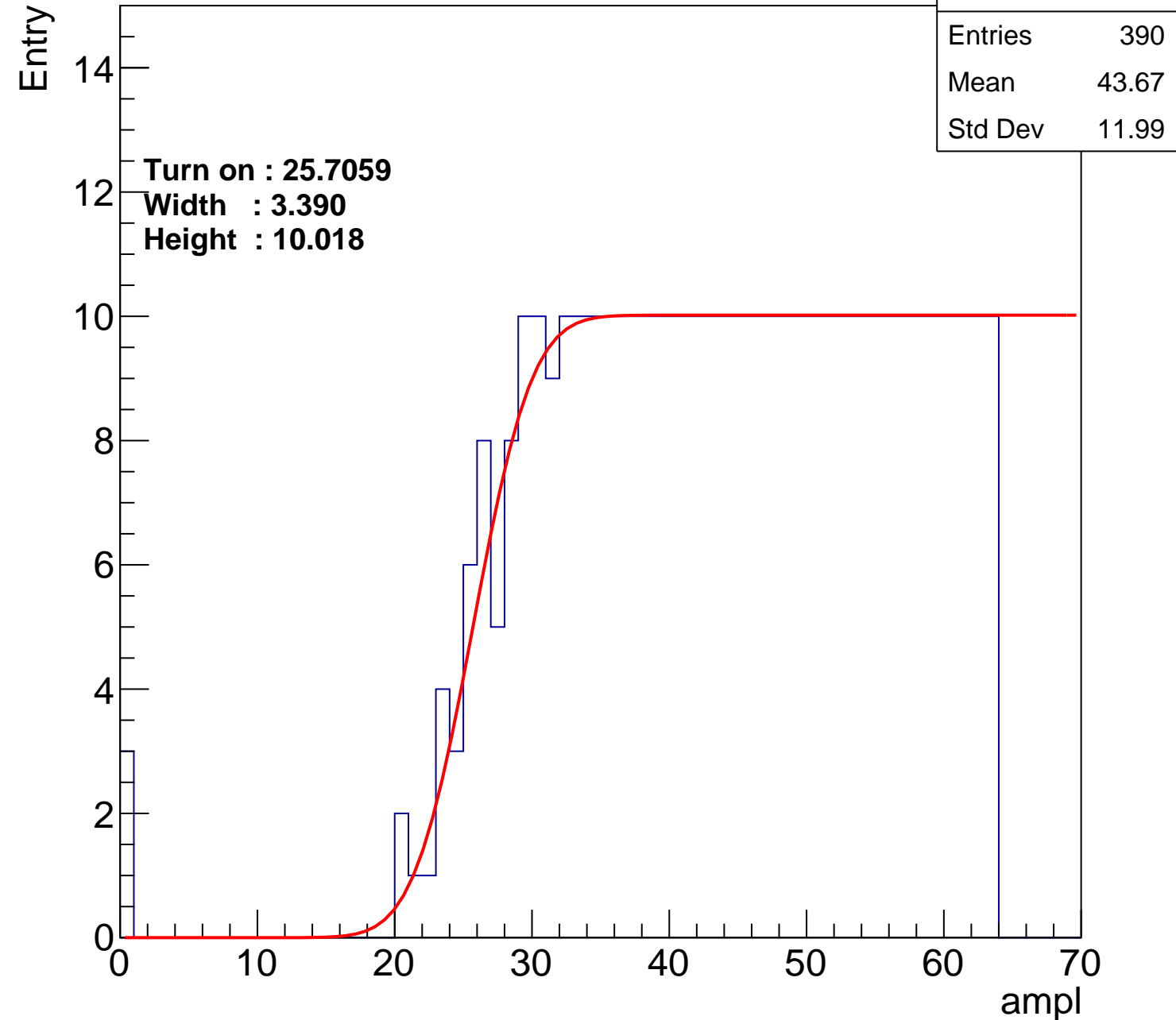
Width : 3.390

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	414
Mean	42.41
Std Dev	12.83

Turn on : 24.0448

Width : 4.044

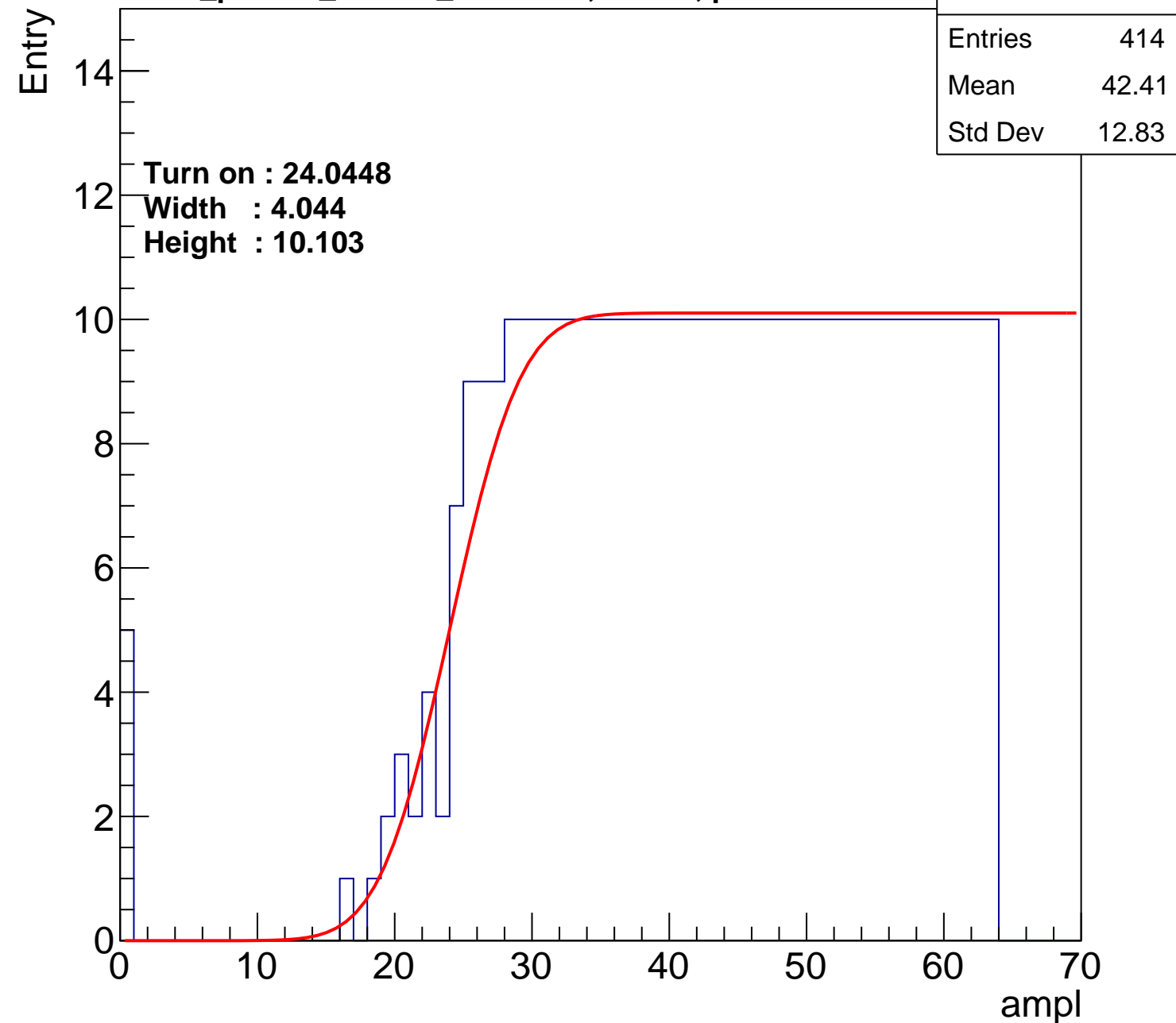
Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L102S, U3-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.85
Std Dev	11.33

Turn on : 28.1016

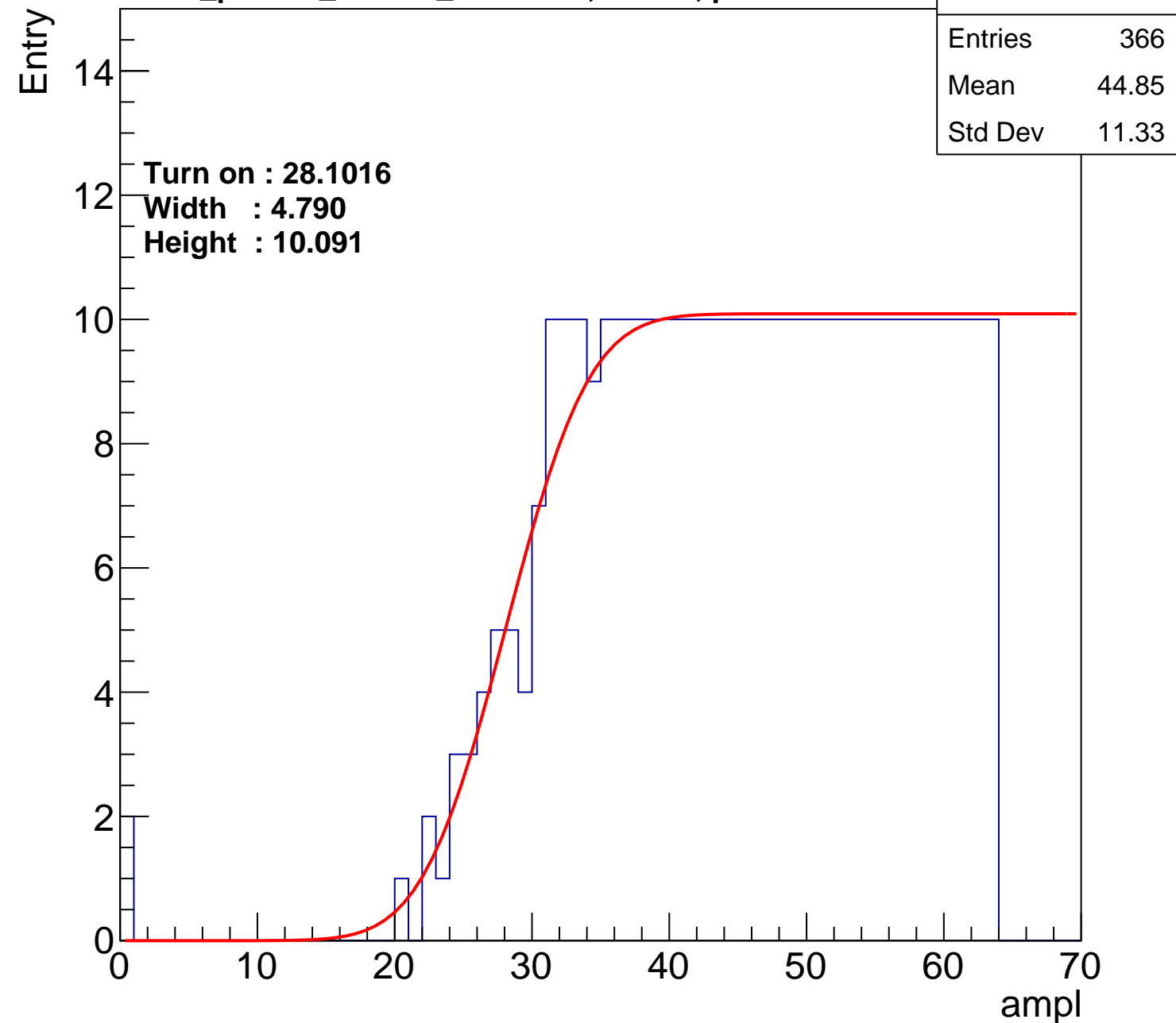
Width : 4.790

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch118

calib_packv5_042523_0143.root, FC#12, port B1

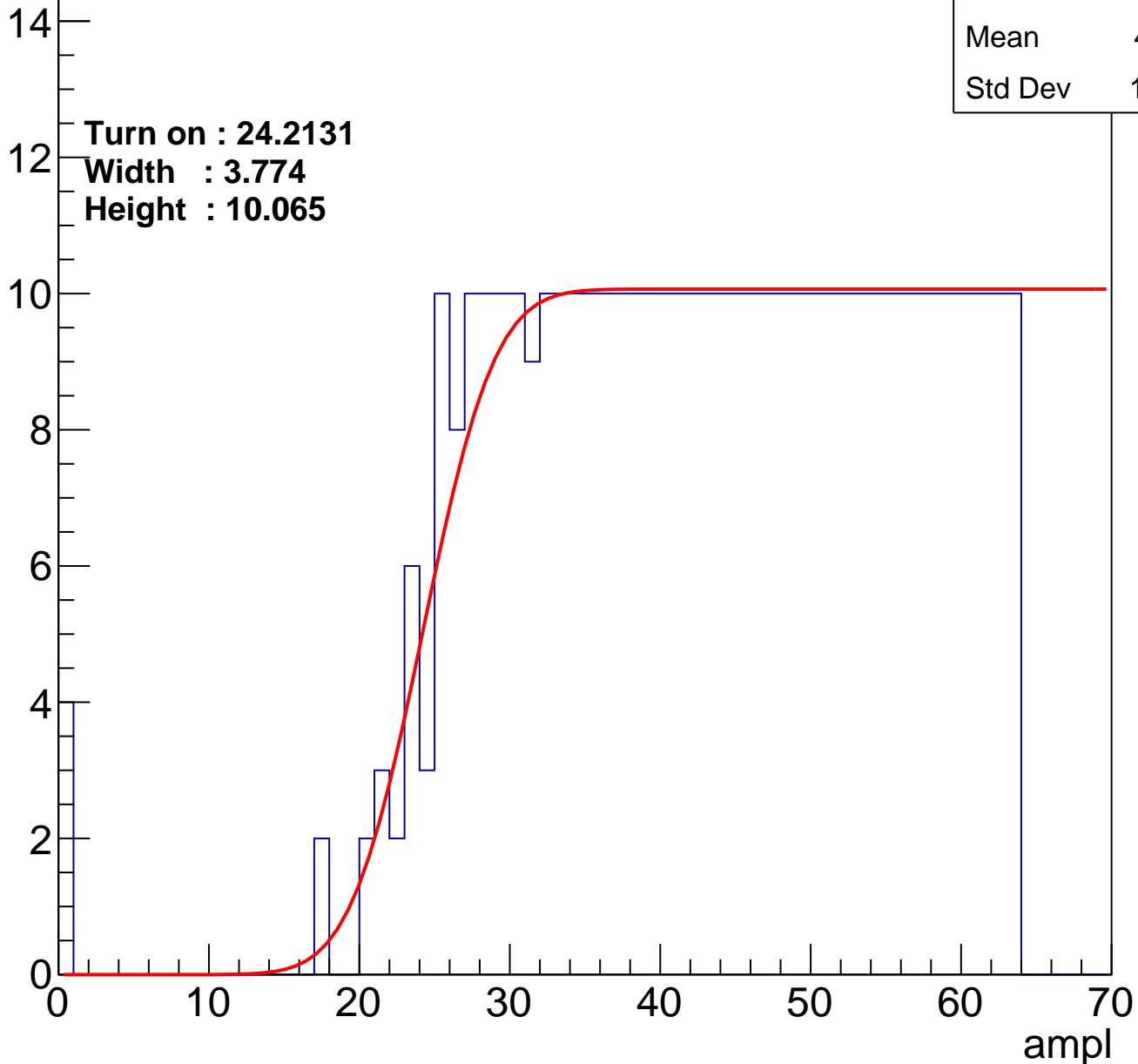
Entries	409
Mean	42.71
Std Dev	12.57

Turn on : 24.2131

Width : 3.774

Height : 10.065

Entry



B0L102S, U3-ch119

calib_packv5_042523_0143.root, FC#12, port B1

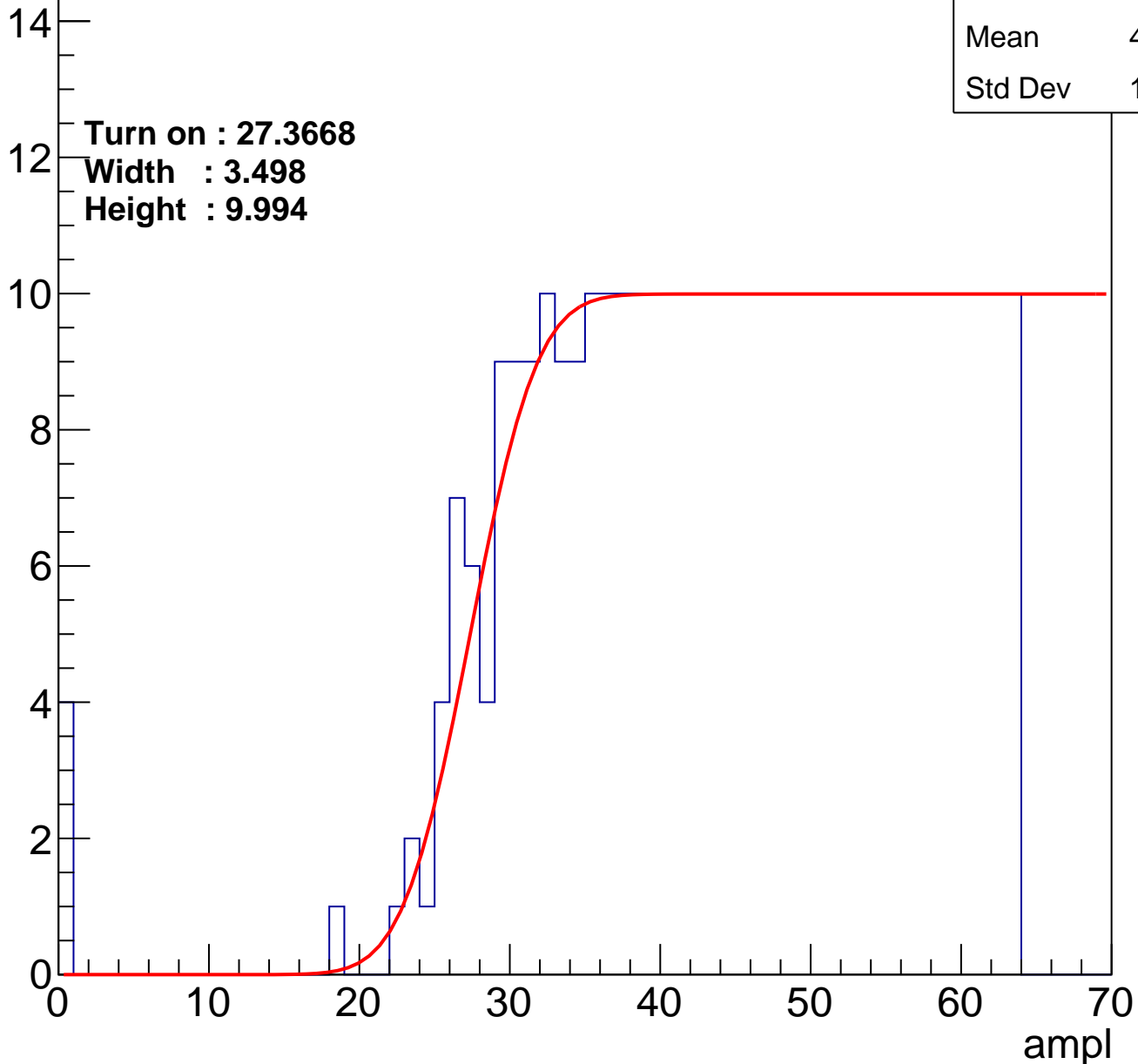
Entries	375
Mean	44.29
Std Dev	11.88

Turn on : 27.3668

Width : 3.498

Height : 9.994

Entry



B0L102S, U3-ch120

calib_packv5_042523_0143.root, FC#12, port B1

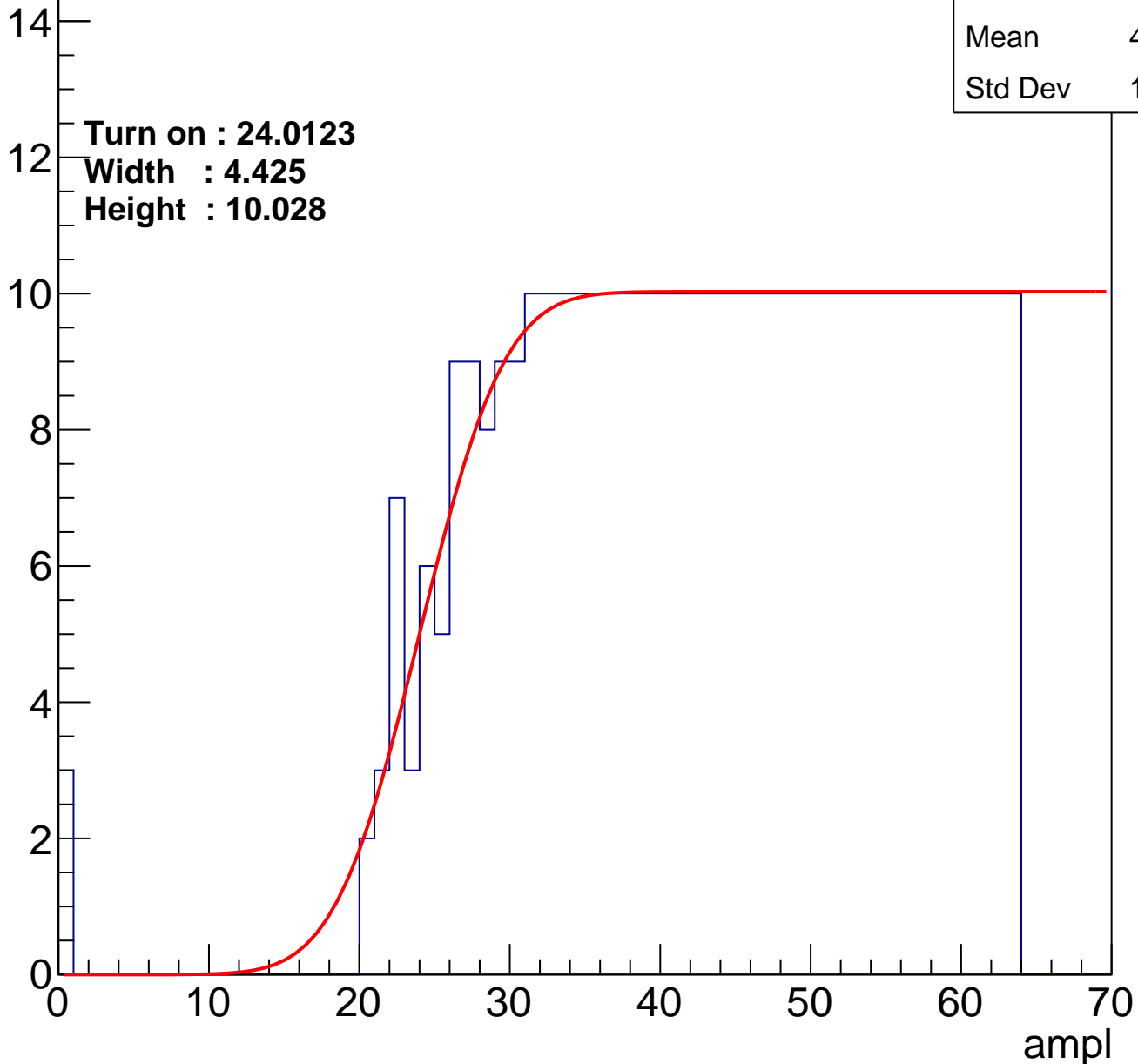
Entries	403
Mean	43.02
Std Dev	12.34

Turn on : 24.0123

Width : 4.425

Height : 10.028

Entry



B0L102S, U3-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.97
Std Dev	11.71

Turn on : 25.8344

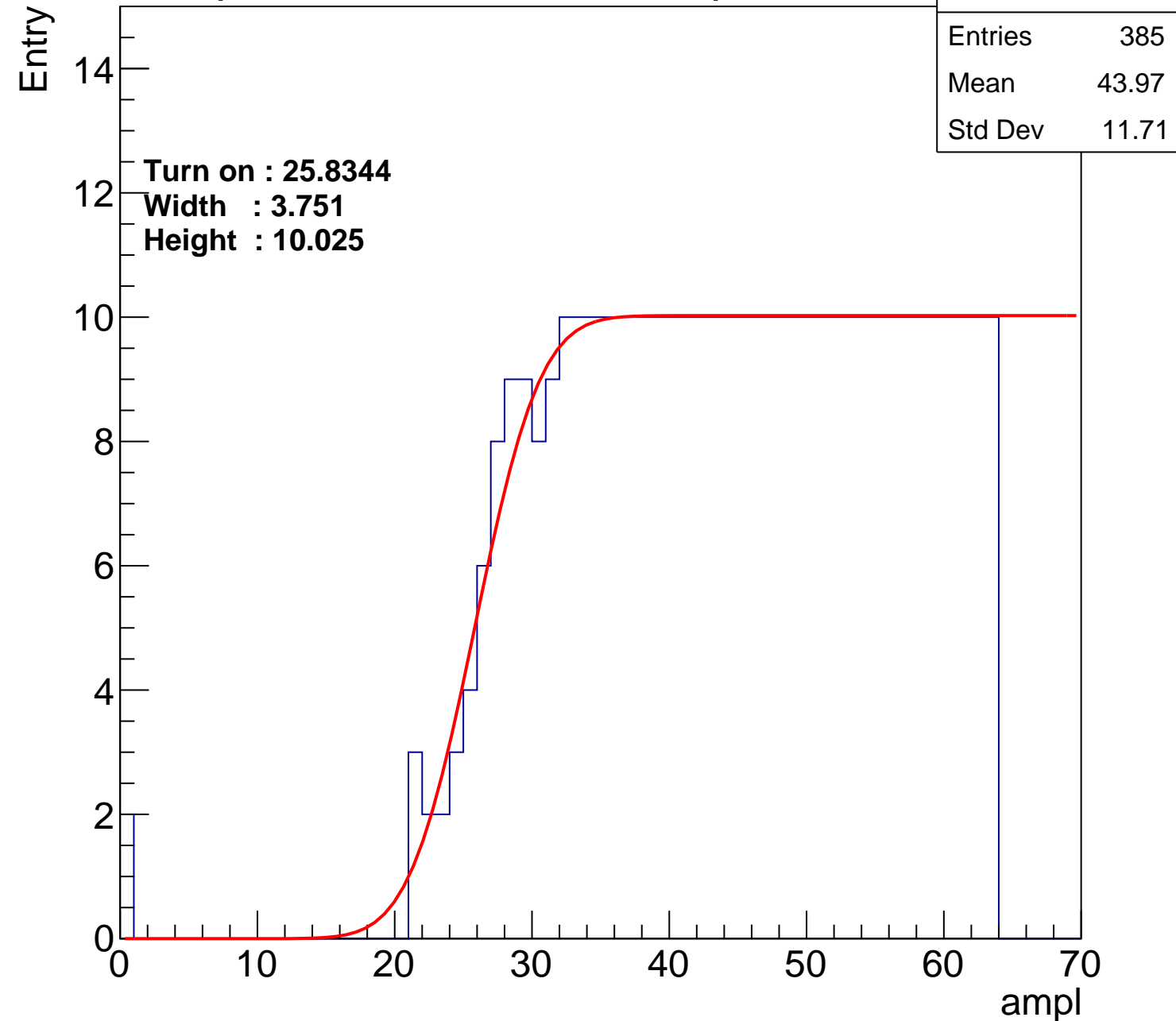
Width : 3.751

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch122

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.39
Std Dev	11.91

Turn on : 24.8697

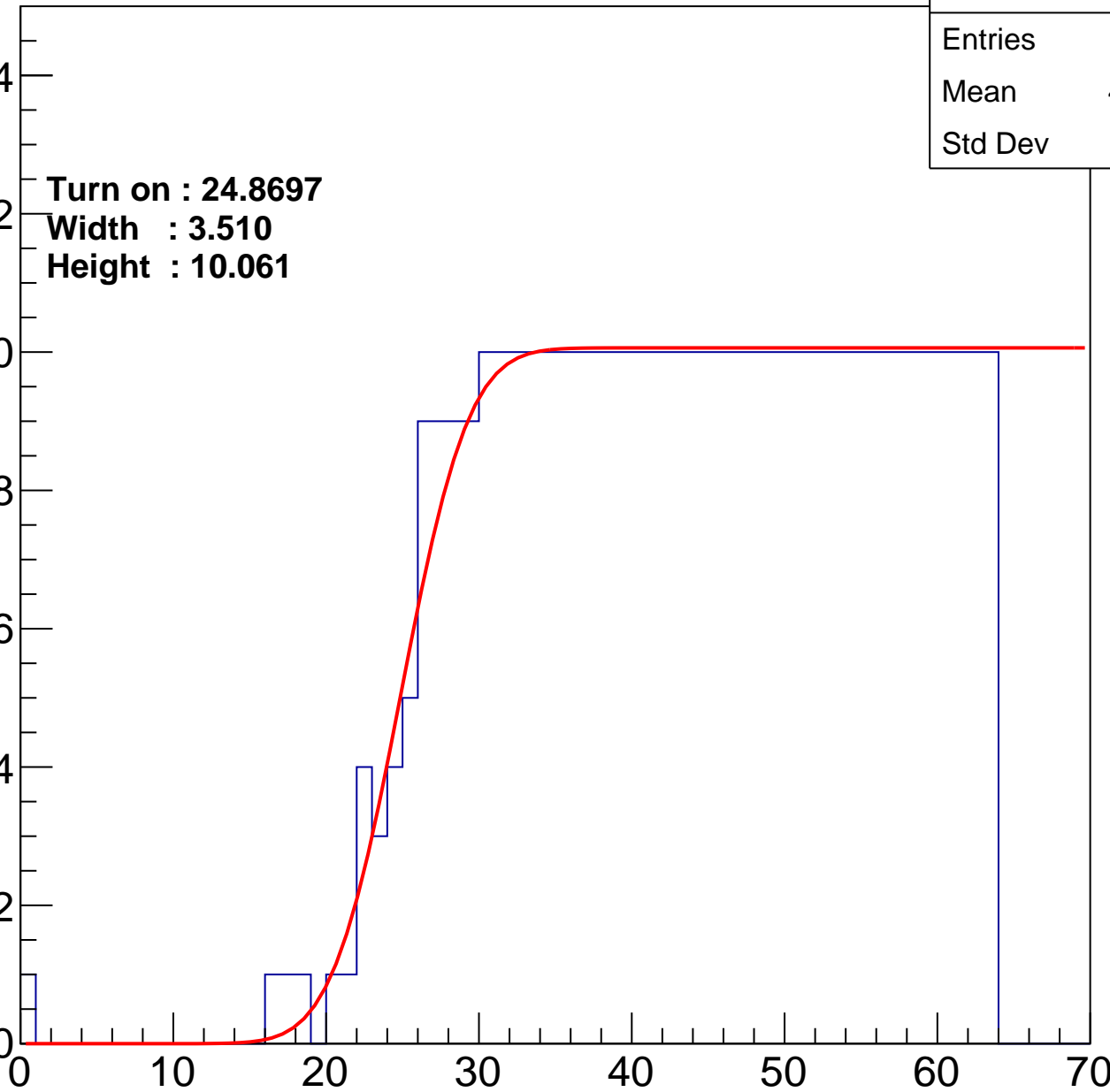
Width : 3.510

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.88
Std Dev	12.31

Turn on : 23.7898

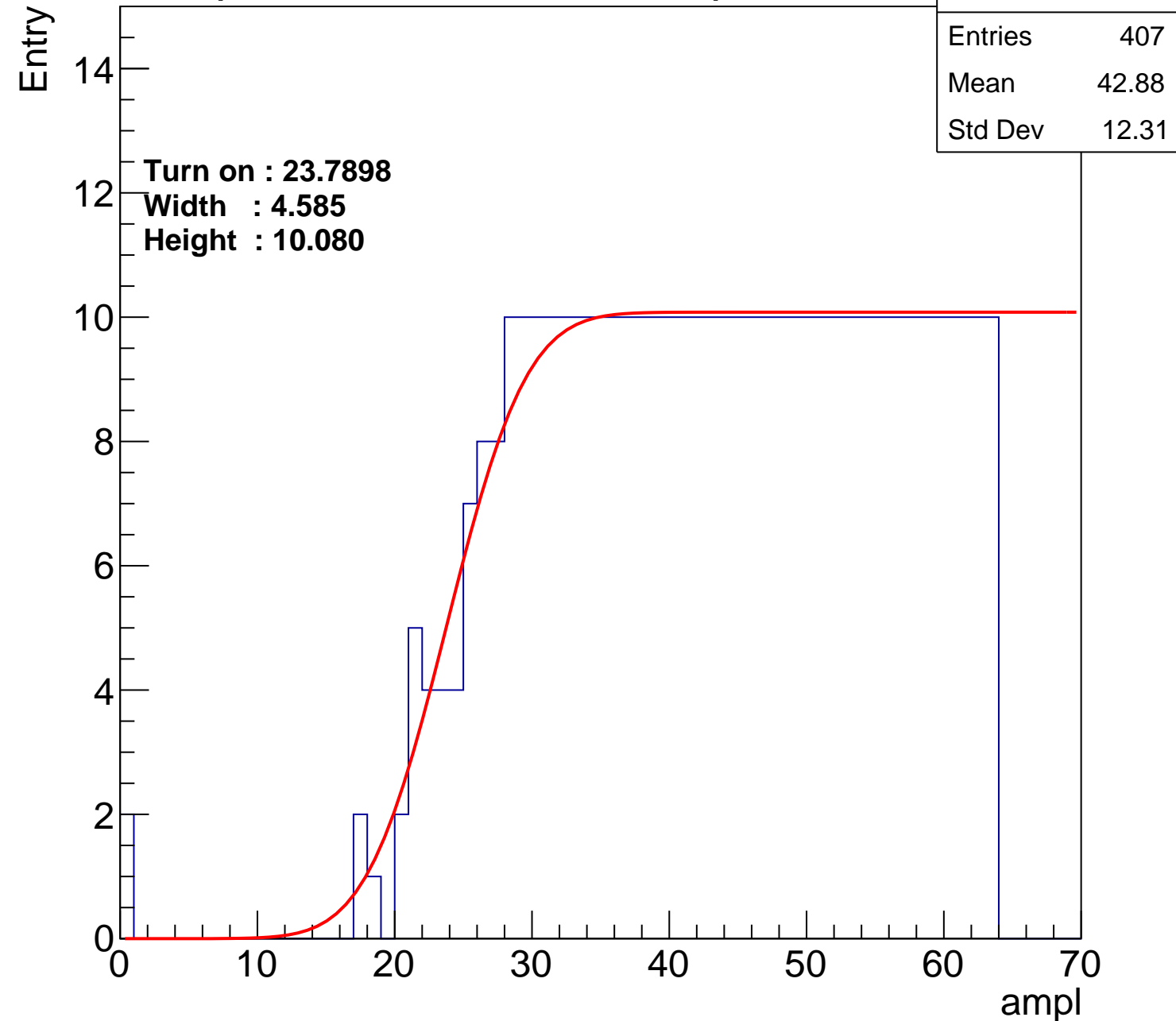
Width : 4.585

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	422
Mean	42.07
Std Dev	12.92

Turn on : 22.4862

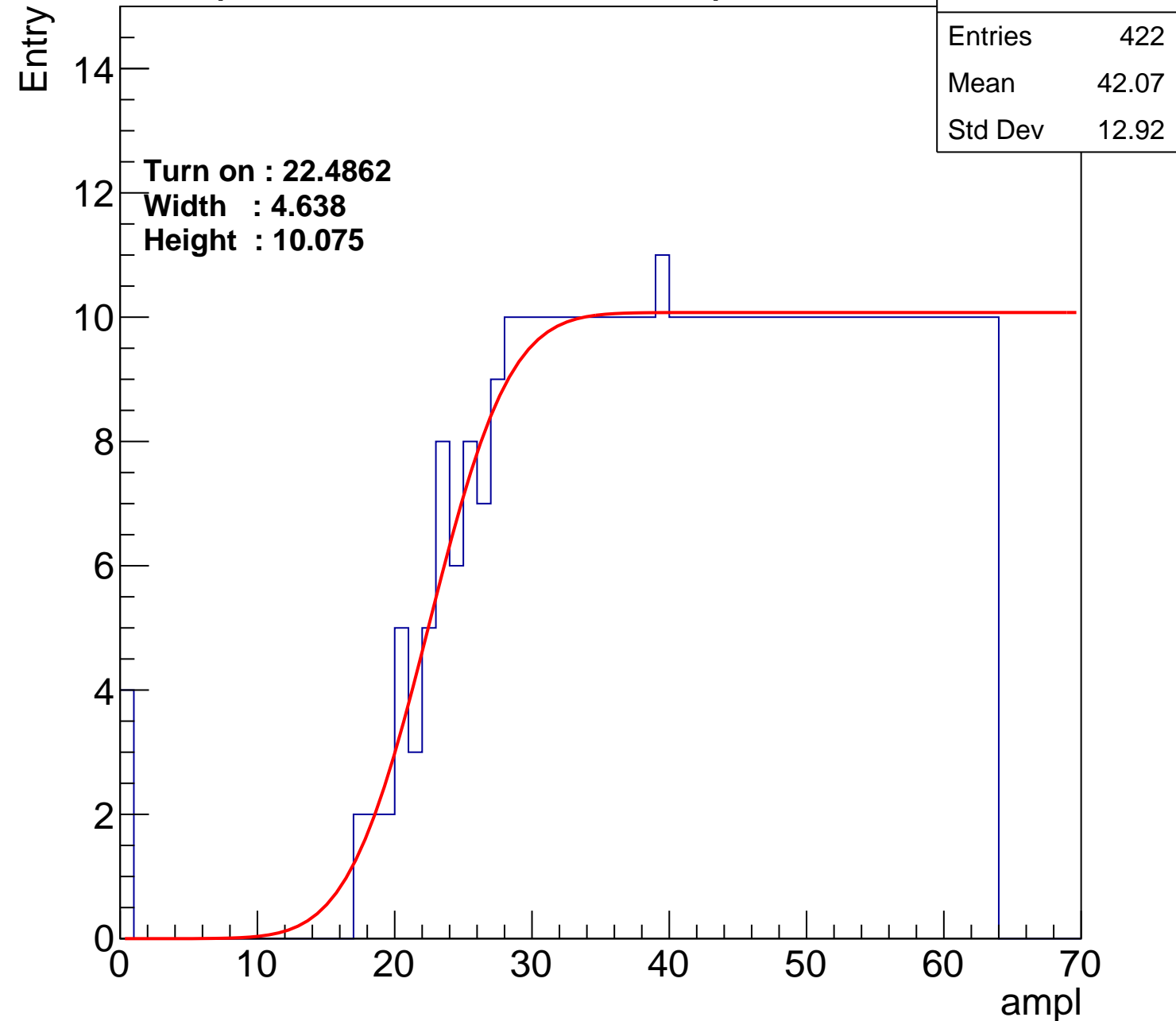
Width : 4.638

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.23
Std Dev	11.38

Turn on : 25.7580

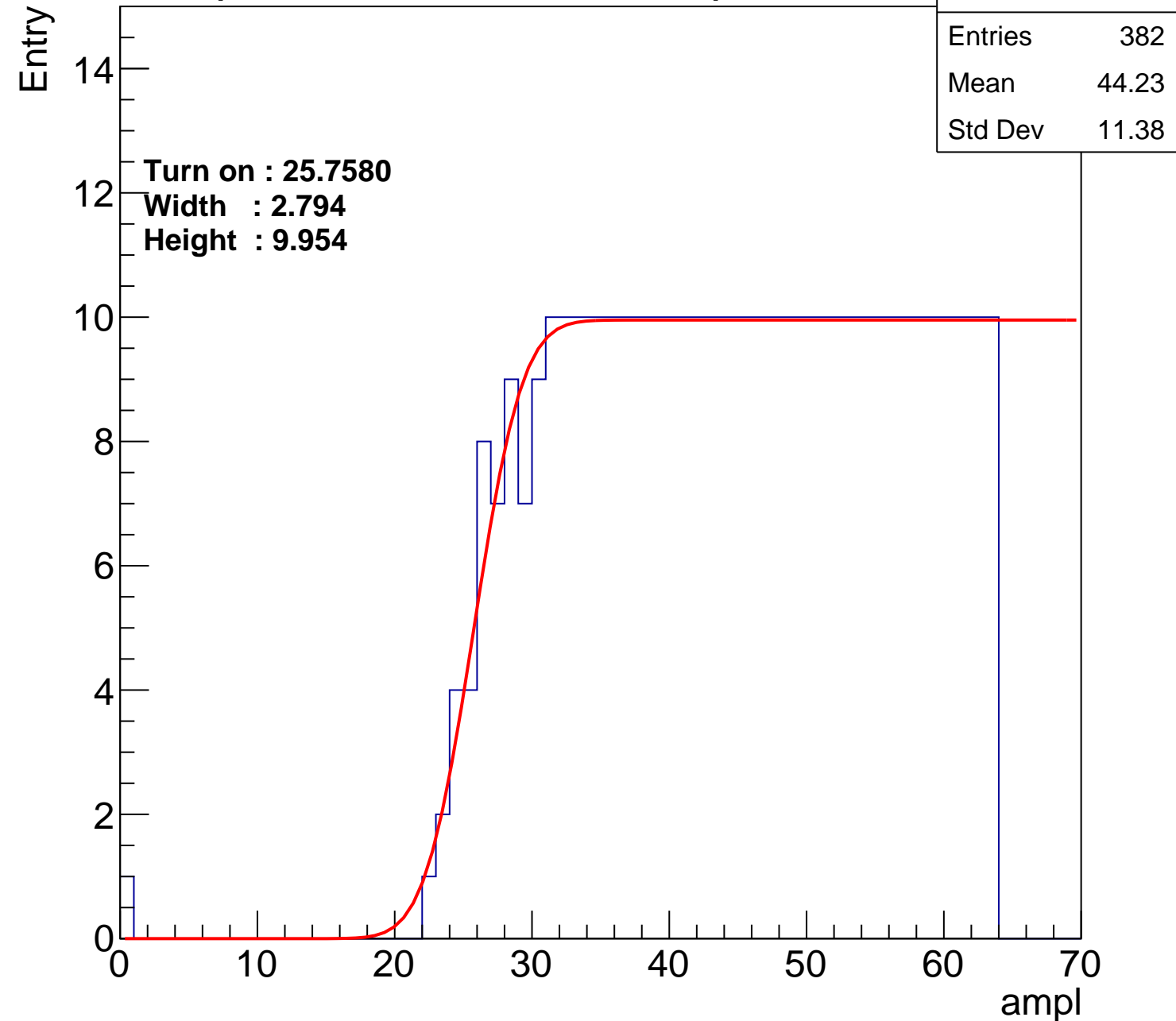
Width : 2.794

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	43.83
Std Dev	12.01

Turn on : 27.0137

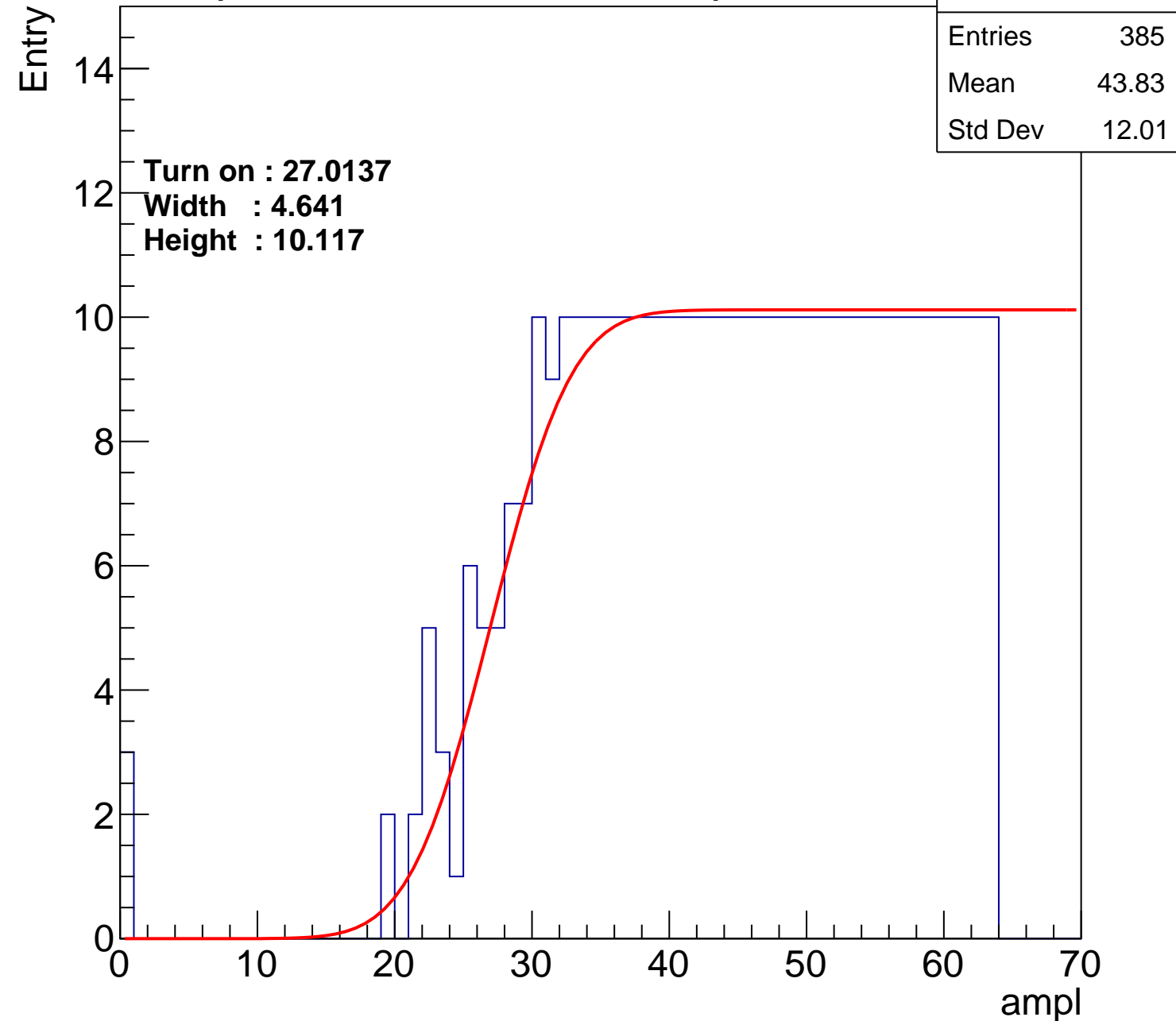
Width : 4.641

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.94
Std Dev	11.93

Turn on : 25.9626

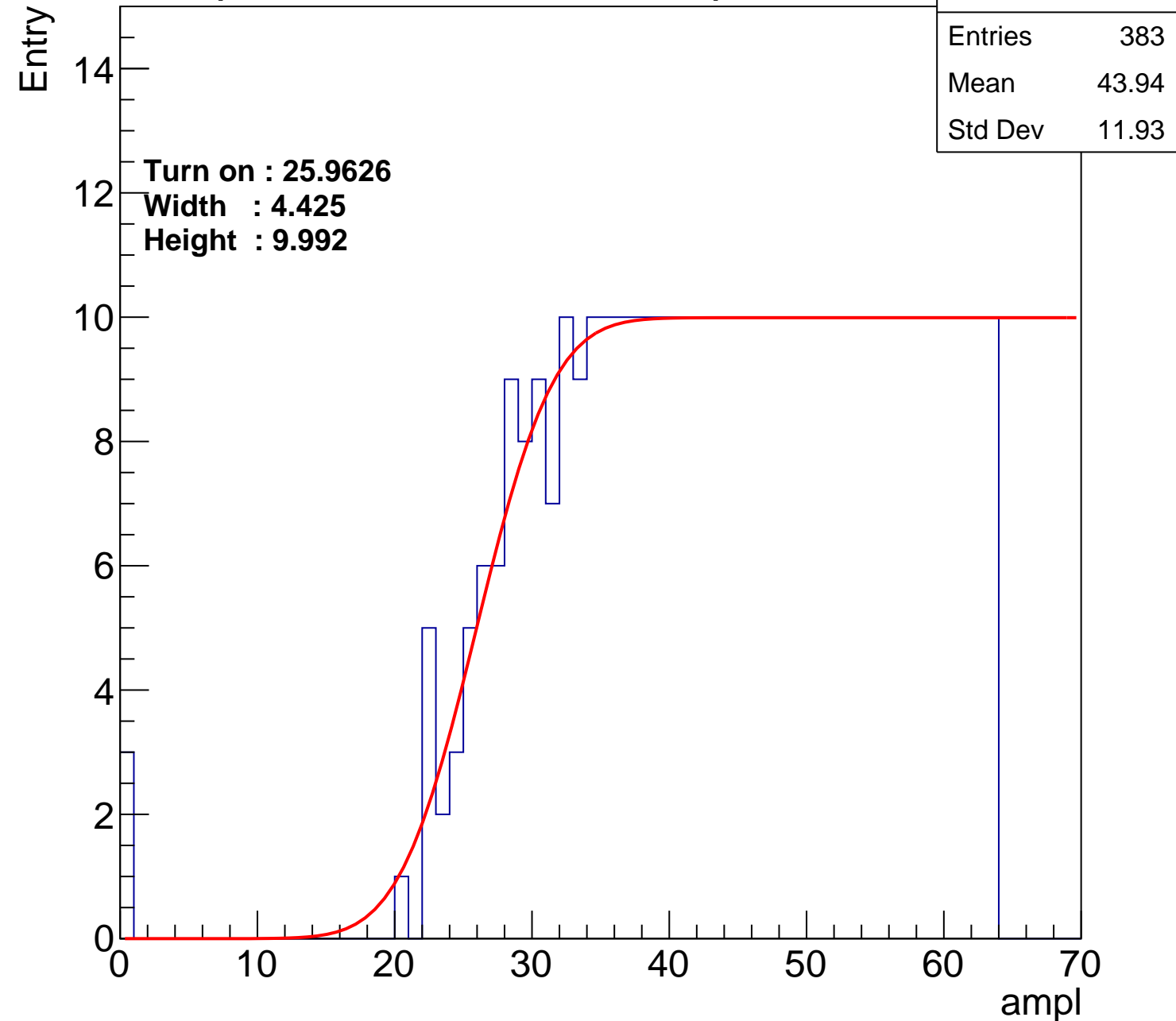
Width : 4.425

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U3-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	43.94
Std Dev	11.93

Turn on : 25.9626

Width : 4.425

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl

