

B0L001S, U14-ch0, adc0

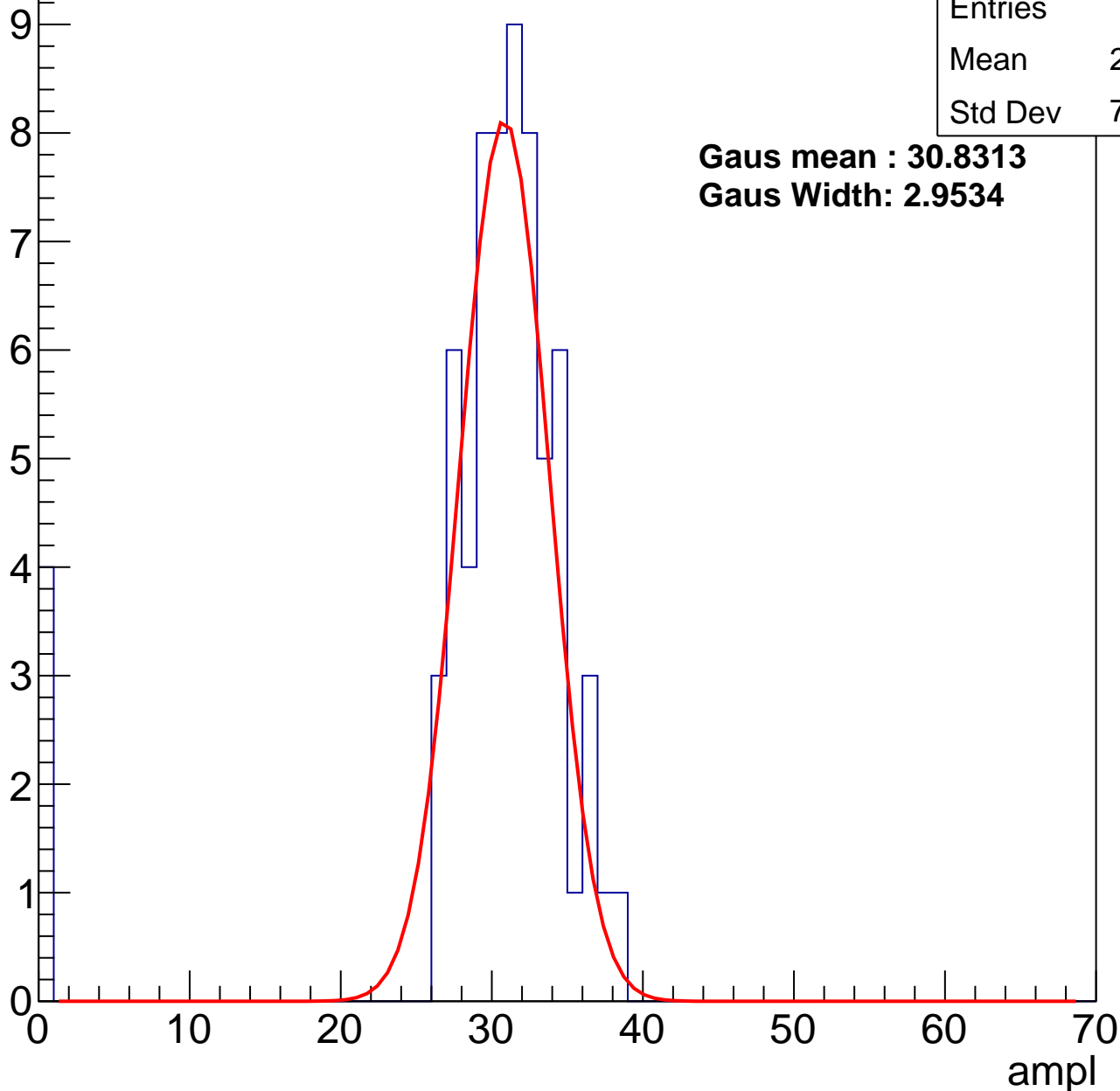
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.04
Std Dev	7.816

Gaus mean : 30.8313

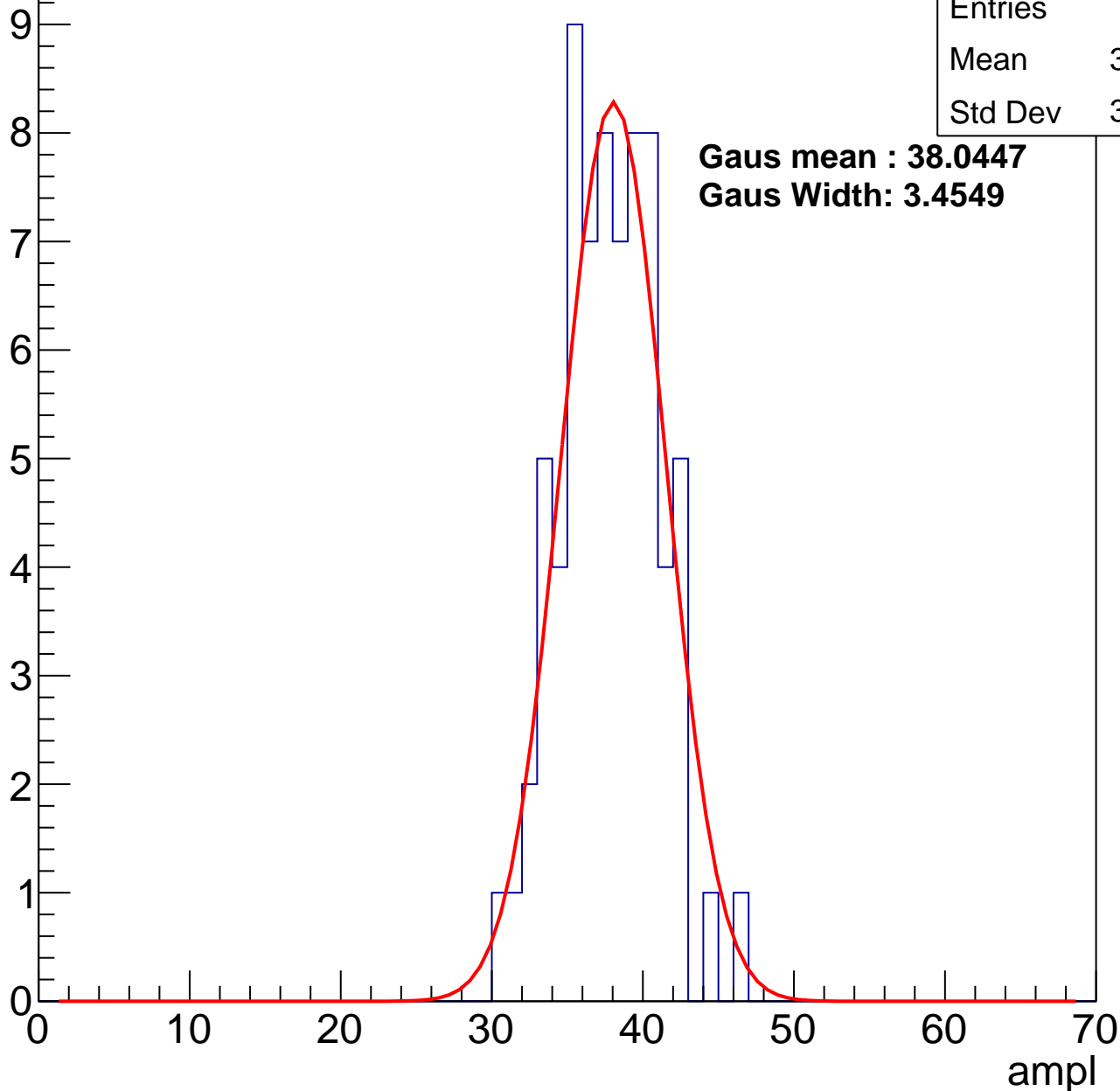
Gaus Width: 2.9534



B0L001S, U14-ch0, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch0, adc2

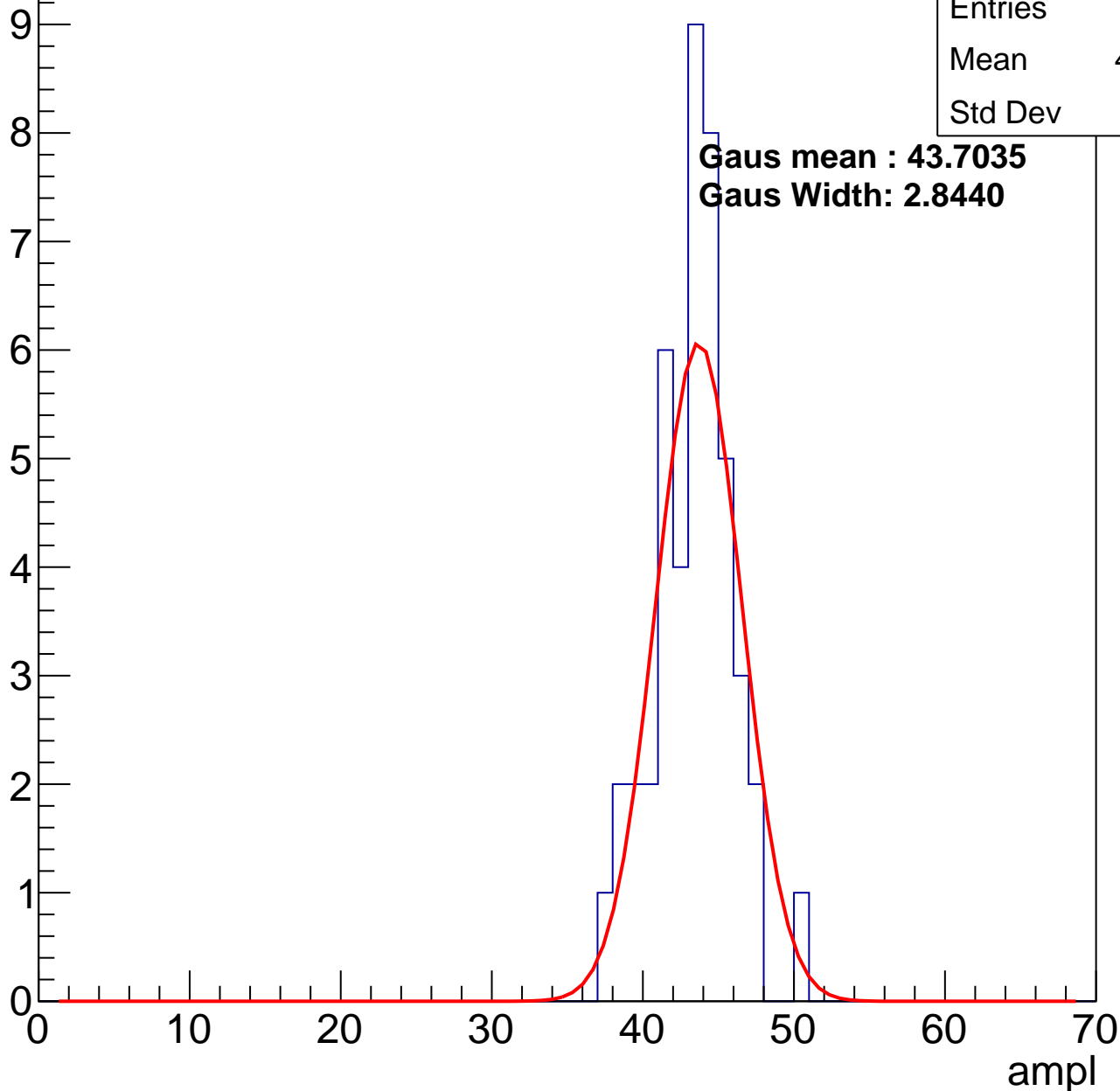
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	42.91
Std Dev	2.58

Gaus mean : 43.7035

Gaus Width: 2.8440

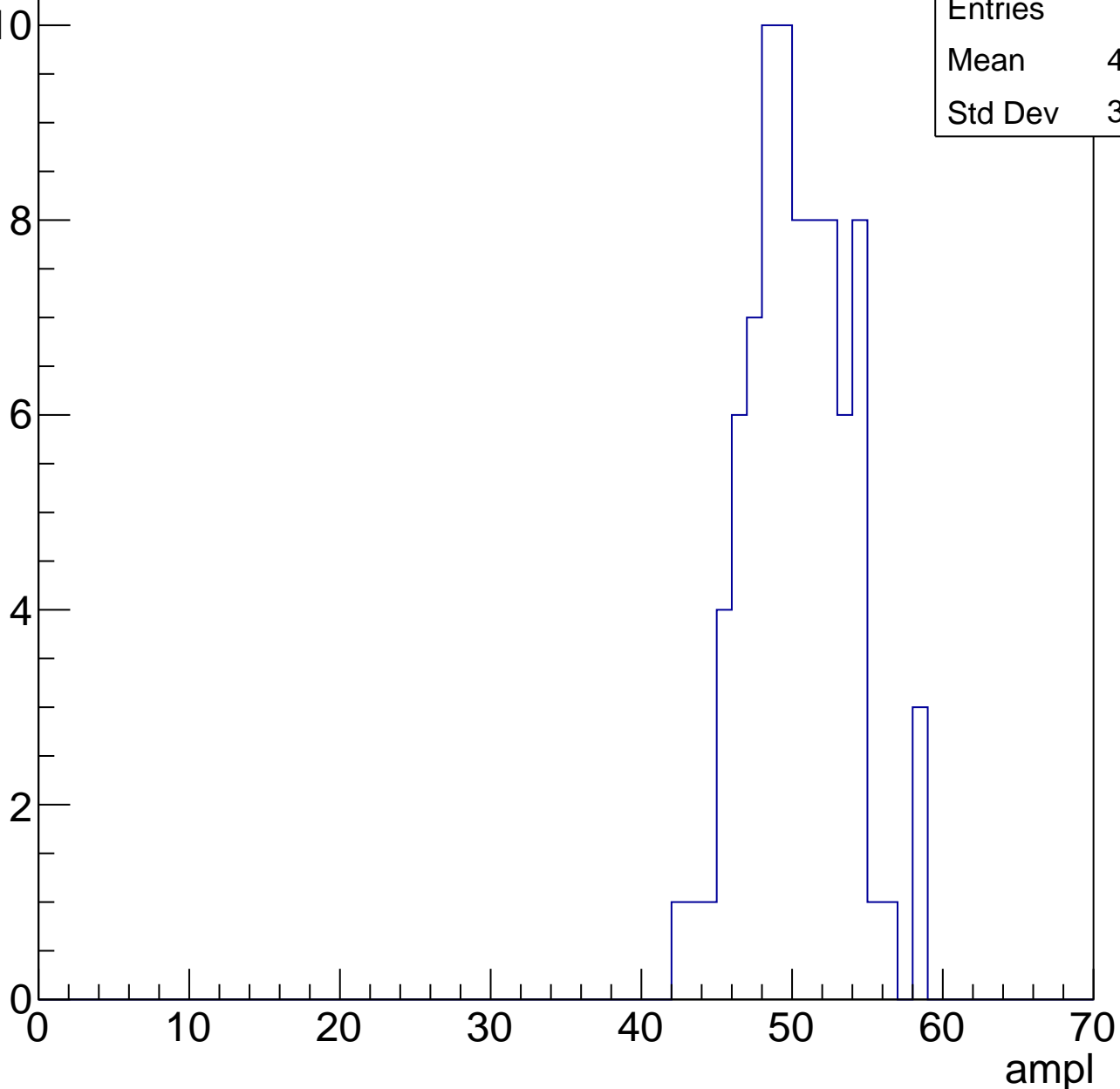


B0L001S, U14-ch0, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	49.92
Std Dev	3.352

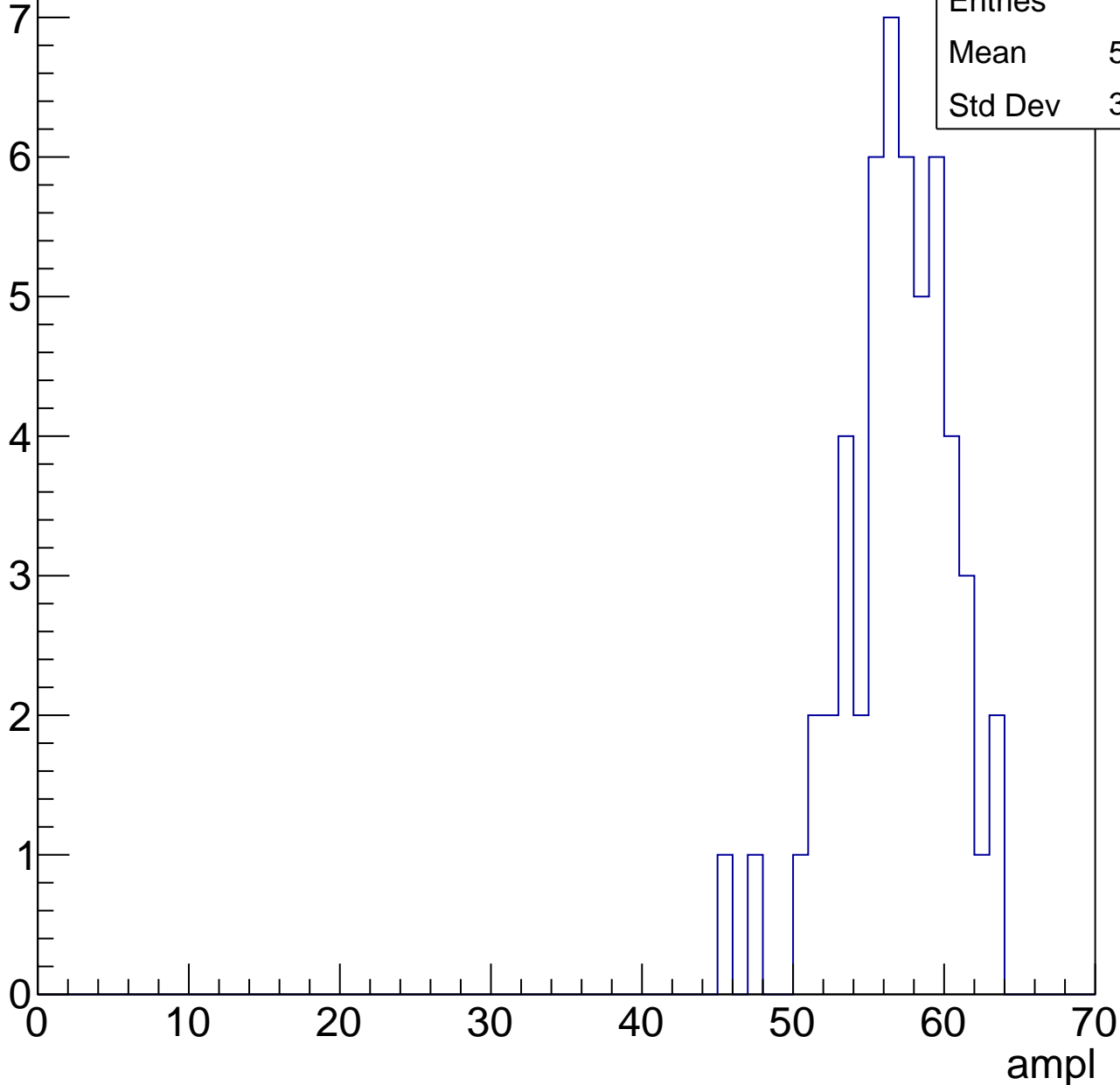


B0L001S, U14-ch0, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.36
Std Dev	3.676

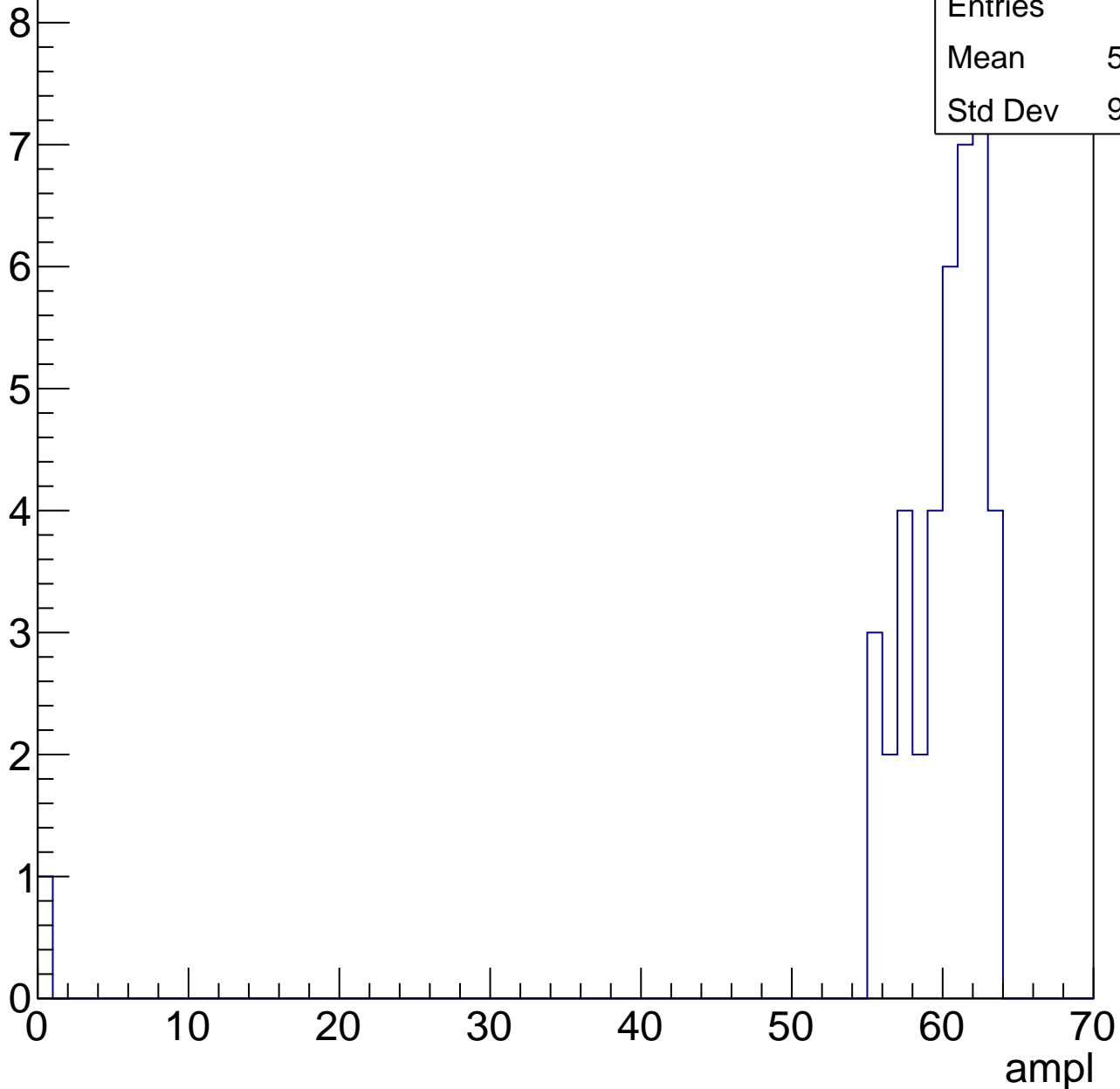


B0L001S, U14-ch0, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

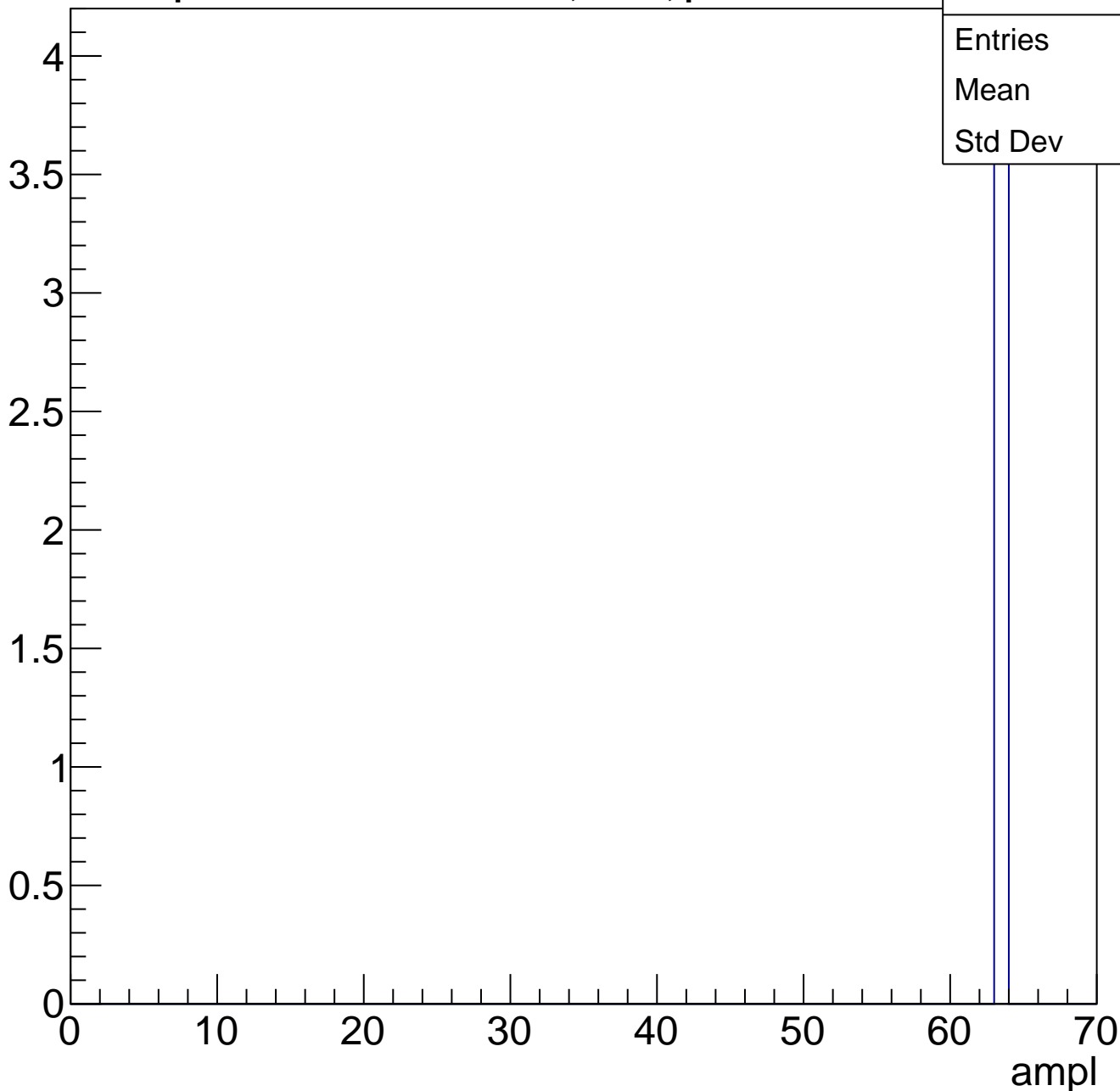
Entries	41
Mean	58.34
Std Dev	9.522



B0L001S, U14-ch0, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch0, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	62
Std Dev	0

B0L001S, U14-ch1, adc0

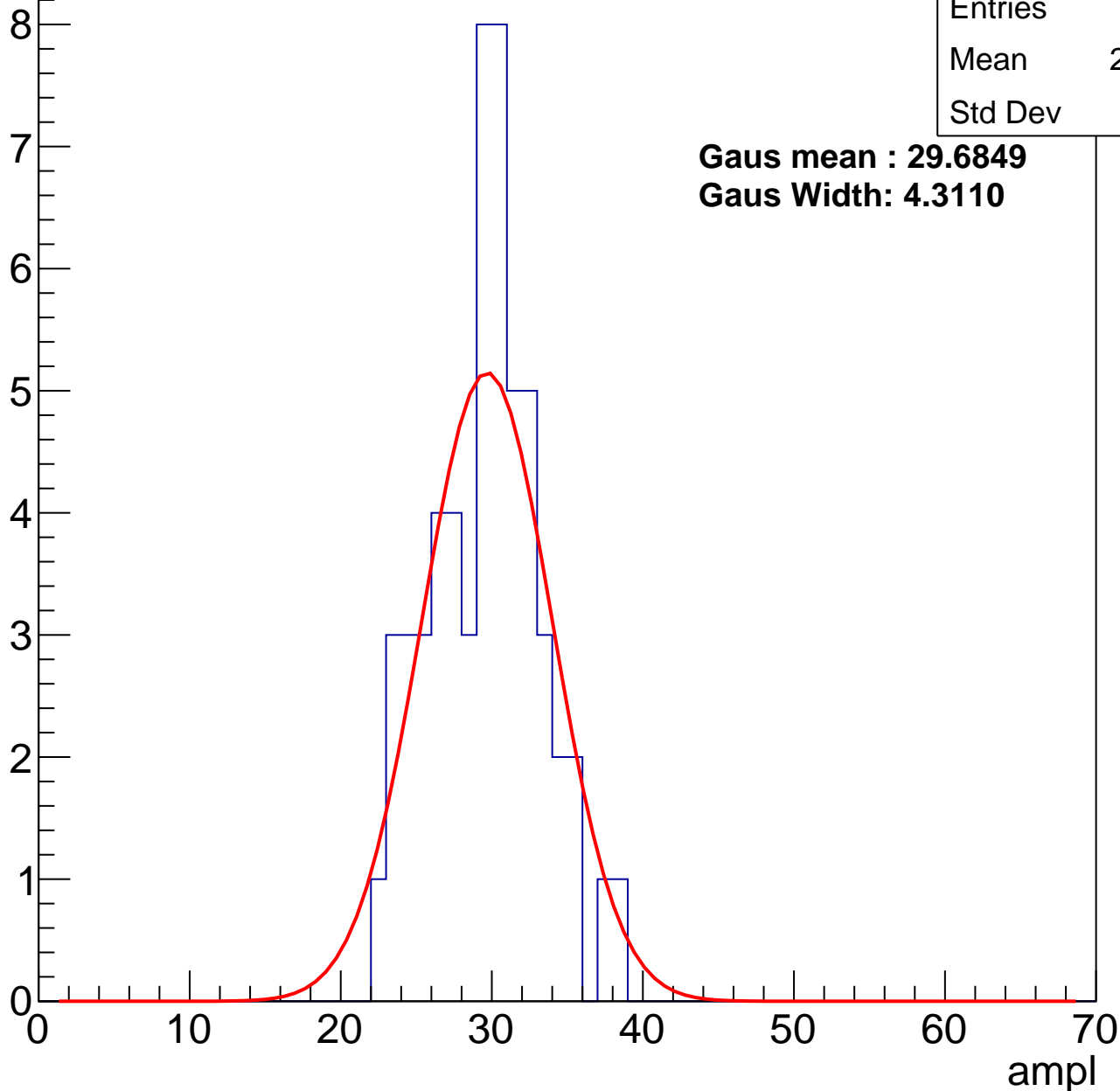
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.16
Std Dev	3.59

Gaus mean : 29.6849

Gaus Width: 4.3110



B0L001S, U14-ch1, adc1

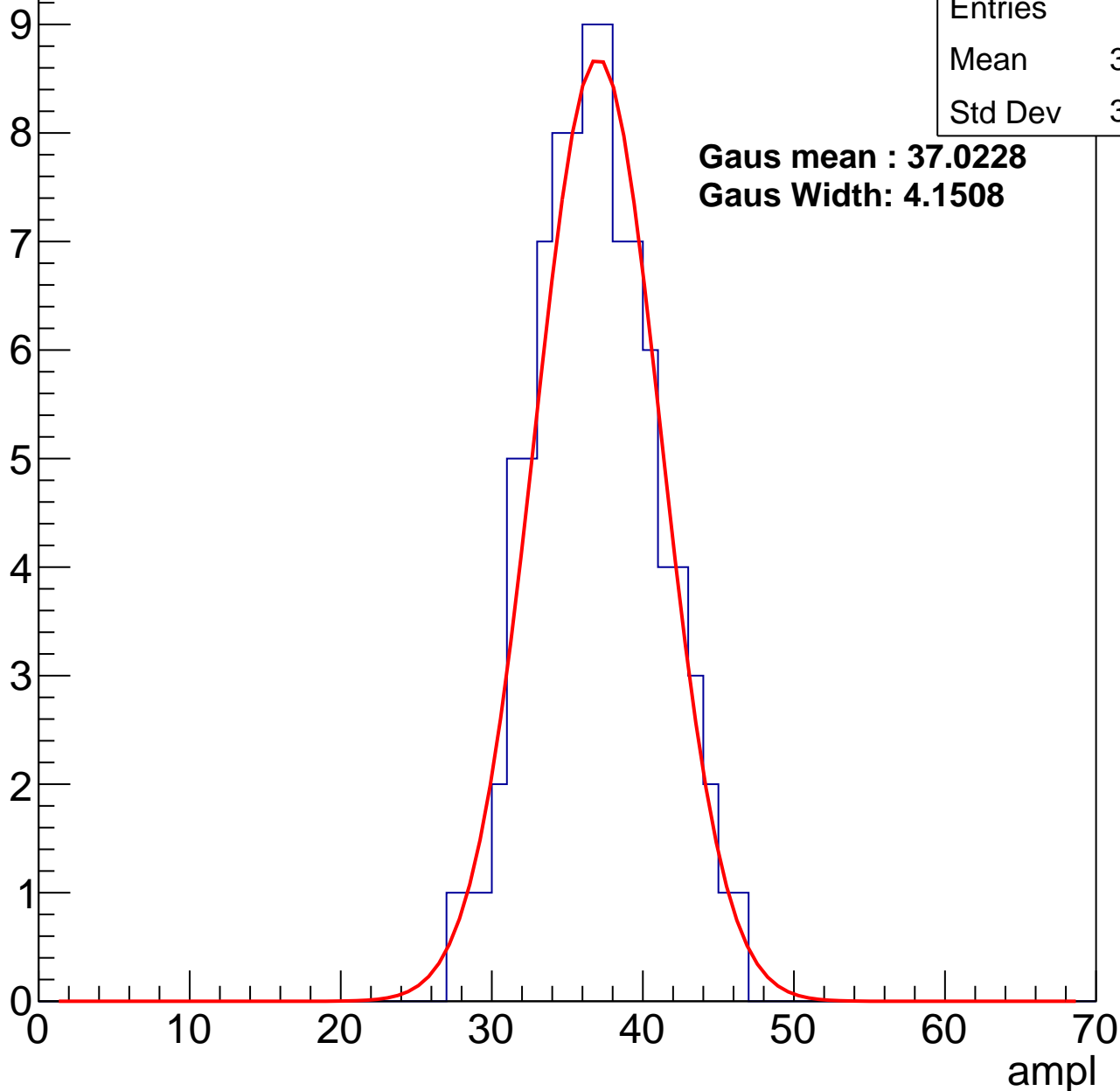
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	91
Mean	36.46
Std Dev	3.993

Gaus mean : 37.0228

Gaus Width: 4.1508



B0L001S, U14-ch1, adc2

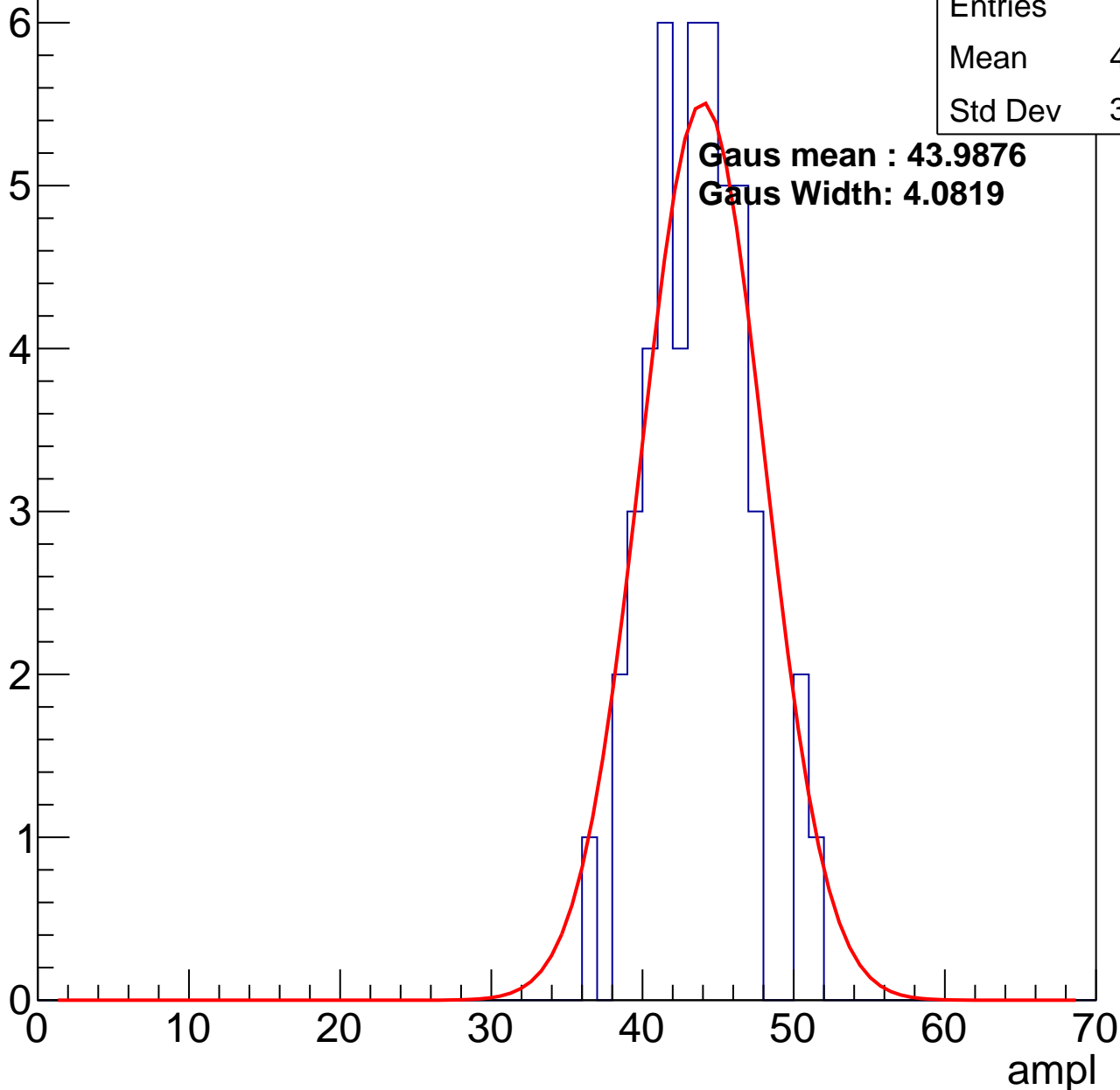
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	43.17
Std Dev	3.197

Gaus mean : 43.9876

Gaus Width: 4.0819

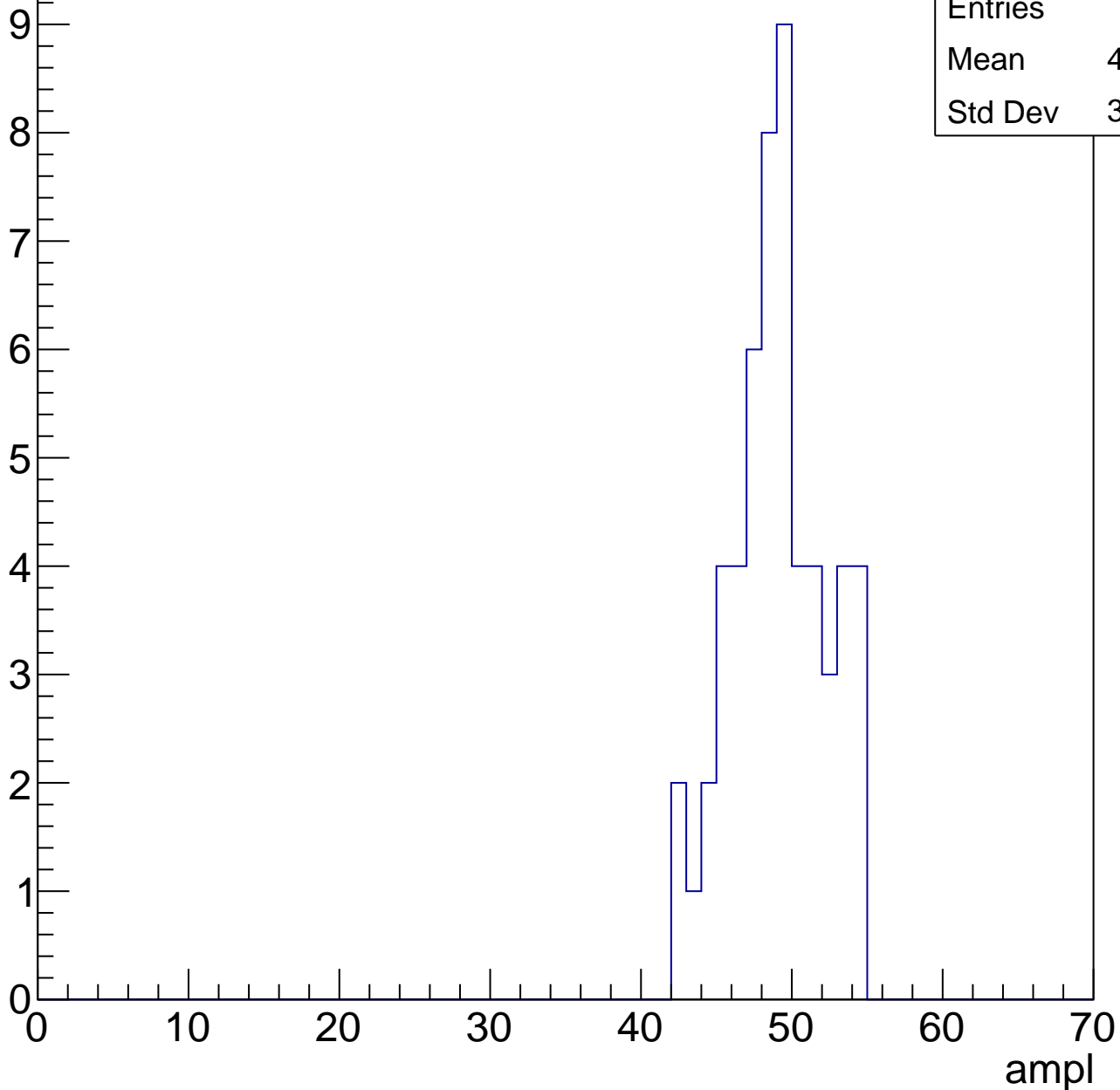


B0L001S, U14-ch1, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	48.62
Std Dev	3.072

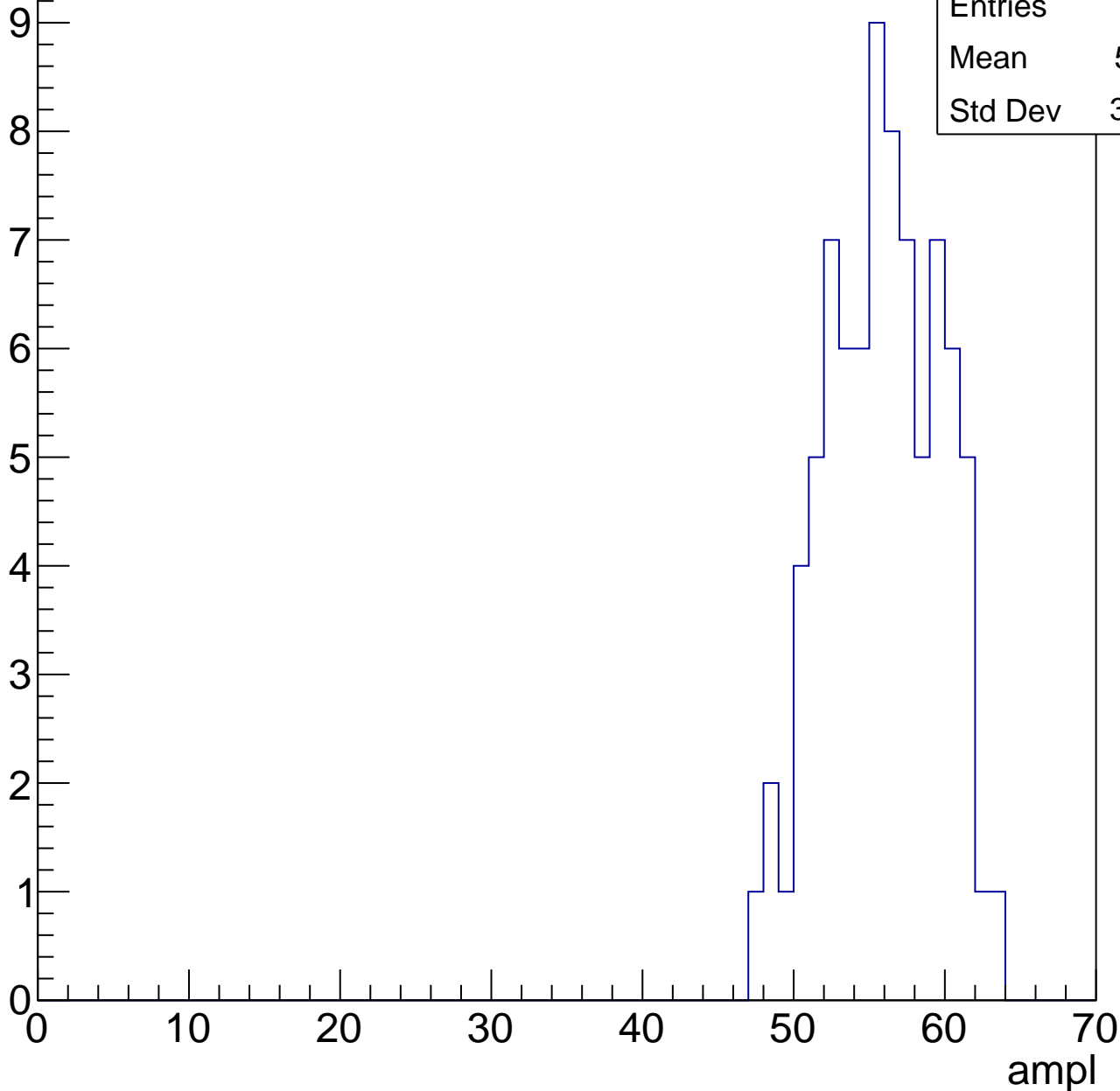


B0L001S, U14-ch1, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	55.41
Std Dev	3.674

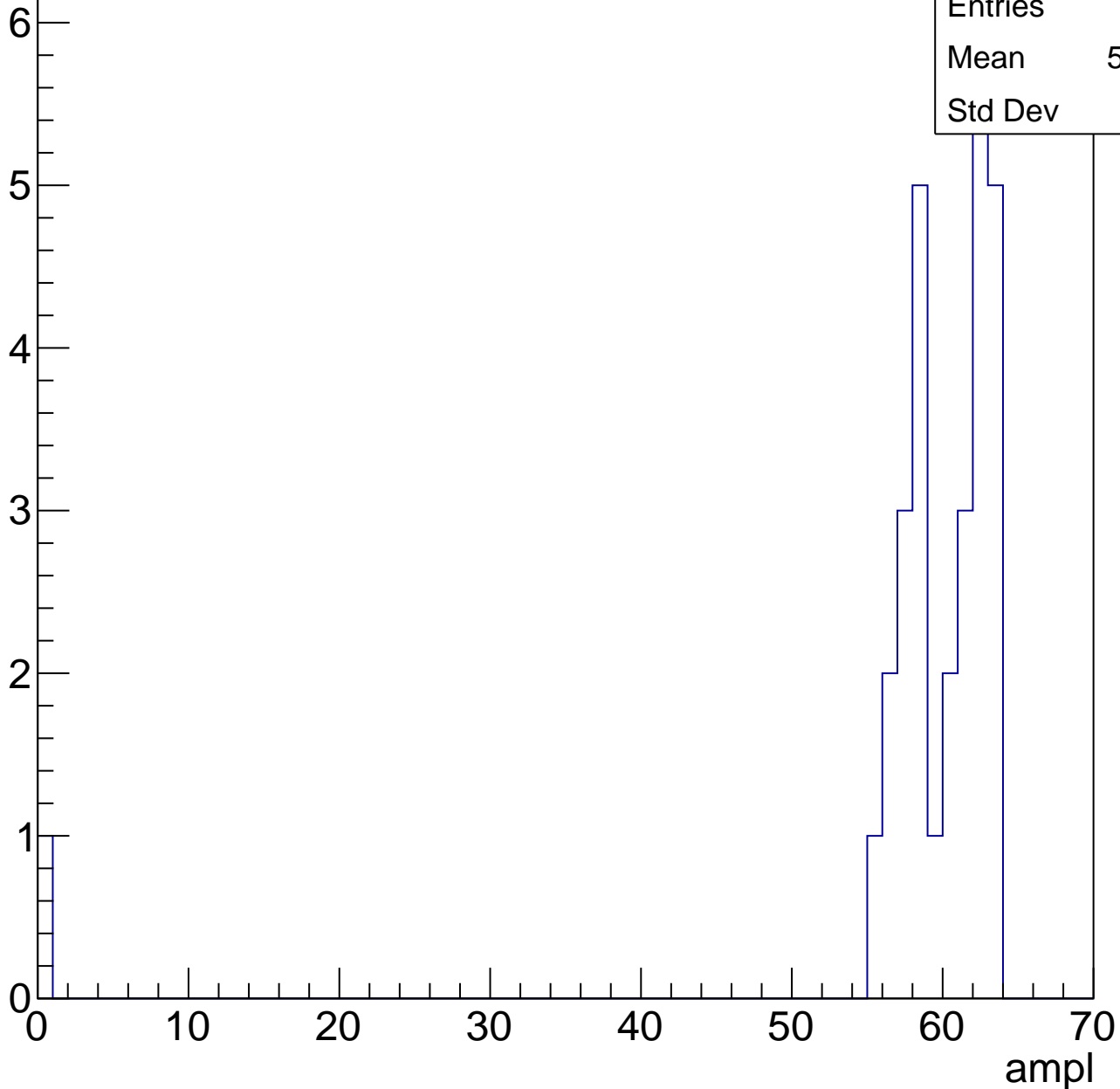


B0L001S, U14-ch1, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

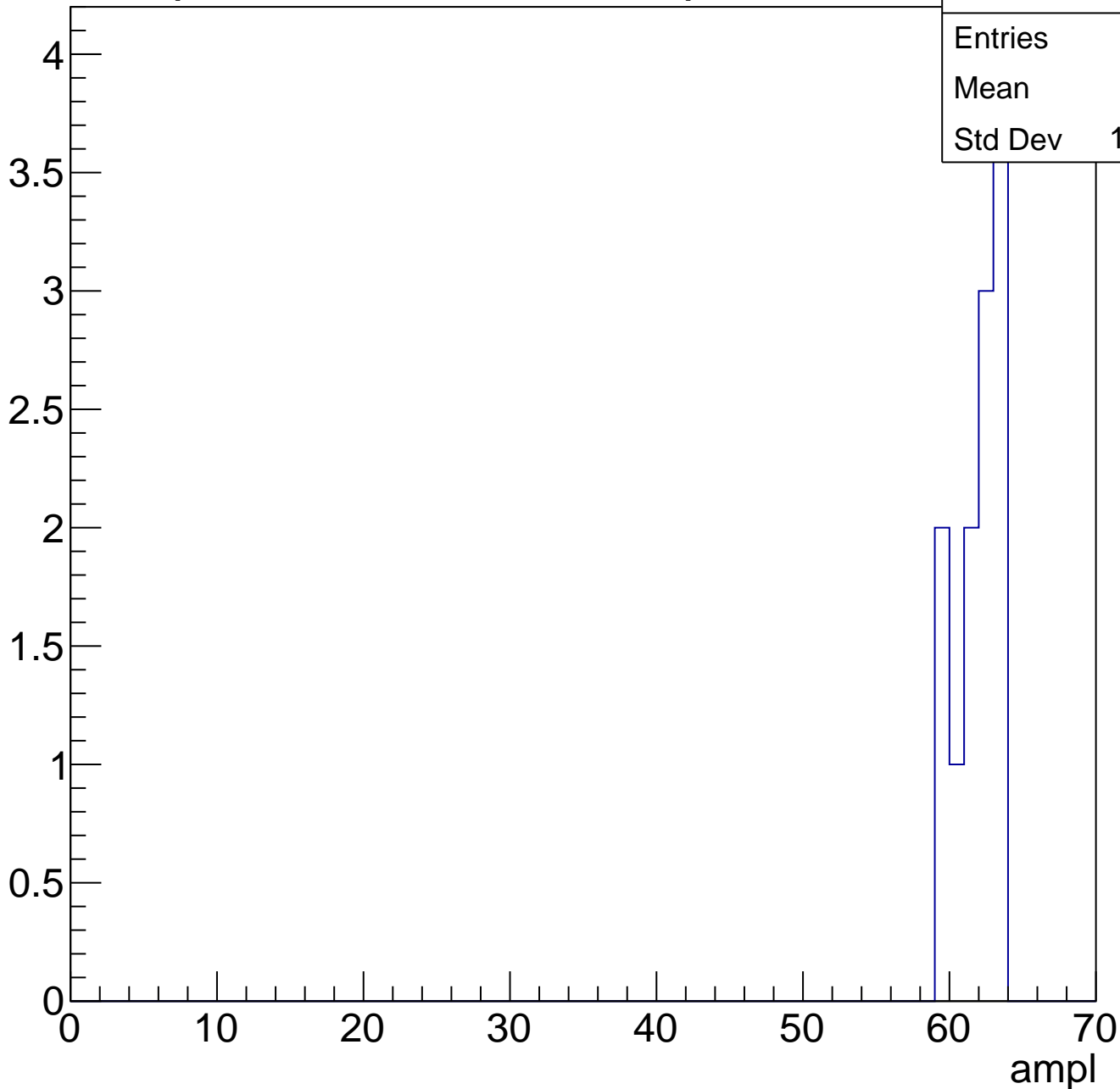
Entries	29
Mean	57.83
Std Dev	11.2



B0L001S, U14-ch1, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch1, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch2, adc0

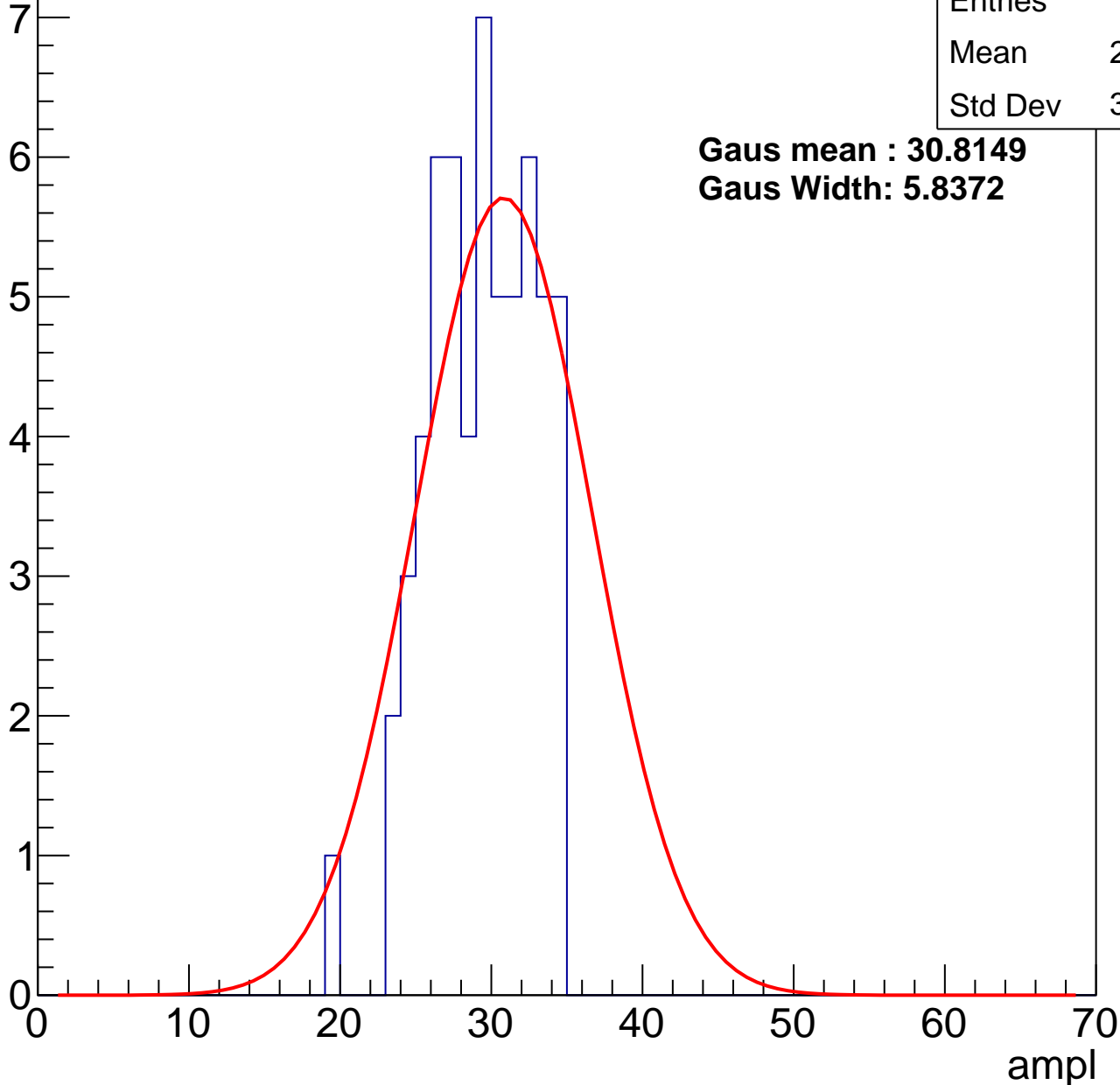
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	28.85
Std Dev	3.384

Gaus mean : 30.8149

Gaus Width: 5.8372



B0L001S, U14-ch2, adc1

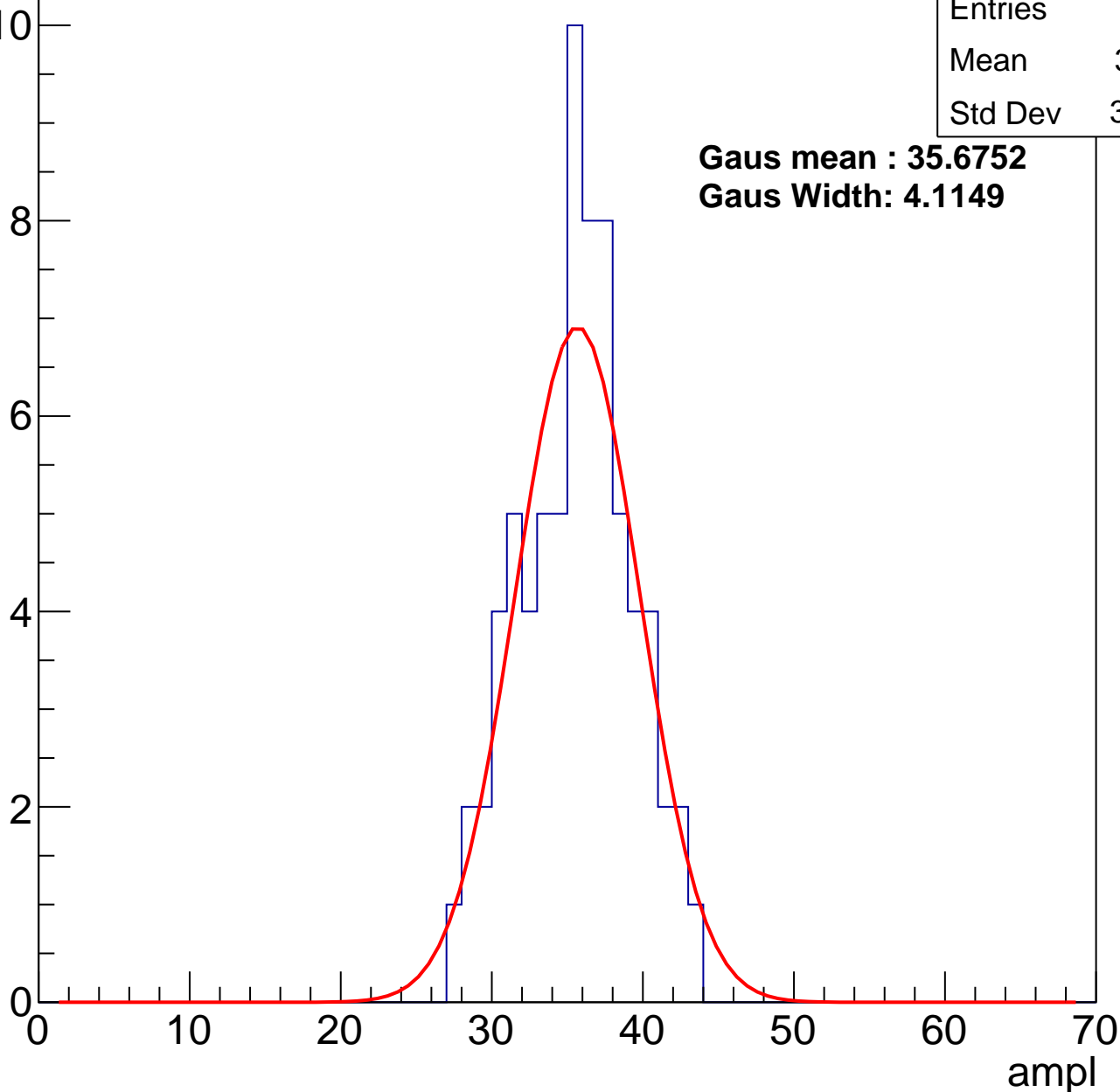
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	35.11
Std Dev	3.646

Gaus mean : 35.6752

Gaus Width: 4.1149



B0L001S, U14-ch2, adc2

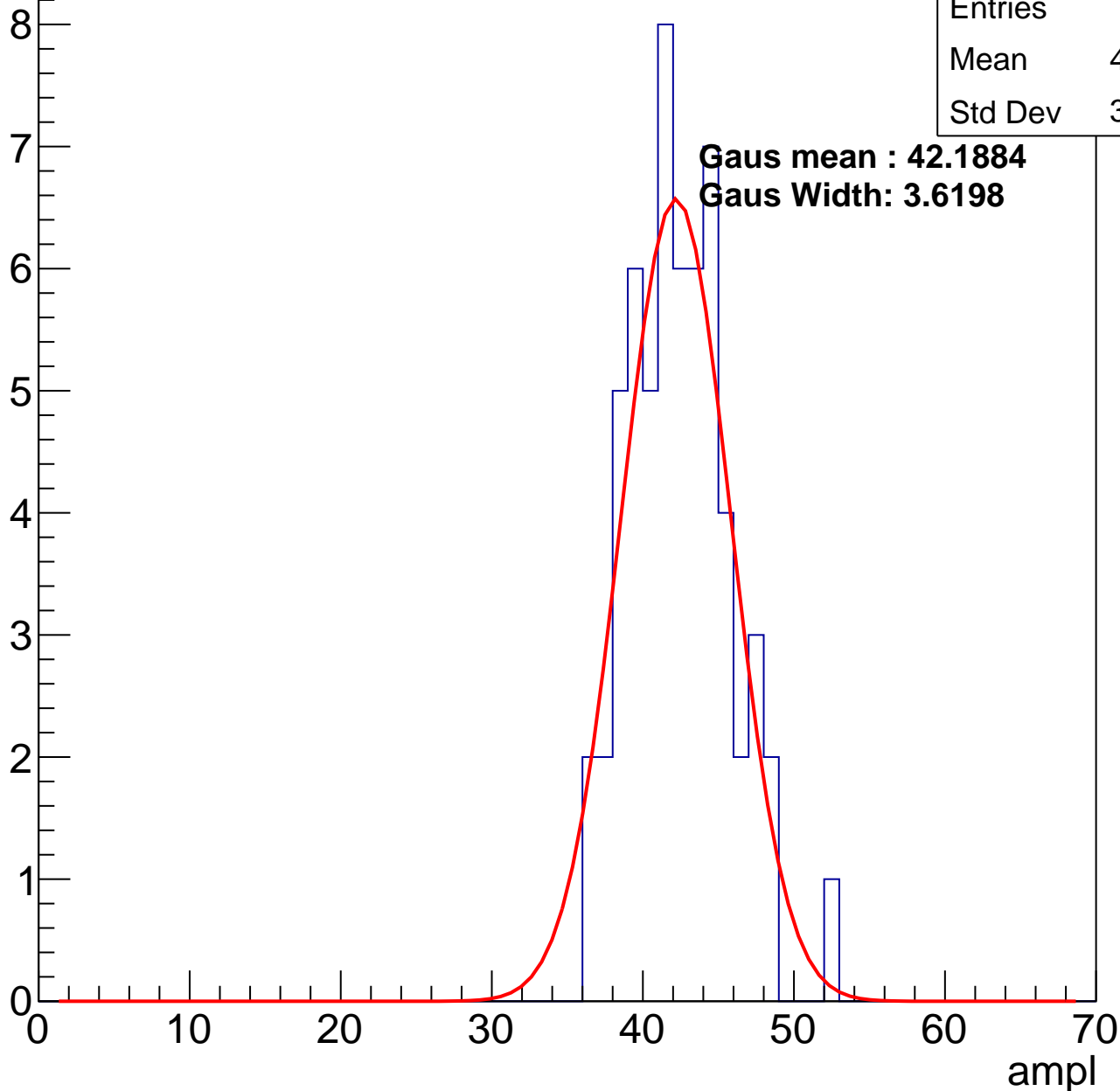
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	41.98
Std Dev	3.275

Gaus mean : 42.1884

Gaus Width: 3.6198

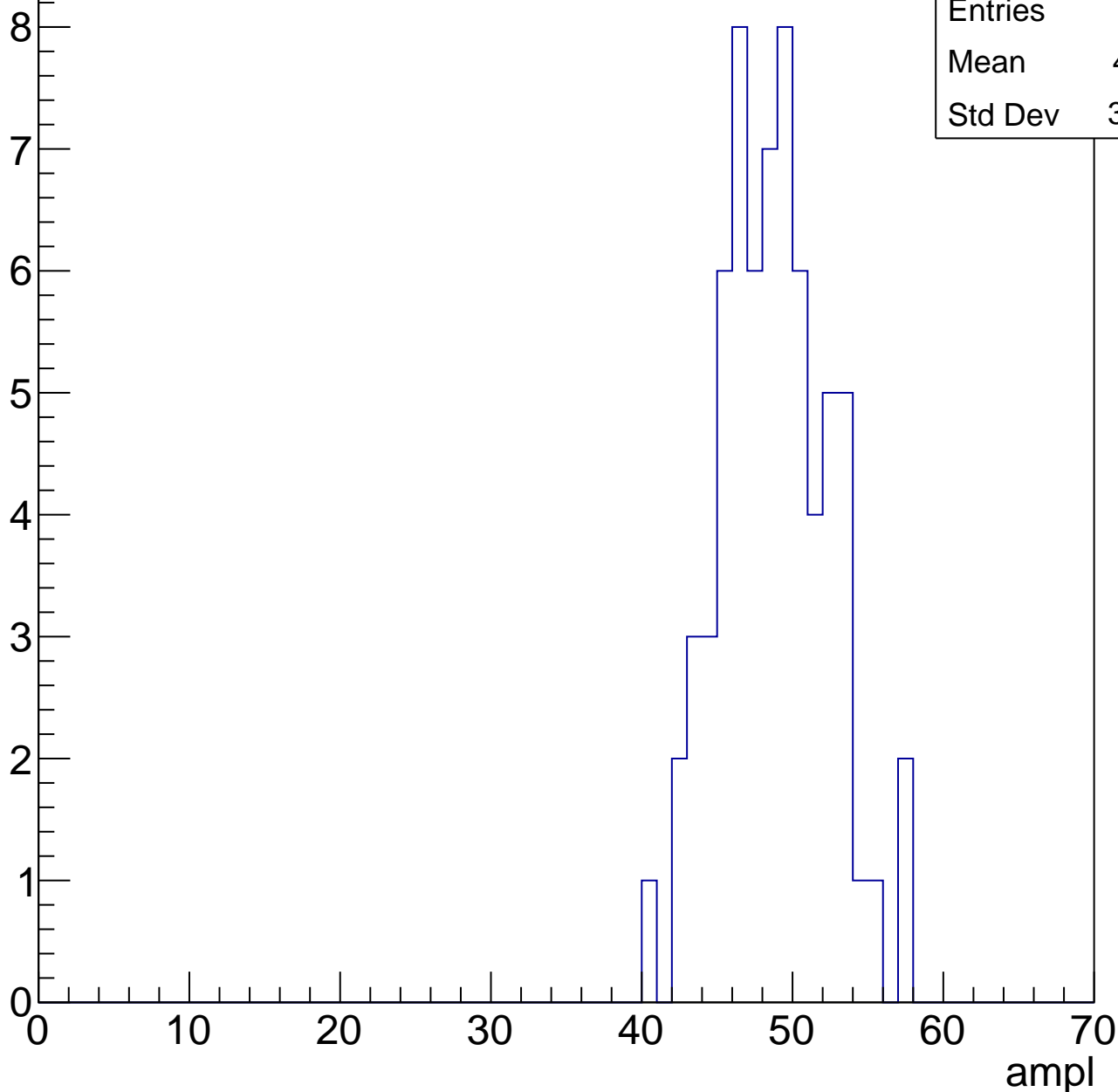


B0L001S, U14-ch2, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	48.31
Std Dev	3.566

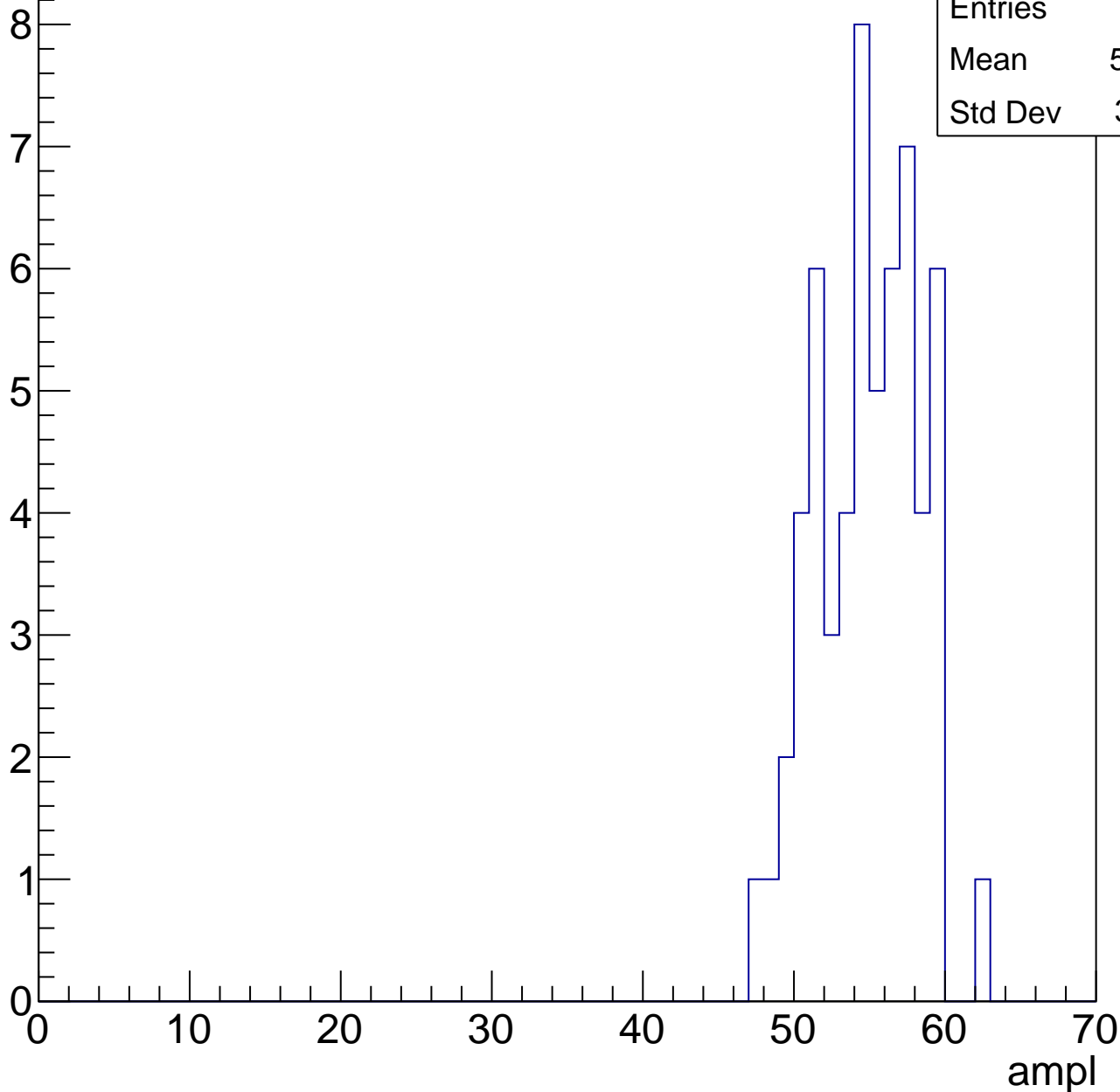


B0L001S, U14-ch2, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	54.43
Std Dev	3.291

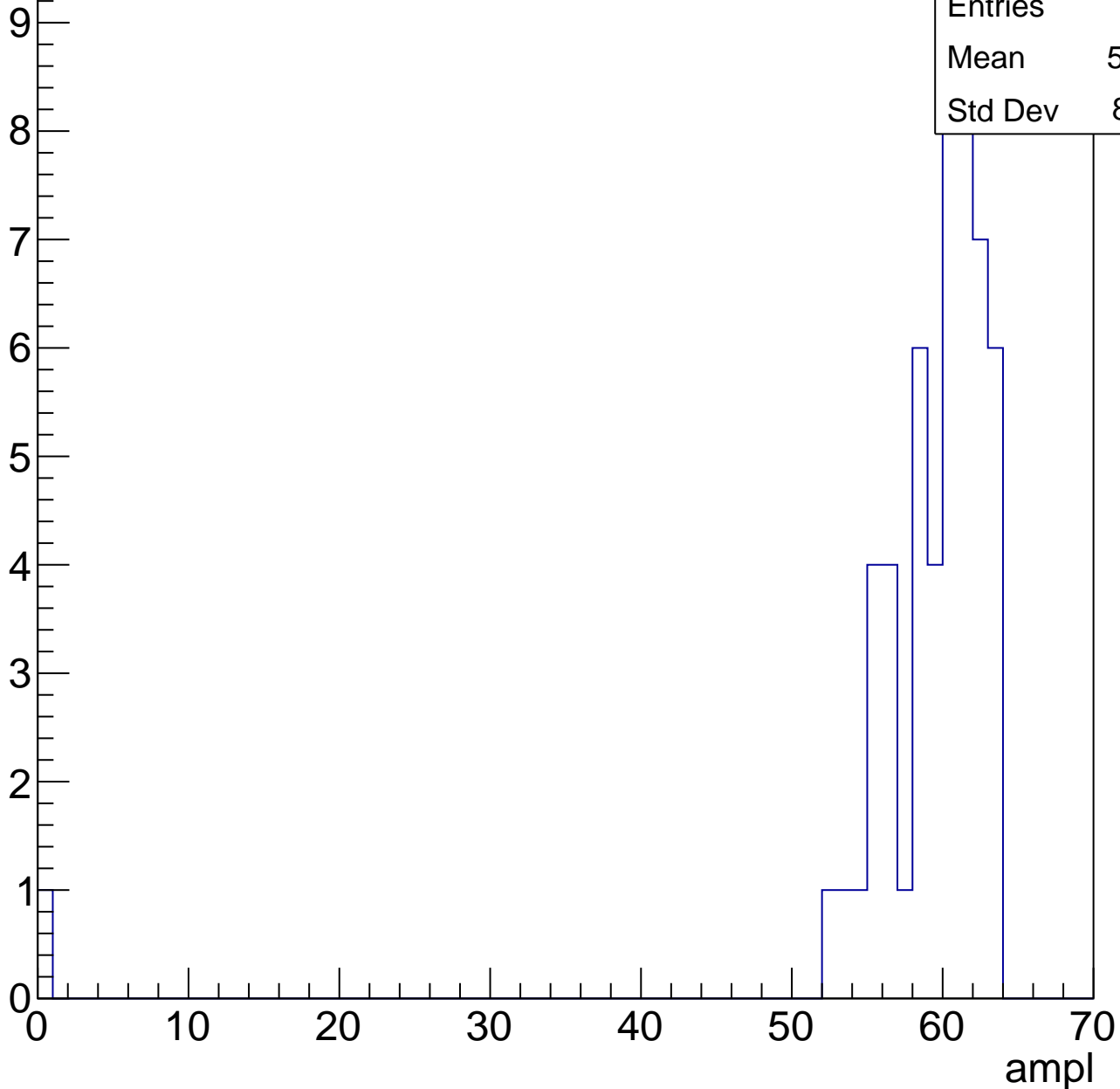


B0L001S, U14-ch2, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

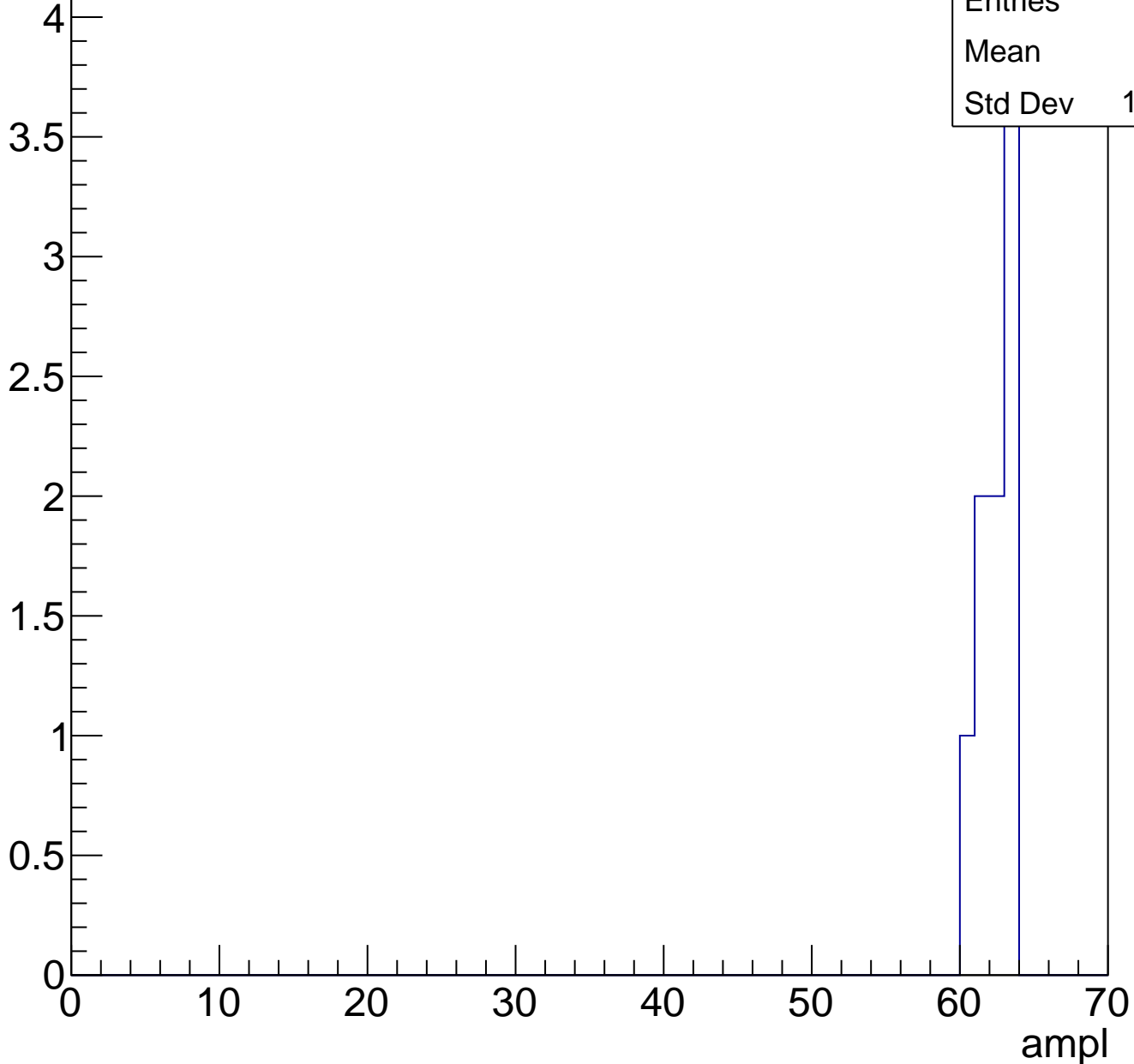
Entries	53
Mean	58.19
Std Dev	8.541



B0L001S, U14-ch2, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch2, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch3, adc0

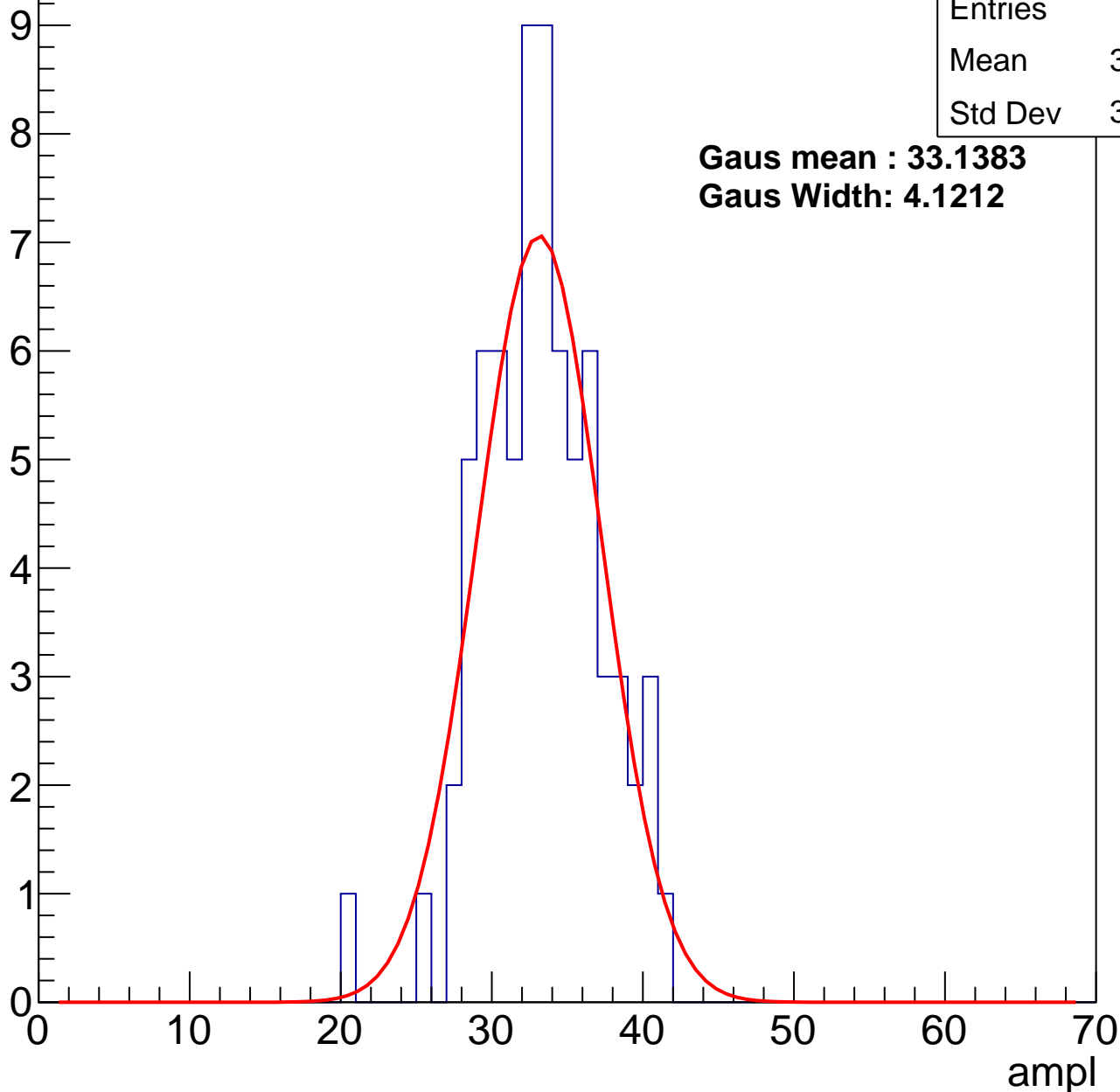
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	32.77
Std Dev	3.866

Gaus mean : 33.1383

Gaus Width: 4.1212



B0L001S, U14-ch3, adc1

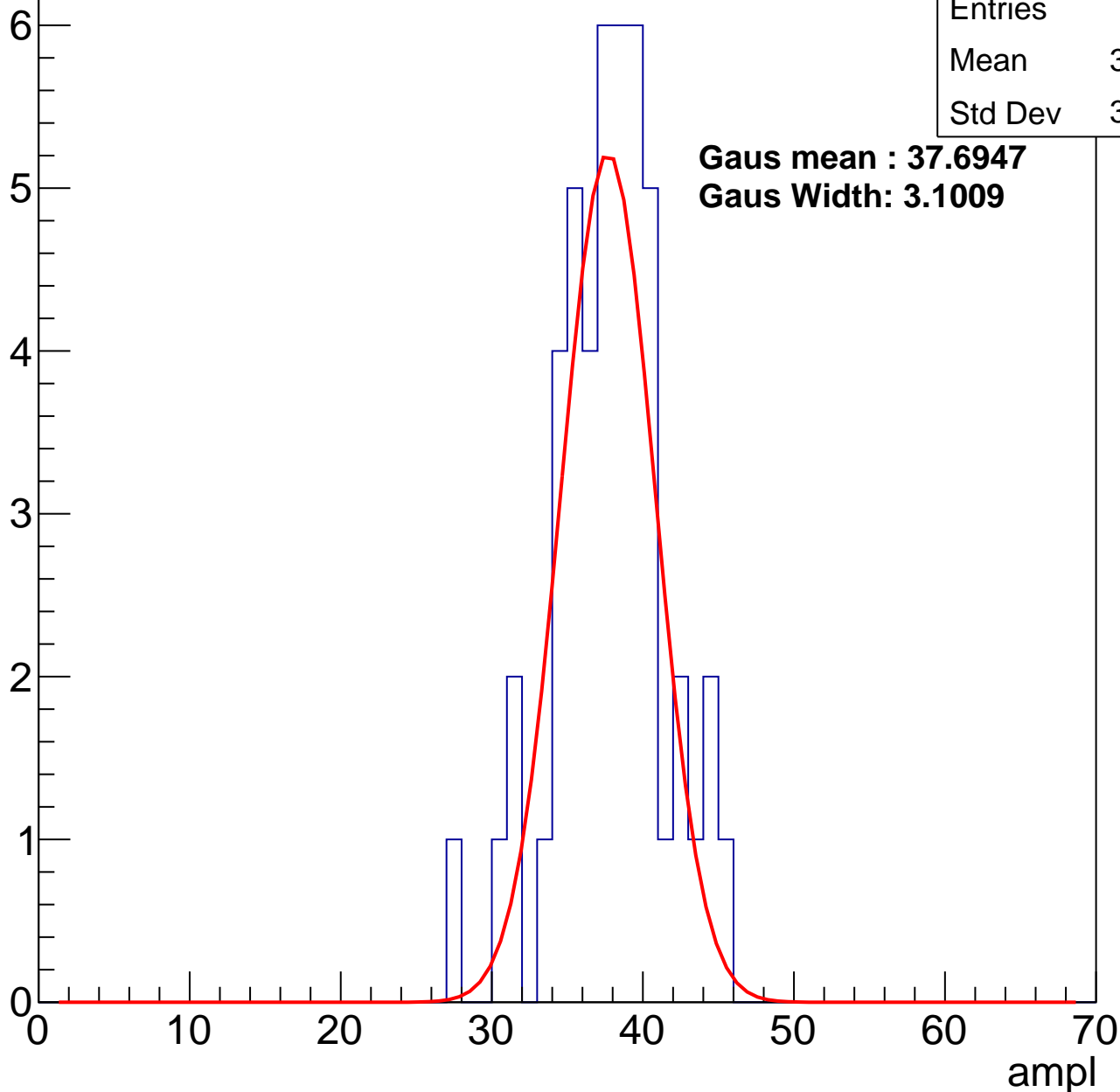
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	37.33
Std Dev	3.625

Gaus mean : 37.6947

Gaus Width: 3.1009



B0L001S, U14-ch3, adc2

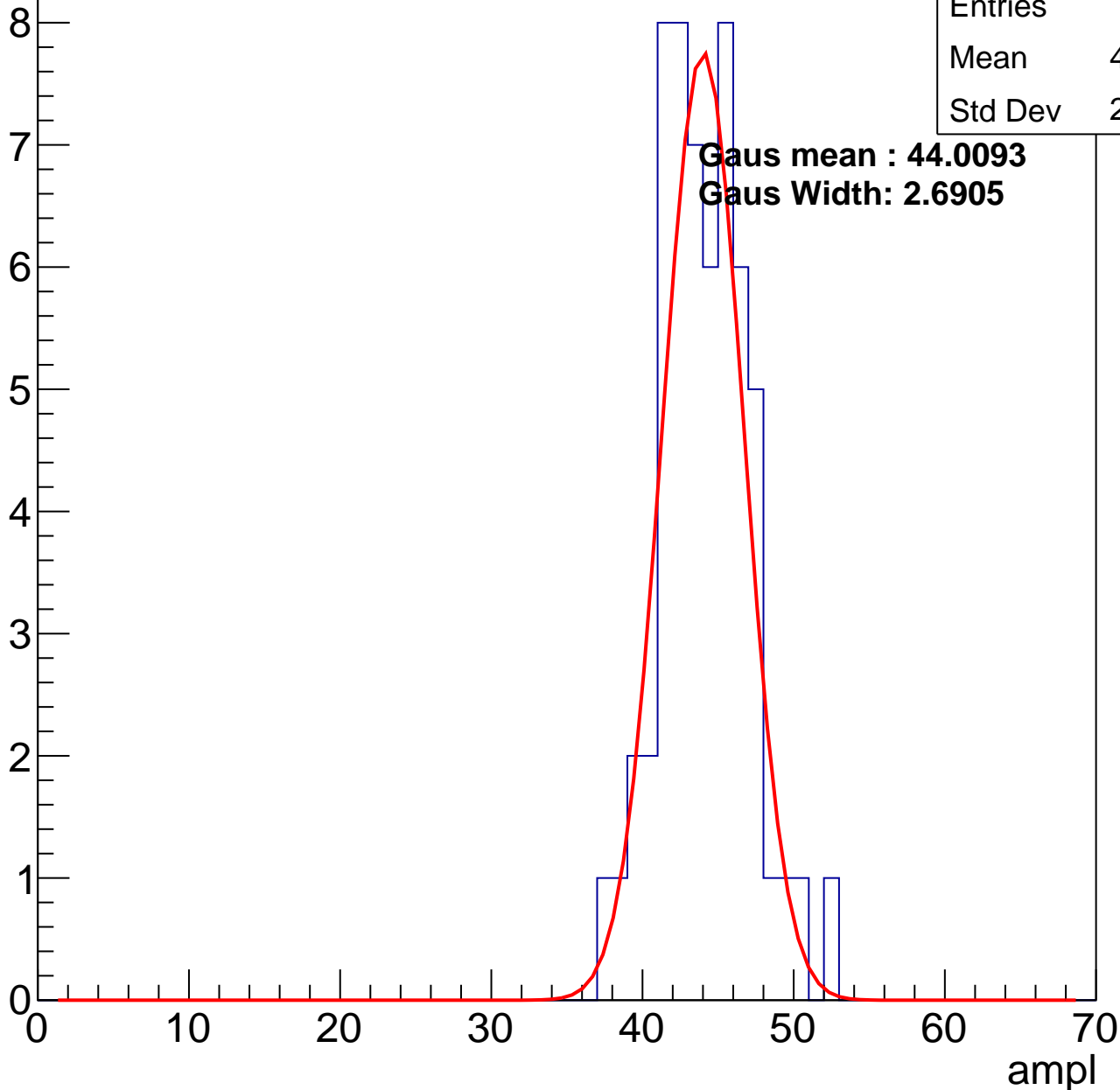
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.66
Std Dev	2.904

Gaus mean : 44.0093

Gaus Width: 2.6905

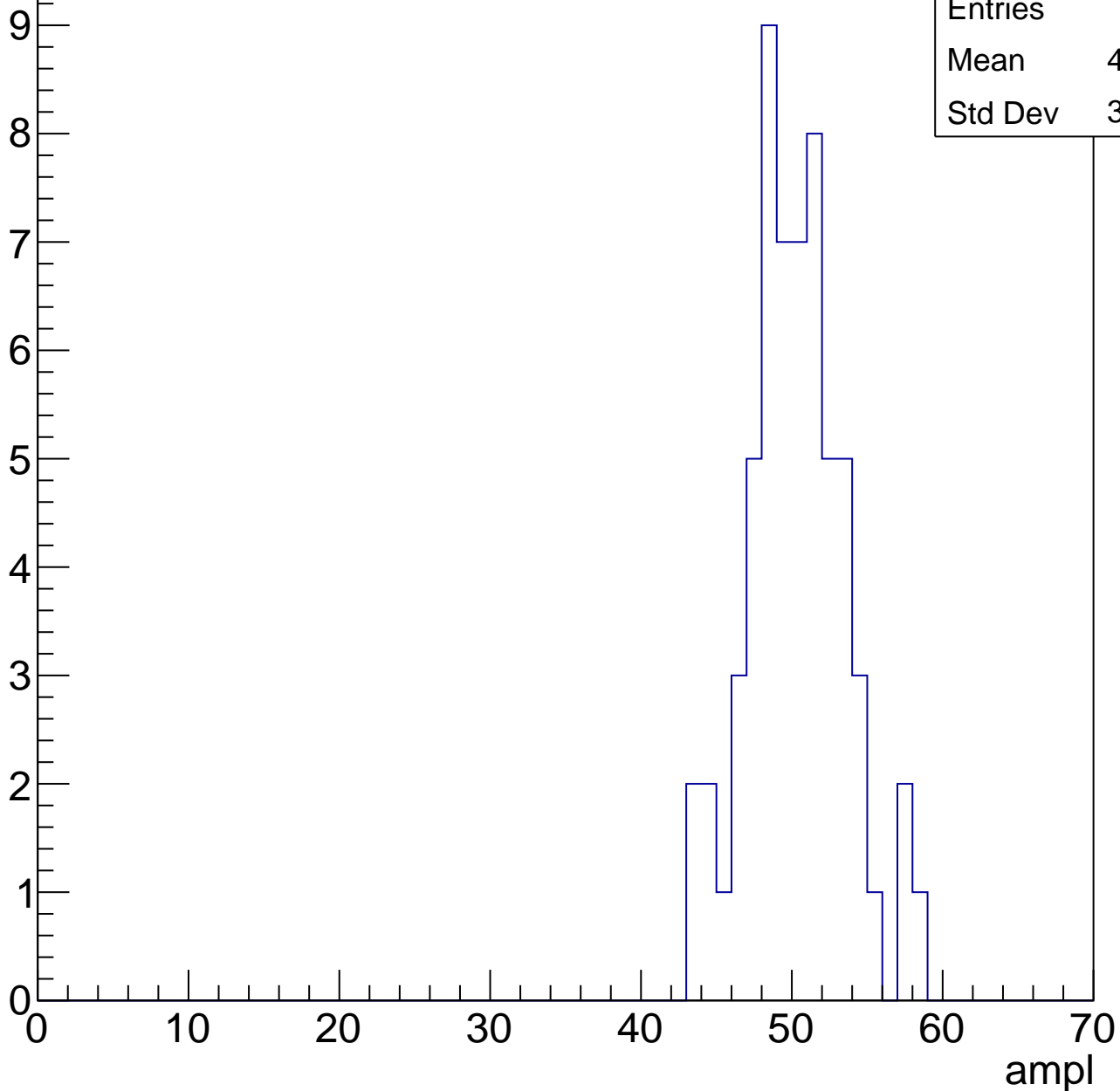


B0L001S, U14-ch3, adc3

calib_packv5_042523_0143.root, FC#9, port A1

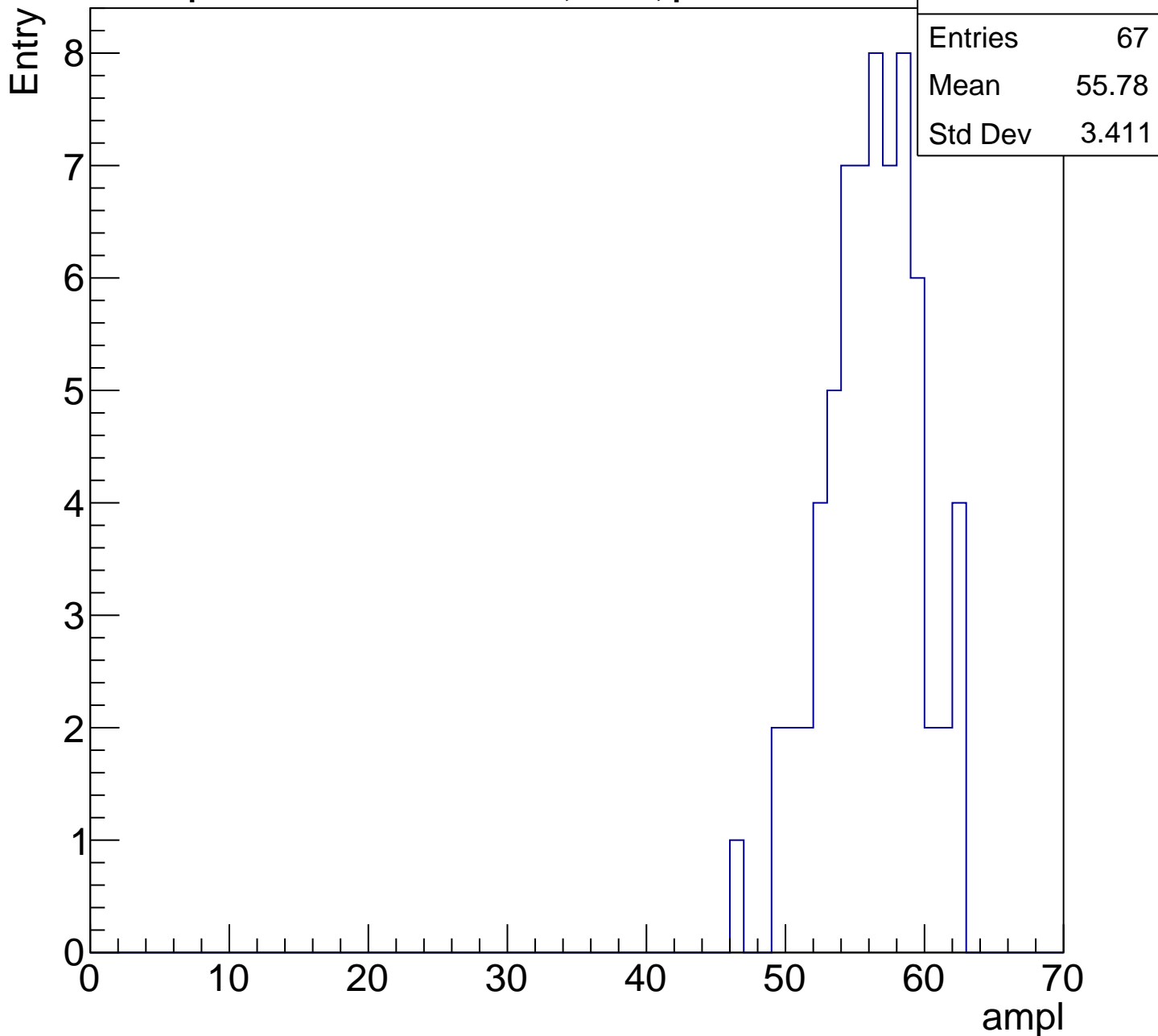
Entry

Entries	61
Mean	49.82
Std Dev	3.232



B0L001S, U14-ch3, adc4

calib_packv5_042523_0143.root, FC#9, port A1

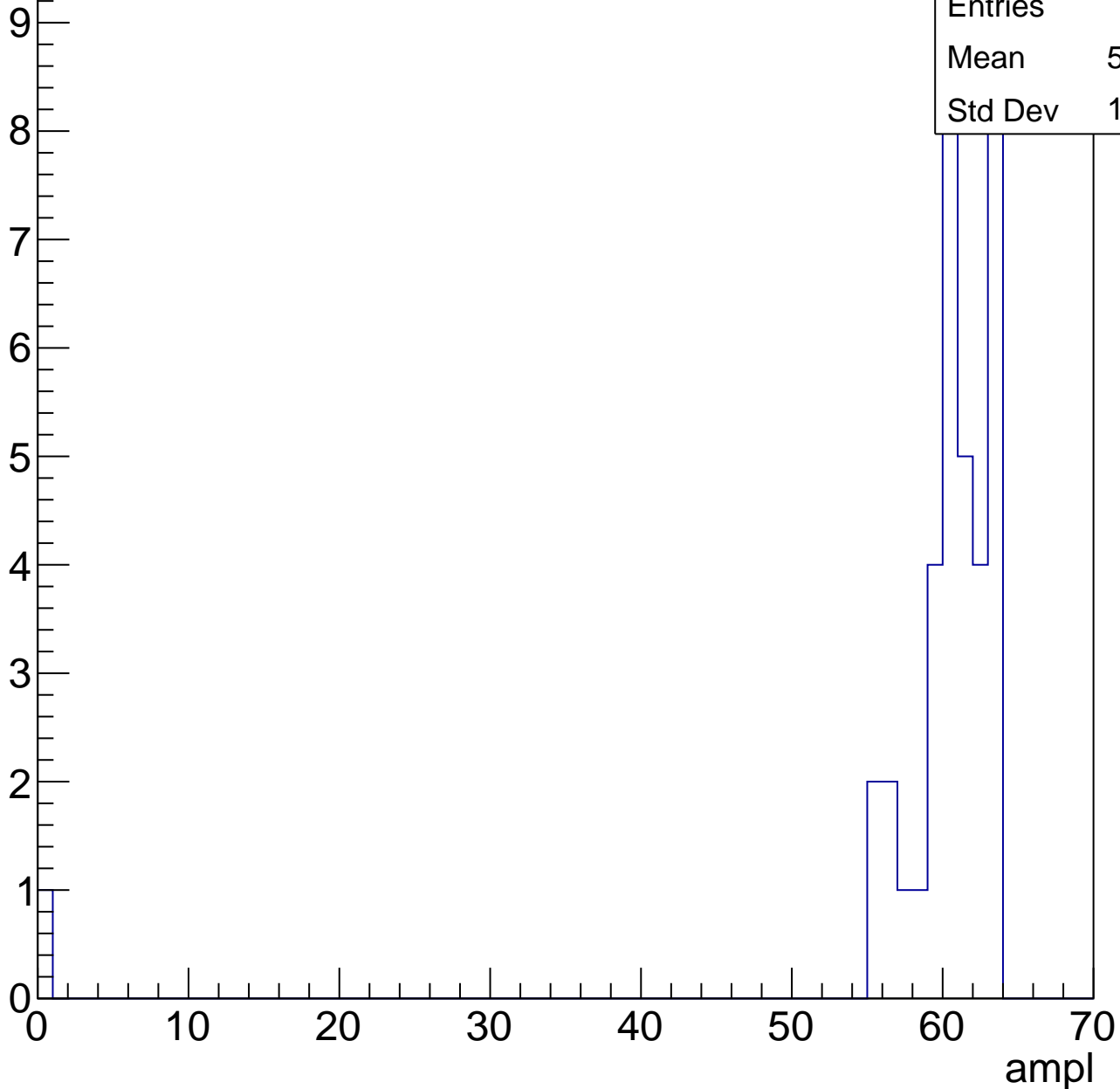


B0L001S, U14-ch3, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

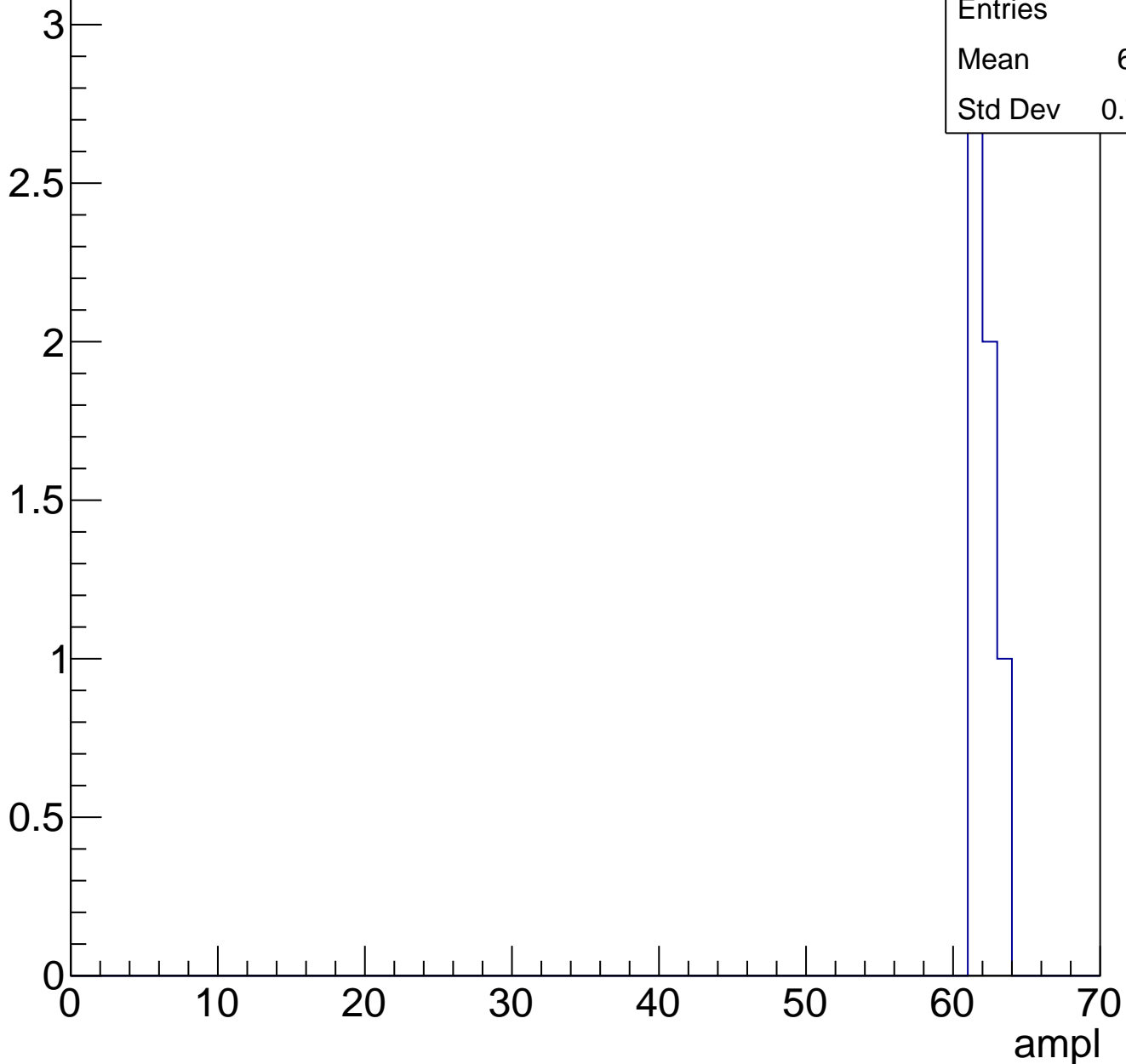
Entries	37
Mean	58.73
Std Dev	10.06



B0L001S, U14-ch3, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch3, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch4, adc0

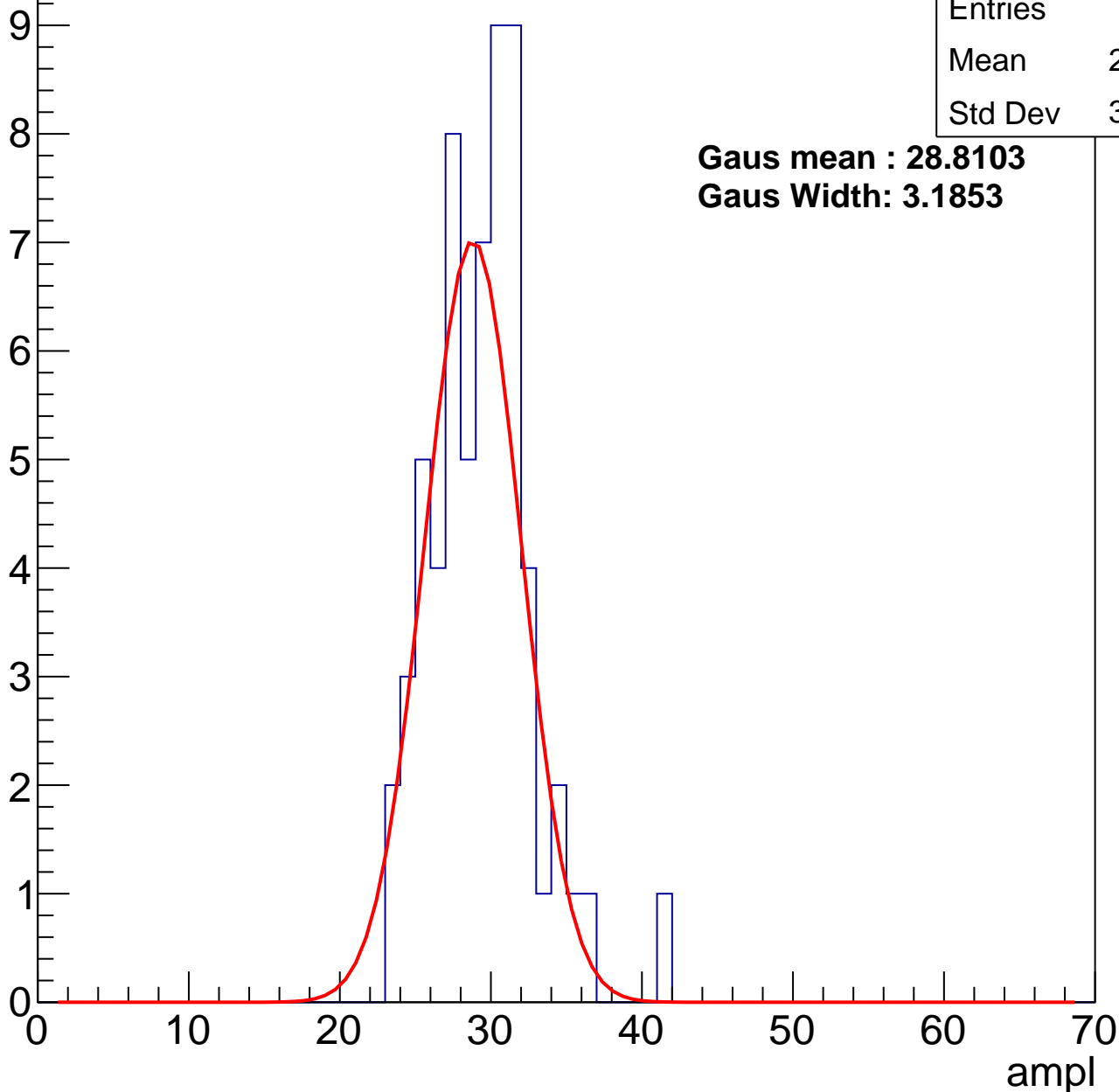
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.97
Std Dev	3.302

Gaus mean : 28.8103

Gaus Width: 3.1853



B0L001S, U14-ch4, adc1

calib_packv5_042523_0143.root, FC#9, port A1

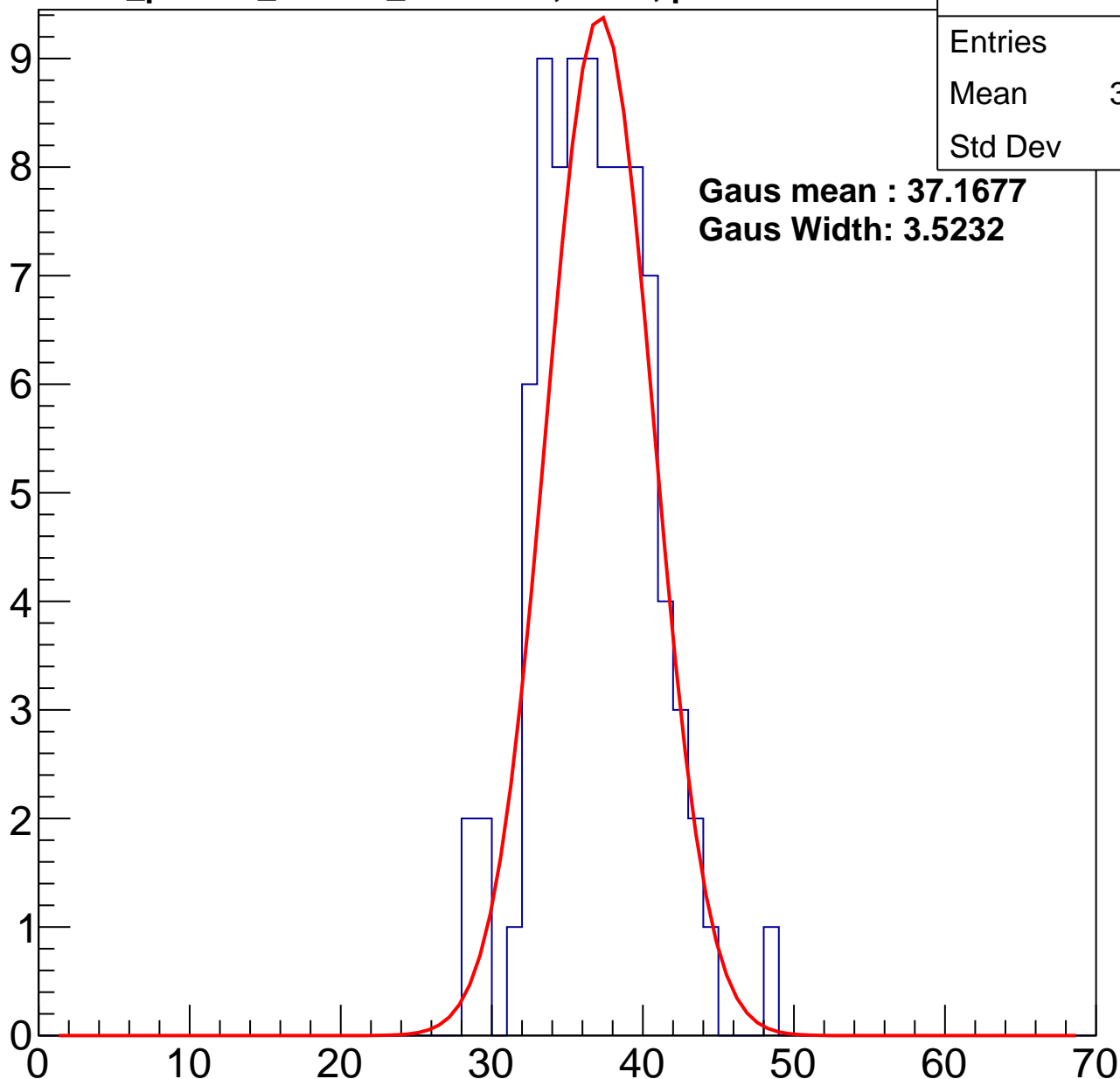
Entry

9
8
7
6
5
4
3
2
1
0

Entries	88
Mean	36.42
Std Dev	3.68

Gaus mean : 37.1677
Gaus Width: 3.5232

ampl

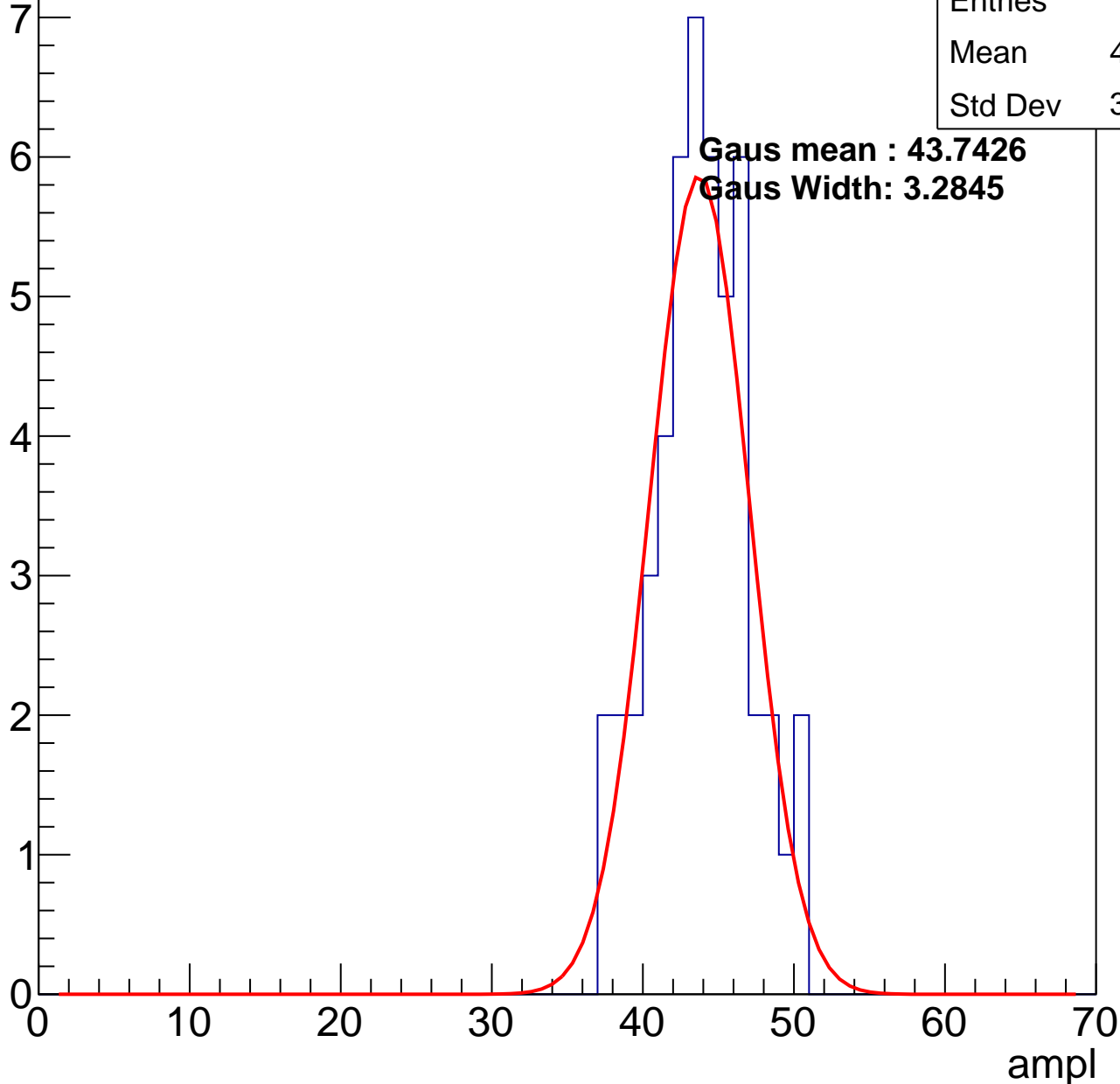


B0L001S, U14-ch4, adc2

calib_packv5_042523_0143.root, FC#9, port A1

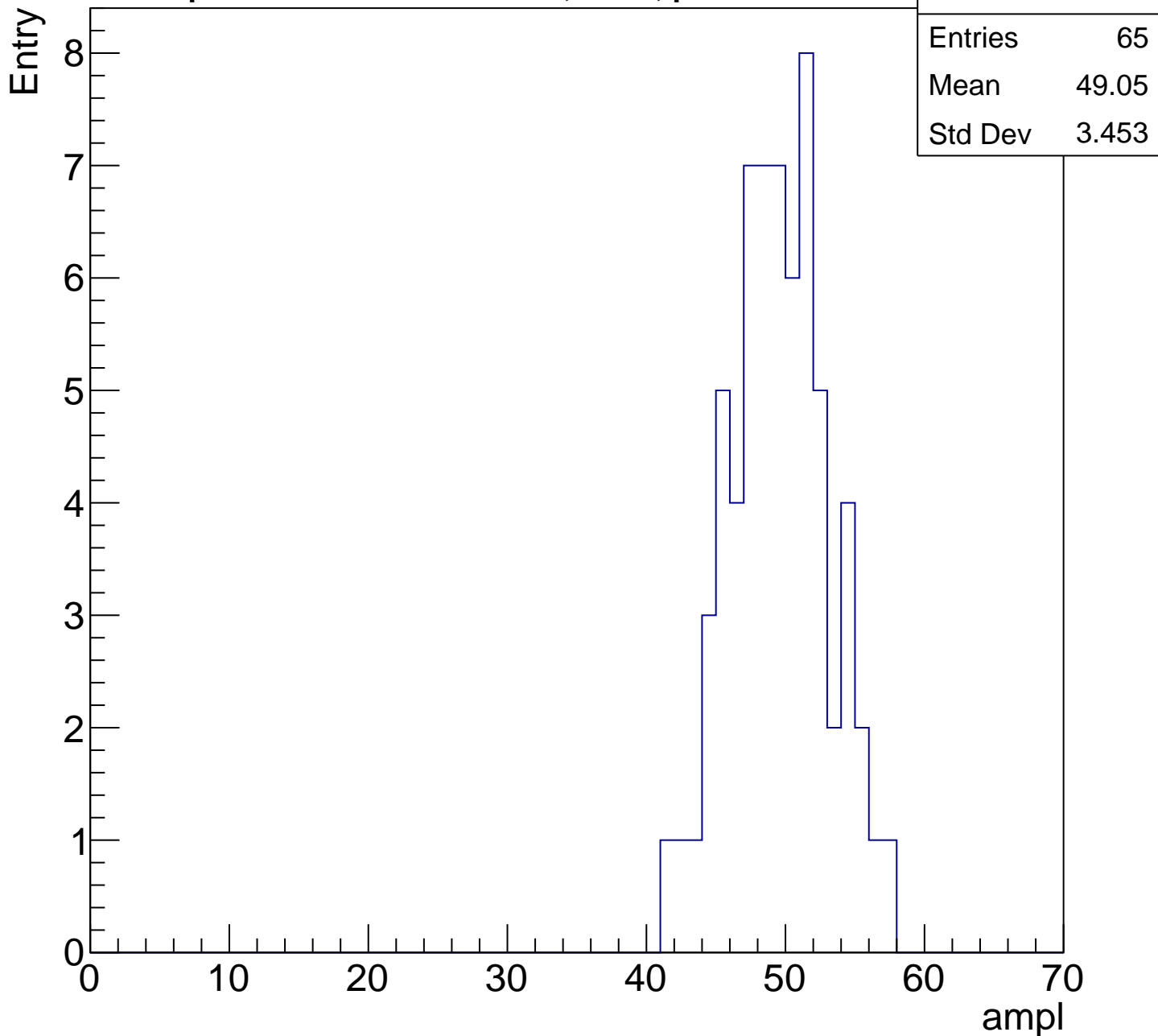
Entry

Entries	50
Mean	43.38
Std Dev	3.136



B0L001S, U14-ch4, adc3

calib_packv5_042523_0143.root, FC#9, port A1

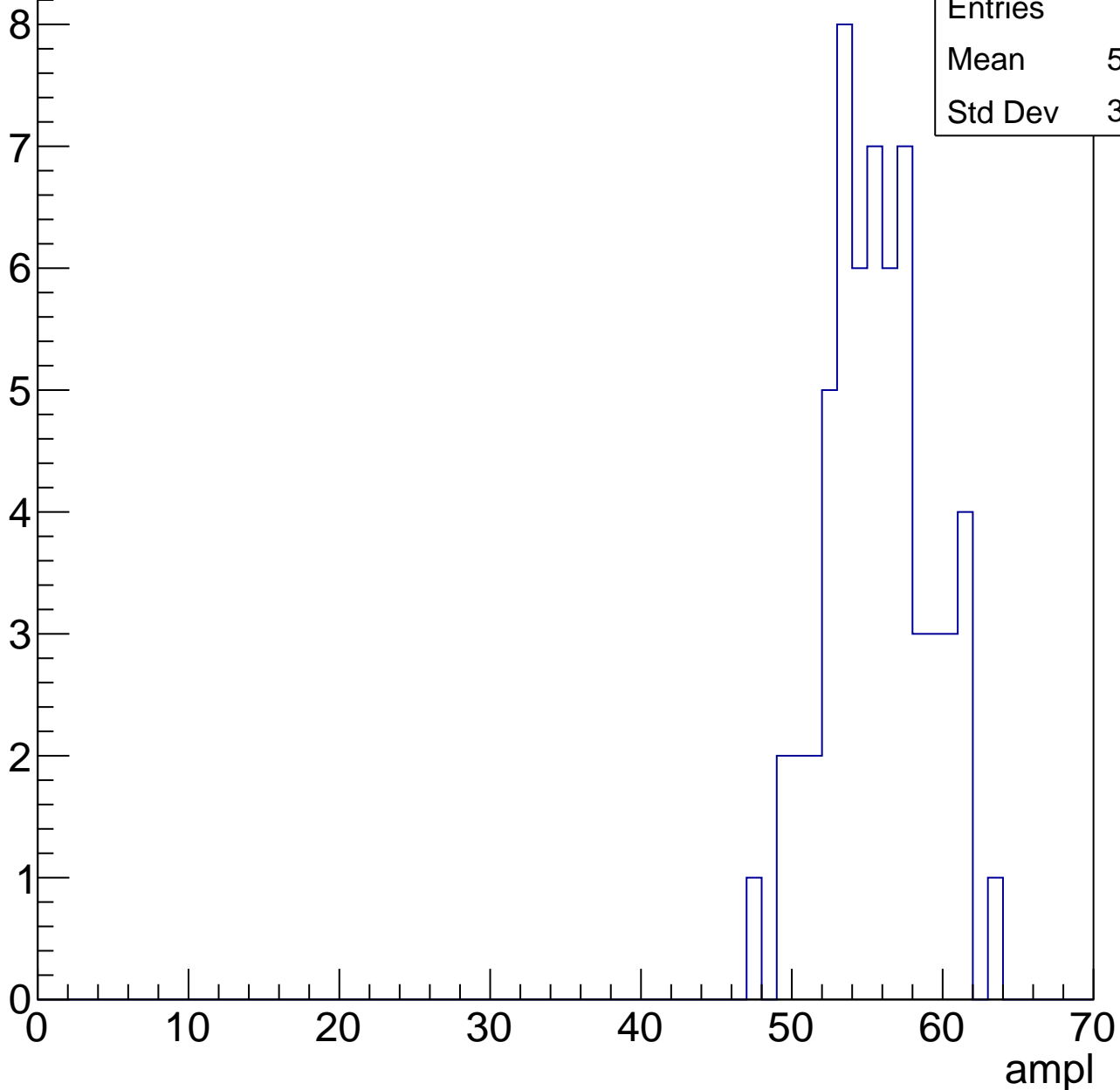


B0L001S, U14-ch4, adc4

calib_packv5_042523_0143.root, FC#9, port A1

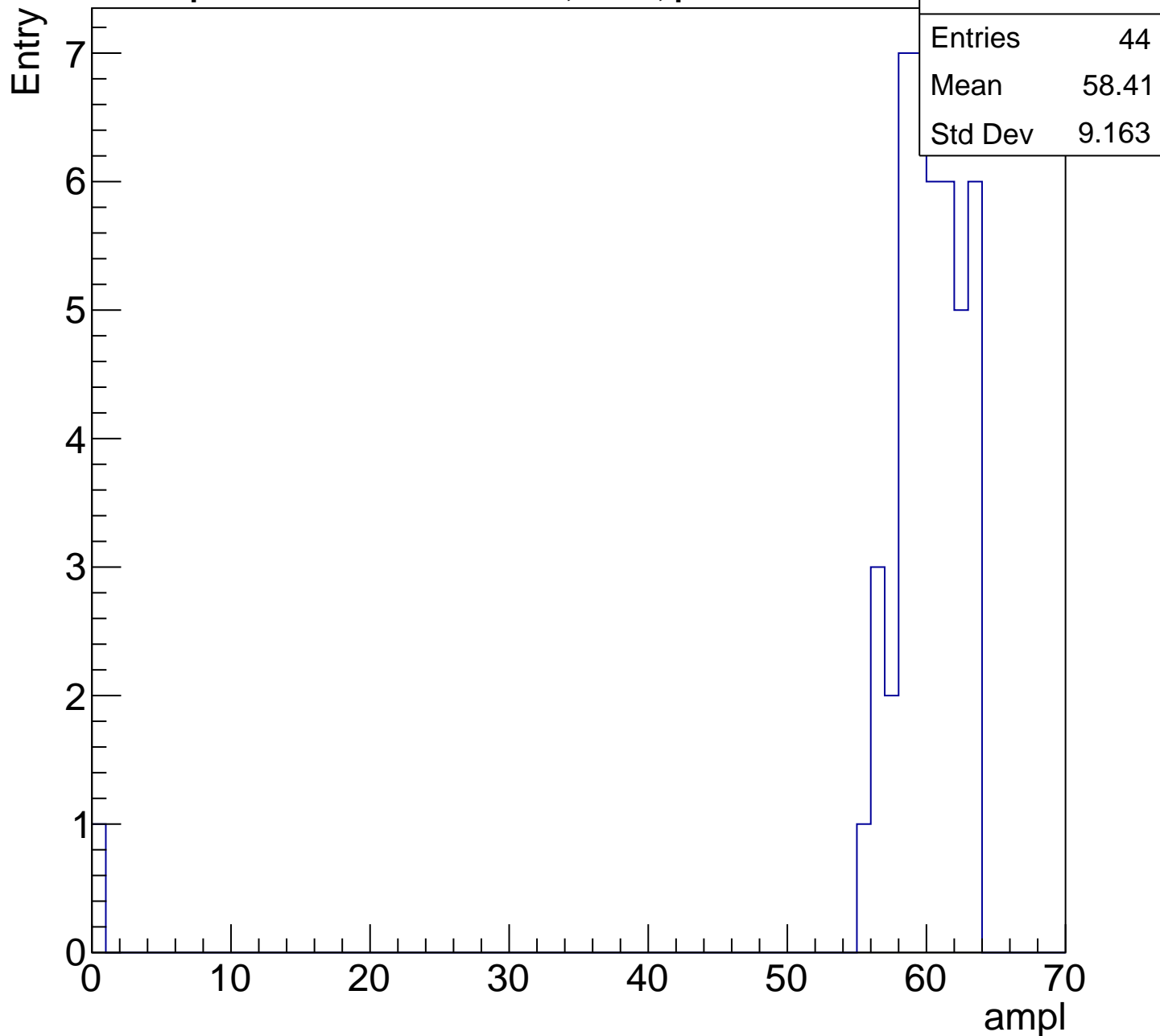
Entry

Entries	60
Mean	55.22
Std Dev	3.392



B0L001S, U14-ch4, adc5

calib_packv5_042523_0143.root, FC#9, port A1

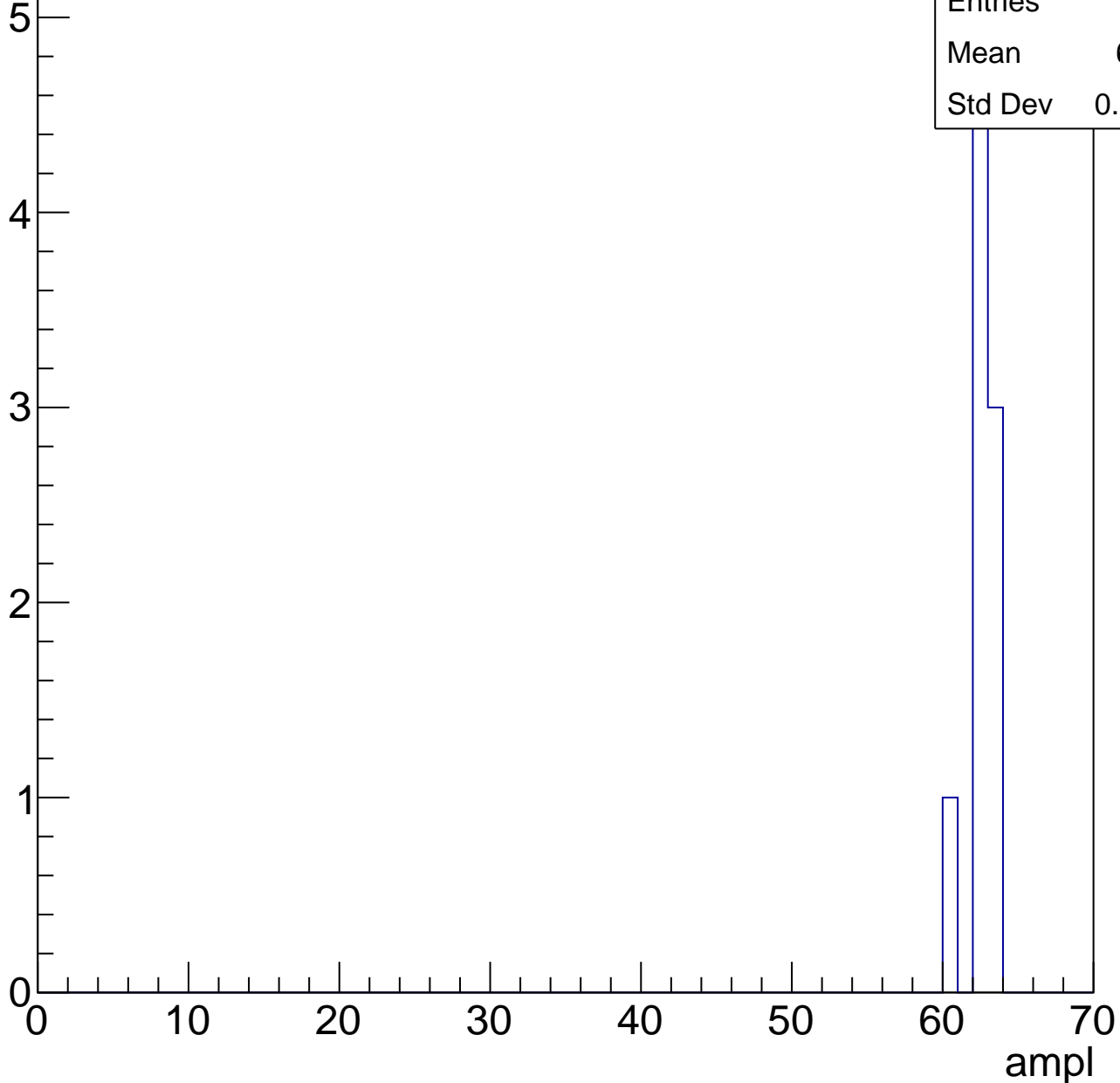


B0L001S, U14-ch4, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	62.11
Std Dev	0.8749



B0L001S, U14-ch4, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch5, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	29.26
Std Dev	6.868

Gaus mean : 31.2893

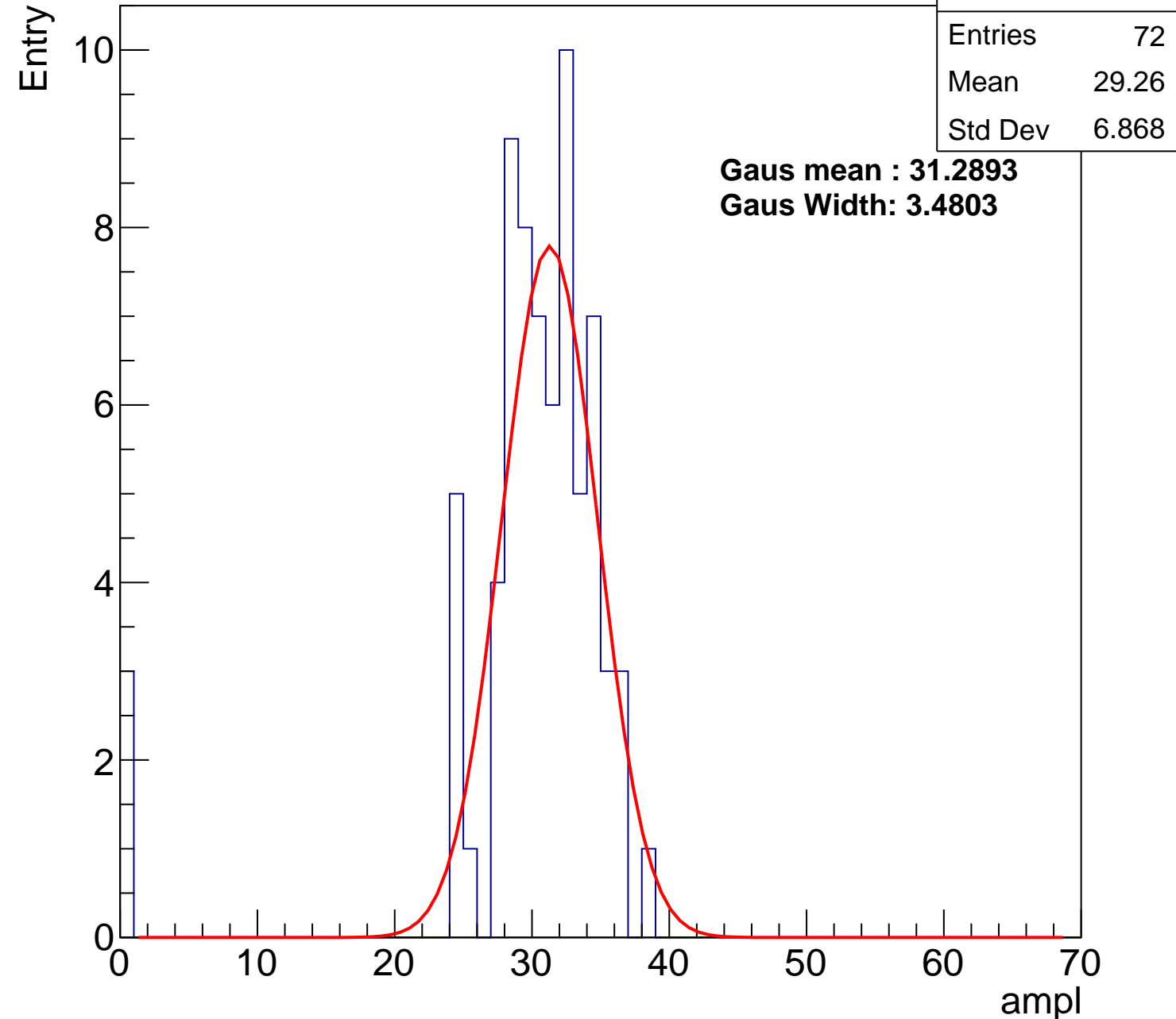
Gaus Width: 3.4803

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



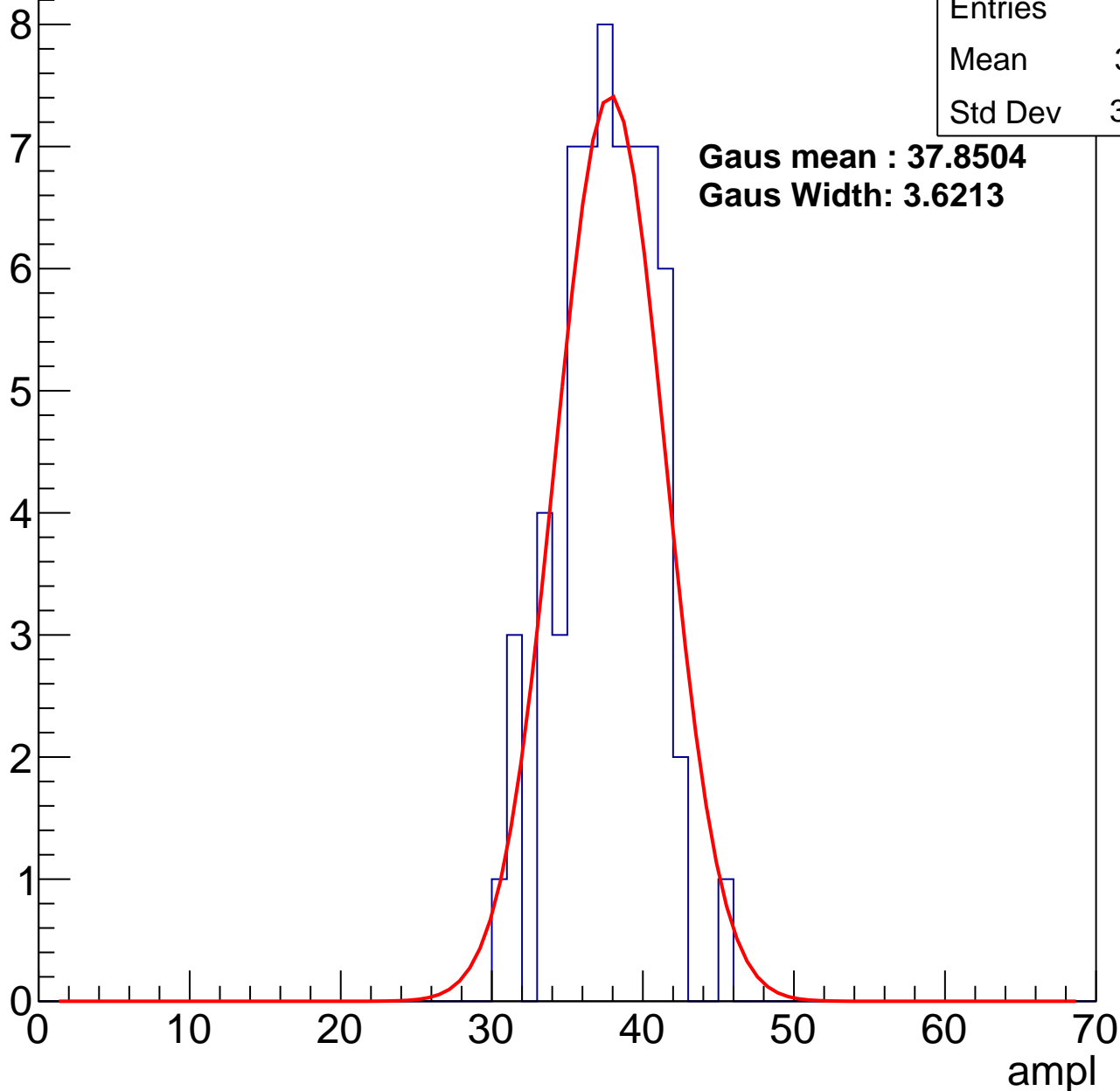
B0L001S, U14-ch5, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.21
Std Dev	3.056

Gaus mean : 37.8504
Gaus Width: 3.6213



B0L001S, U14-ch5, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.92
Std Dev	3.414

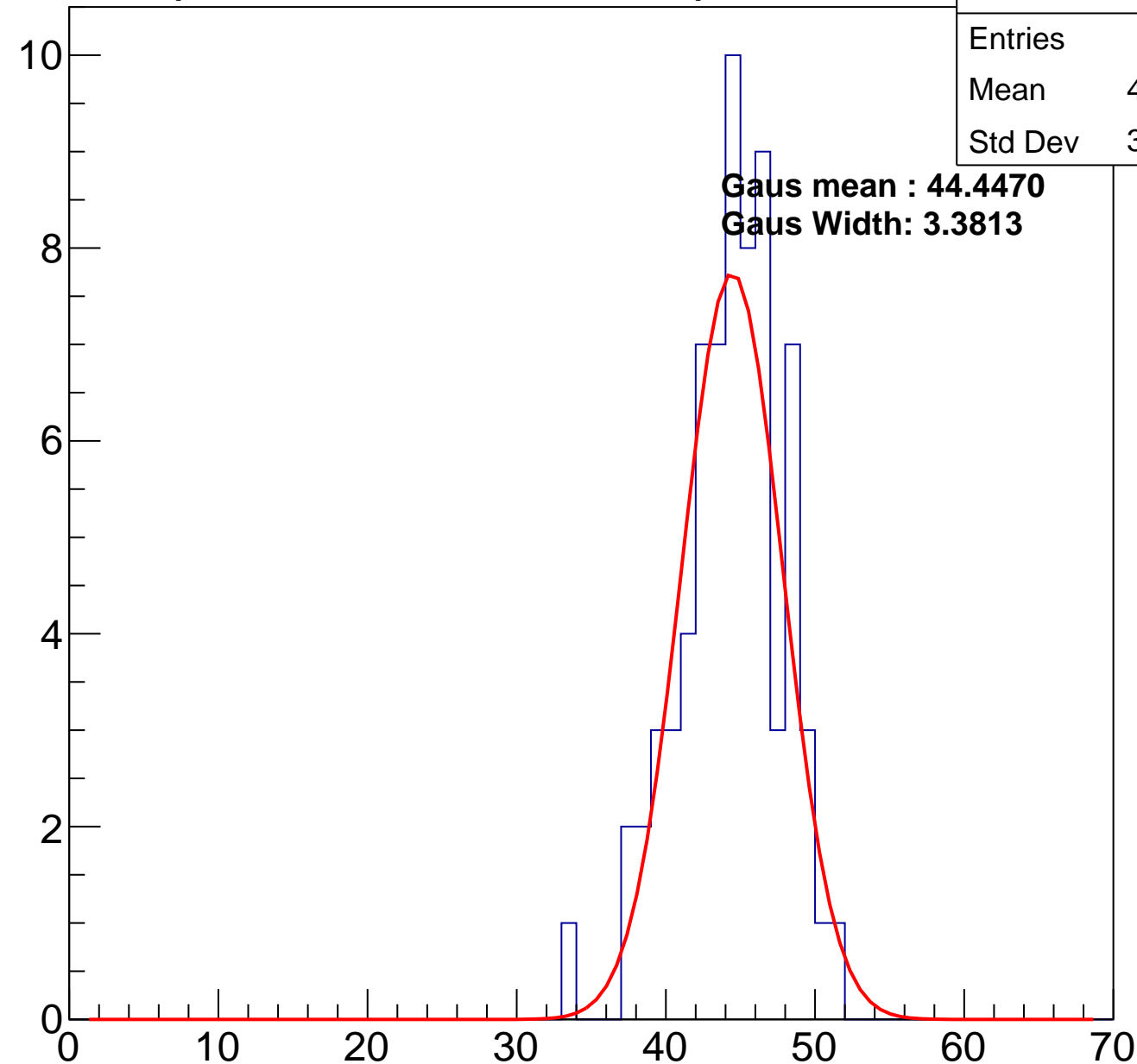
Gaus mean : 44.4470

Gaus Width: 3.3813

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

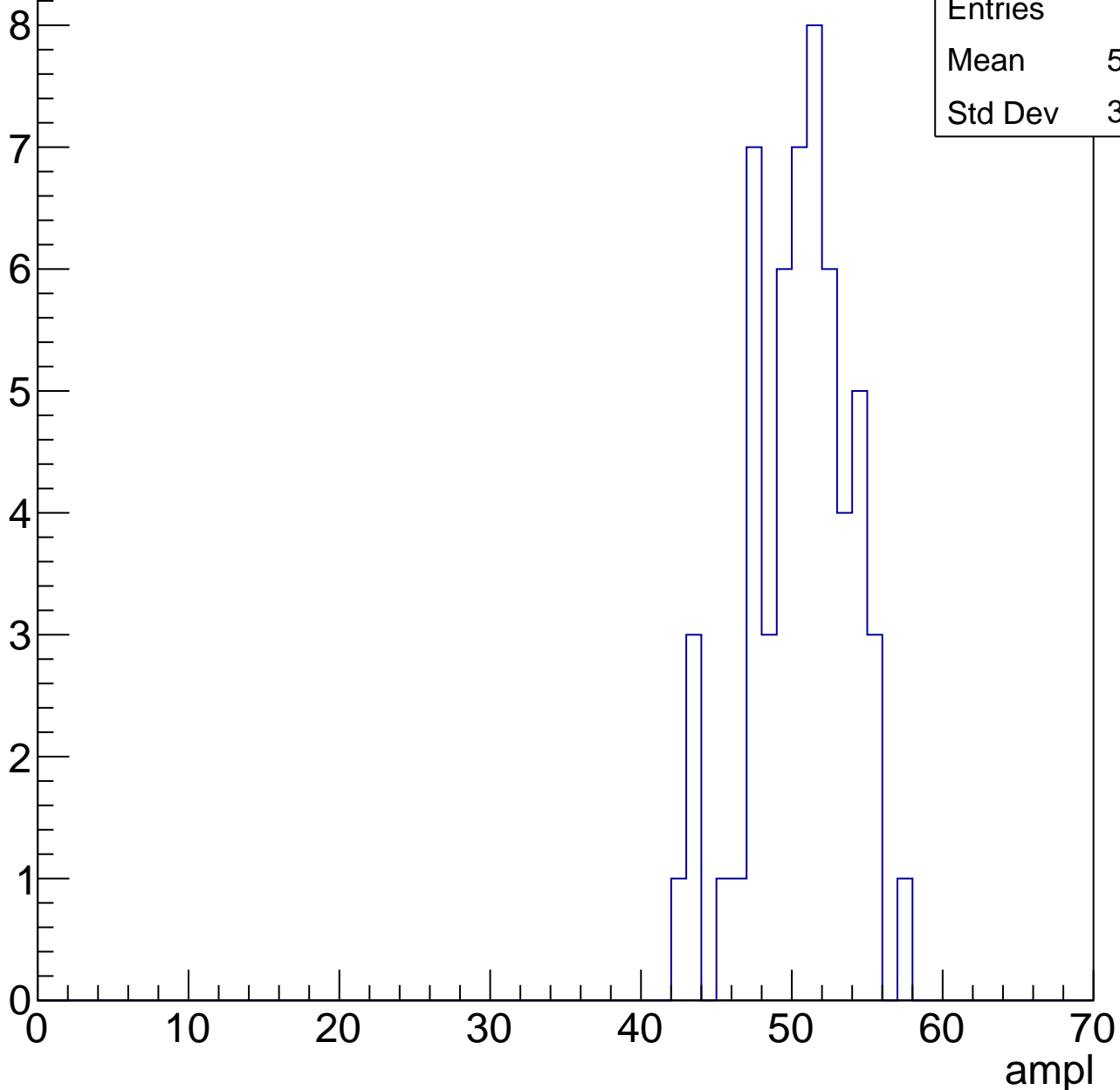


B0L001S, U14-ch5, adc3

calib_packv5_042523_0143.root, FC#9, port A1

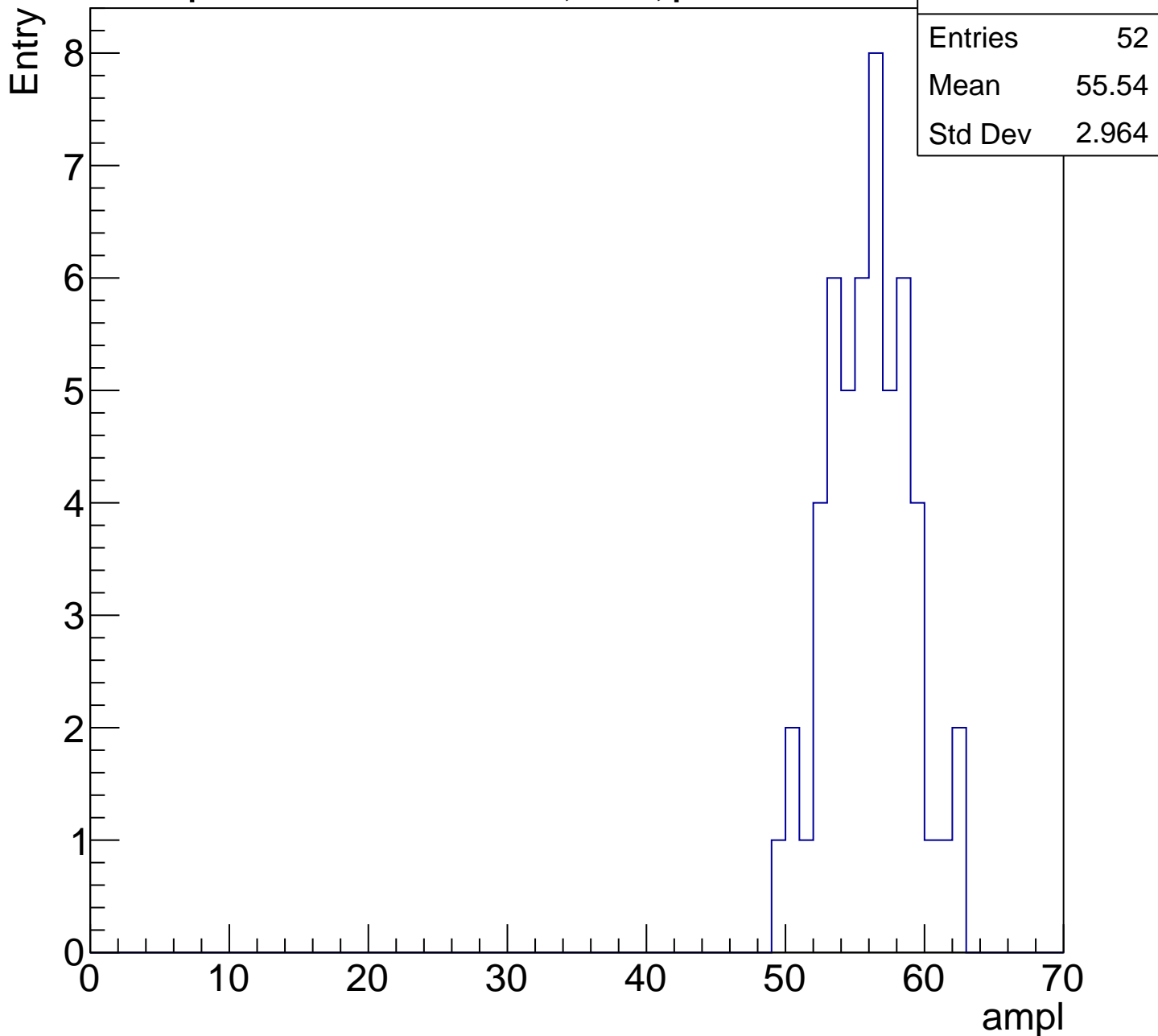
Entry

Entries	56
Mean	50.05
Std Dev	3.286



B0L001S, U14-ch5, adc4

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U14-ch5, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.06
Std Dev	8.608

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

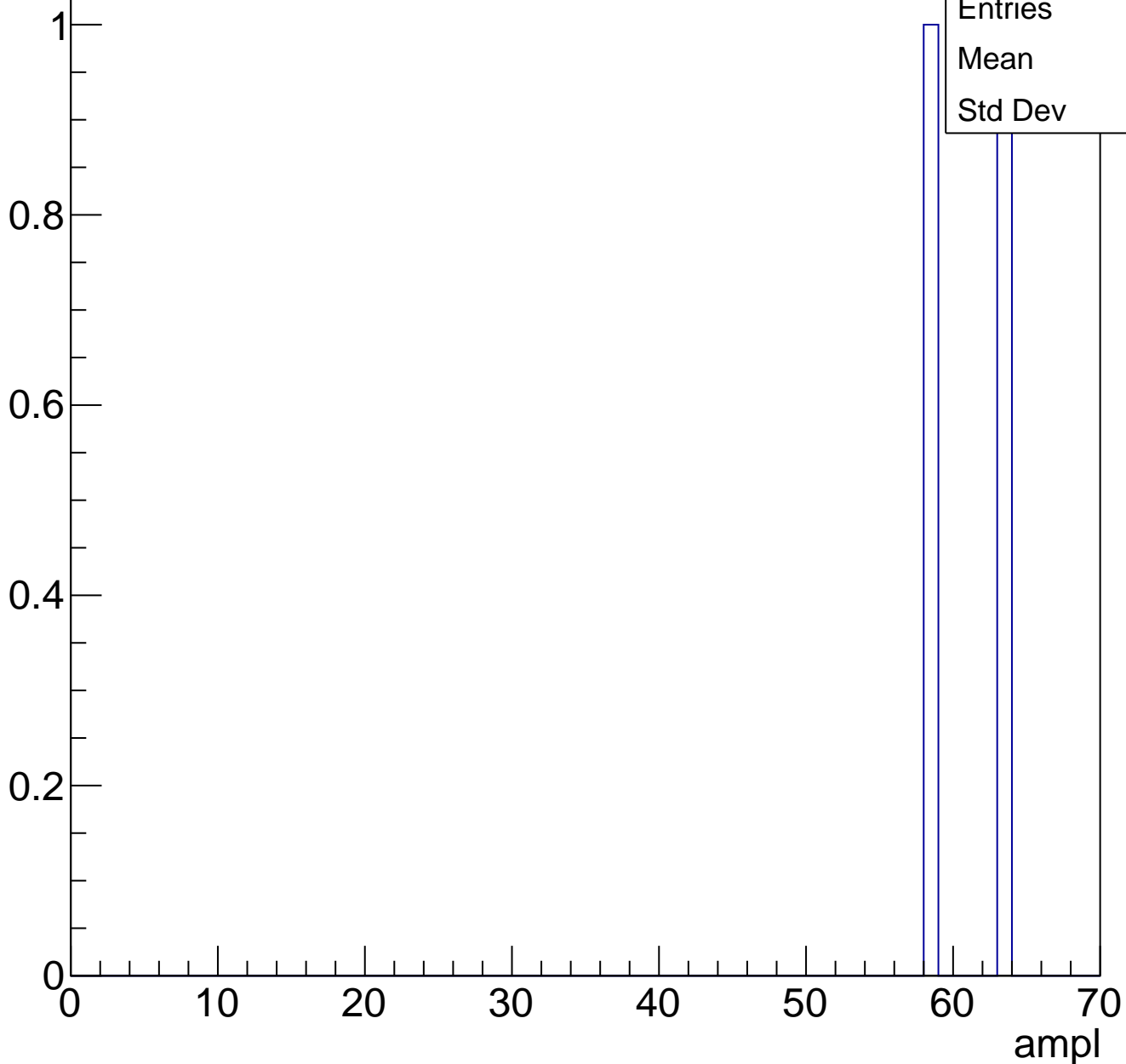
8

9

B0L001S, U14-ch5, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch5, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch6, adc0

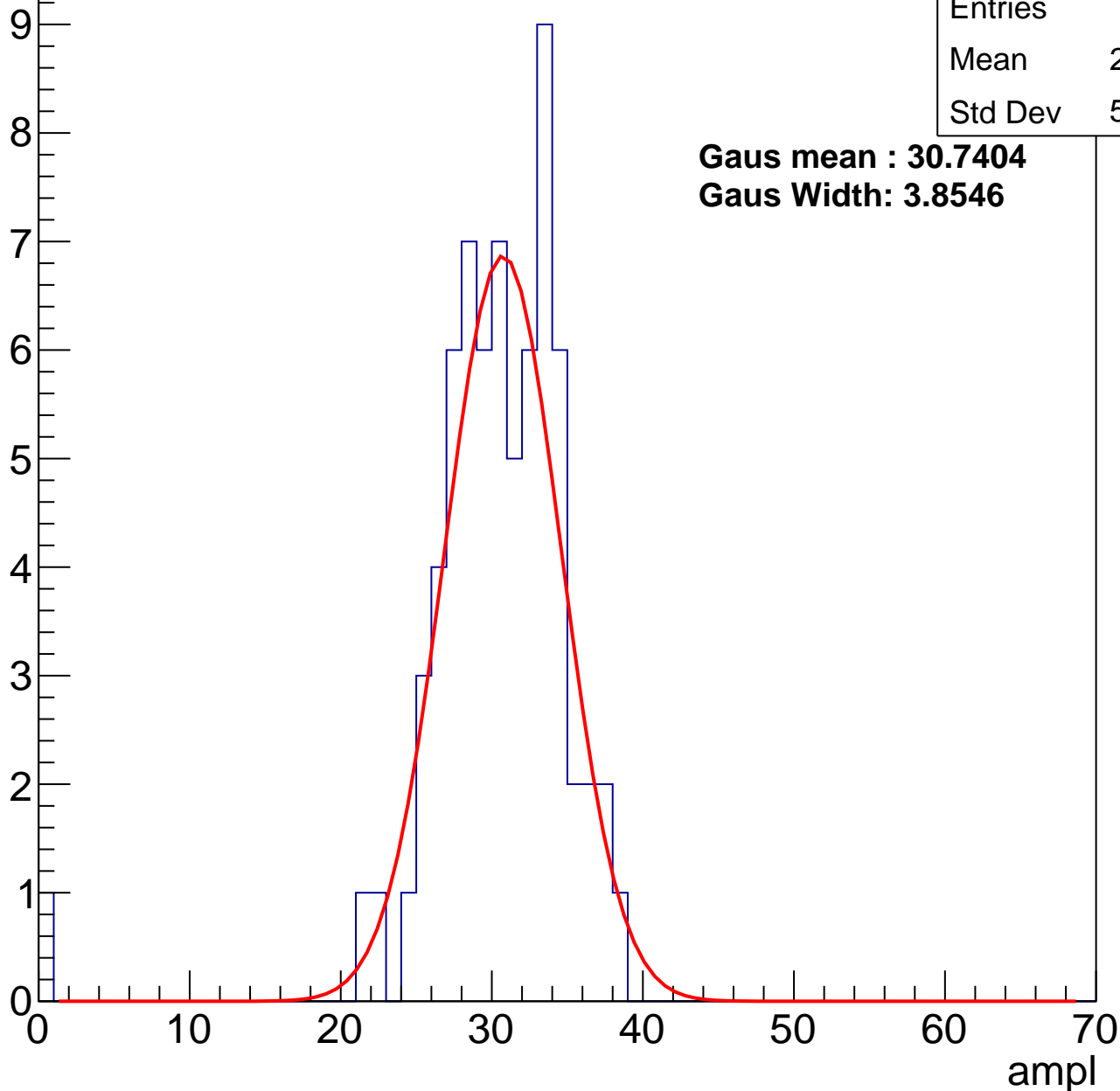
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.86
Std Dev	5.066

Gaus mean : 30.7404

Gaus Width: 3.8546



B0L001S, U14-ch6, adc1

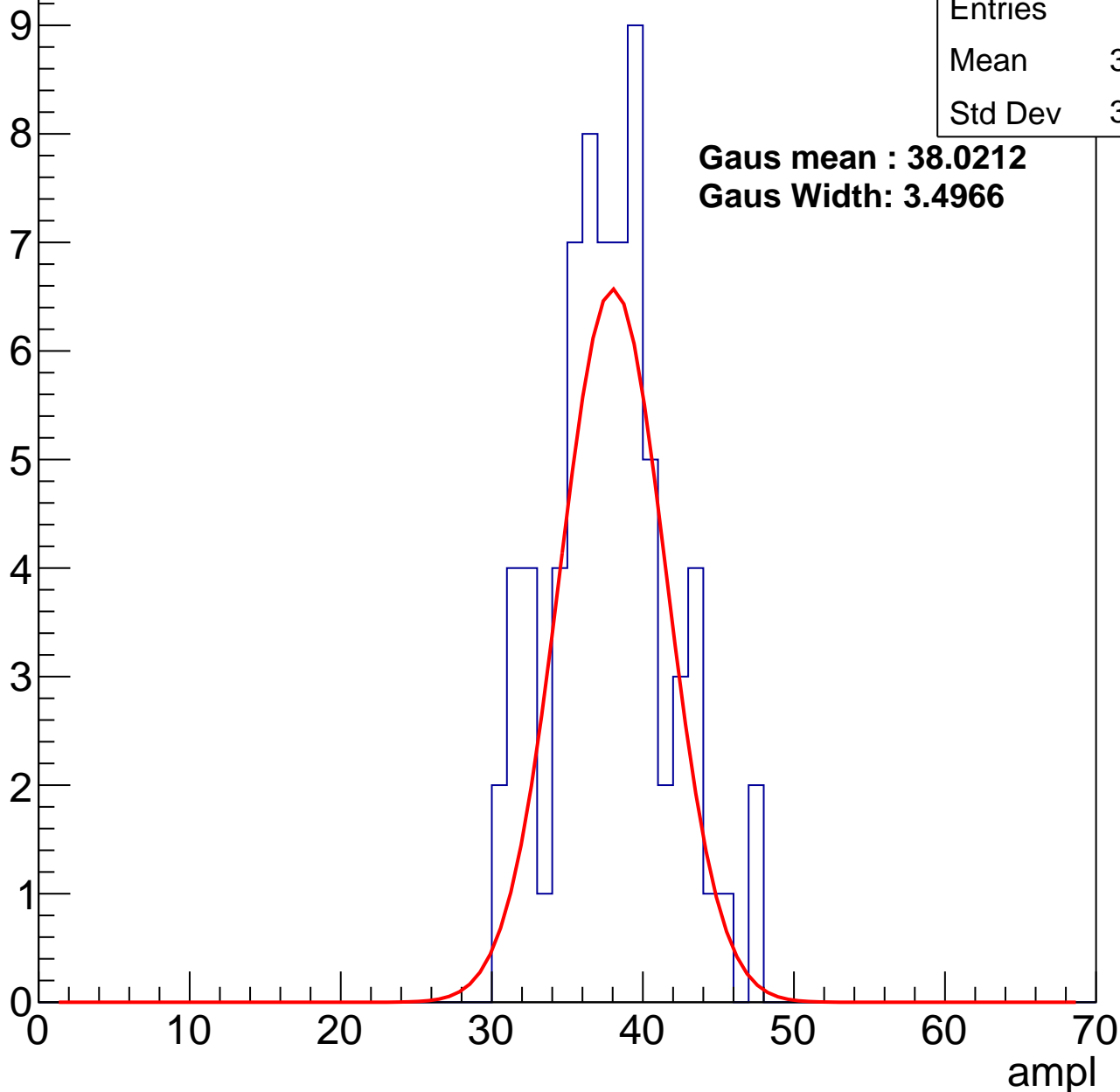
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.37
Std Dev	3.887

Gaus mean : 38.0212

Gaus Width: 3.4966



B0L001S, U14-ch6, adc2

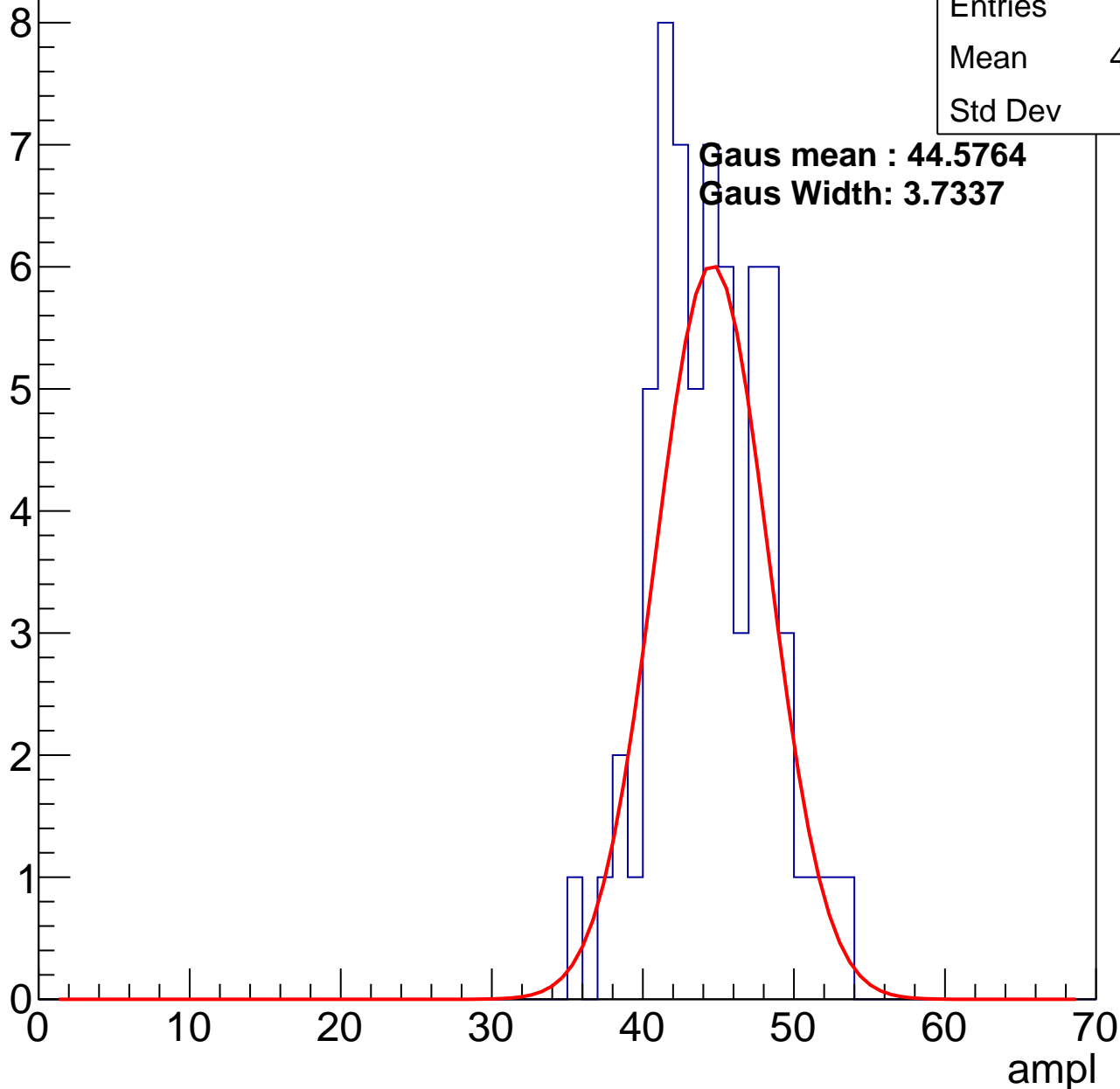
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.05
Std Dev	3.69

Gaus mean : 44.5764

Gaus Width: 3.7337

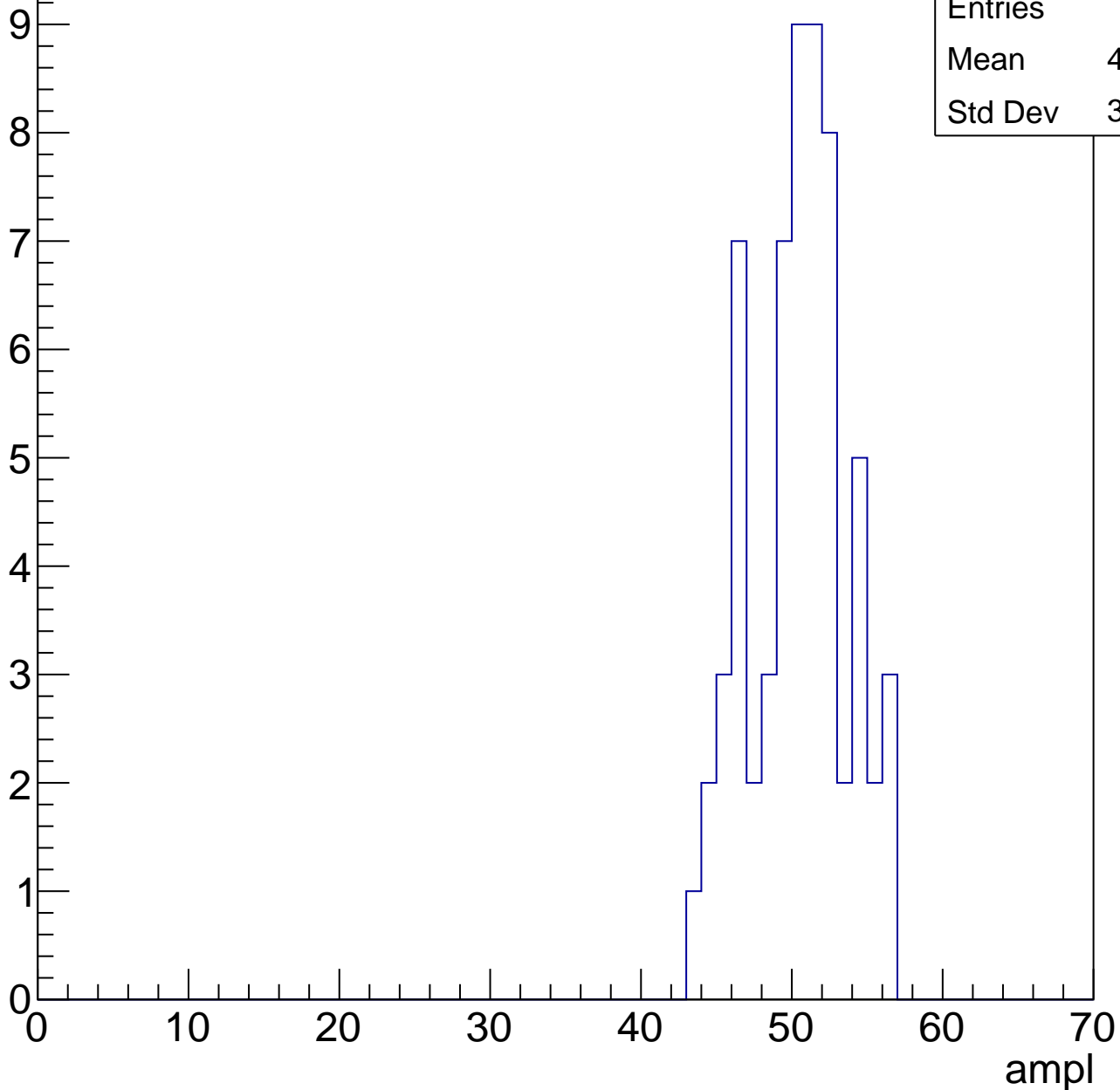


B0L001S, U14-ch6, adc3

calib_packv5_042523_0143.root, FC#9, port A1

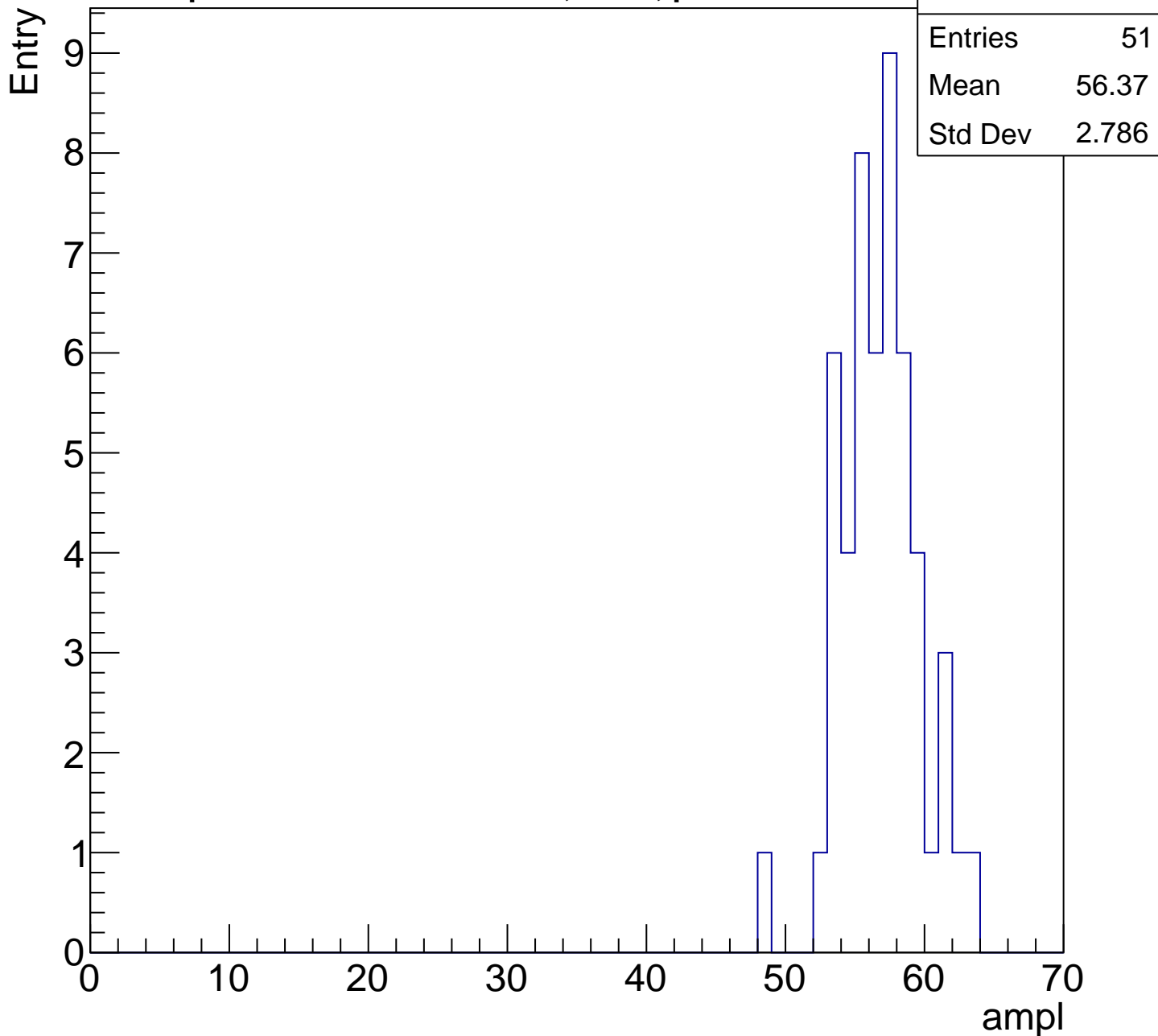
Entry

Entries	63
Mean	49.97
Std Dev	3.192



B0L001S, U14-ch6, adc4

calib_packv5_042523_0143.root, FC#9, port A1

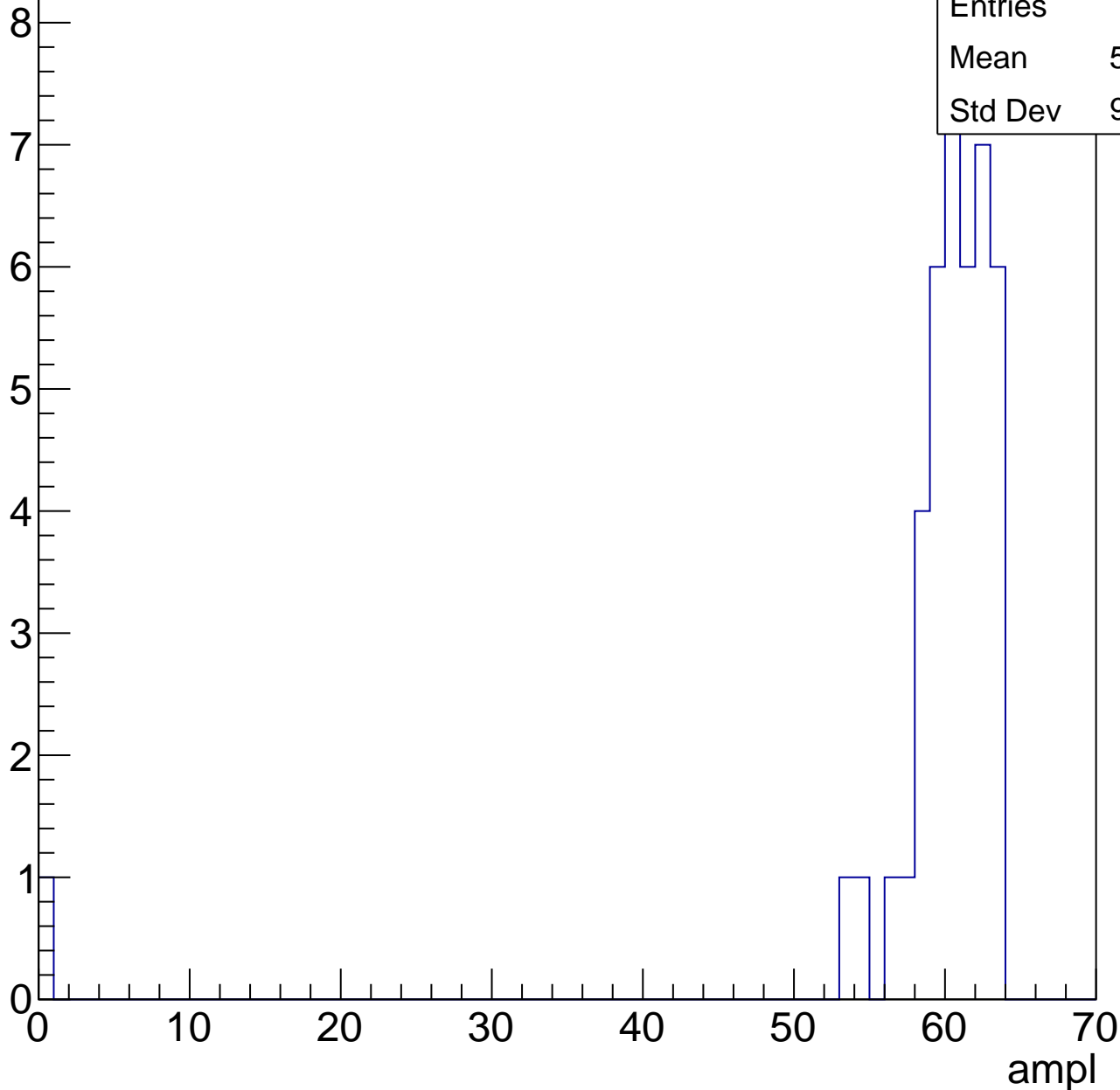


B0L001S, U14-ch6, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.67
Std Dev	9.443



B0L001S, U14-ch6, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch6, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch7, adc0

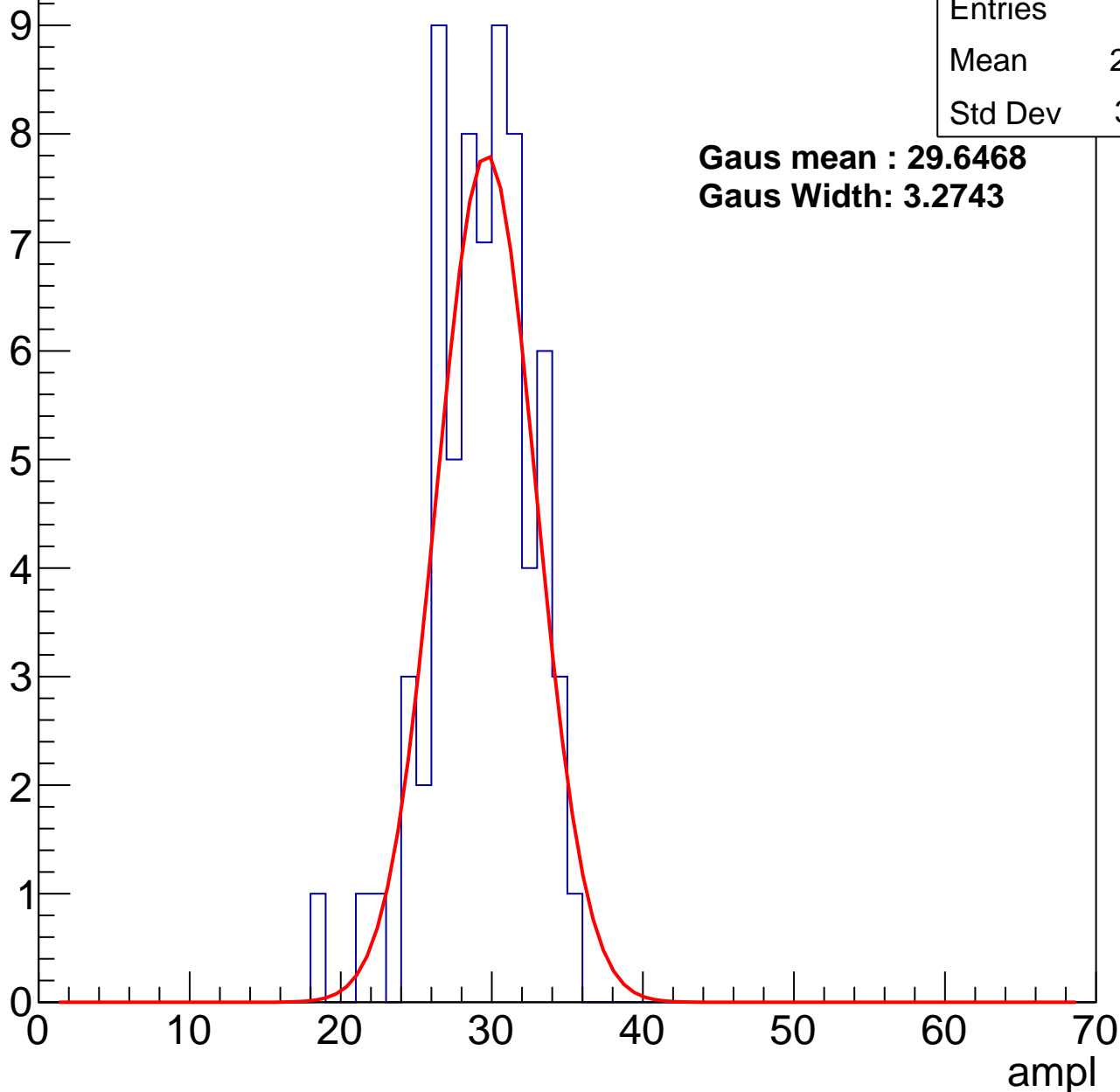
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.82
Std Dev	3.281

Gaus mean : 29.6468

Gaus Width: 3.2743



B0L001S, U14-ch7, adc1

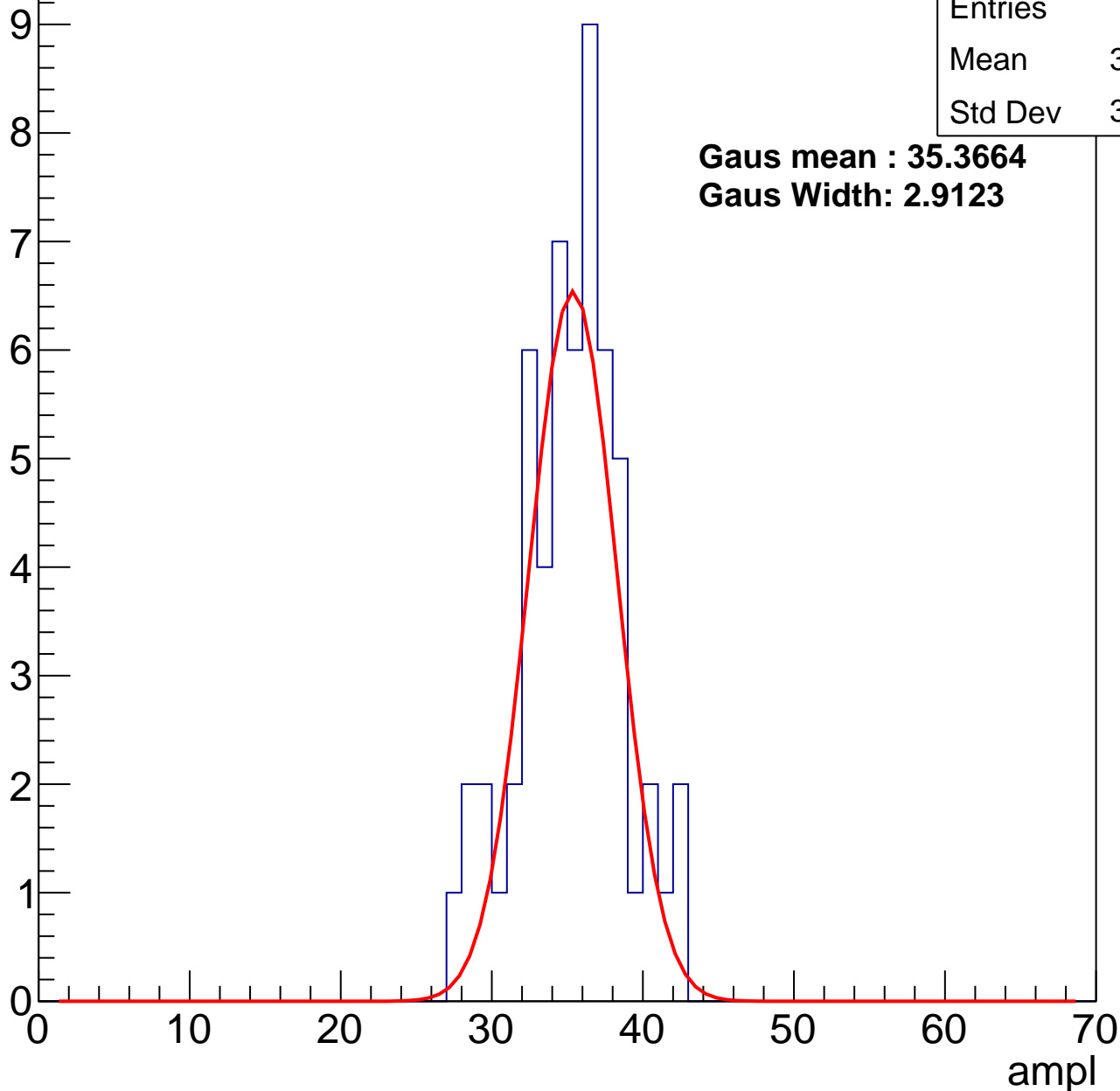
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	34.82
Std Dev	3.362

Gaus mean : 35.3664

Gaus Width: 2.9123



B0L001S, U14-ch7, adc2

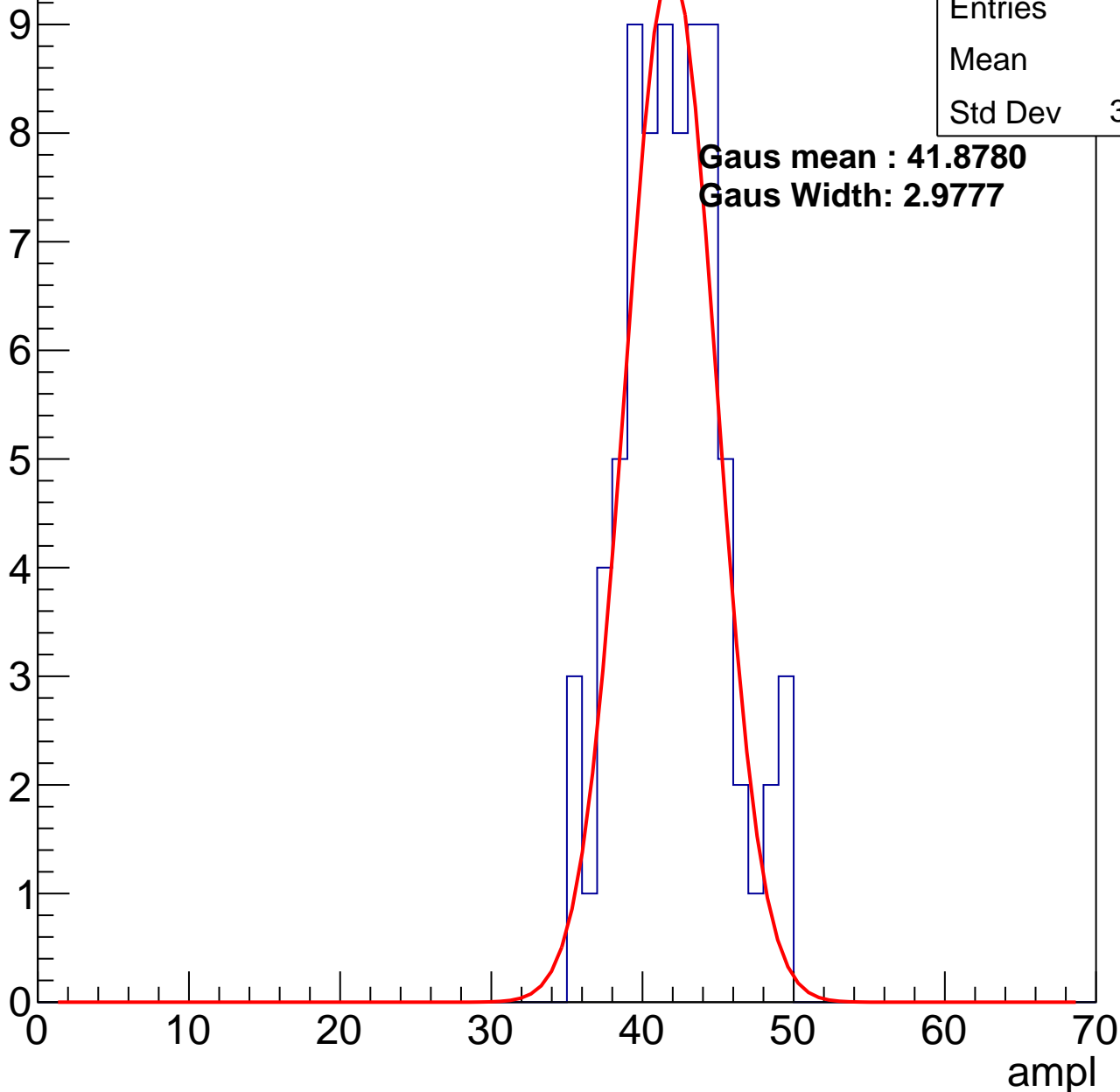
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	41.6
Std Dev	3.279

Gaus mean : 41.8780

Gaus Width: 2.9777

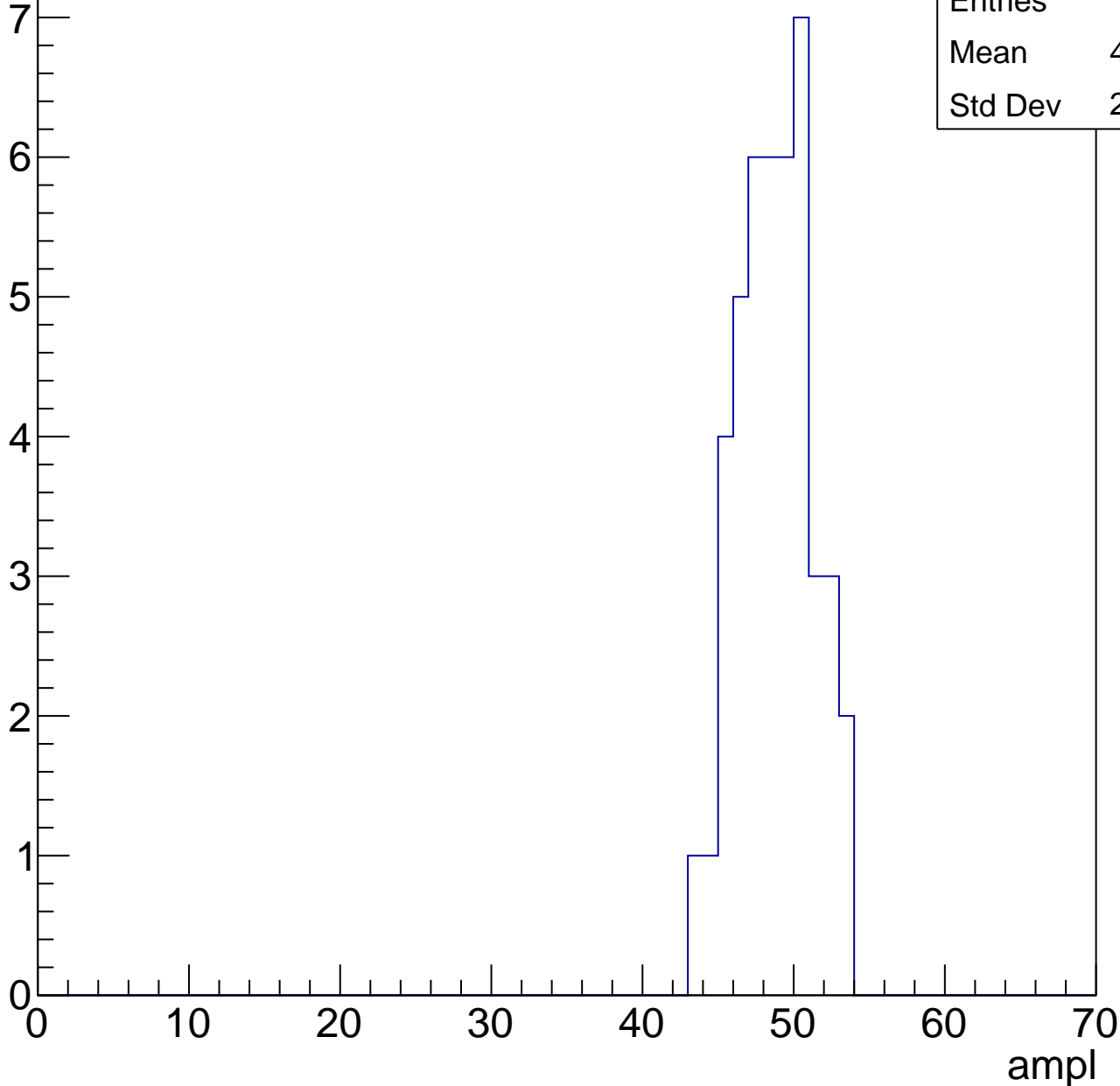


B0L001S, U14-ch7, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	48.32
Std Dev	2.419

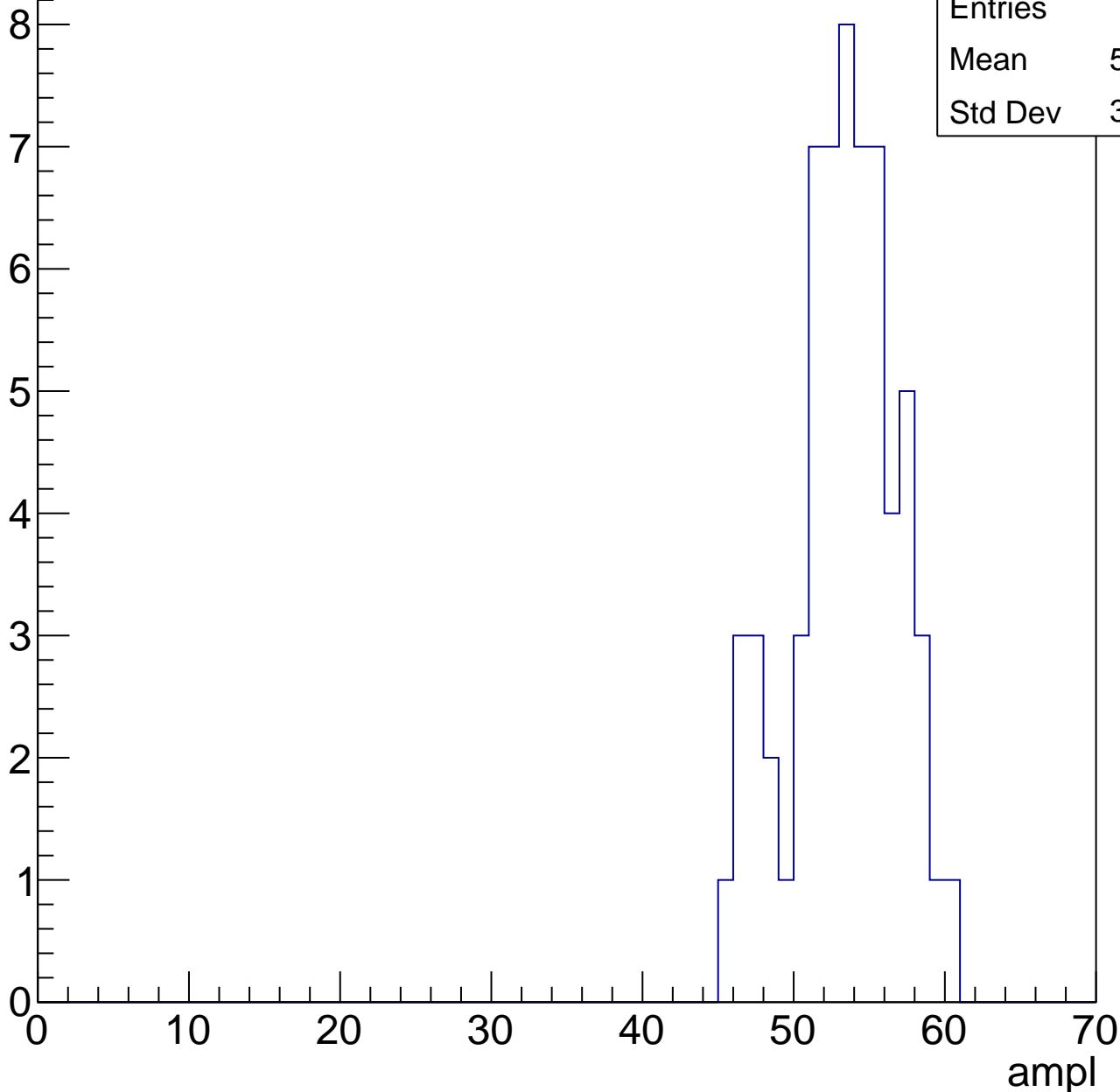


B0L001S, U14-ch7, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

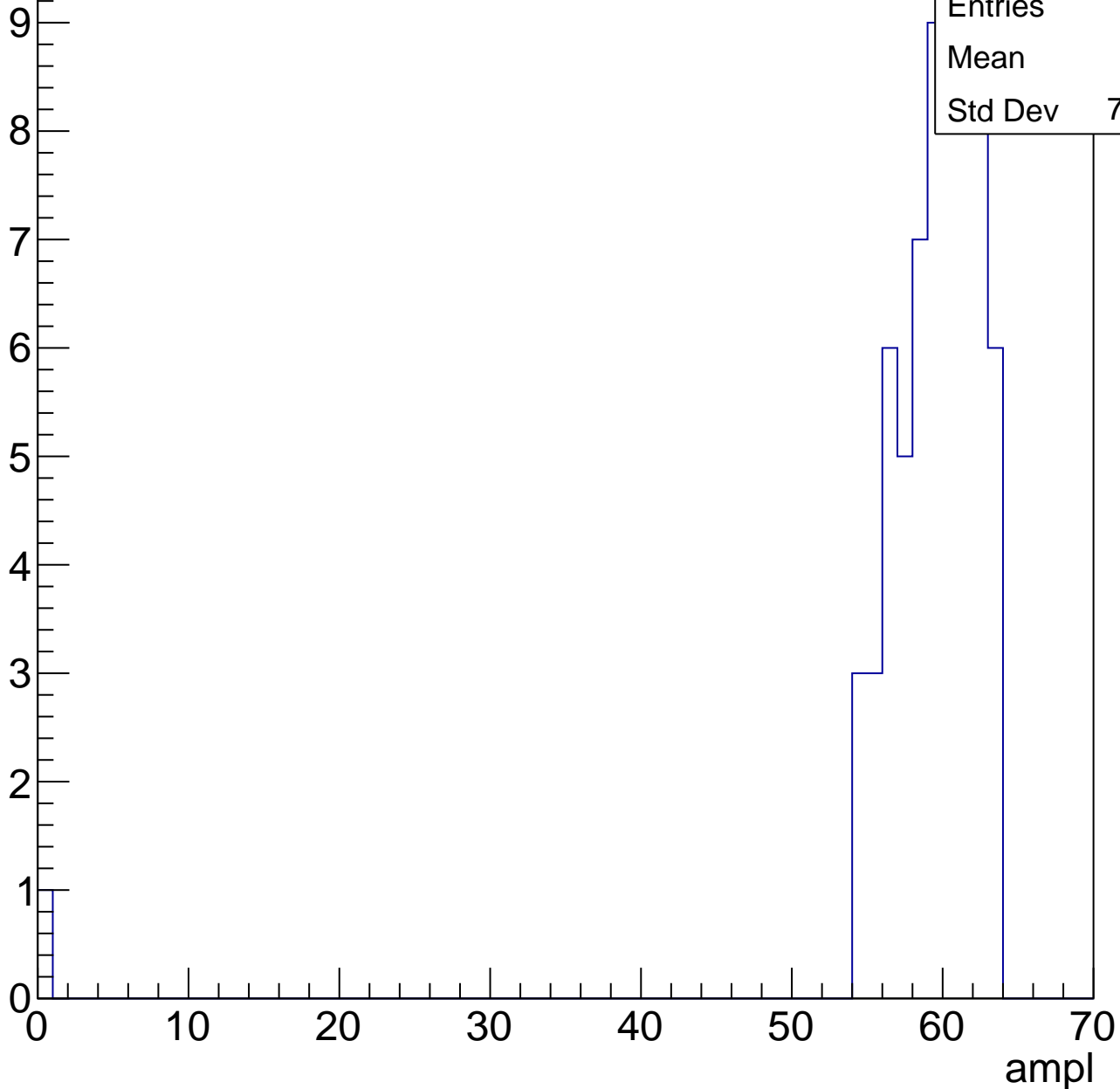
Entries	63
Mean	52.84
Std Dev	3.465



B0L001S, U14-ch7, adc5

calib_packv5_042523_0143.root, FC#9, port A1

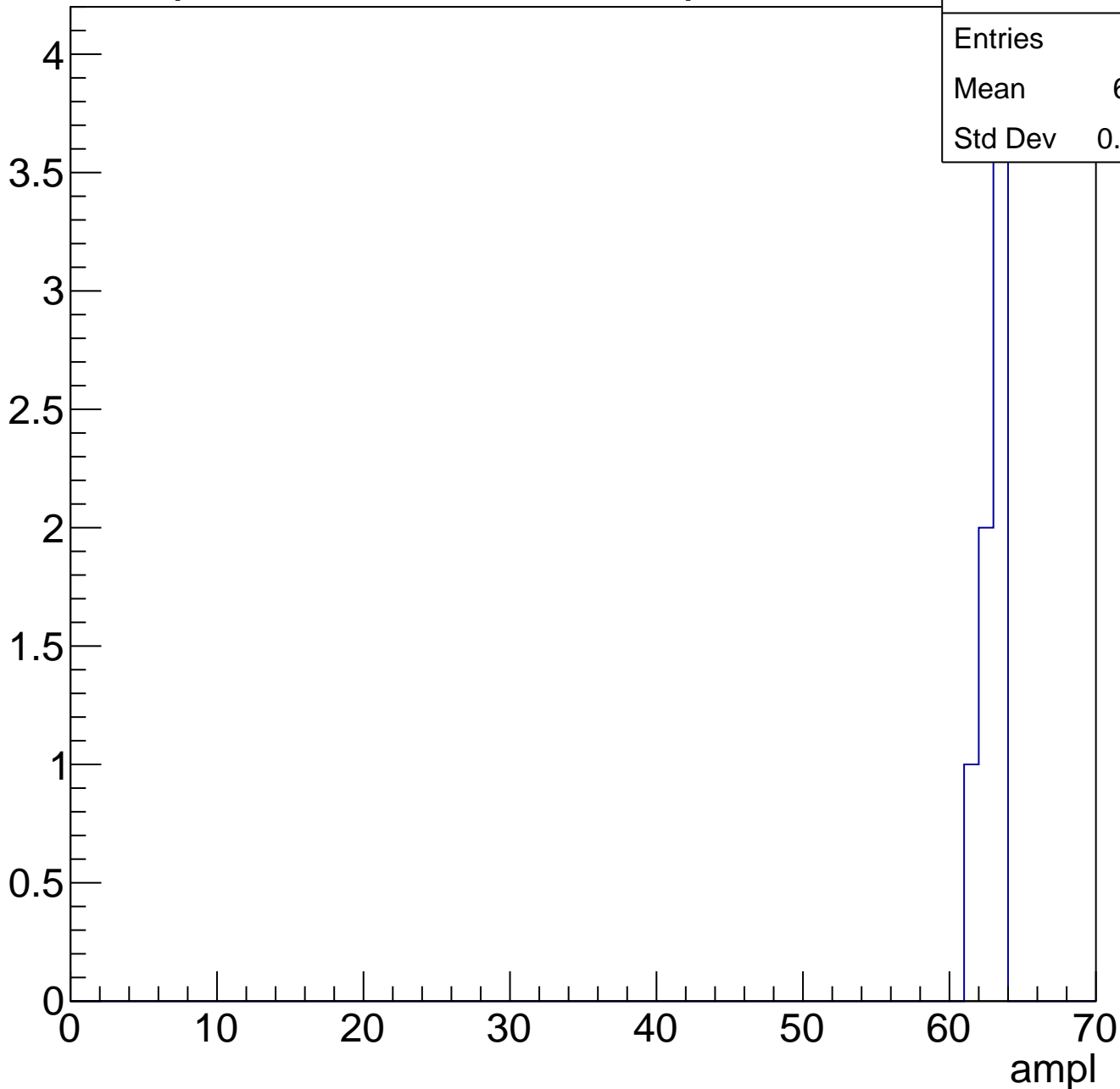
Entry



B0L001S, U14-ch7, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch7, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch8, adc0

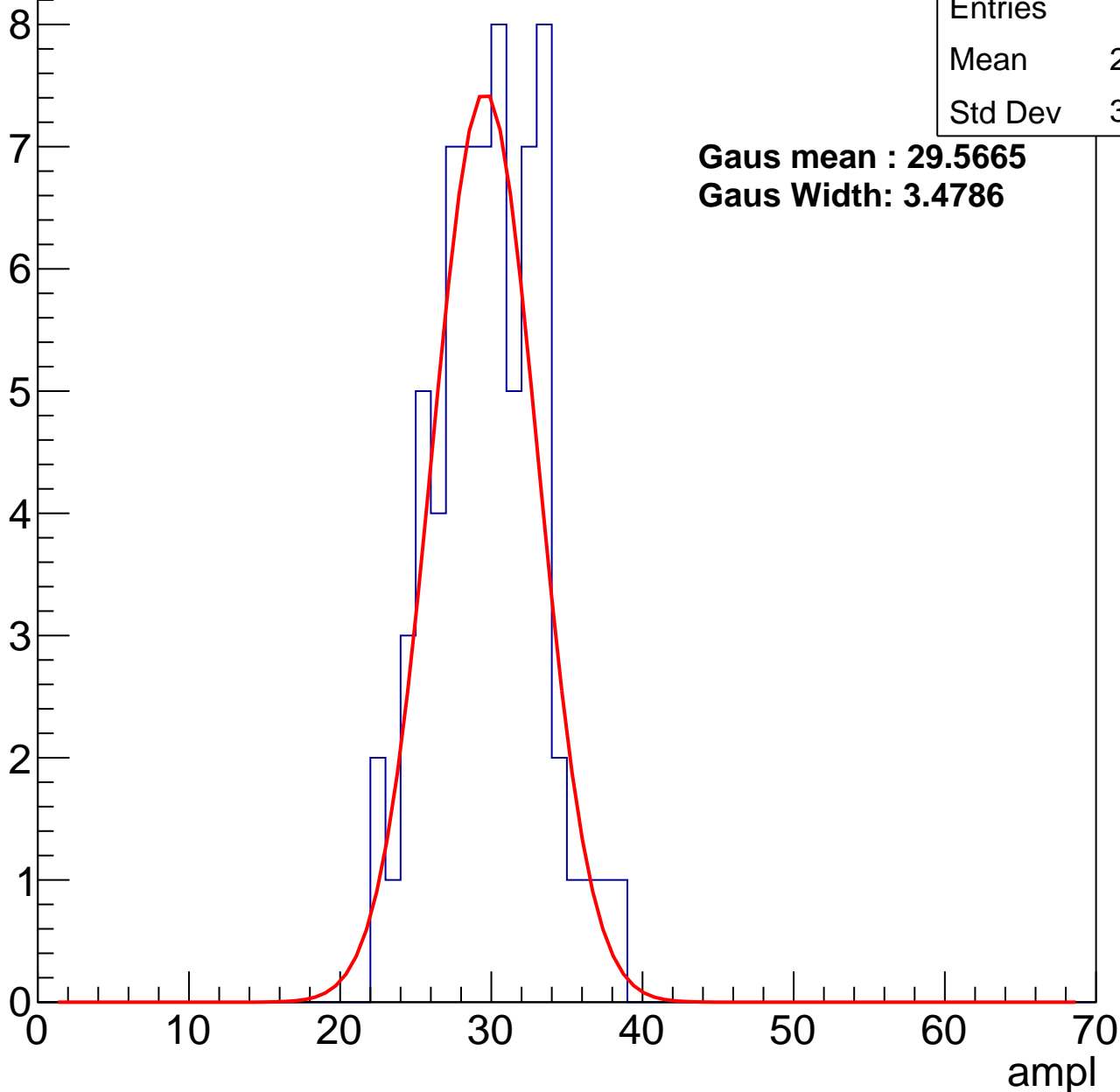
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.33
Std Dev	3.488

Gaus mean : 29.5665

Gaus Width: 3.4786



B0L001S, U14-ch8, adc1

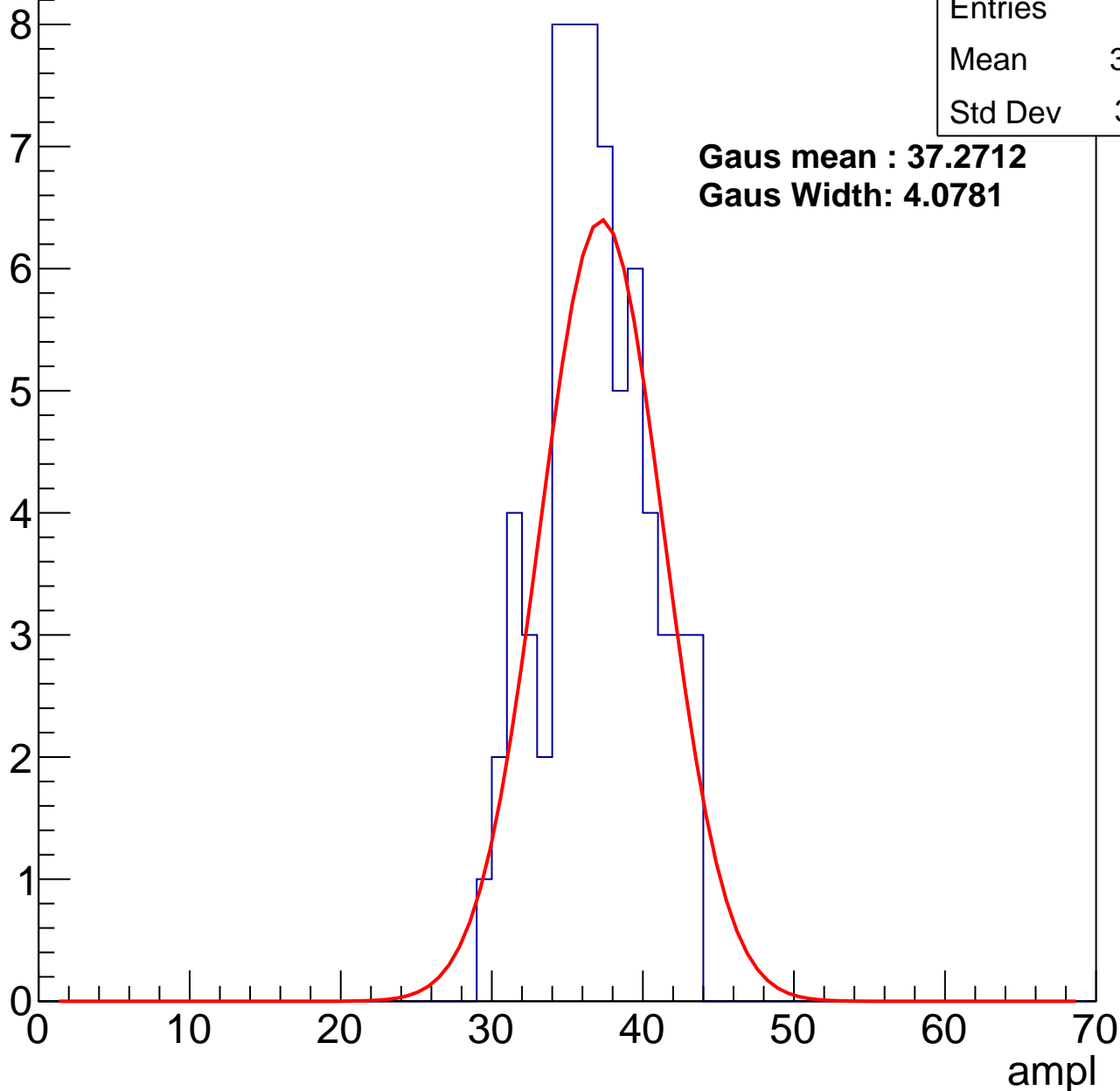
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.36
Std Dev	3.441

Gaus mean : 37.2712

Gaus Width: 4.0781



B0L001S, U14-ch8, adc2

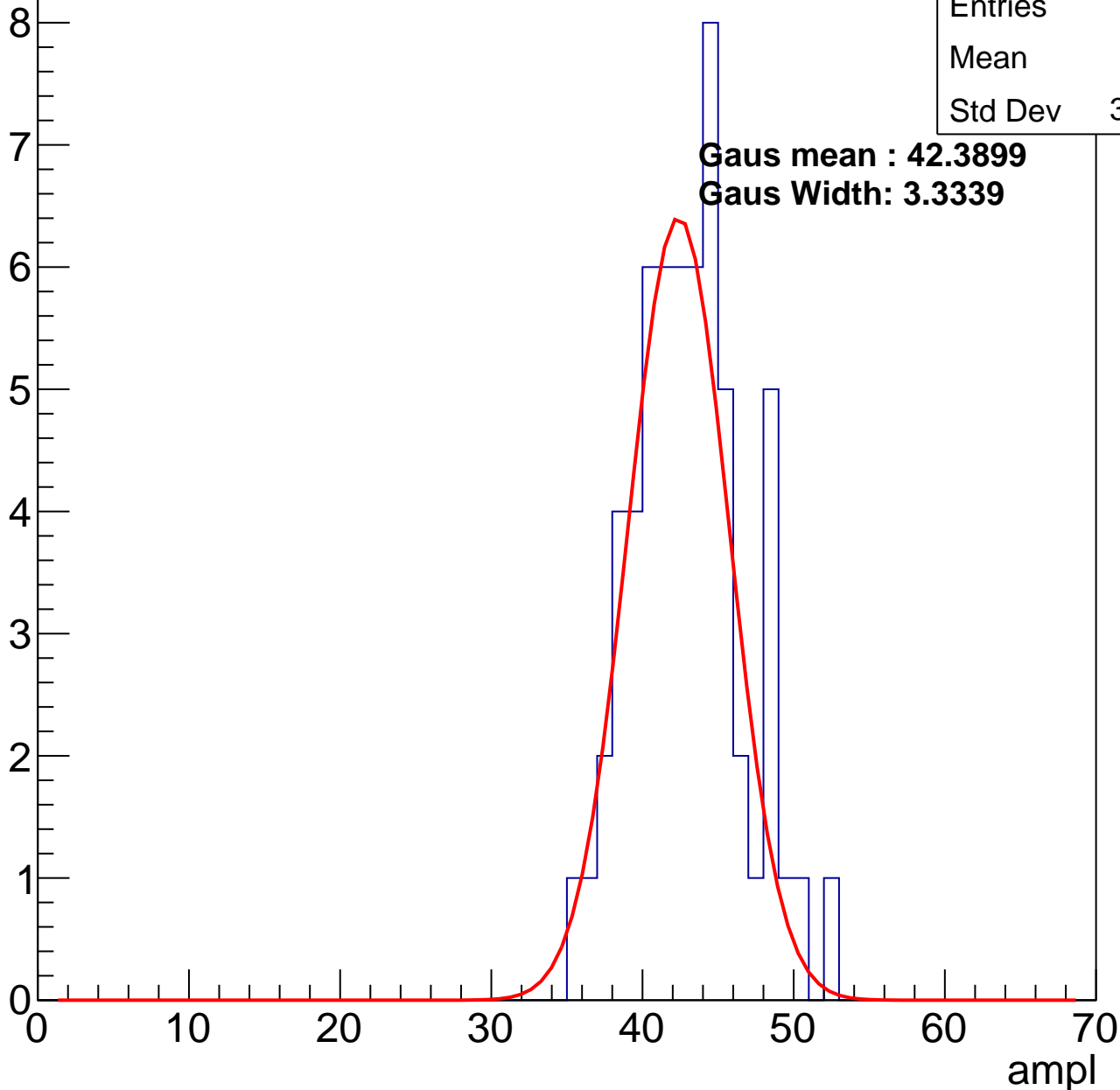
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	42.6
Std Dev	3.597

Gaus mean : 42.3899

Gaus Width: 3.3339

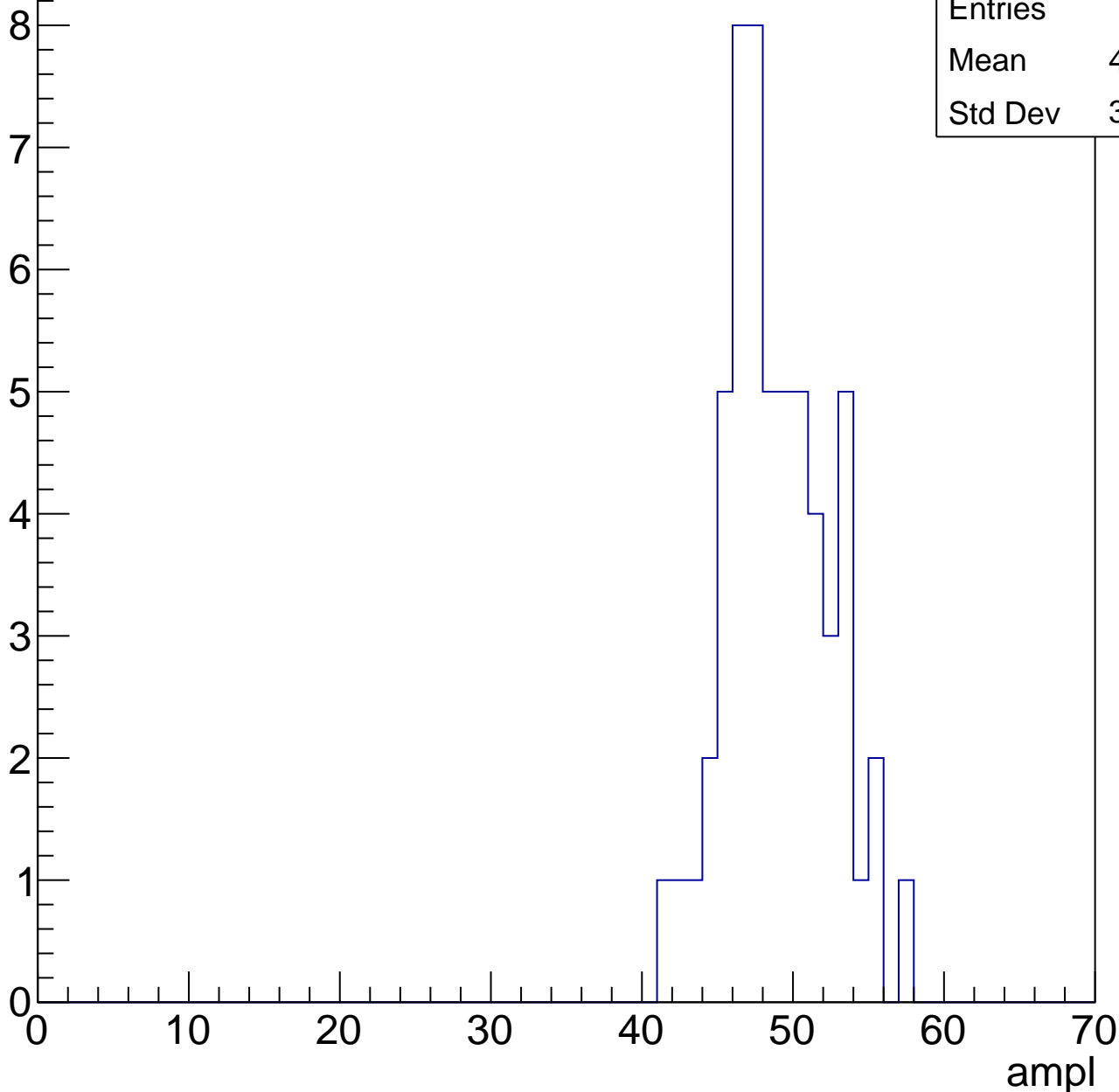


B0L001S, U14-ch8, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	48.49
Std Dev	3.409

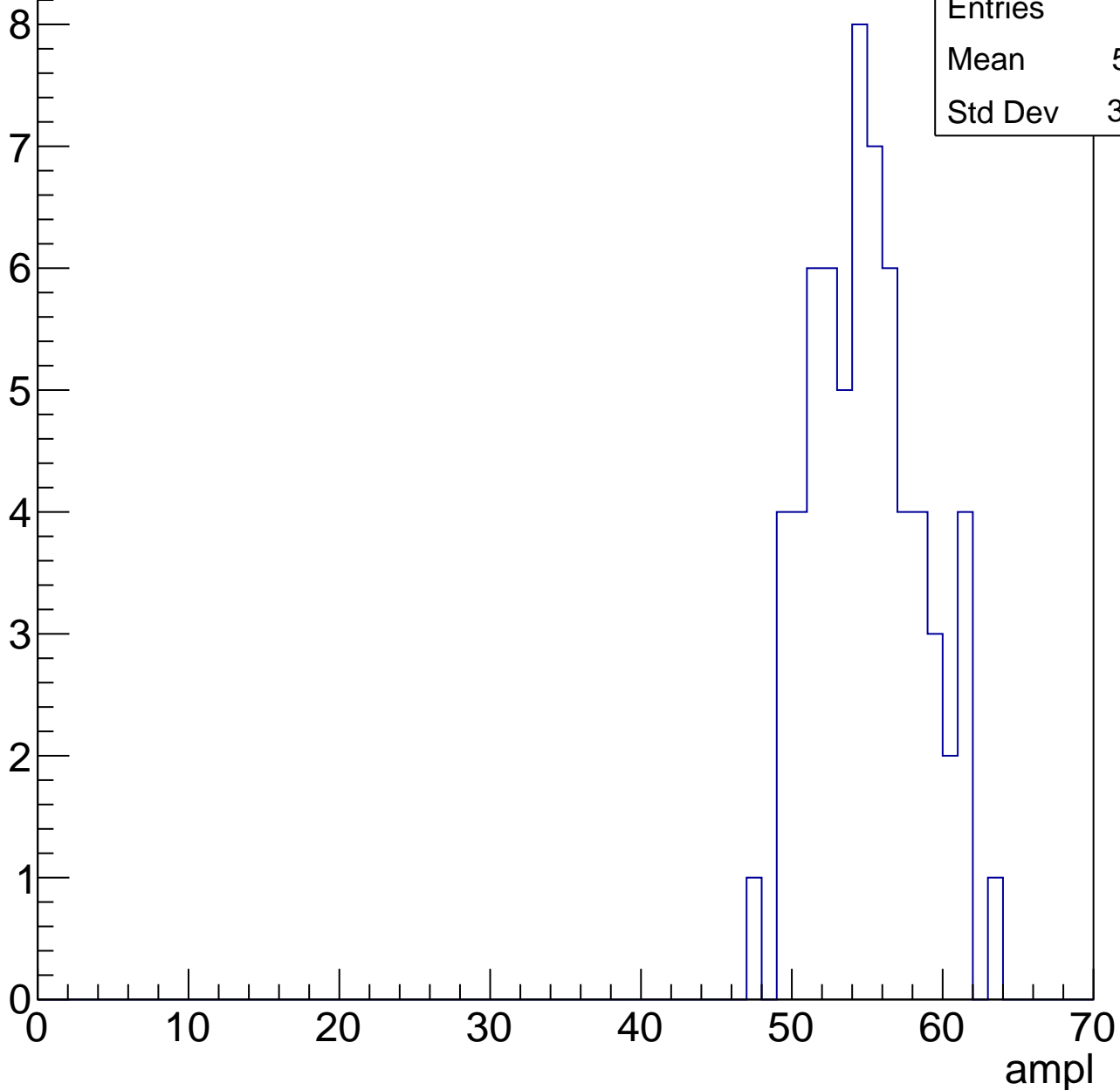


B0L001S, U14-ch8, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	54.51
Std Dev	3.583



B0L001S, U14-ch8, adc5

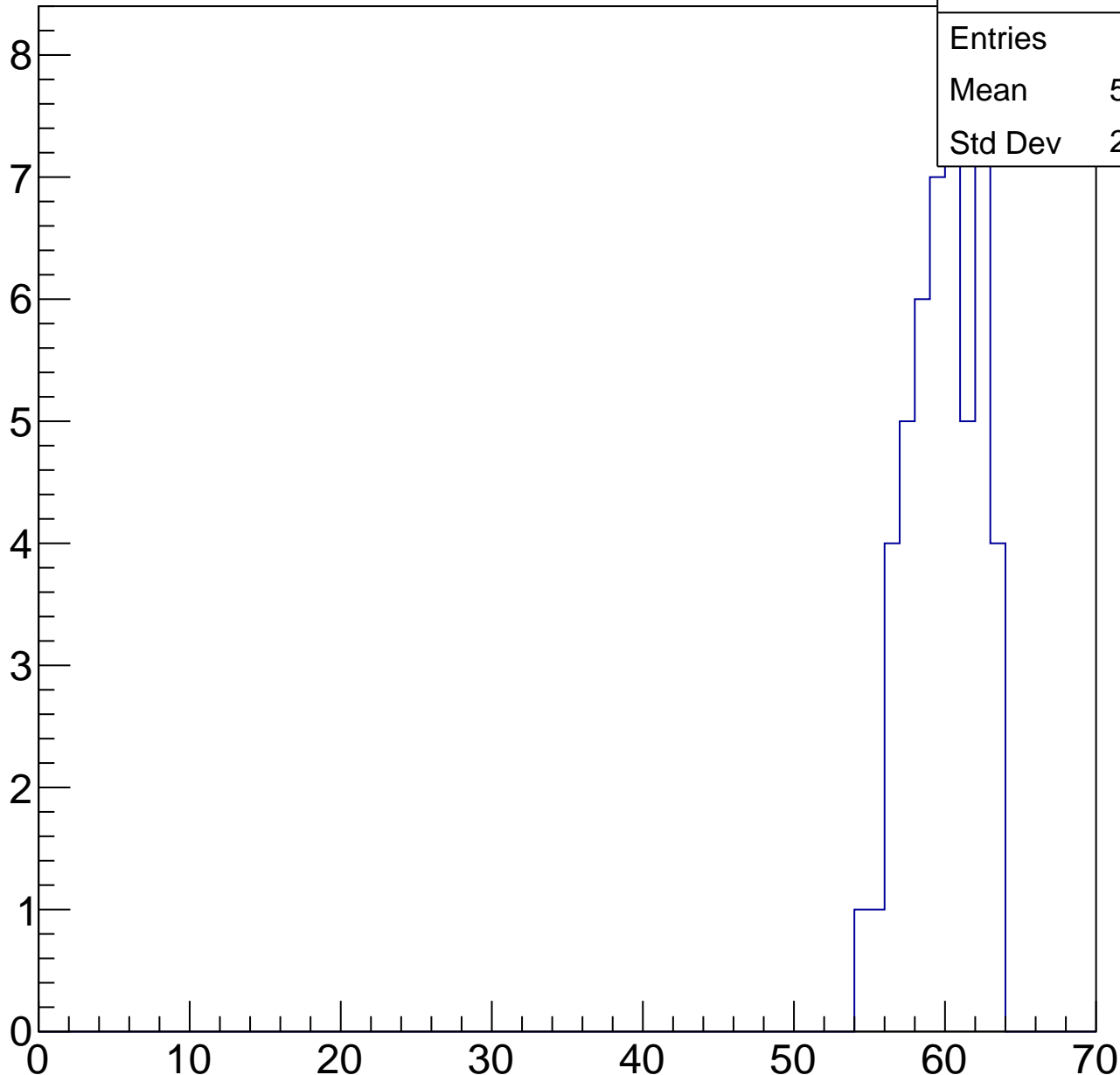
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.43
Std Dev	2.295

ampl

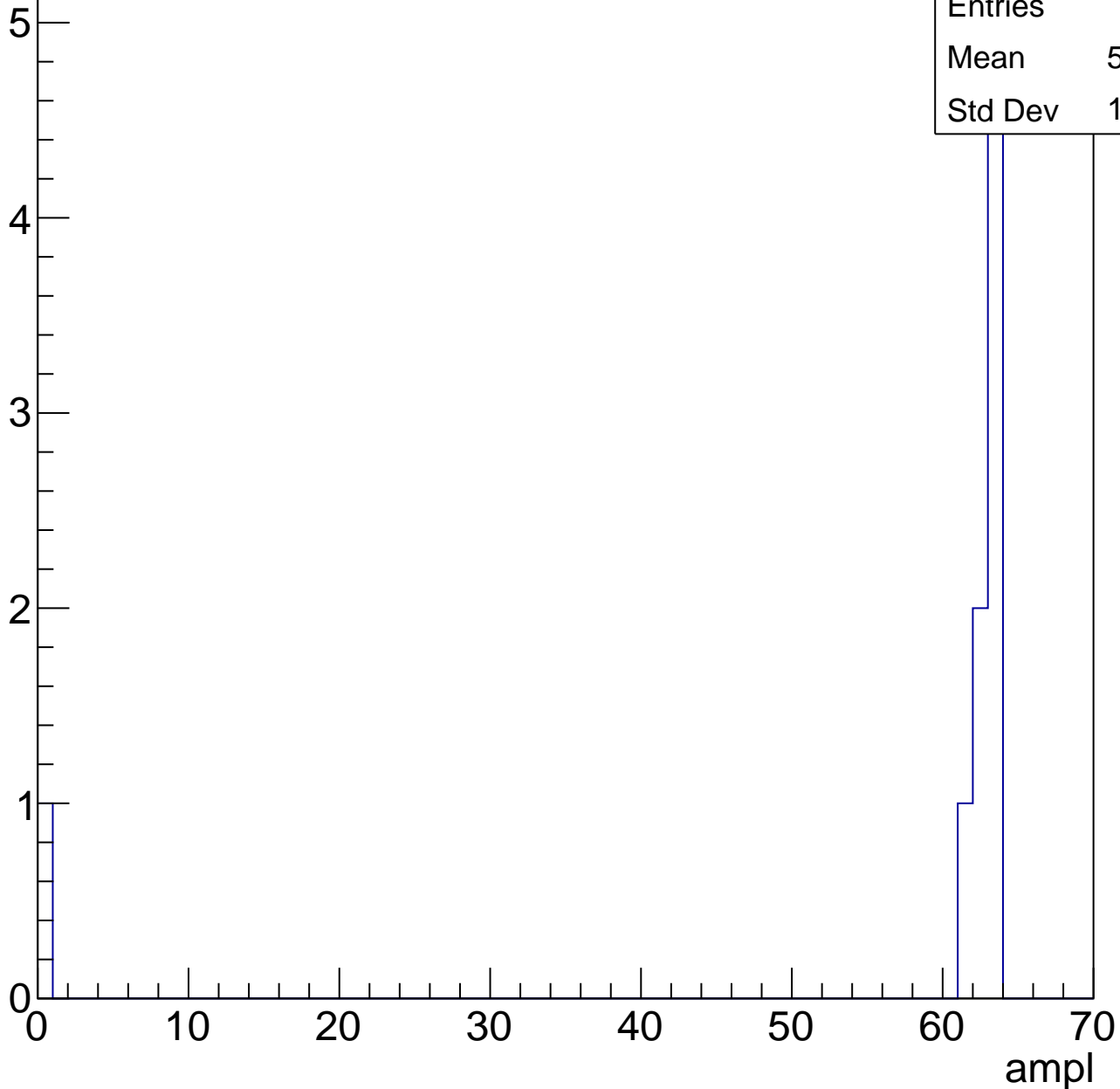


B0L001S, U14-ch8, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	55.56
Std Dev	19.65



B0L001S, U14-ch8, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch9, adc0

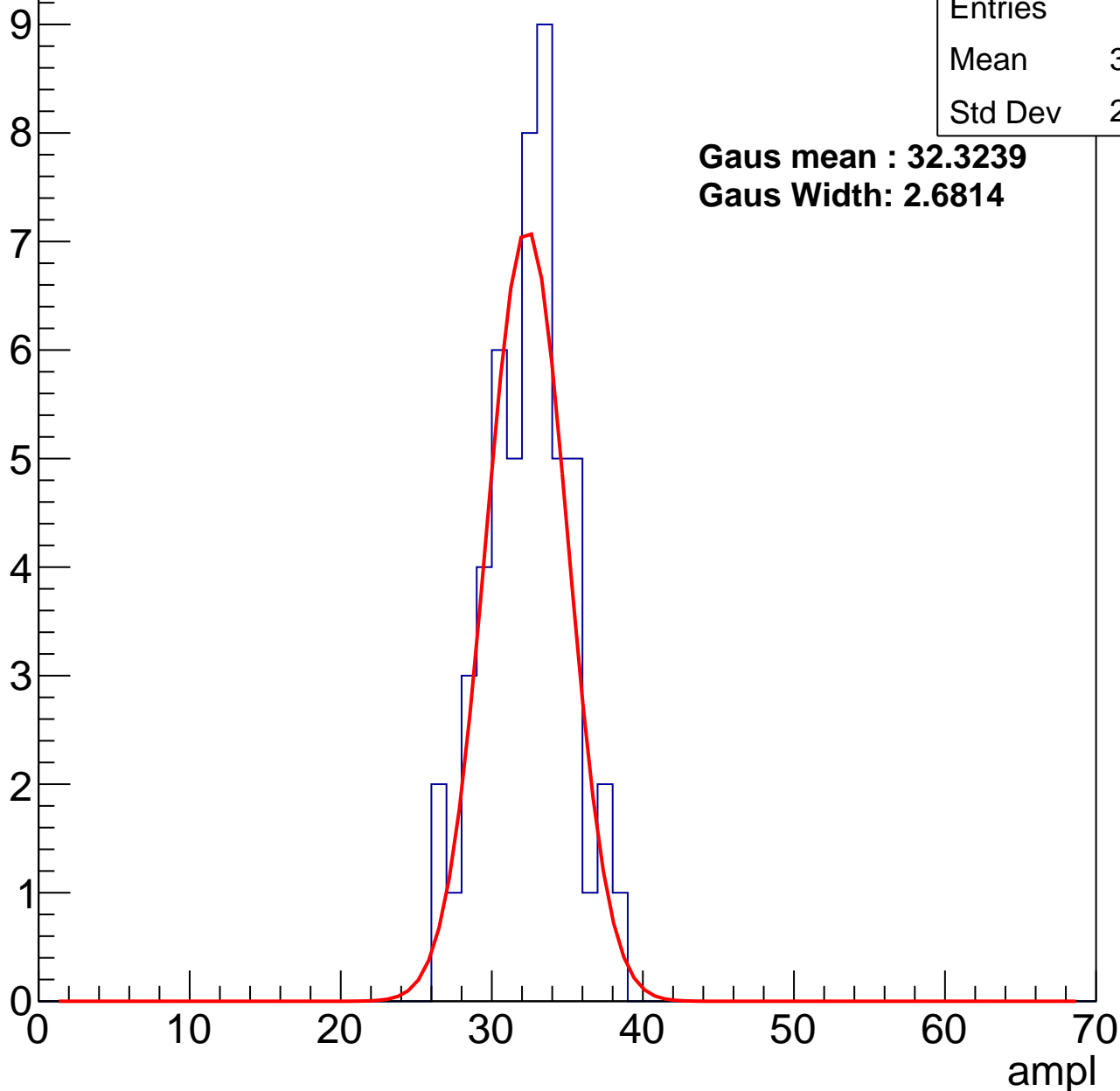
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	31.92
Std Dev	2.723

Gaus mean : 32.3239

Gaus Width: 2.6814



B0L001S, U14-ch9, adc1

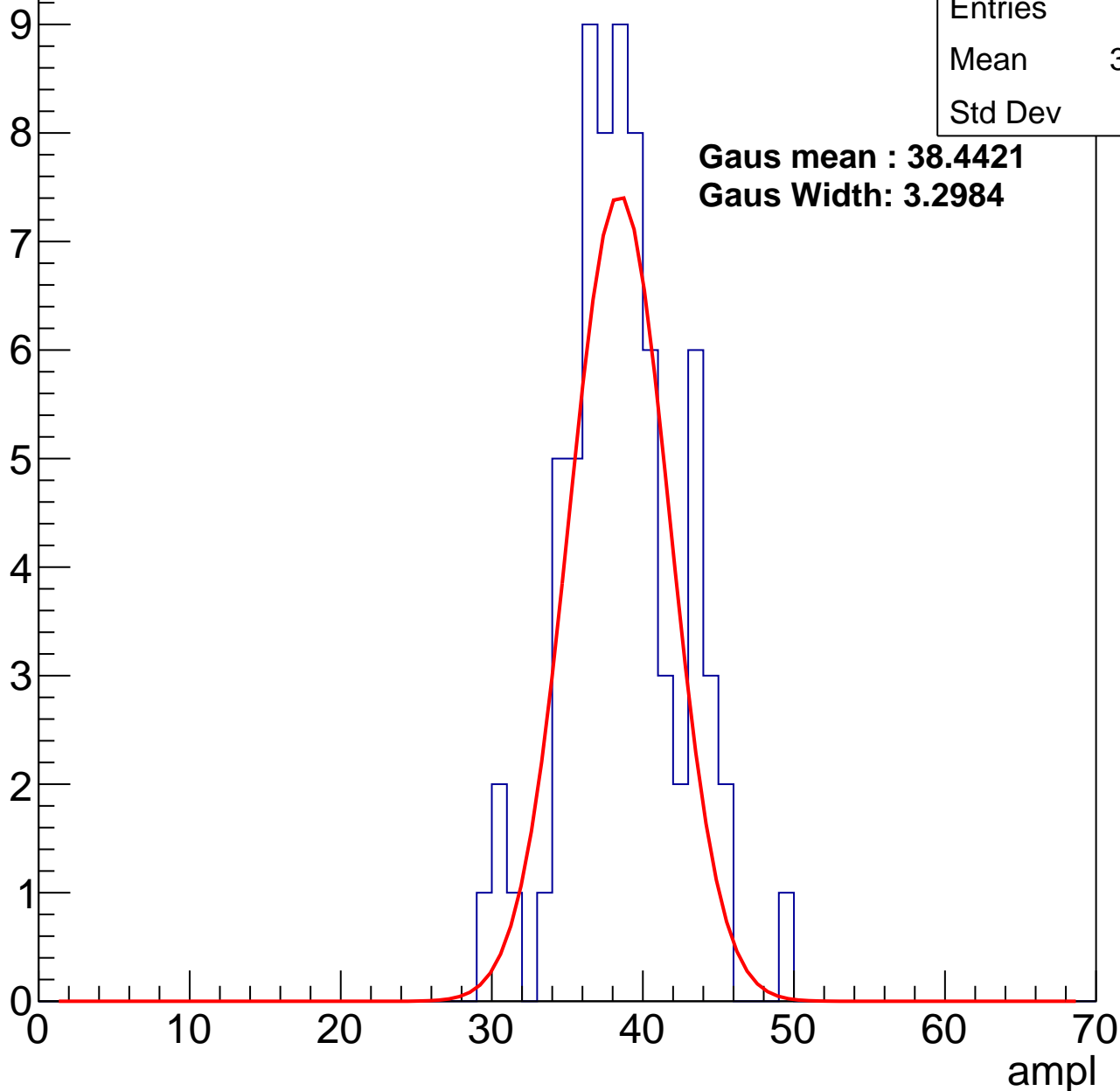
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.17
Std Dev	3.76

Gaus mean : 38.4421

Gaus Width: 3.2984



B0L001S, U14-ch9, adc2

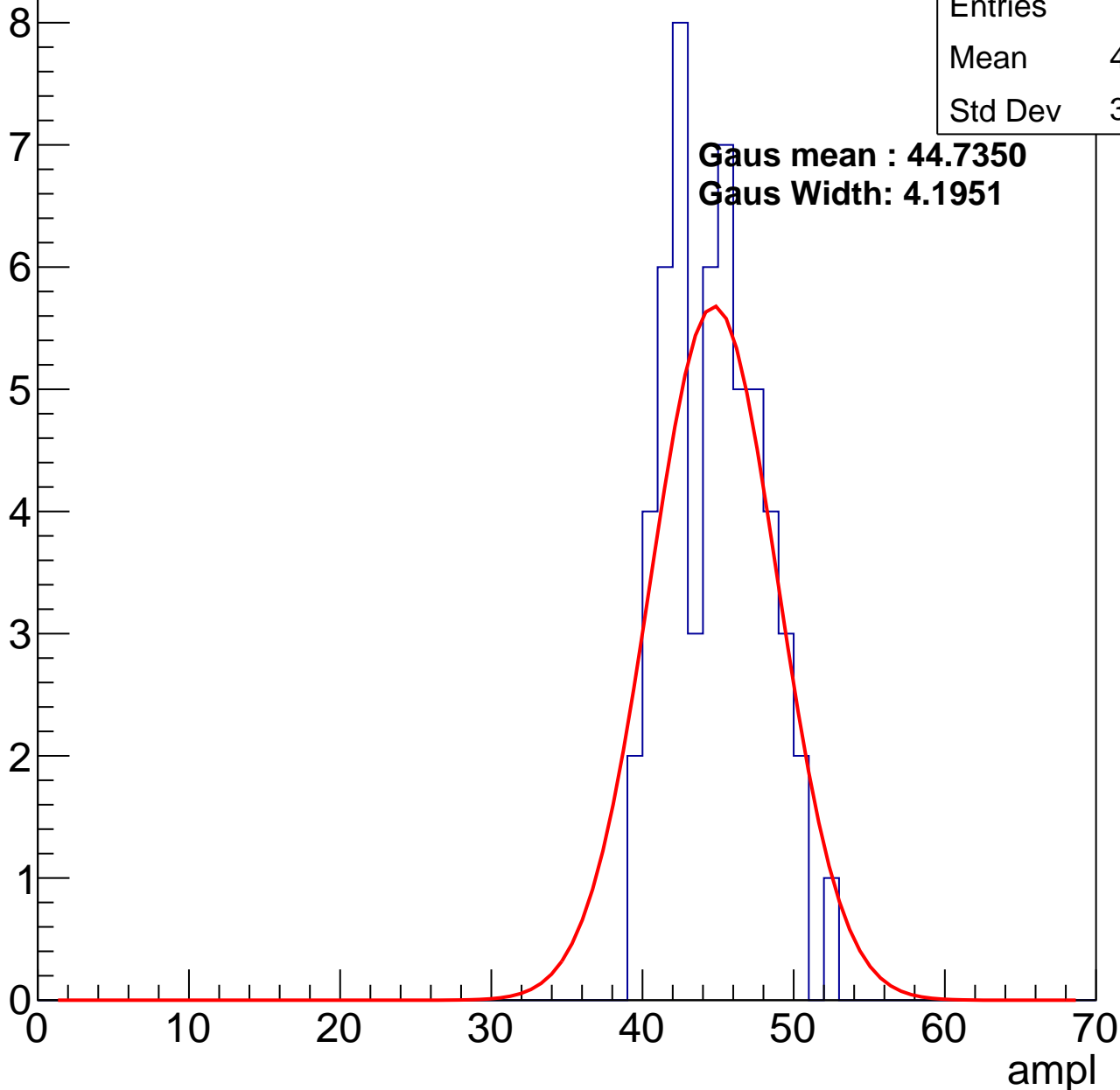
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	44.36
Std Dev	3.113

Gaus mean : 44.7350

Gaus Width: 4.1951

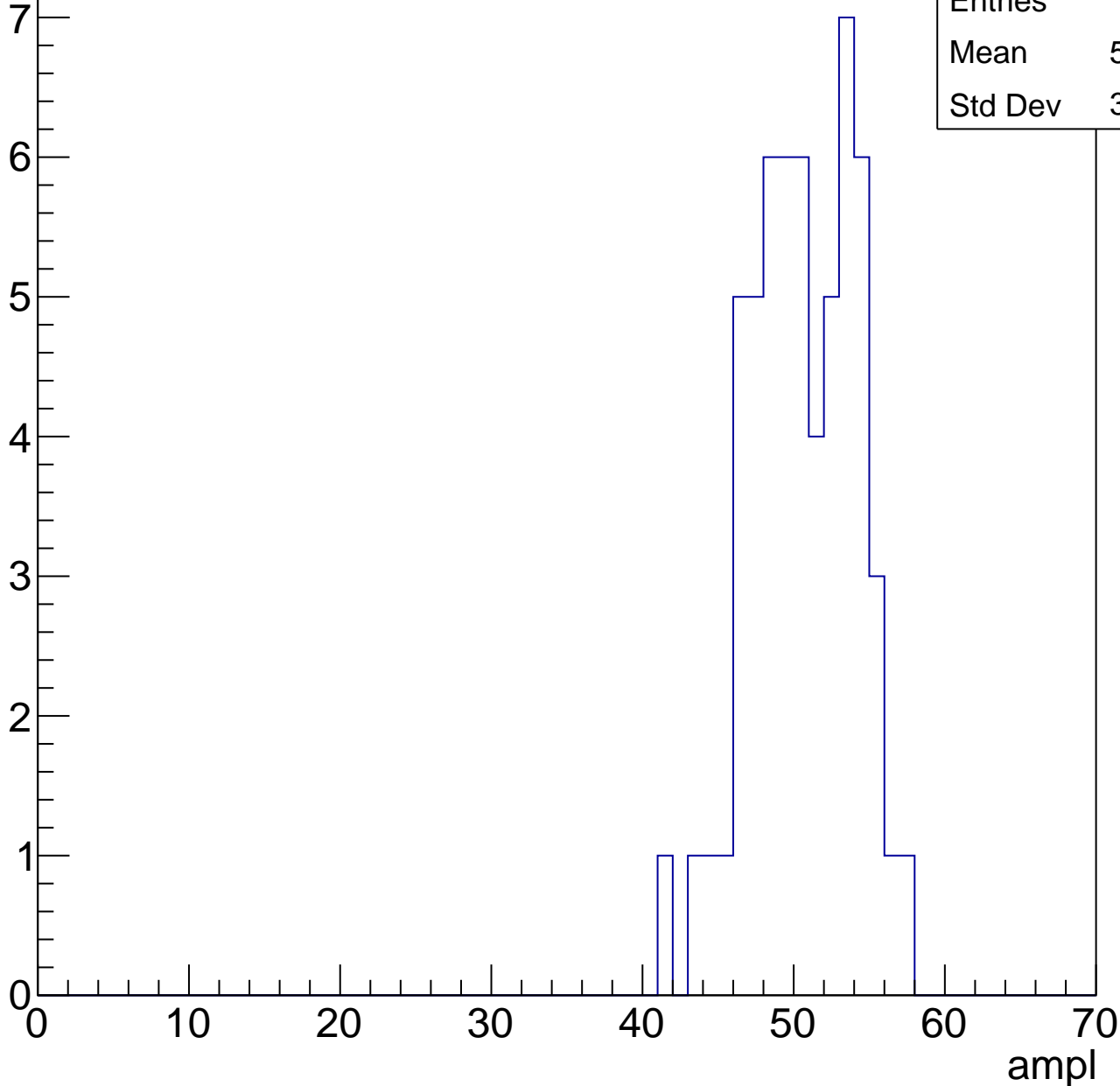


B0L001S, U14-ch9, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.12
Std Dev	3.415



B0L001S, U14-ch9, adc4

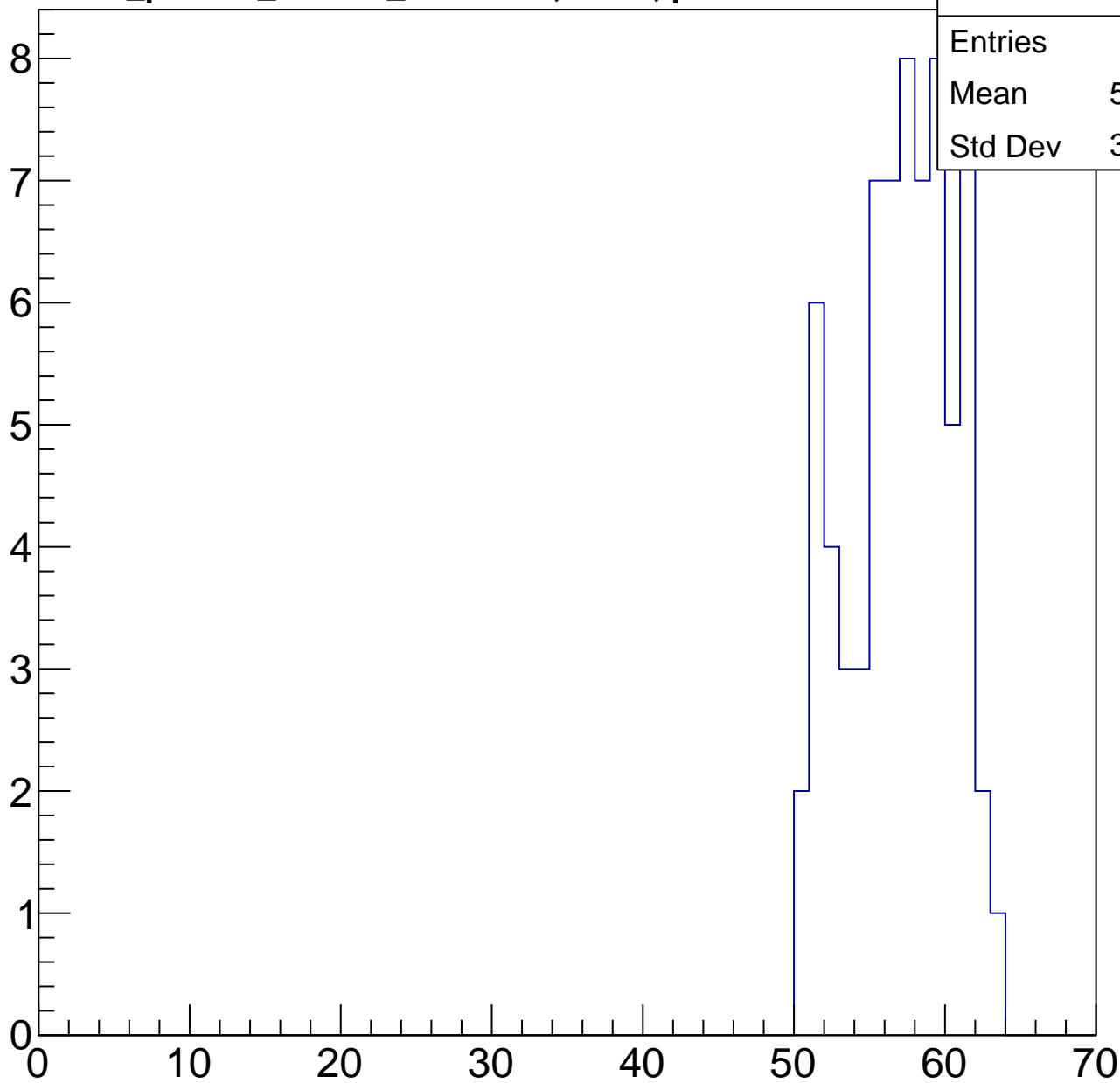
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	56.63
Std Dev	3.383

ampl

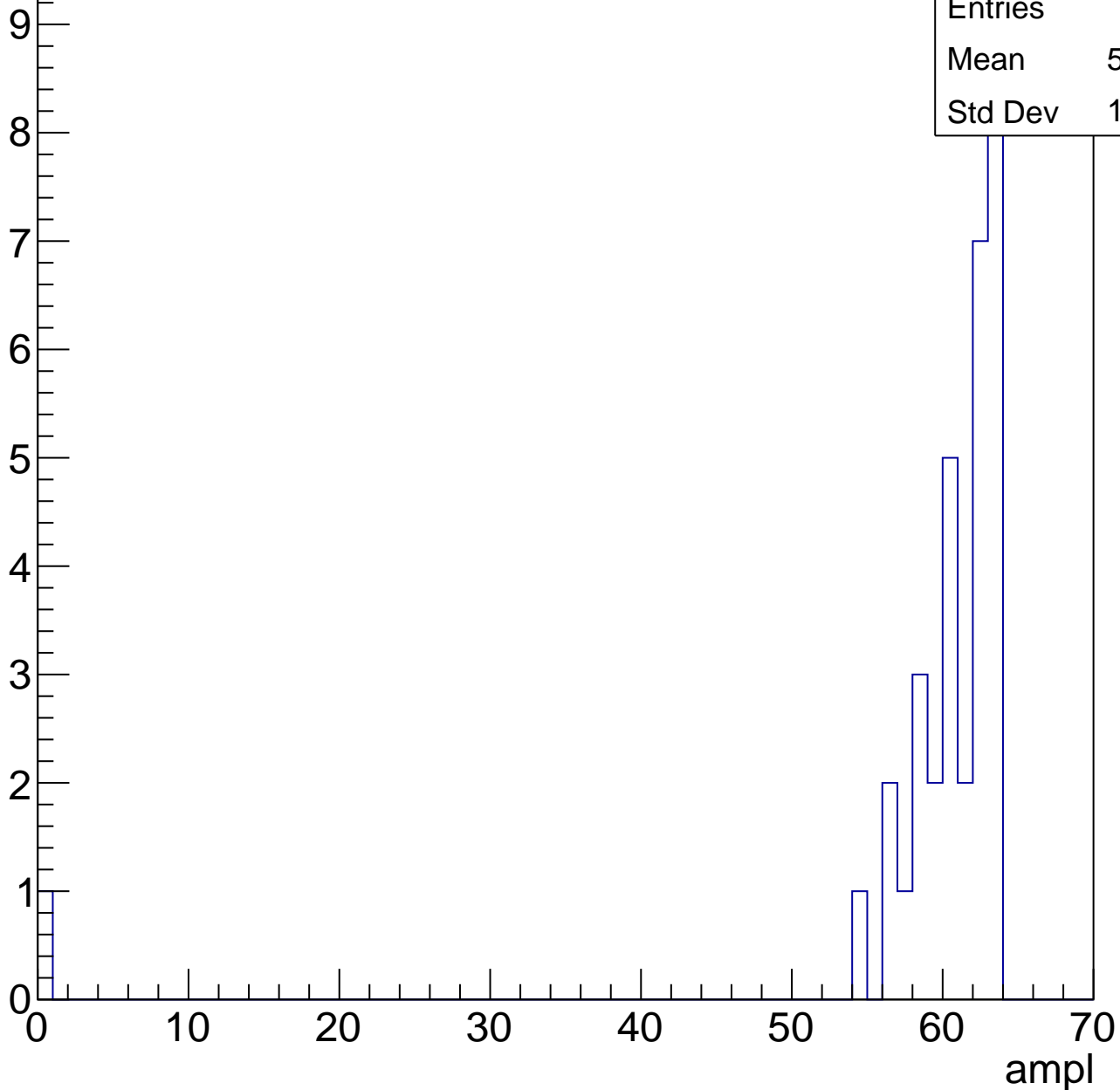


B0L001S, U14-ch9, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.73
Std Dev	10.66



B0L001S, U14-ch9, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch9, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch10, adc0

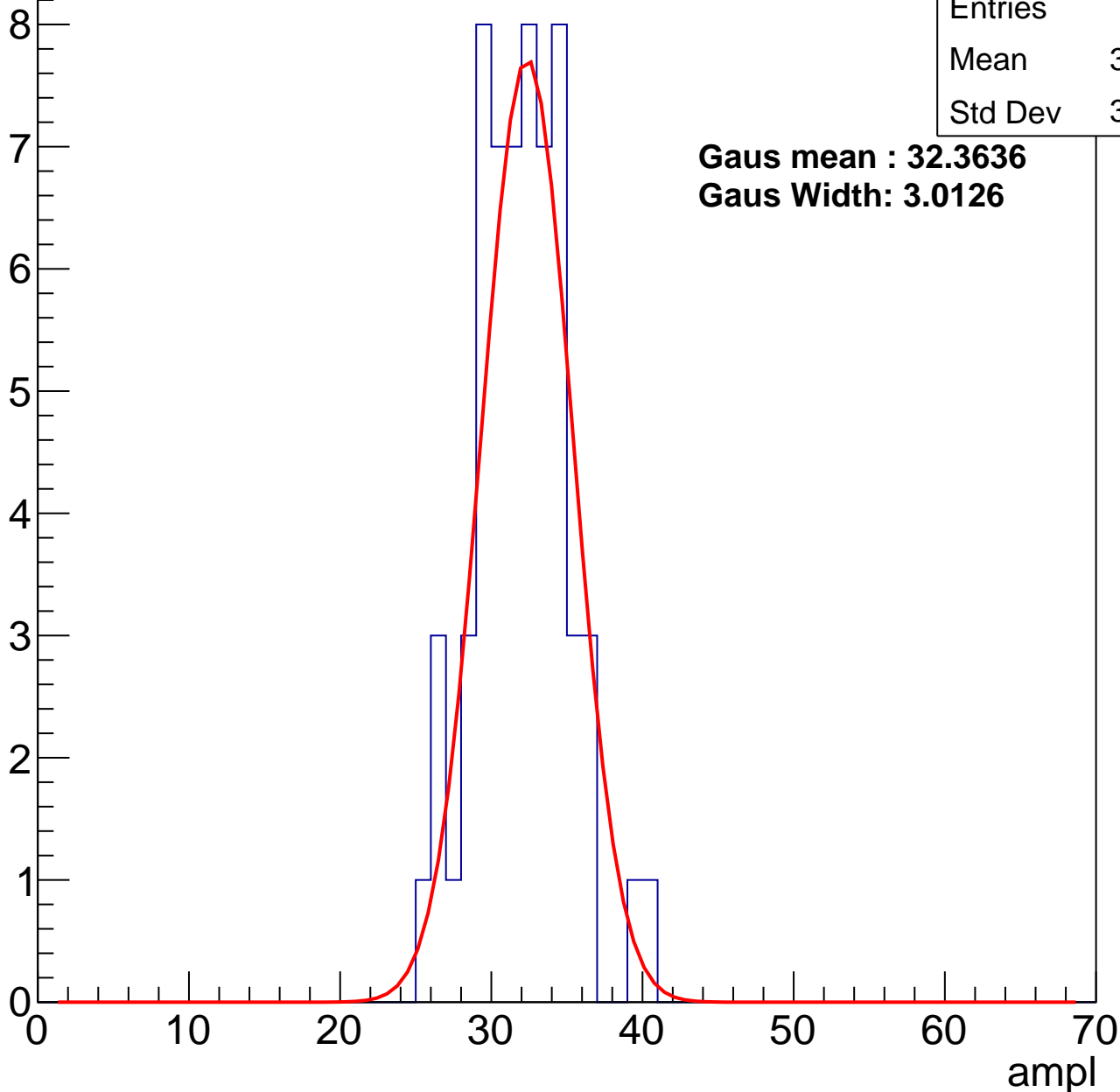
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	31.54
Std Dev	3.006

Gaus mean : 32.3636

Gaus Width: 3.0126



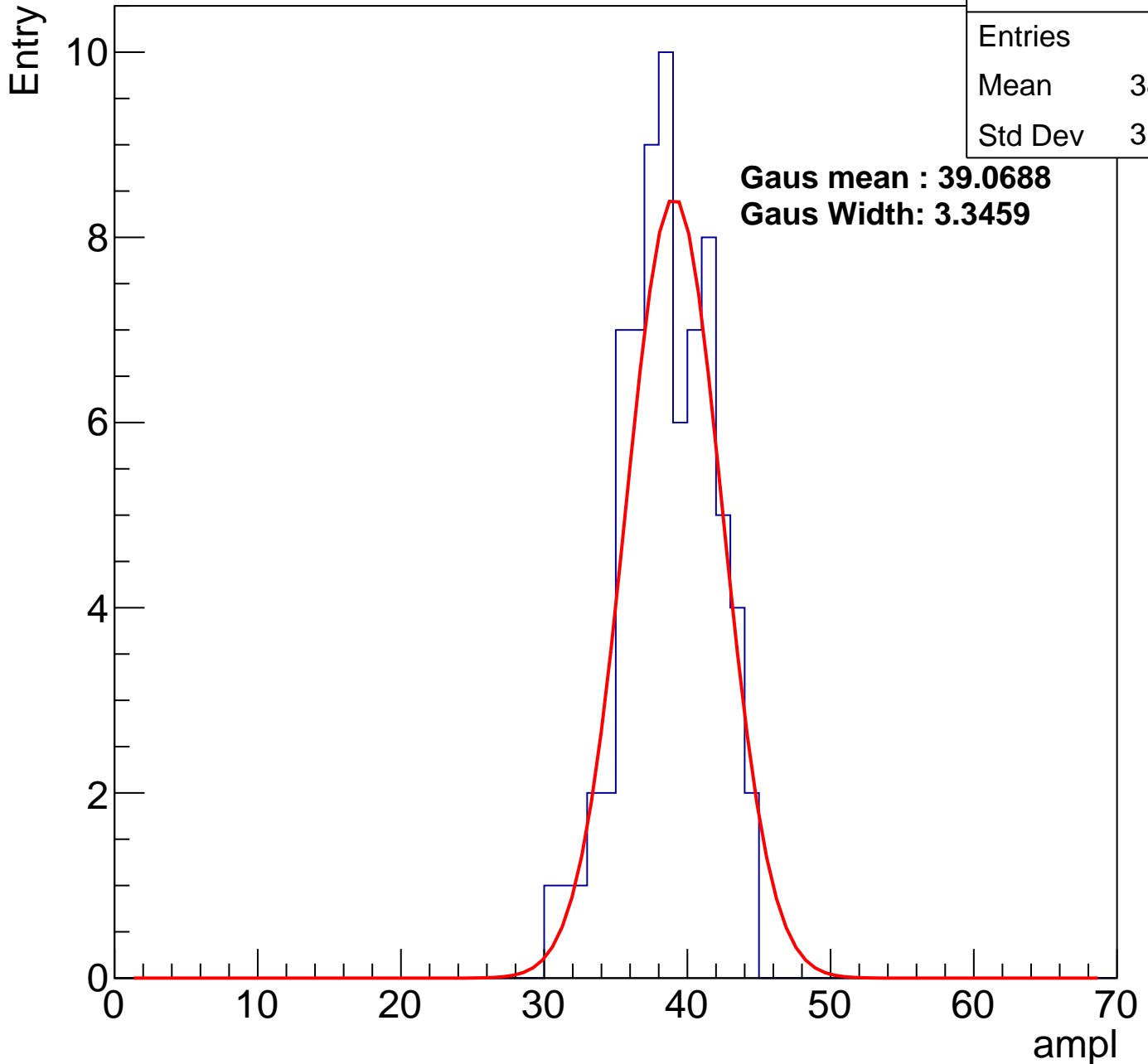
B0L001S, U14-ch10, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	38.18
Std Dev	3.088

Gaus mean : 39.0688

Gaus Width: 3.3459



B0L001S, U14-ch10, adc2

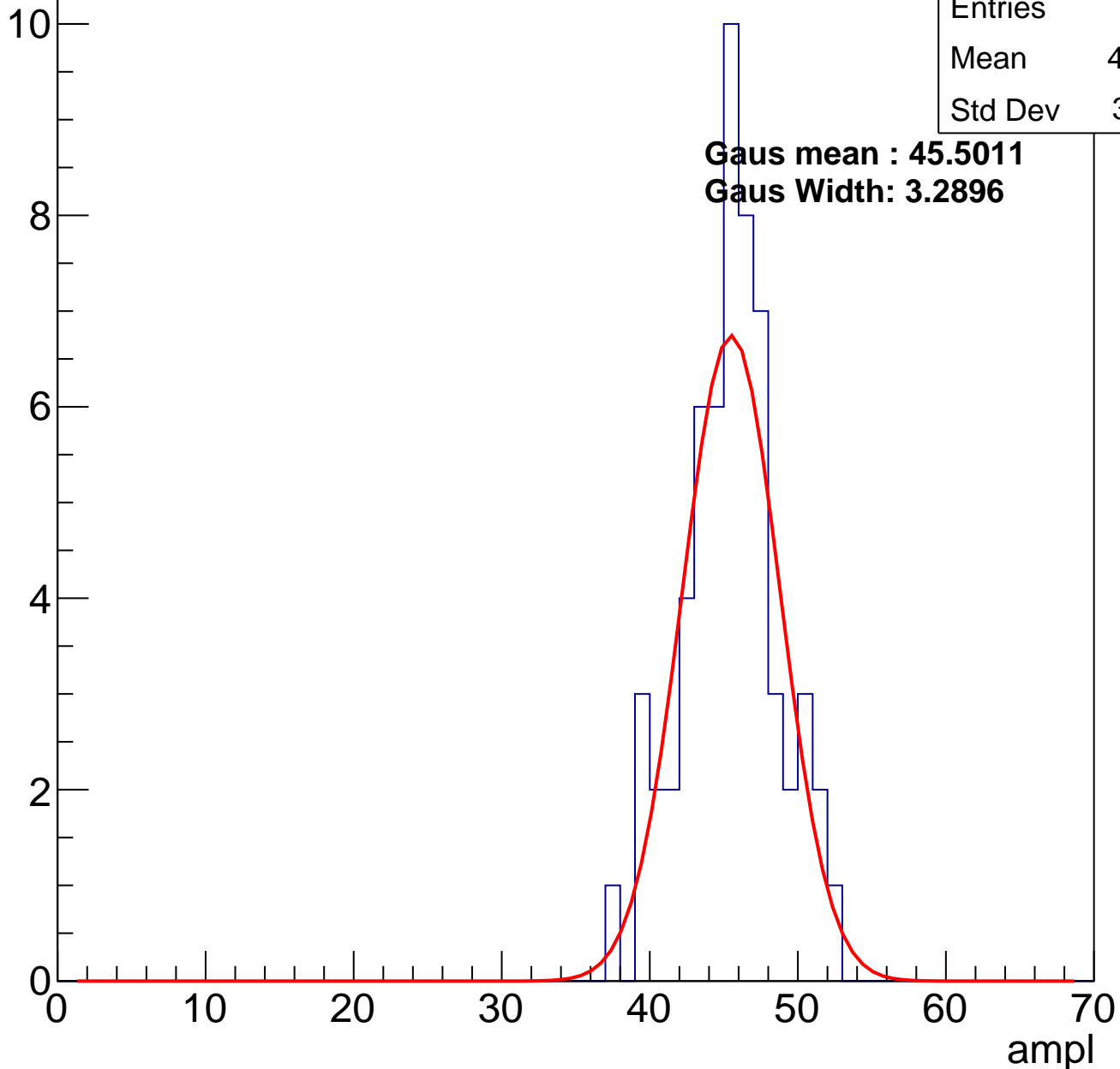
calib_packv5_042523_0143.root, FC#9, port A1

Entries	60
Mean	44.98
Std Dev	3.191

Gaus mean : 45.5011

Gaus Width: 3.2896

Entry

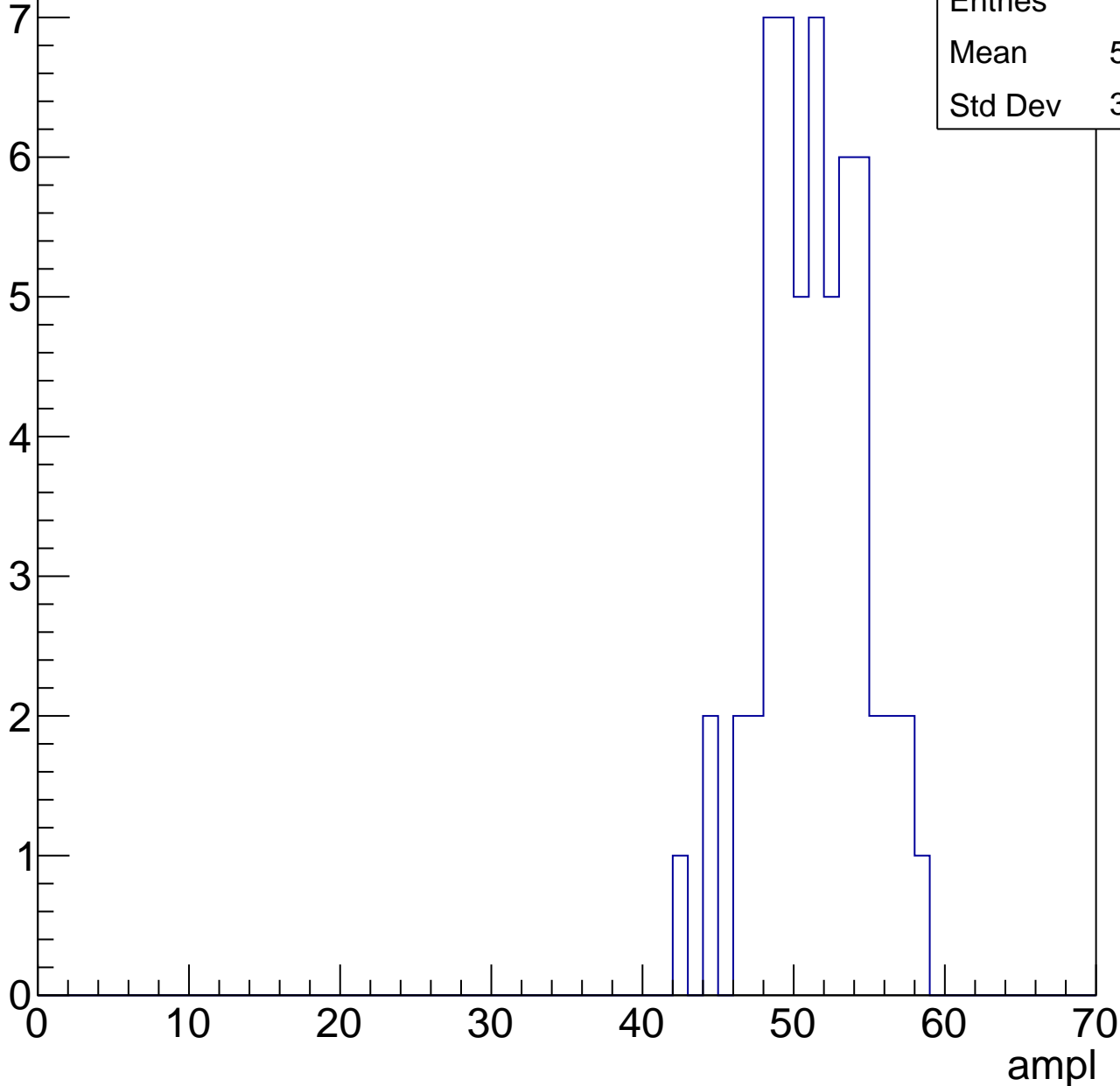


B0L001S, U14-ch10, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.84
Std Dev	3.355

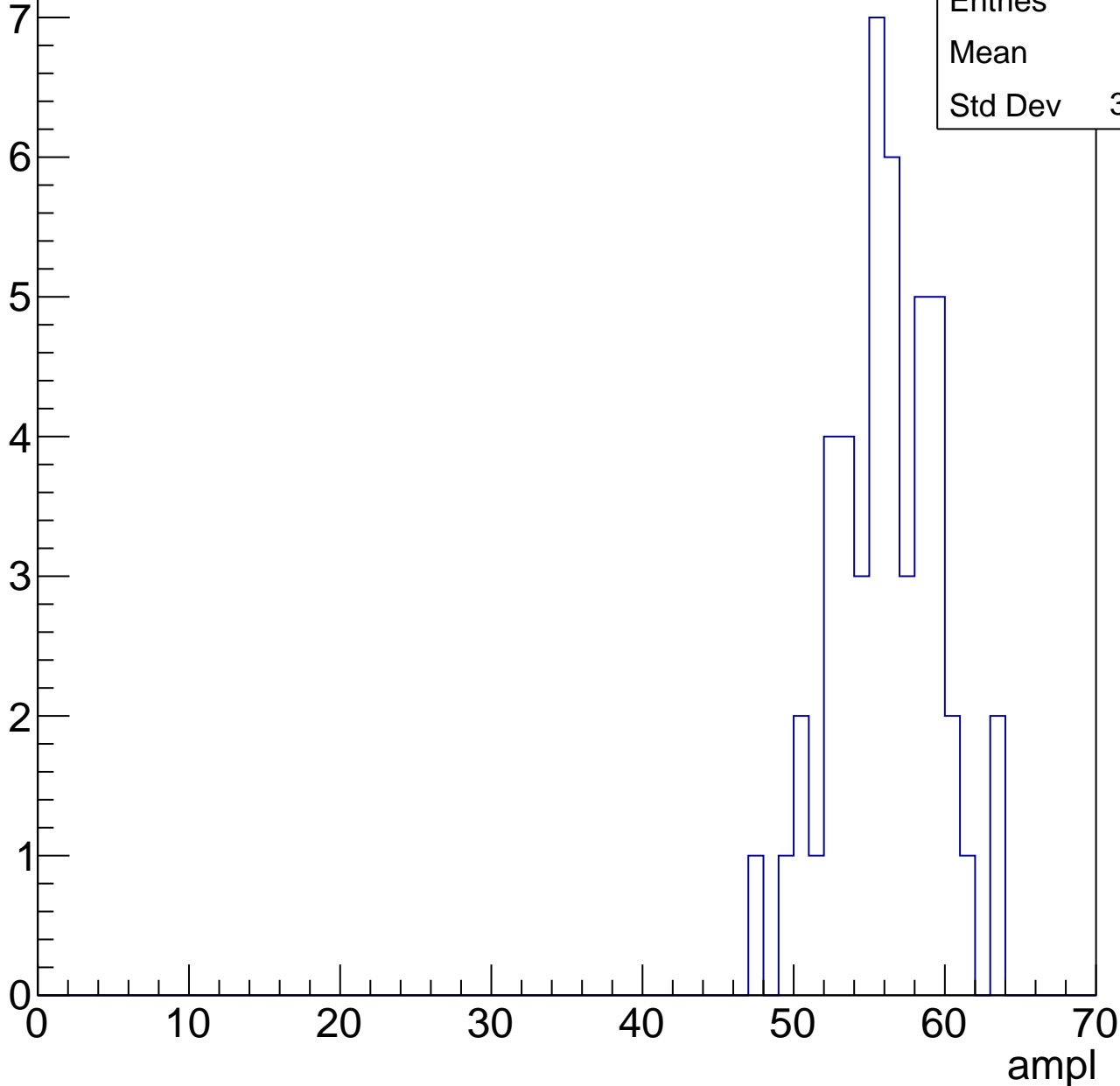


B0L001S, U14-ch10, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

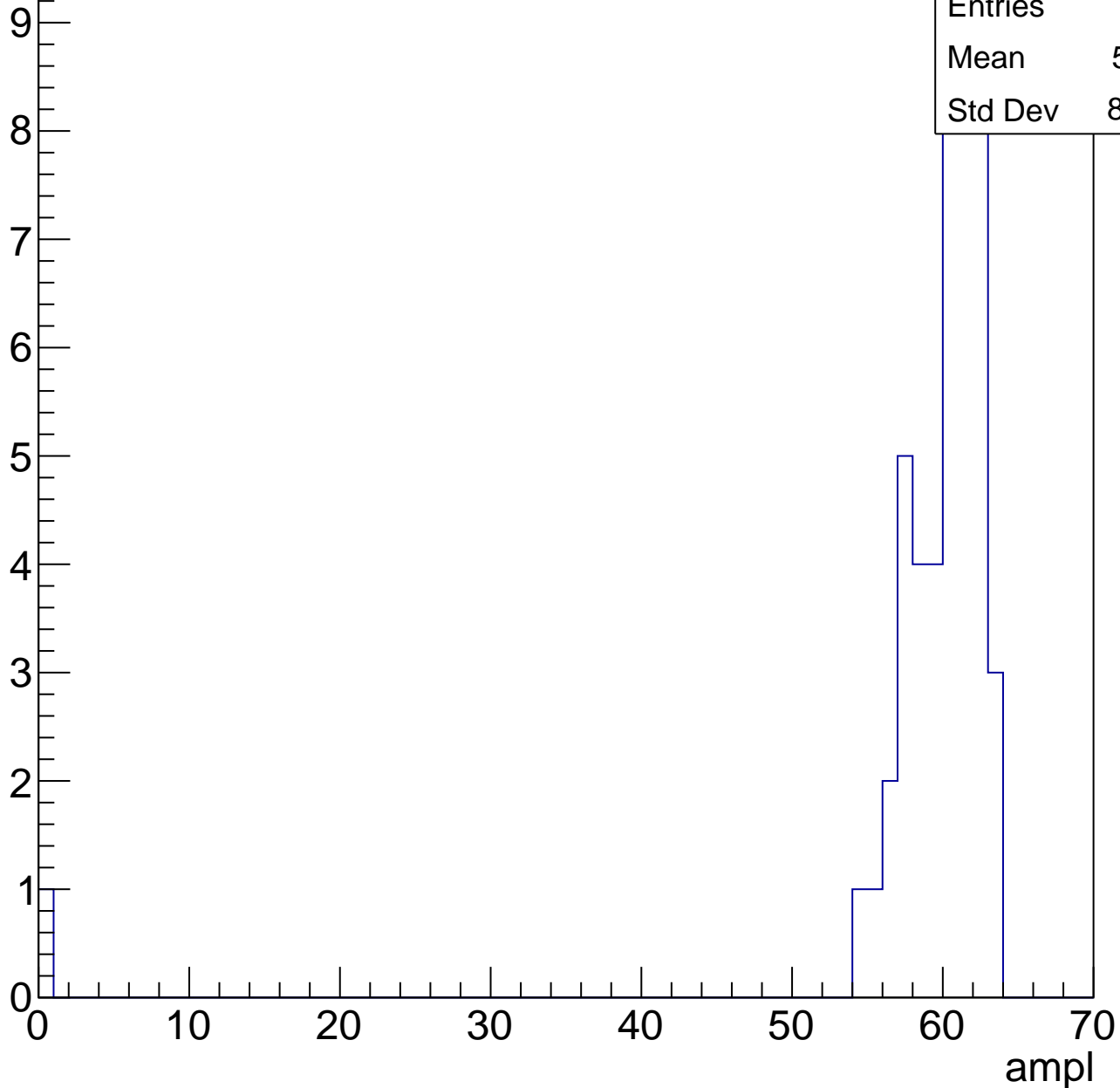
Entries	47
Mean	55.6
Std Dev	3.456



B0L001S, U14-ch10, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

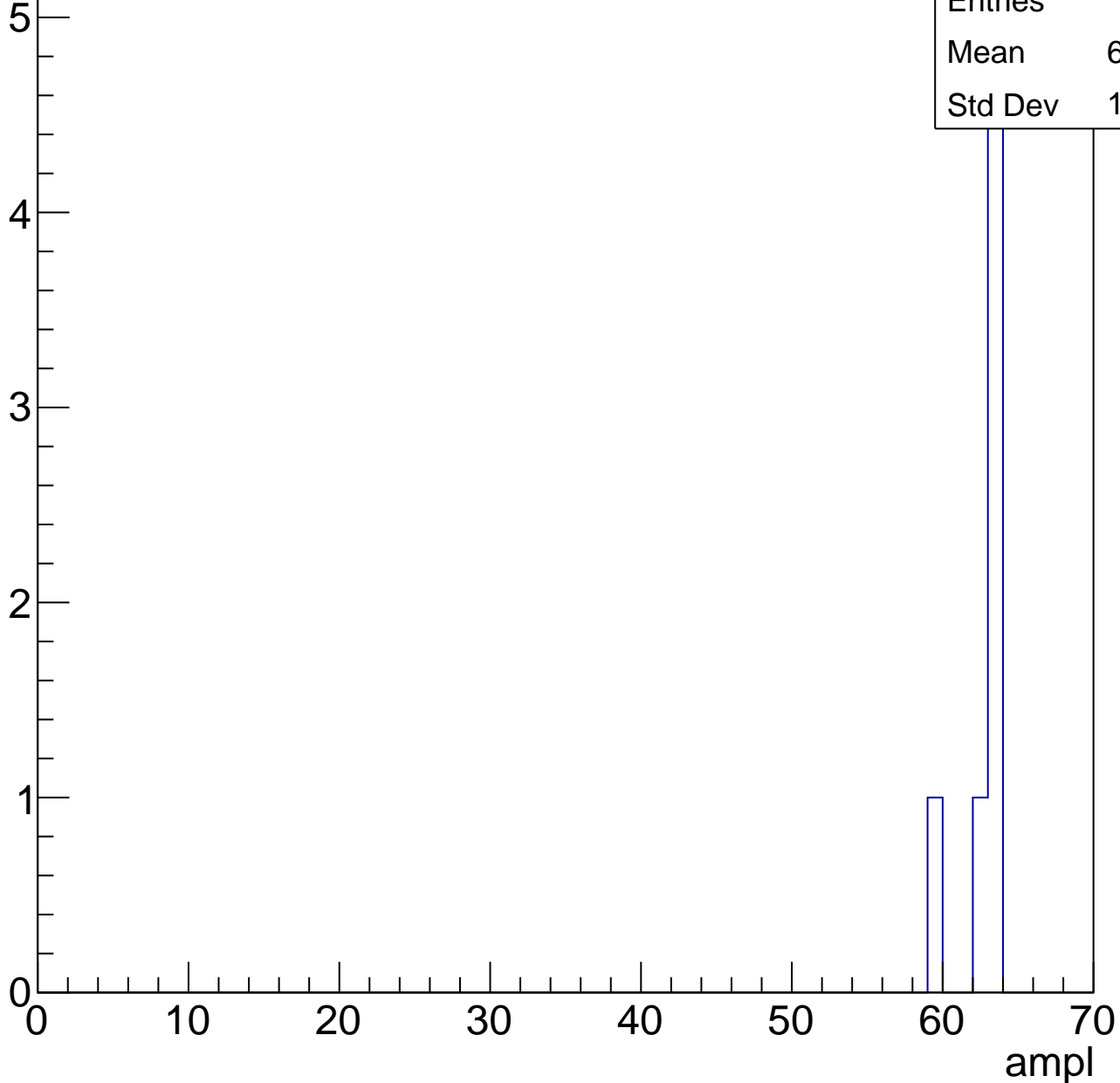


B0L001S, U14-ch10, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	7
Mean	62.29
Std Dev	1.385



B0L001S, U14-ch10, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch11, adc0

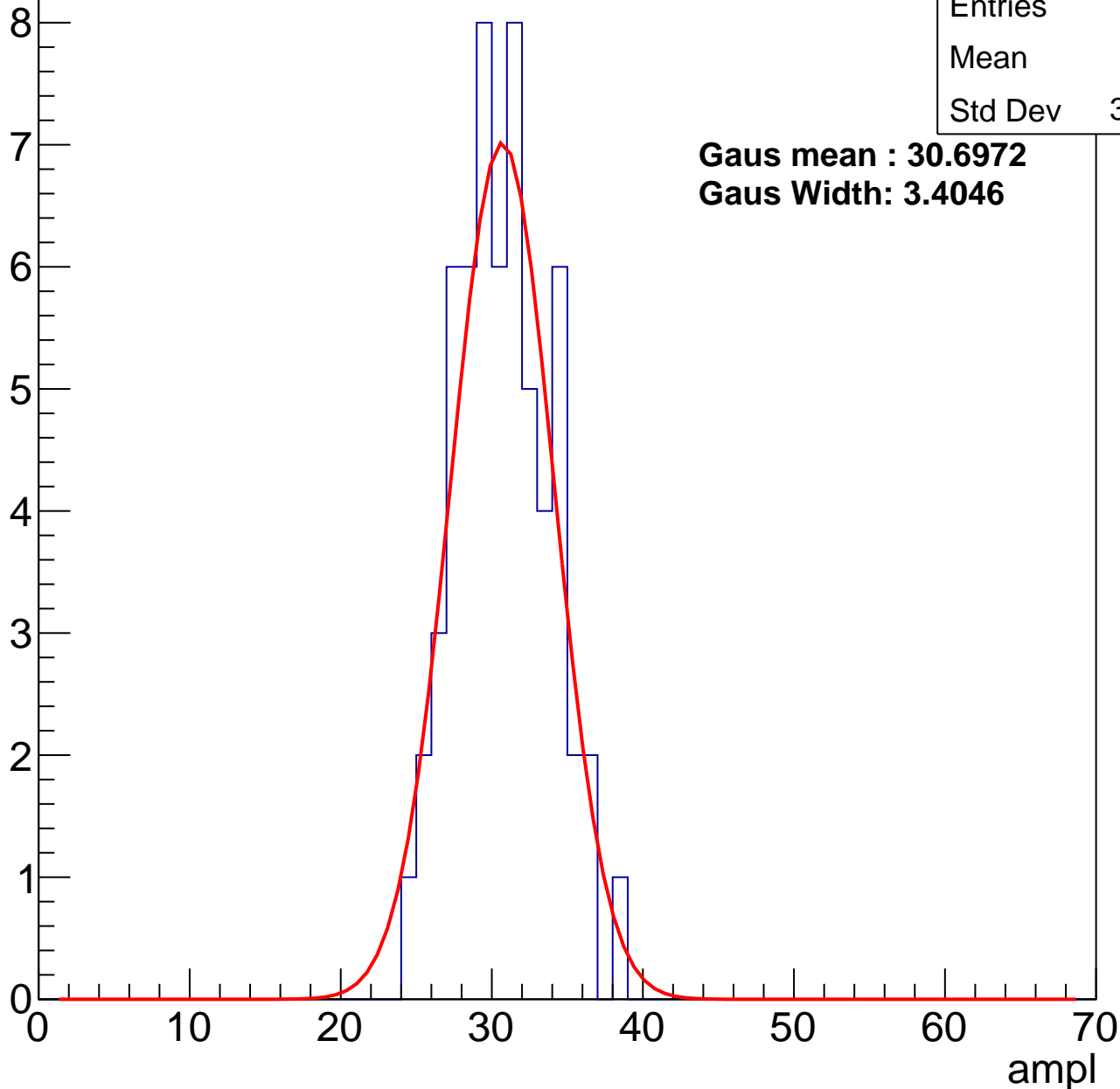
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	30.3
Std Dev	3.057

Gaus mean : 30.6972

Gaus Width: 3.4046



B0L001S, U14-ch11, adc1

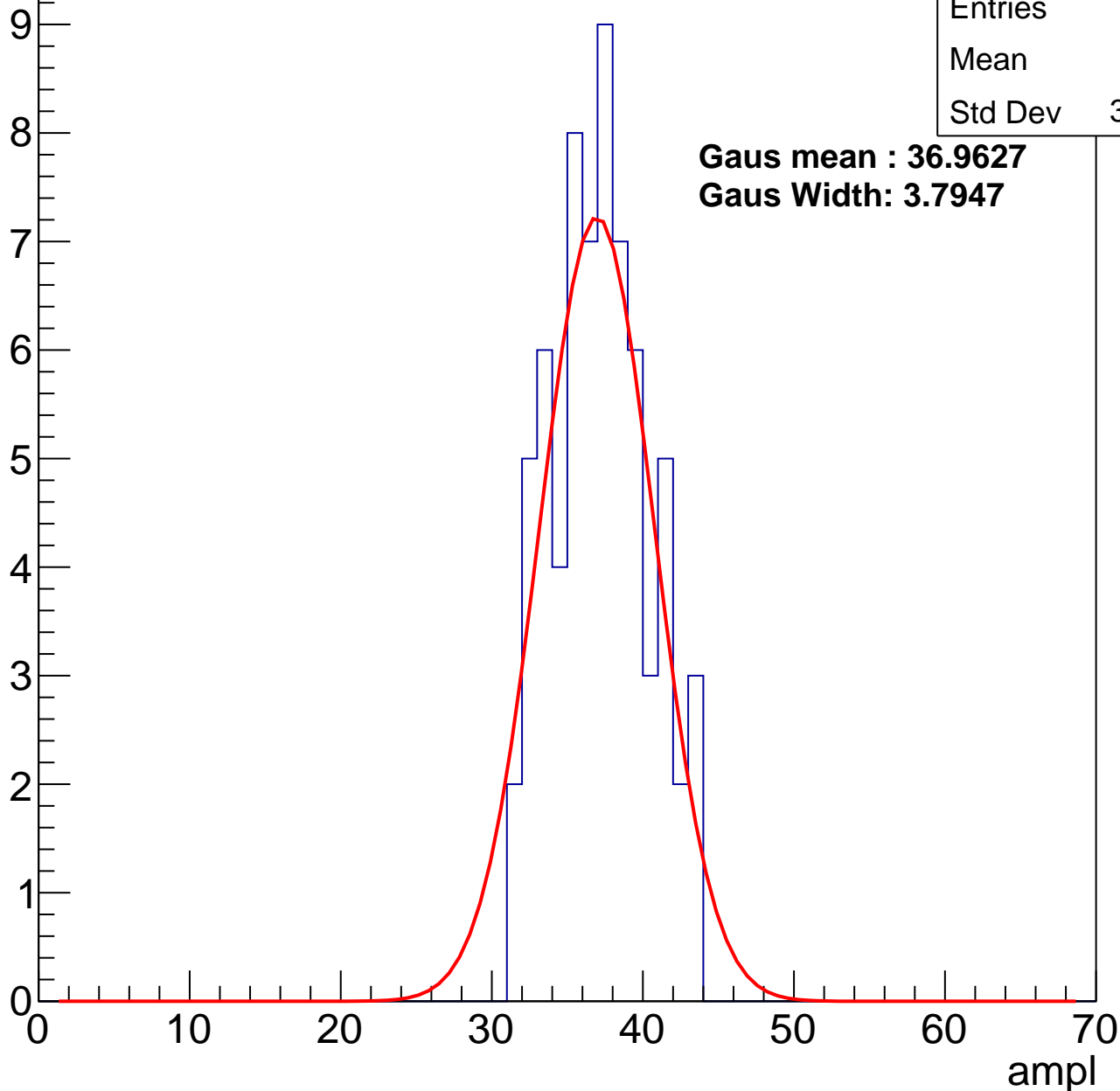
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.7
Std Dev	3.134

Gaus mean : 36.9627

Gaus Width: 3.7947



B0L001S, U14-ch11, adc2

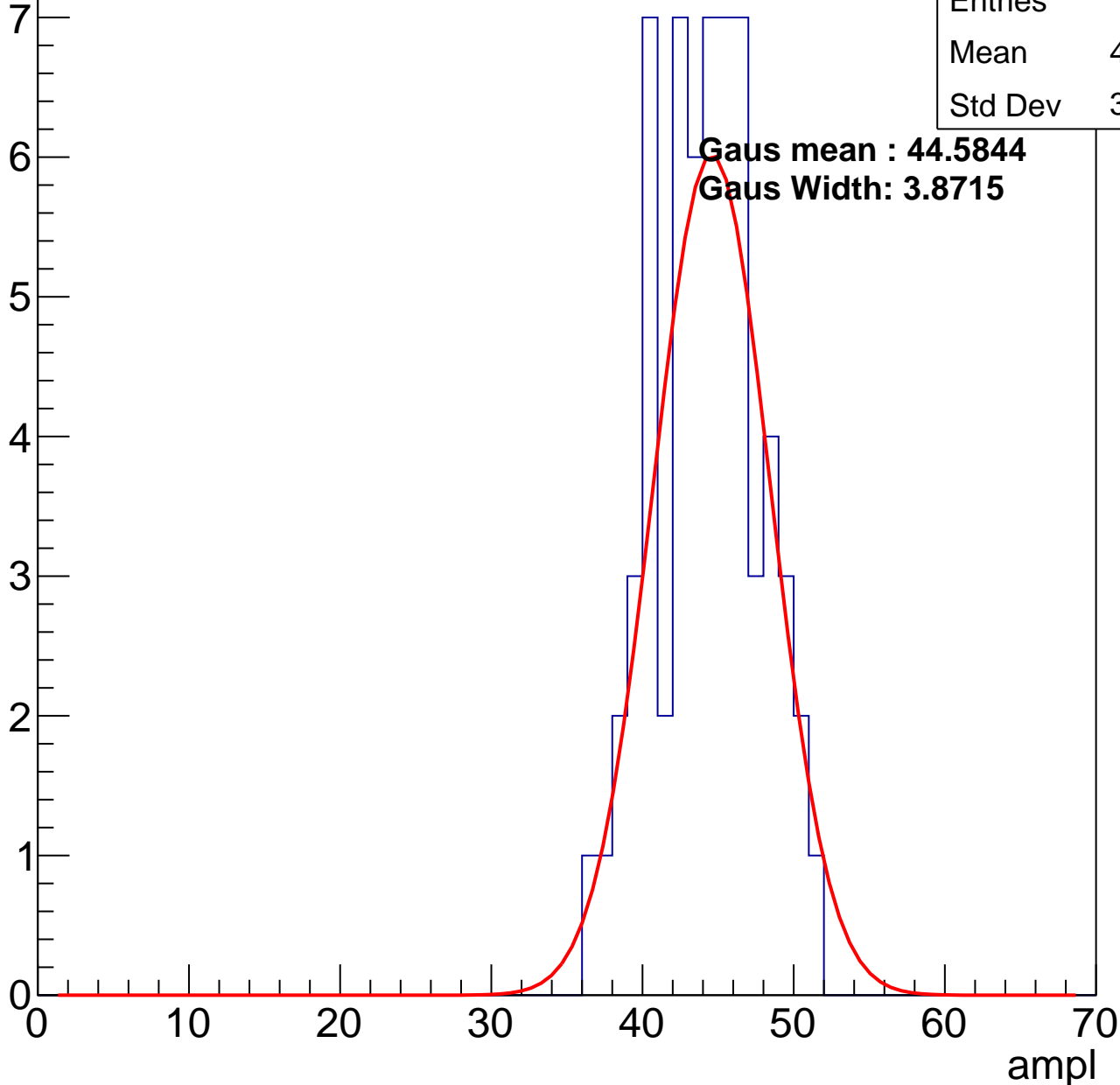
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.75
Std Dev	3.432

Gaus mean : 44.5844

Gaus Width: 3.8715

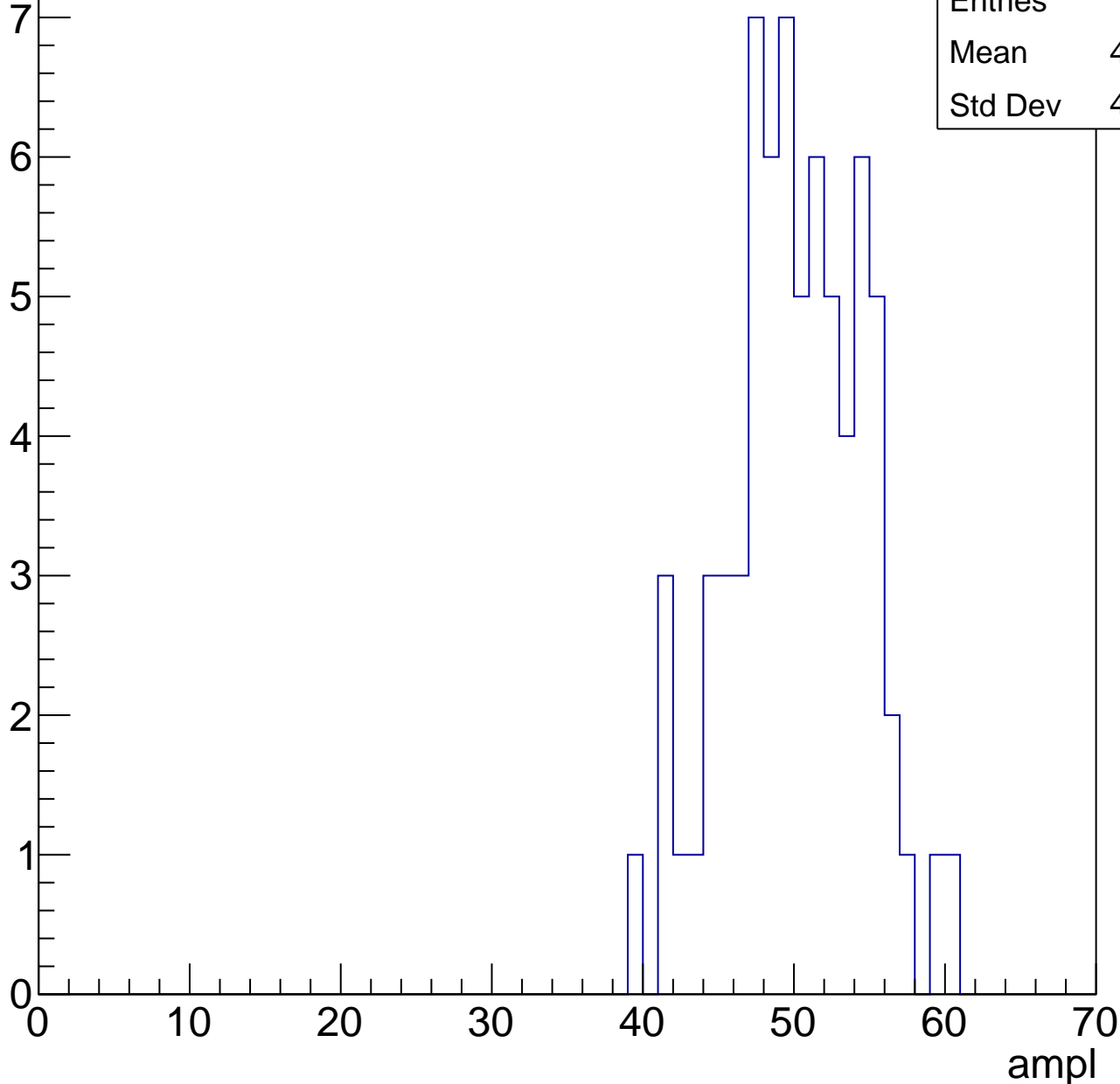


B0L001S, U14-ch11, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.68
Std Dev	4.408

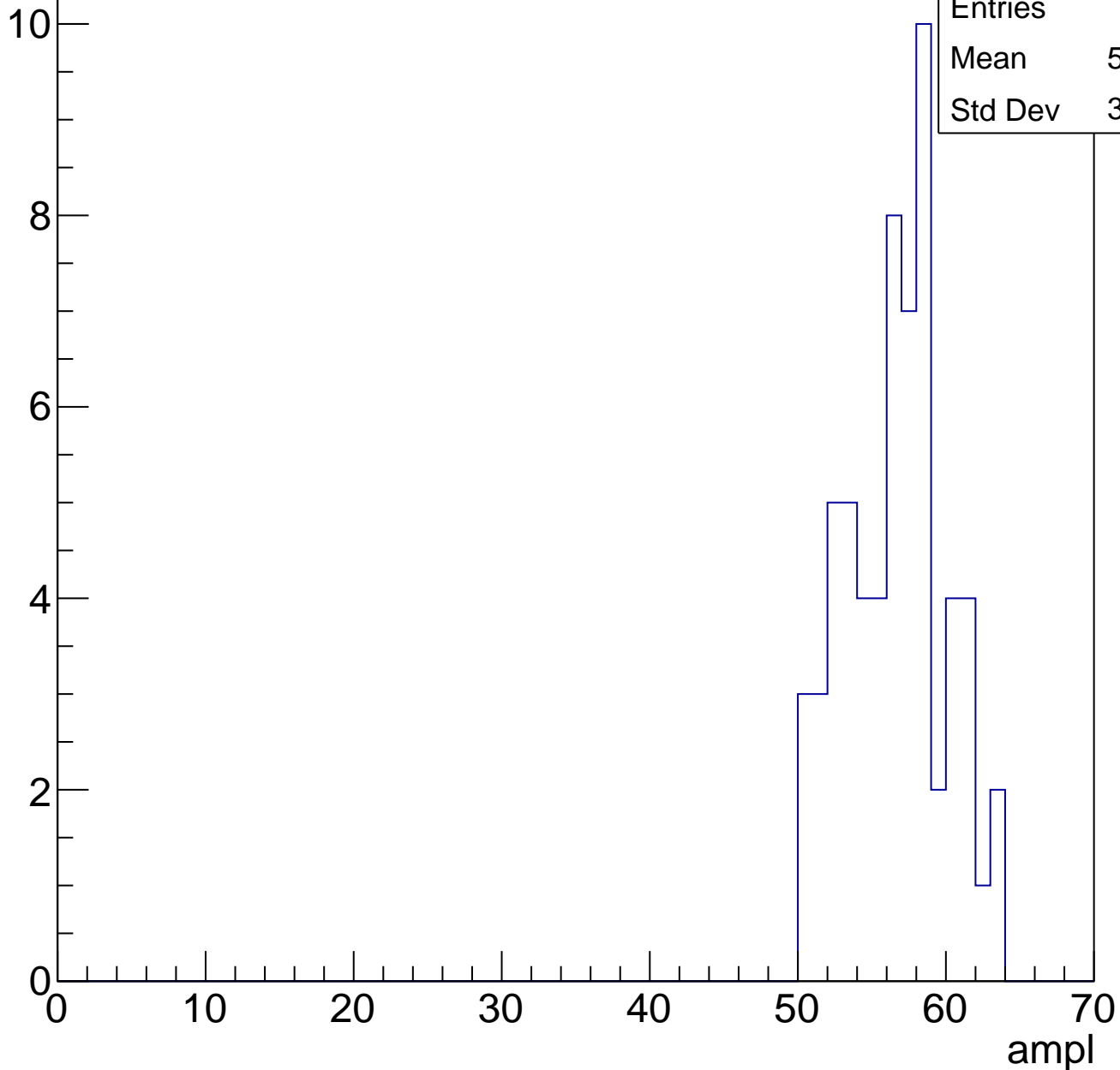


B0L001S, U14-ch11, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	62
Mean	56.15
Std Dev	3.335

Entry



B0L001S, U14-ch11, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

7

6

5

4

3

2

1

0

Entries 32

Mean 58.31

Std Dev 10.73

ampl

0

10

20

30

40

50

60

70

B0L001S, U14-ch11, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

62.12

Std Dev

0.7806

B0L001S, U14-ch11, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch12, adc0

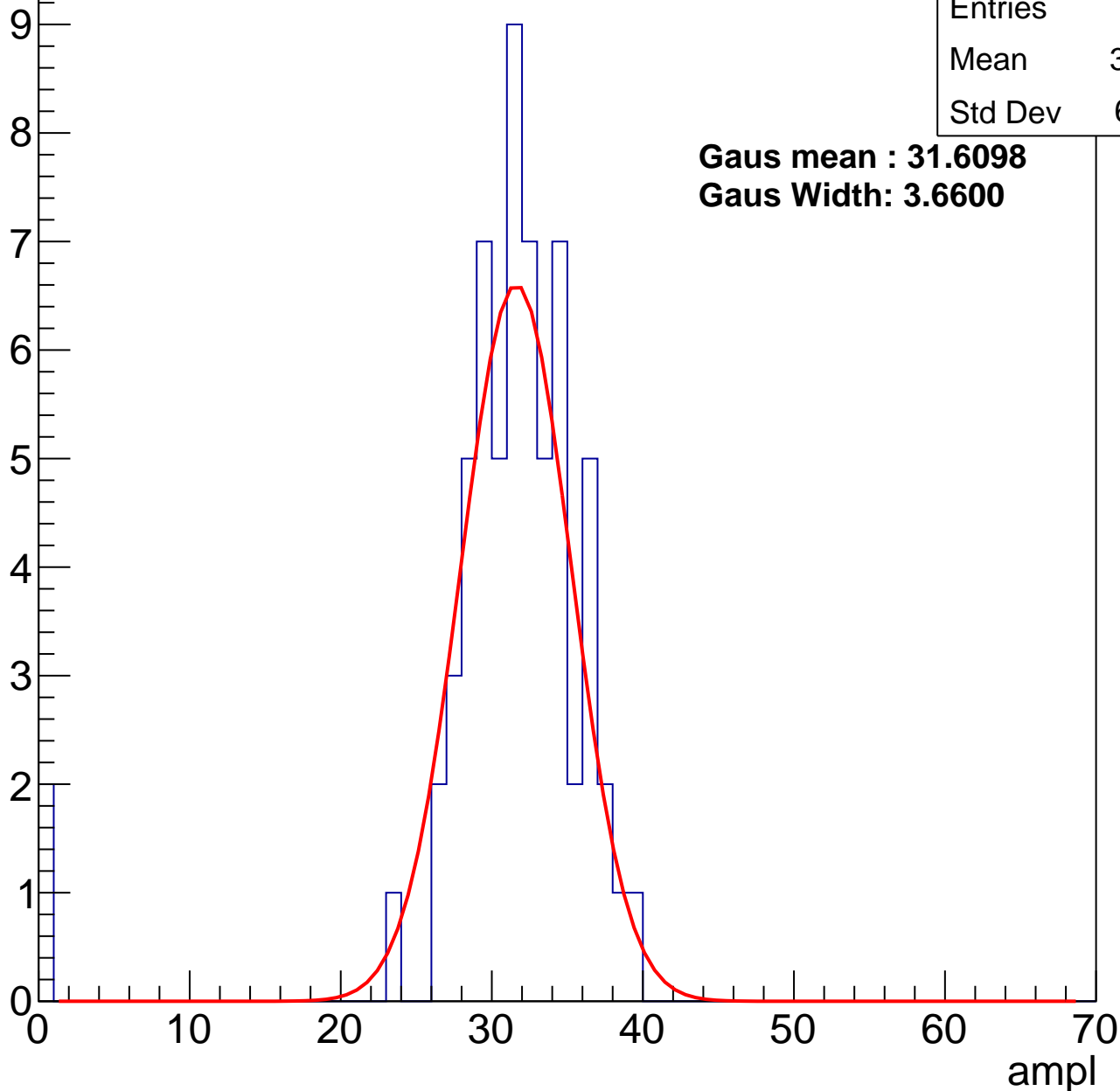
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.56
Std Dev	6.351

Gaus mean : 31.6098

Gaus Width: 3.6600



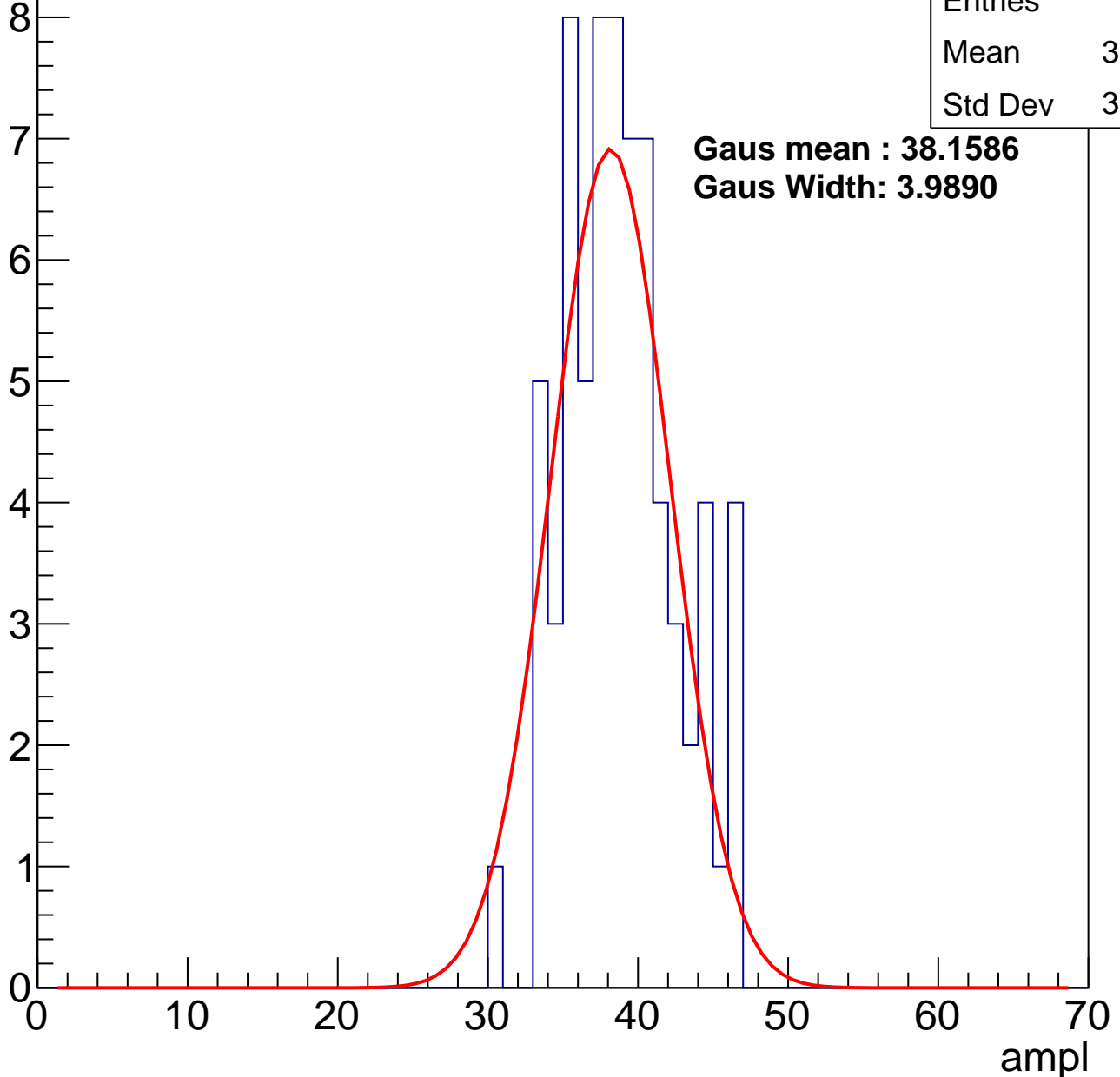
B0L001S, U14-ch12, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	38.44
Std Dev	3.667

Gaus mean : 38.1586
Gaus Width: 3.9890



B0L001S, U14-ch12, adc2

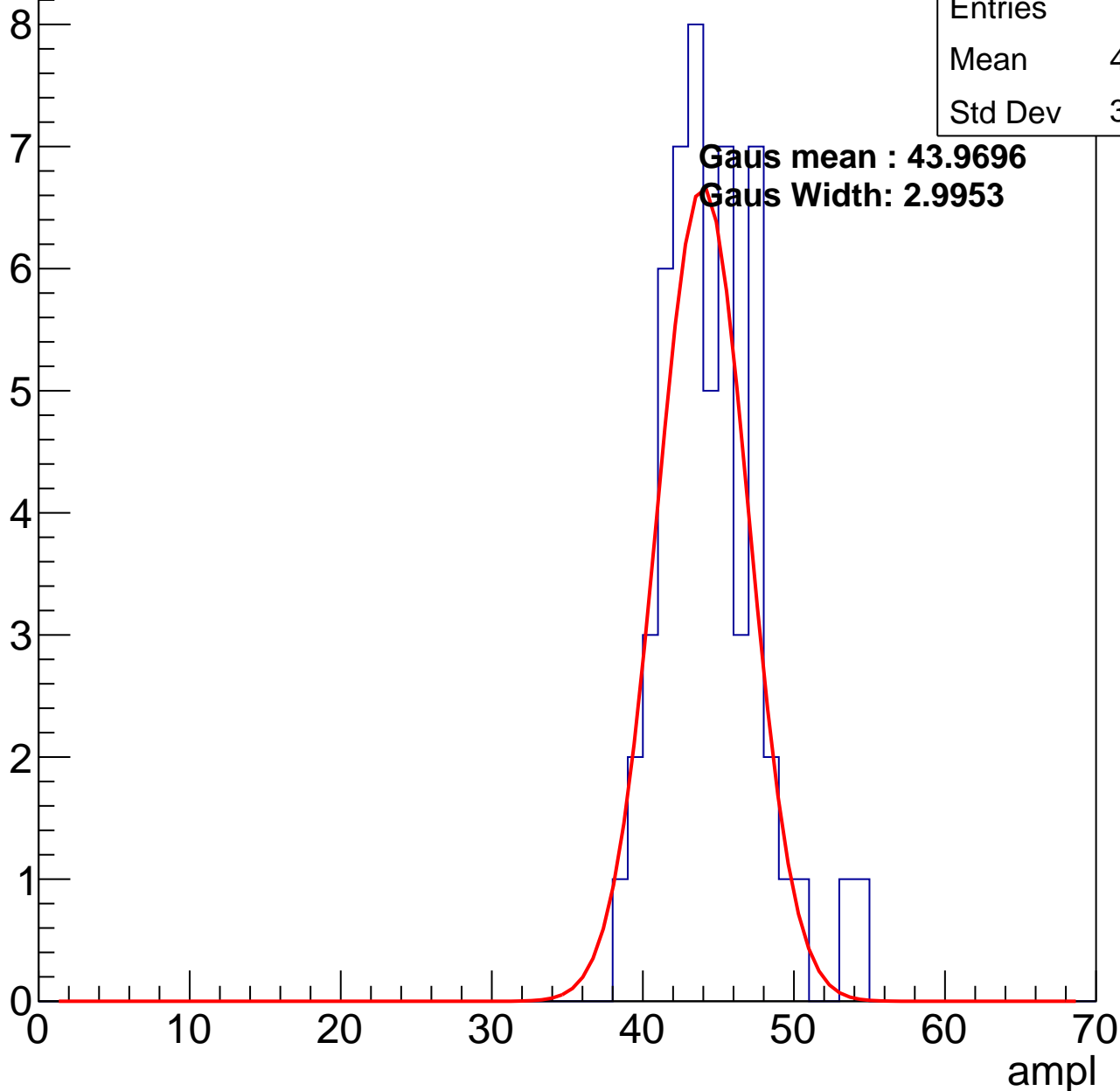
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	44.07
Std Dev	3.247

Gaus mean : 43.9696

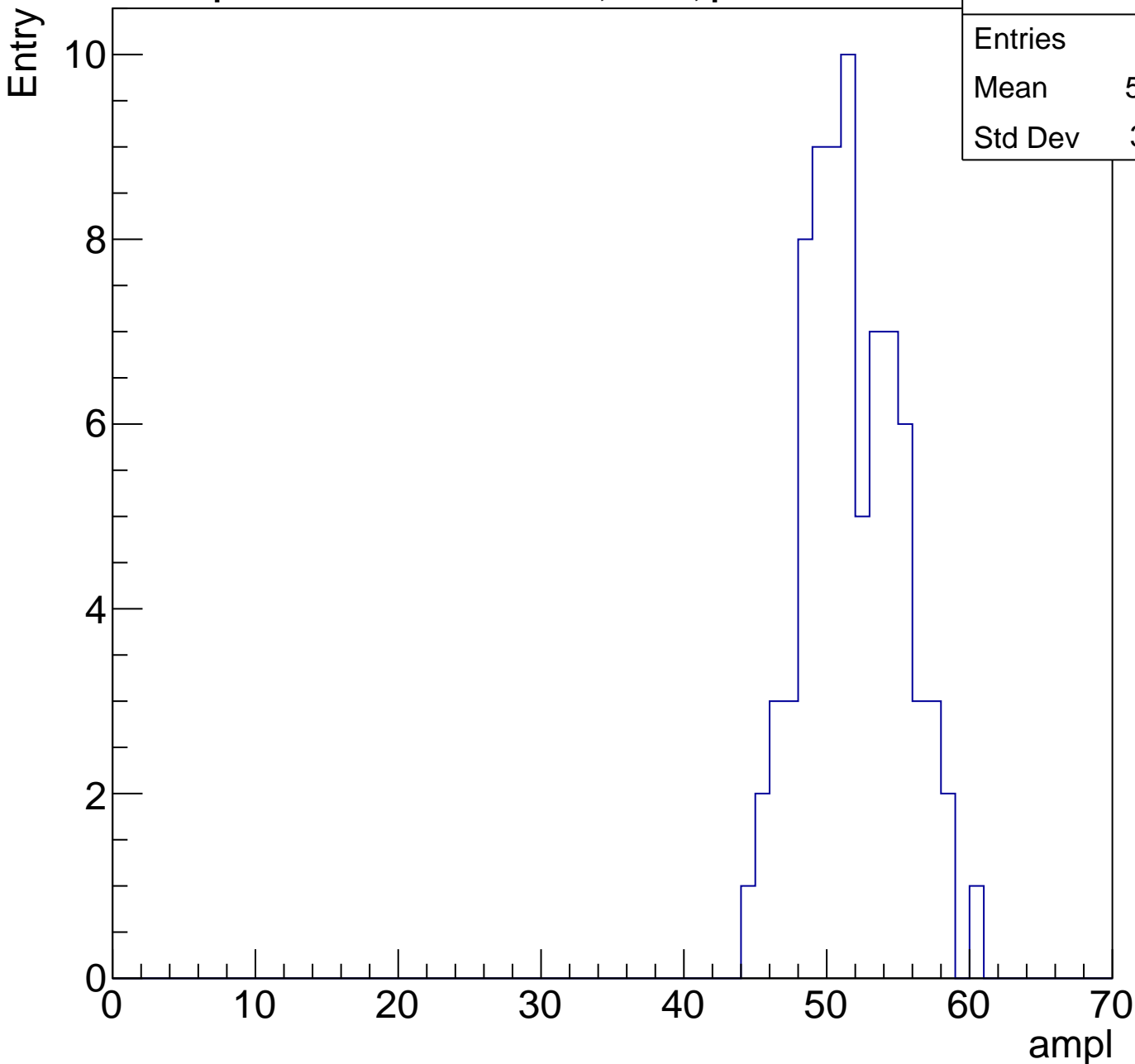
Gaus Width: 2.9953



B0L001S, U14-ch12, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	51.29
Std Dev	3.391

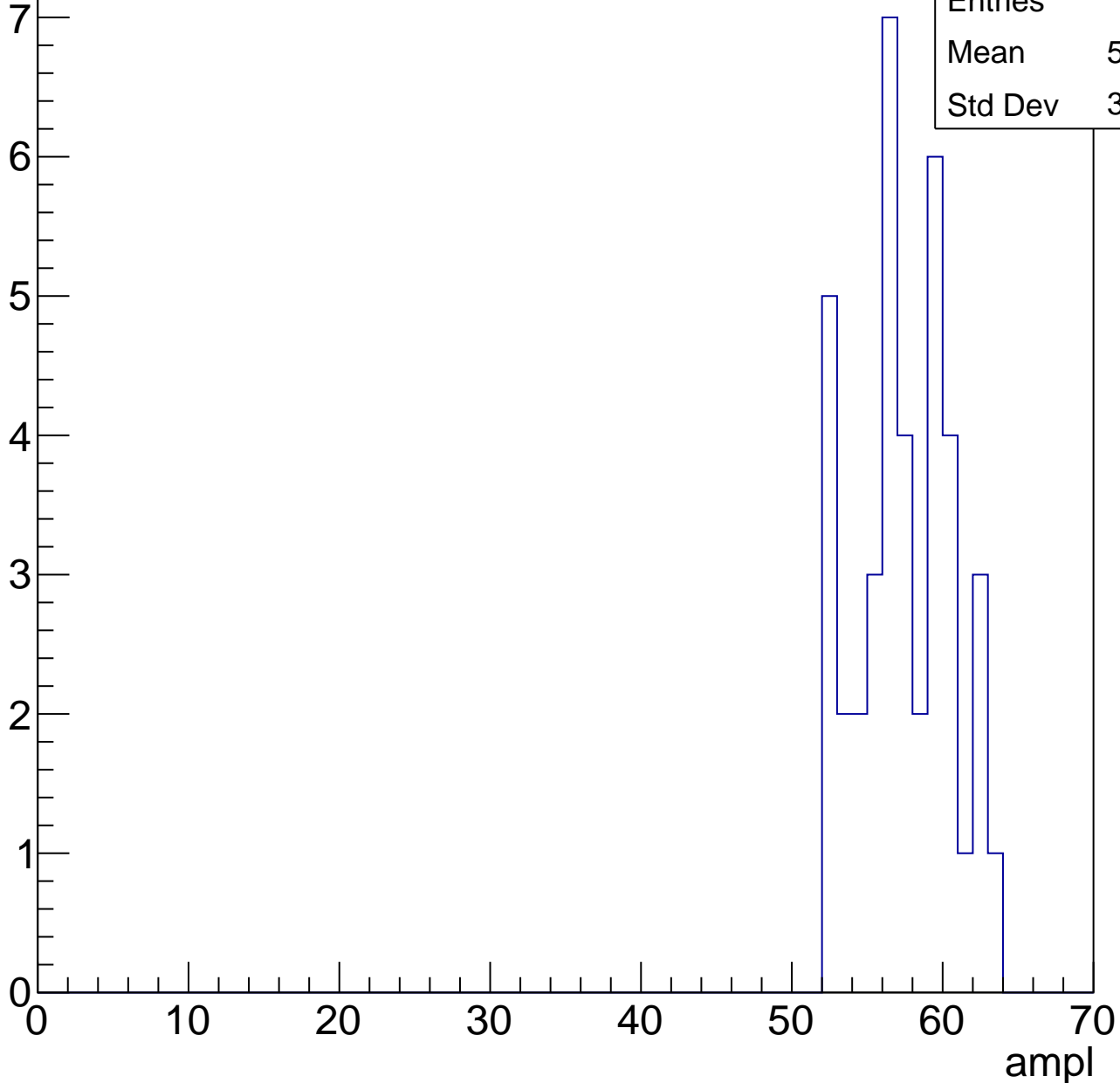


B0L001S, U14-ch12, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	56.98
Std Dev	3.094

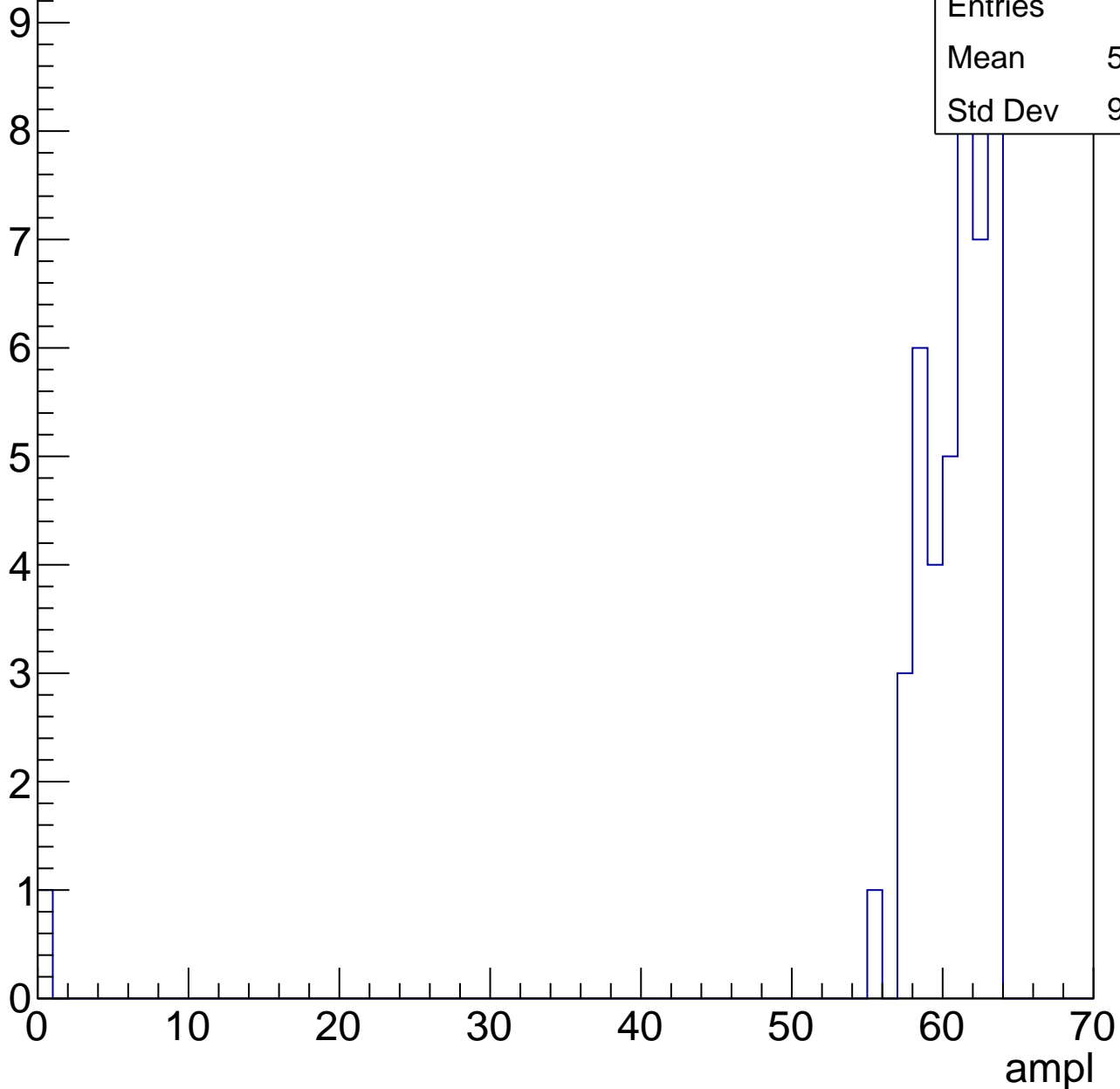


B0L001S, U14-ch12, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	59.02
Std Dev	9.228



B0L001S, U14-ch12, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch12, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch13, adc0

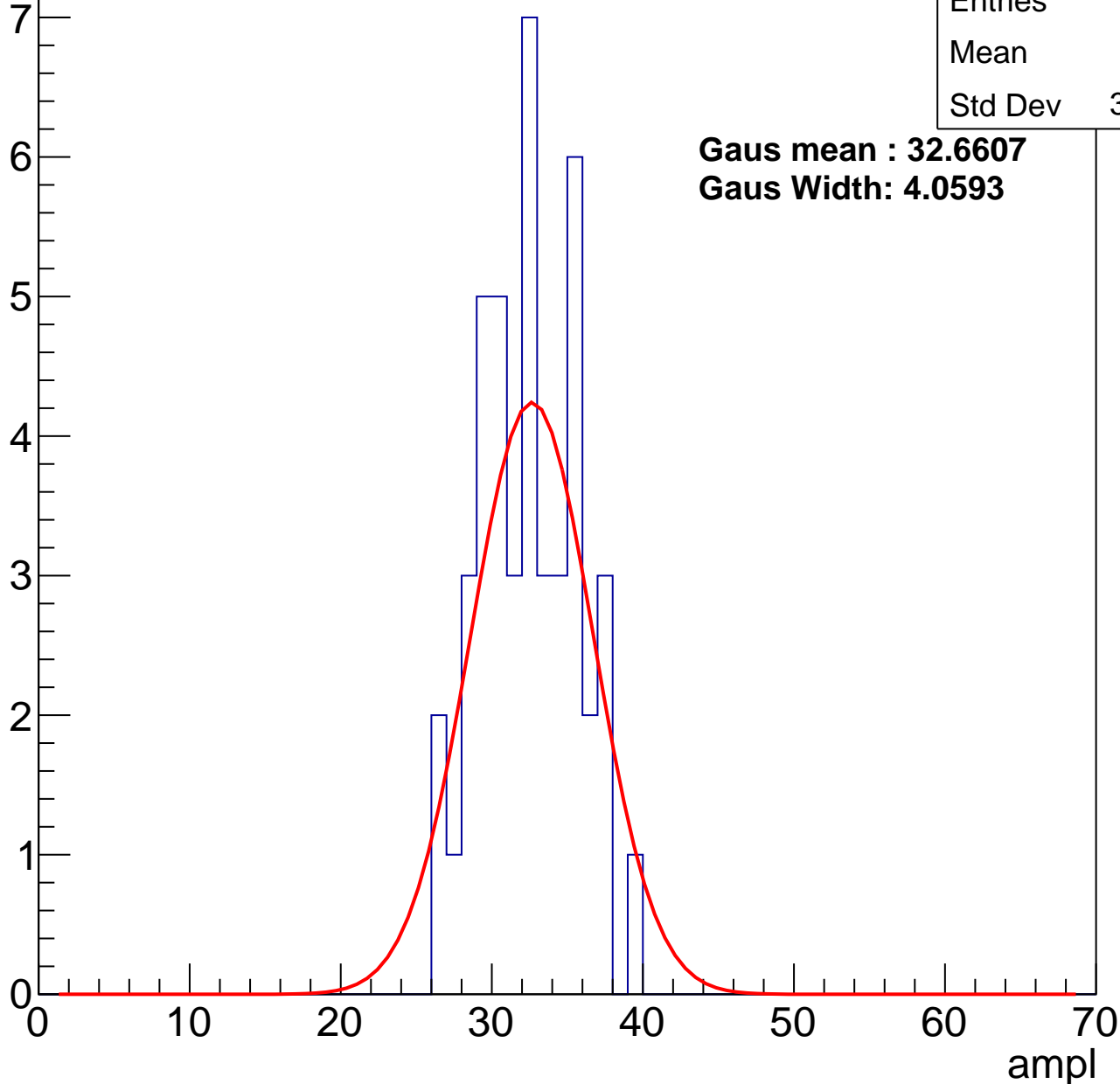
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	32
Std Dev	3.155

Gaus mean : 32.6607

Gaus Width: 4.0593



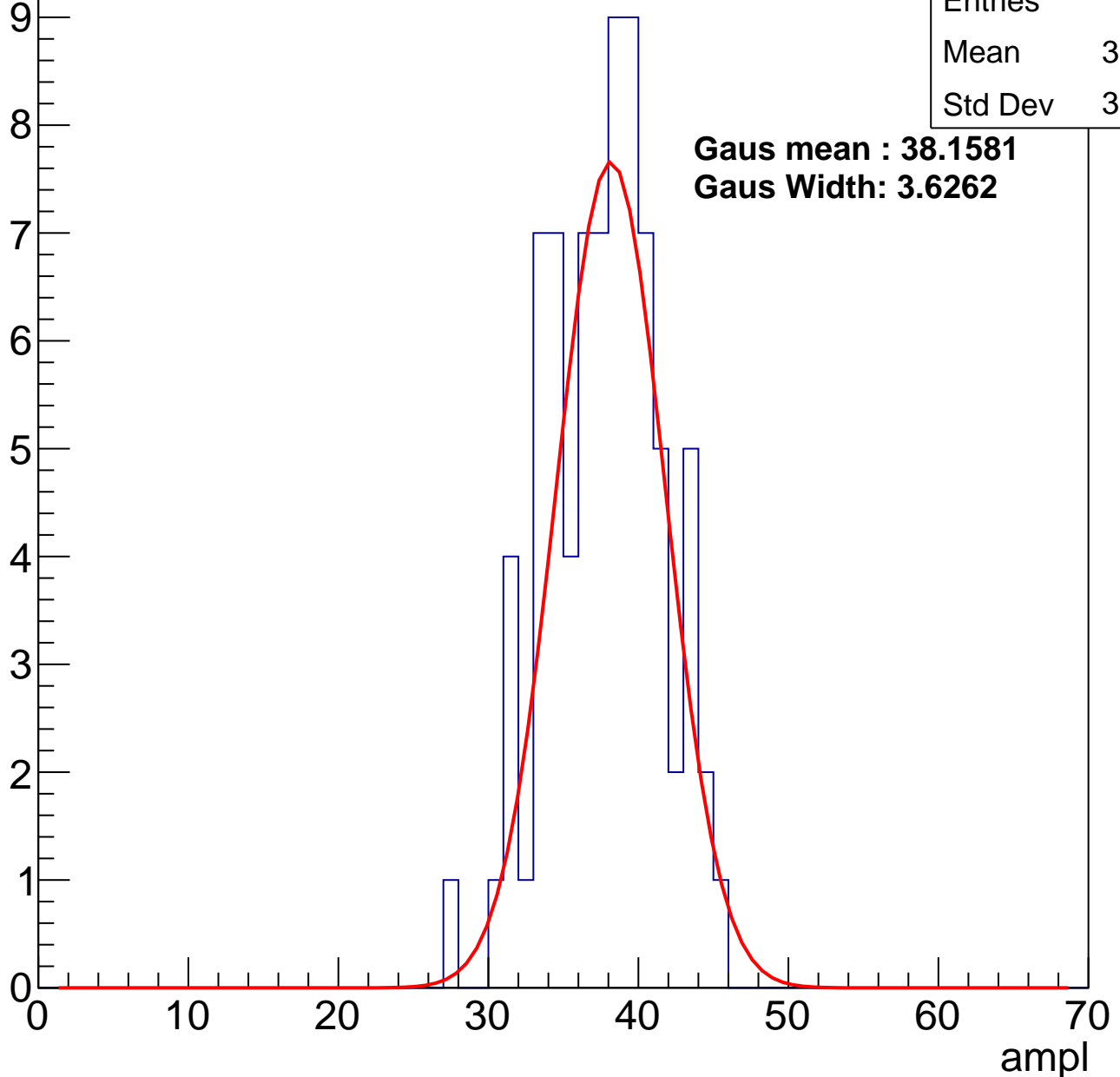
B0L001S, U14-ch13, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	37.25
Std Dev	3.716

Gaus mean : 38.1581
Gaus Width: 3.6262



B0L001S, U14-ch13, adc2

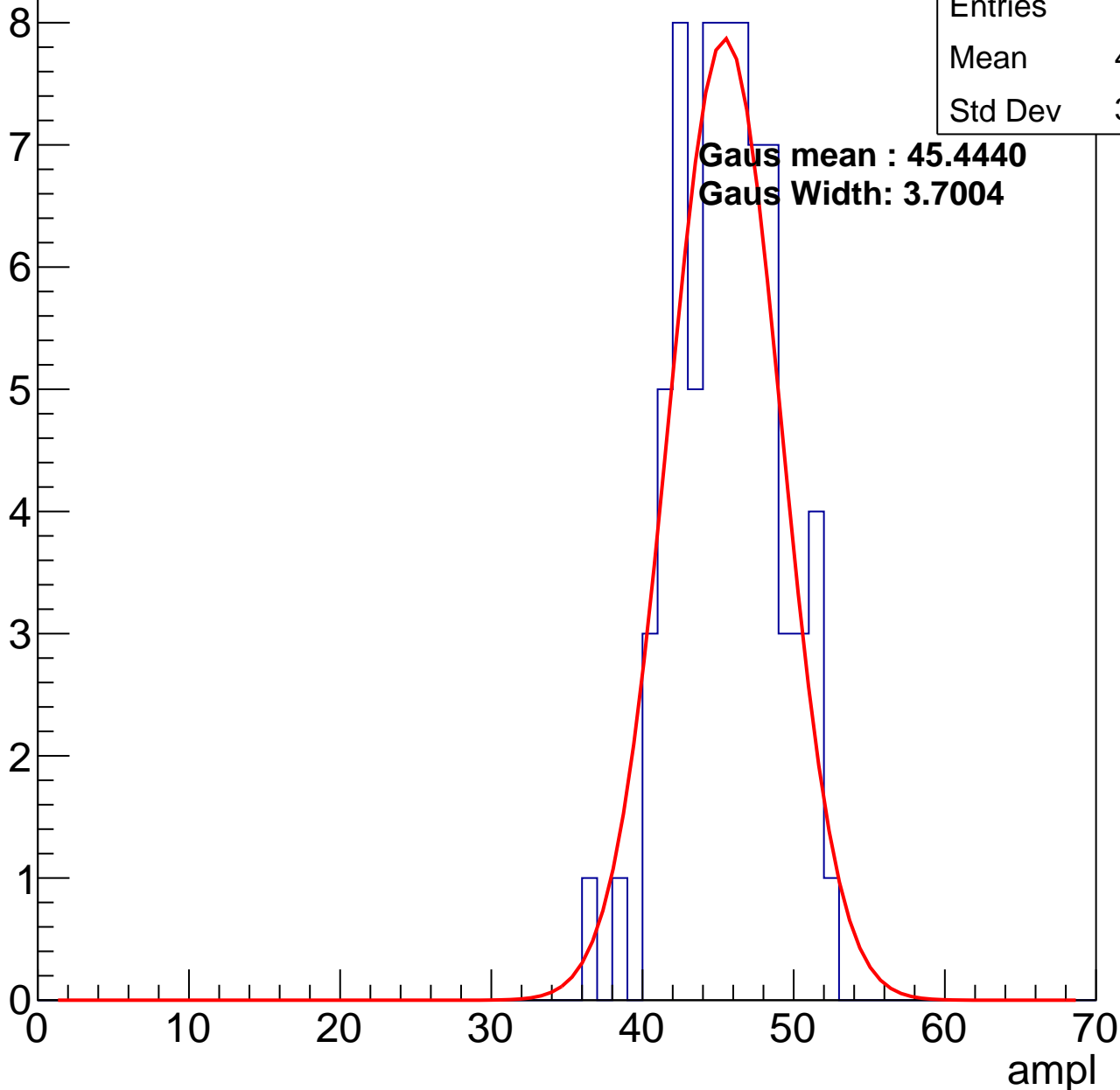
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	45.11
Std Dev	3.331

Gaus mean : 45.4440

Gaus Width: 3.7004

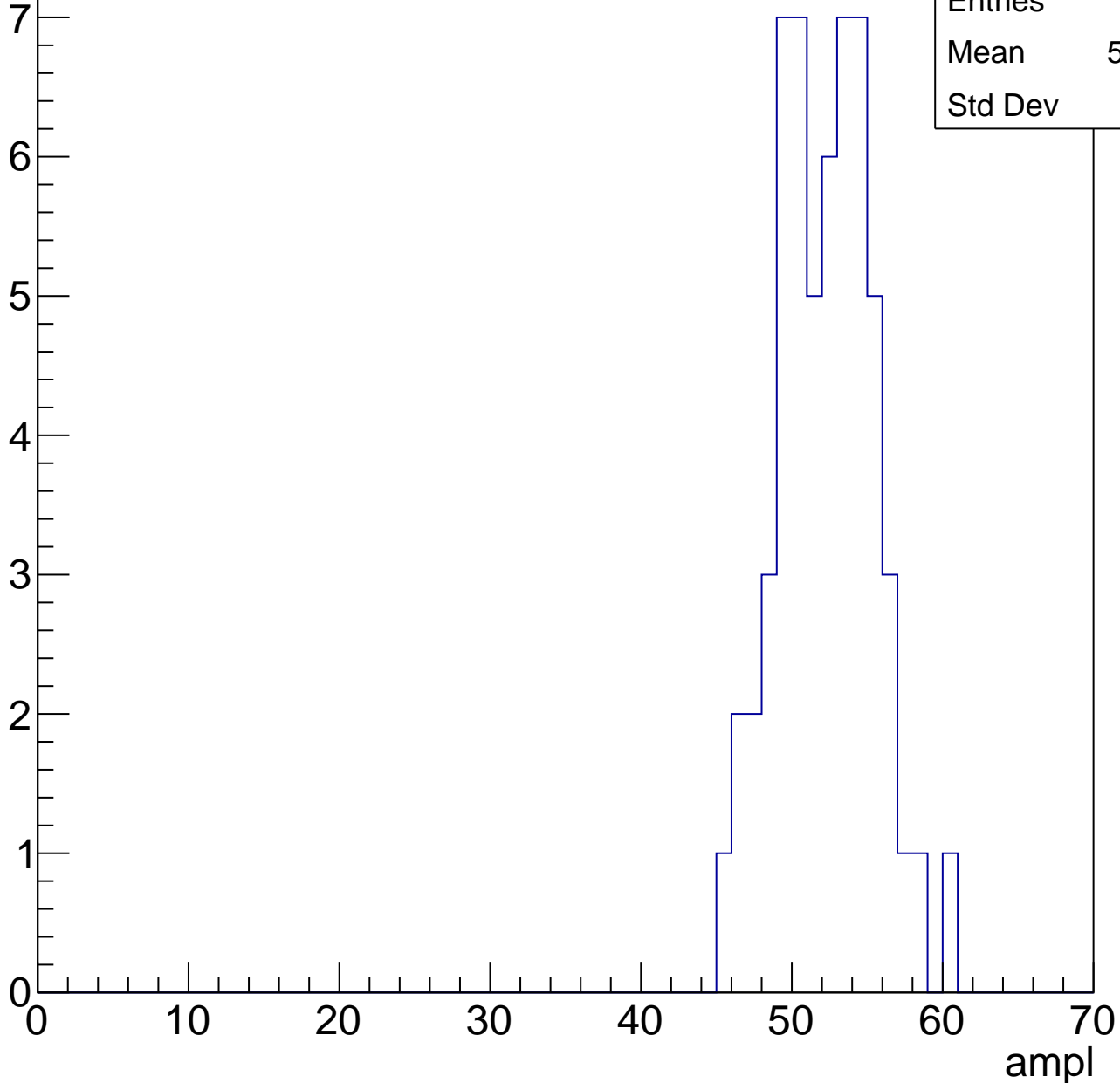


B0L001S, U14-ch13, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	51.76
Std Dev	3.12

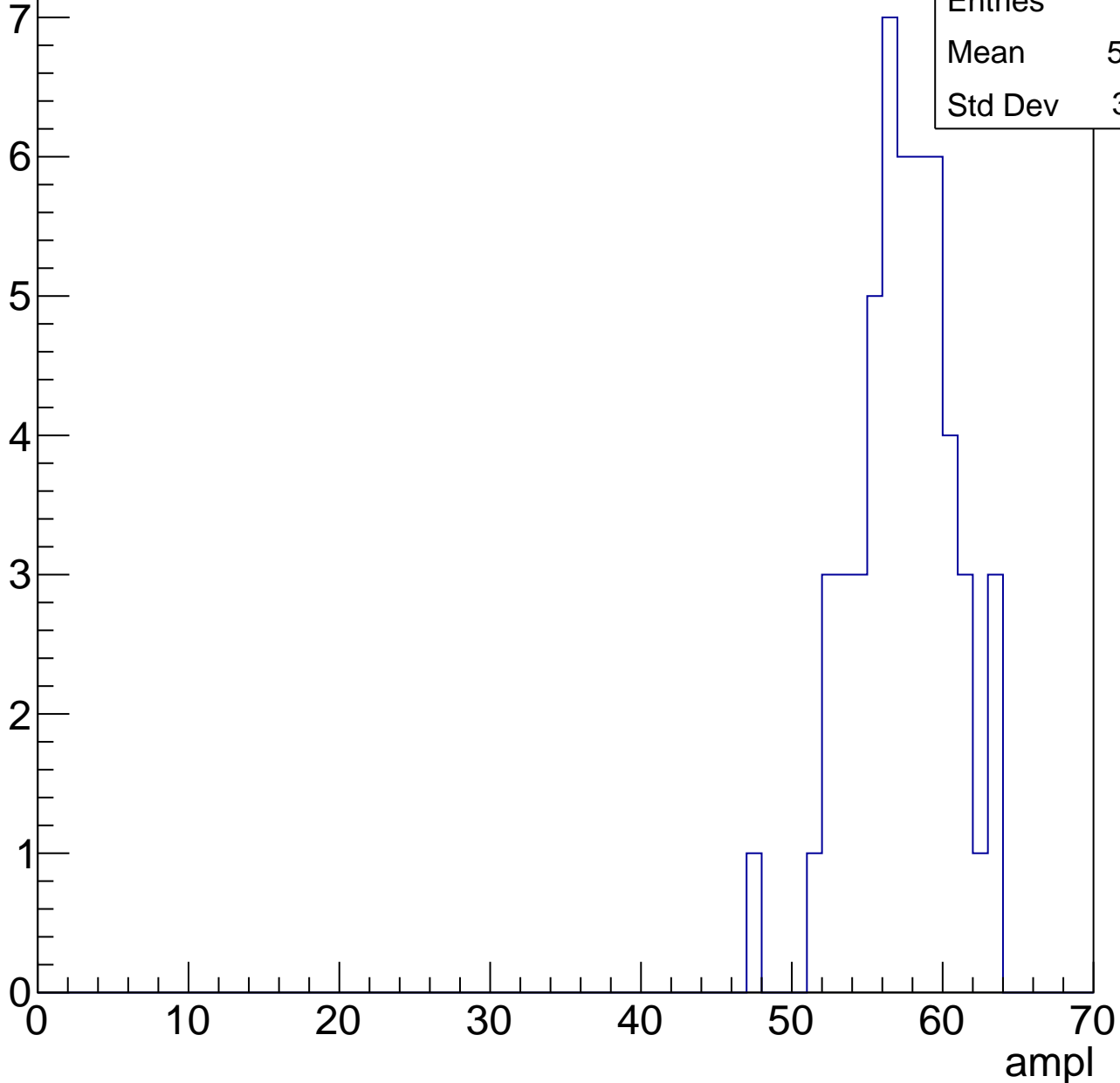


B0L001S, U14-ch13, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.92
Std Dev	3.281

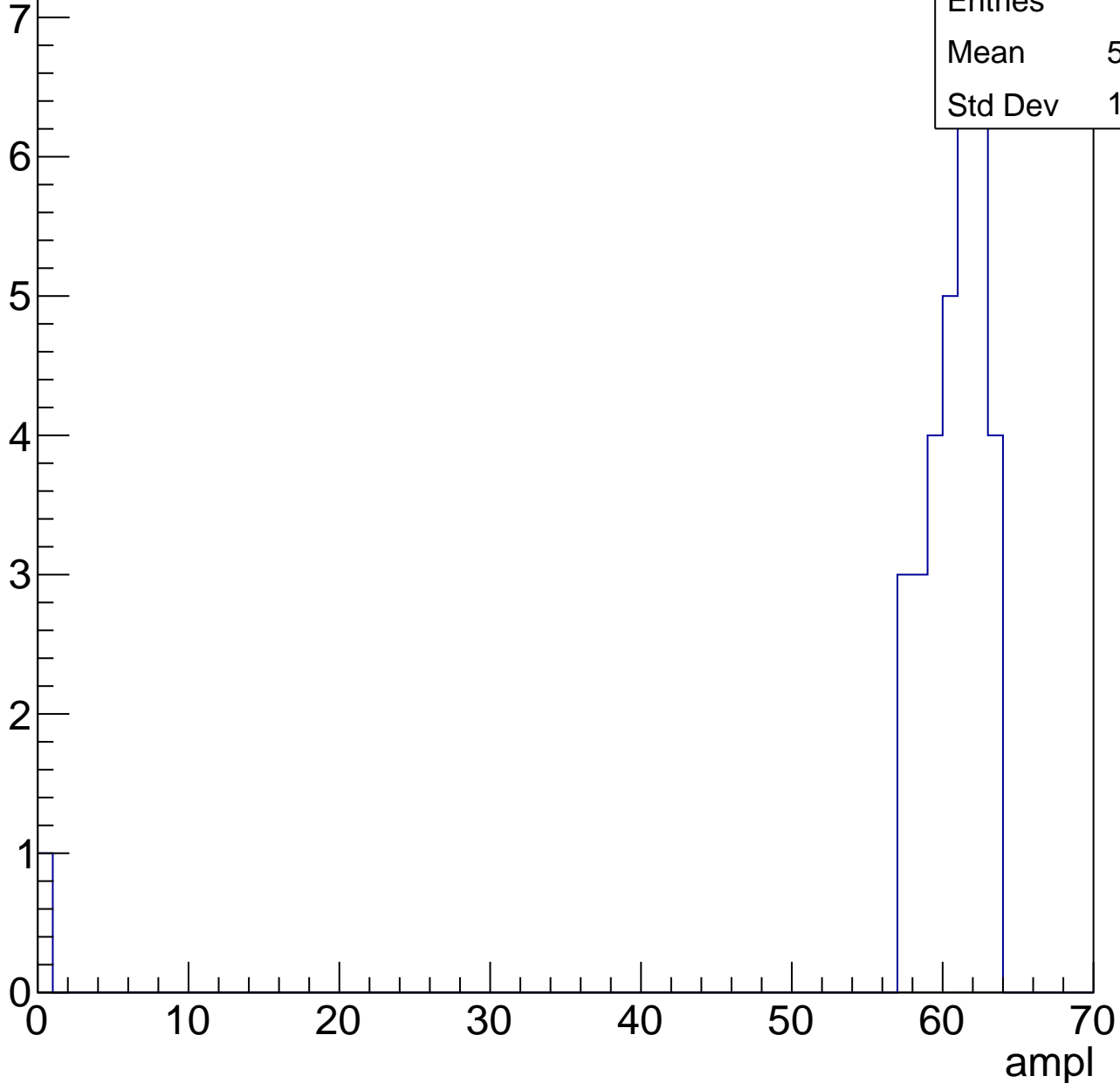


B0L001S, U14-ch13, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.65
Std Dev	10.36



B0L001S, U14-ch13, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch13, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch14, adc0

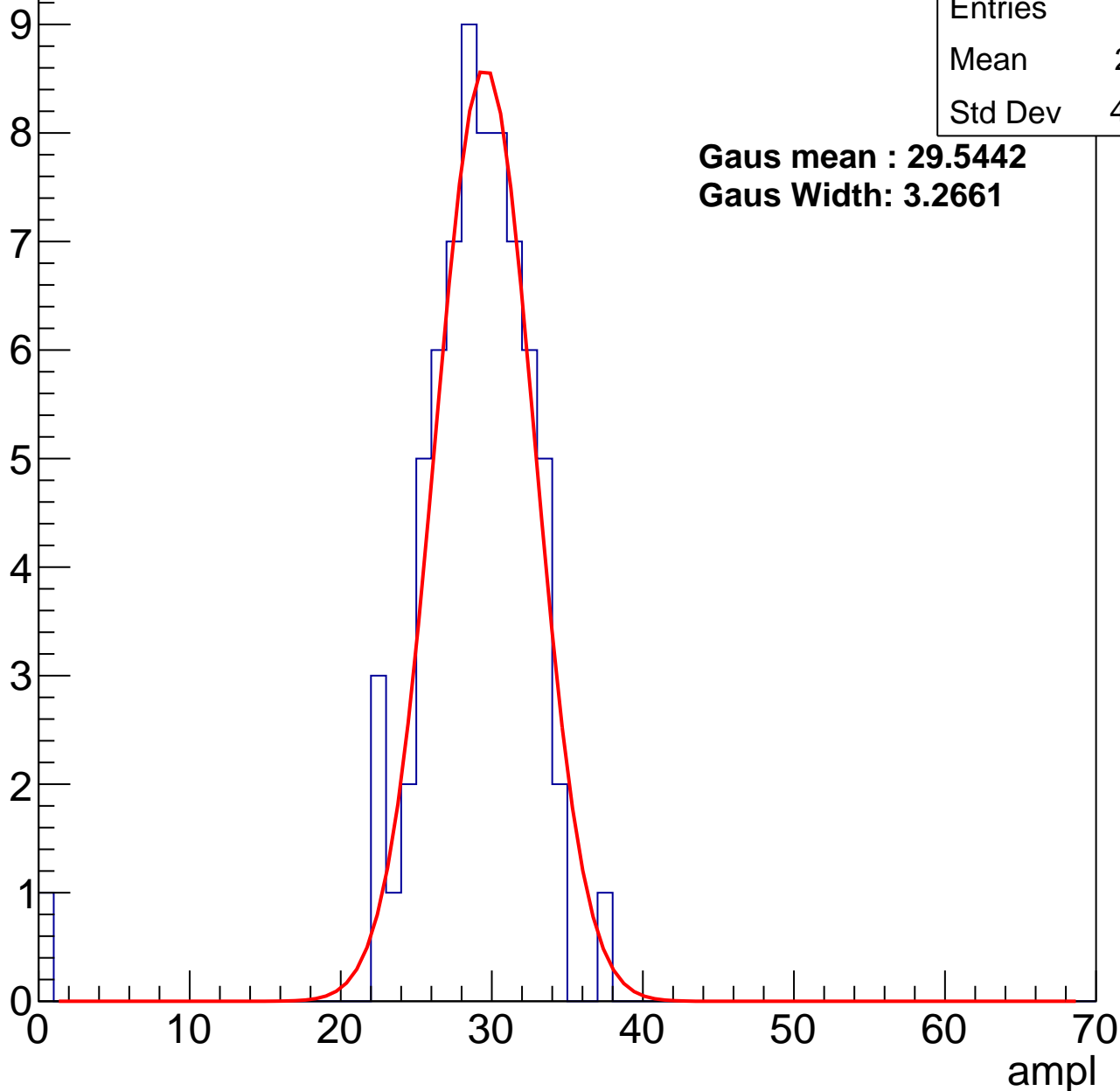
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	28.31
Std Dev	4.589

Gaus mean : 29.5442

Gaus Width: 3.2661



B0L001S, U14-ch14, adc1

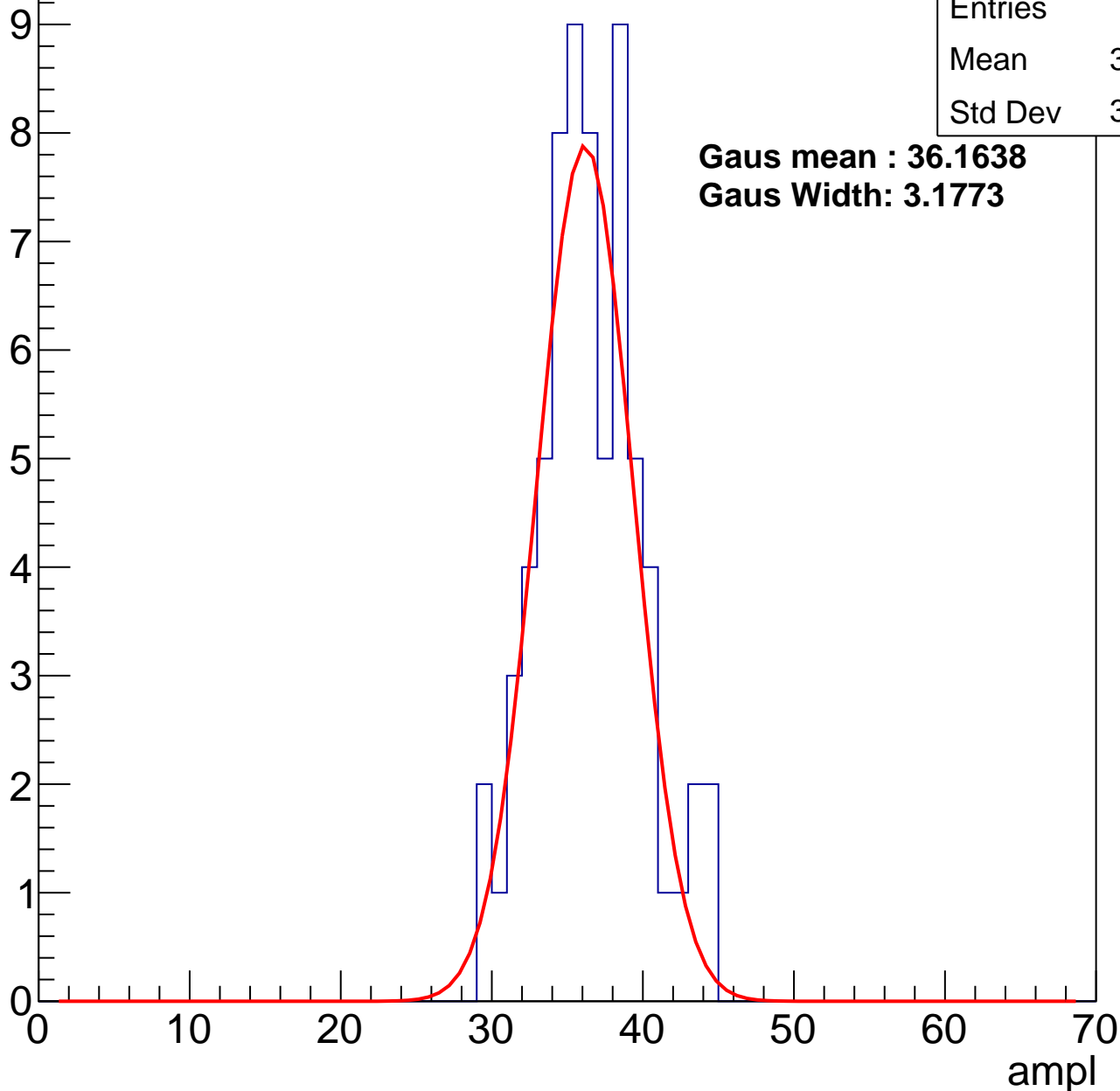
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36.06
Std Dev	3.396

Gaus mean : 36.1638

Gaus Width: 3.1773



B0L001S, U14-ch14, adc2

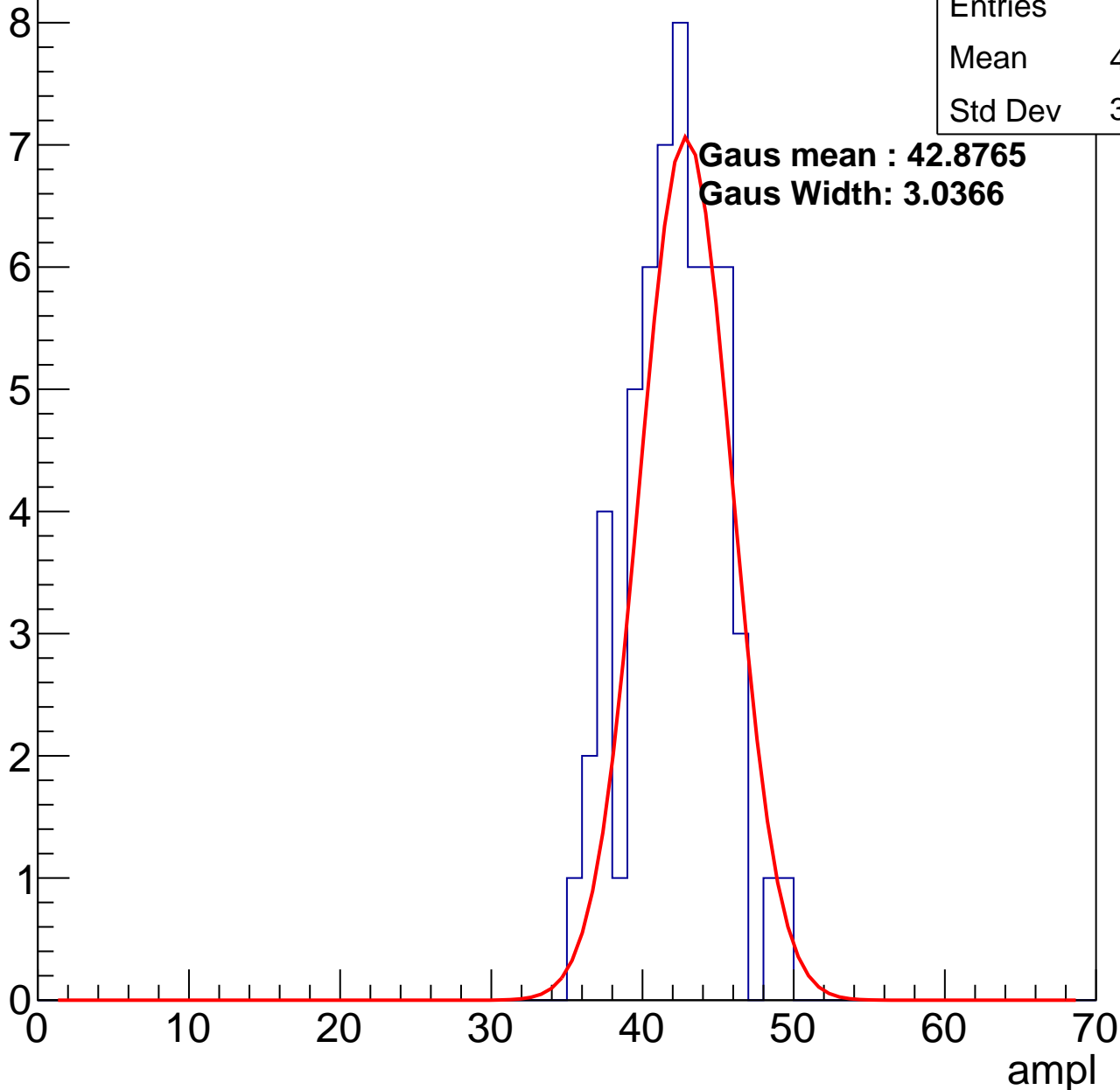
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	41.72
Std Dev	3.036

Gaus mean : 42.8765

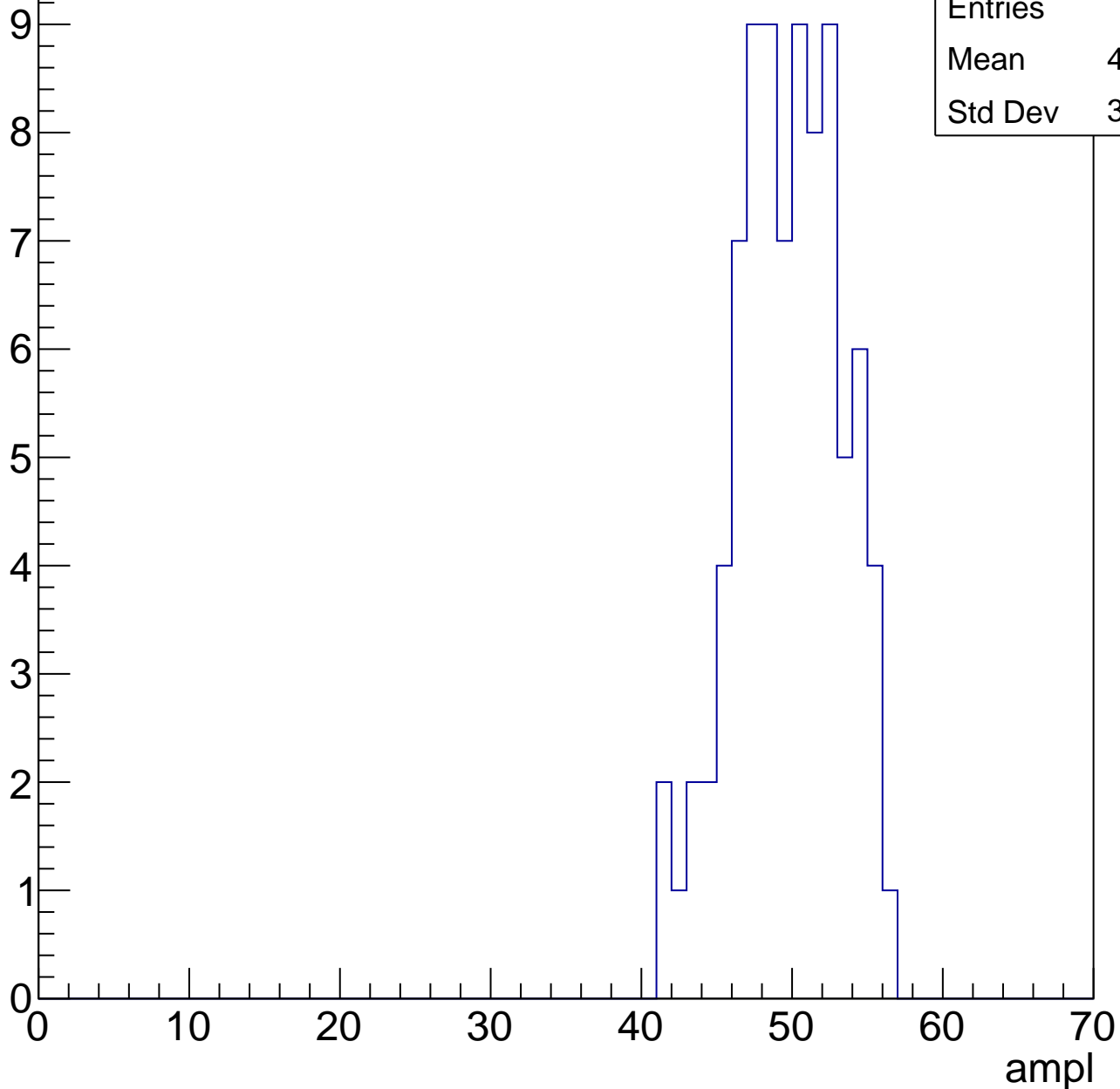
Gaus Width: 3.0366



B0L001S, U14-ch14, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



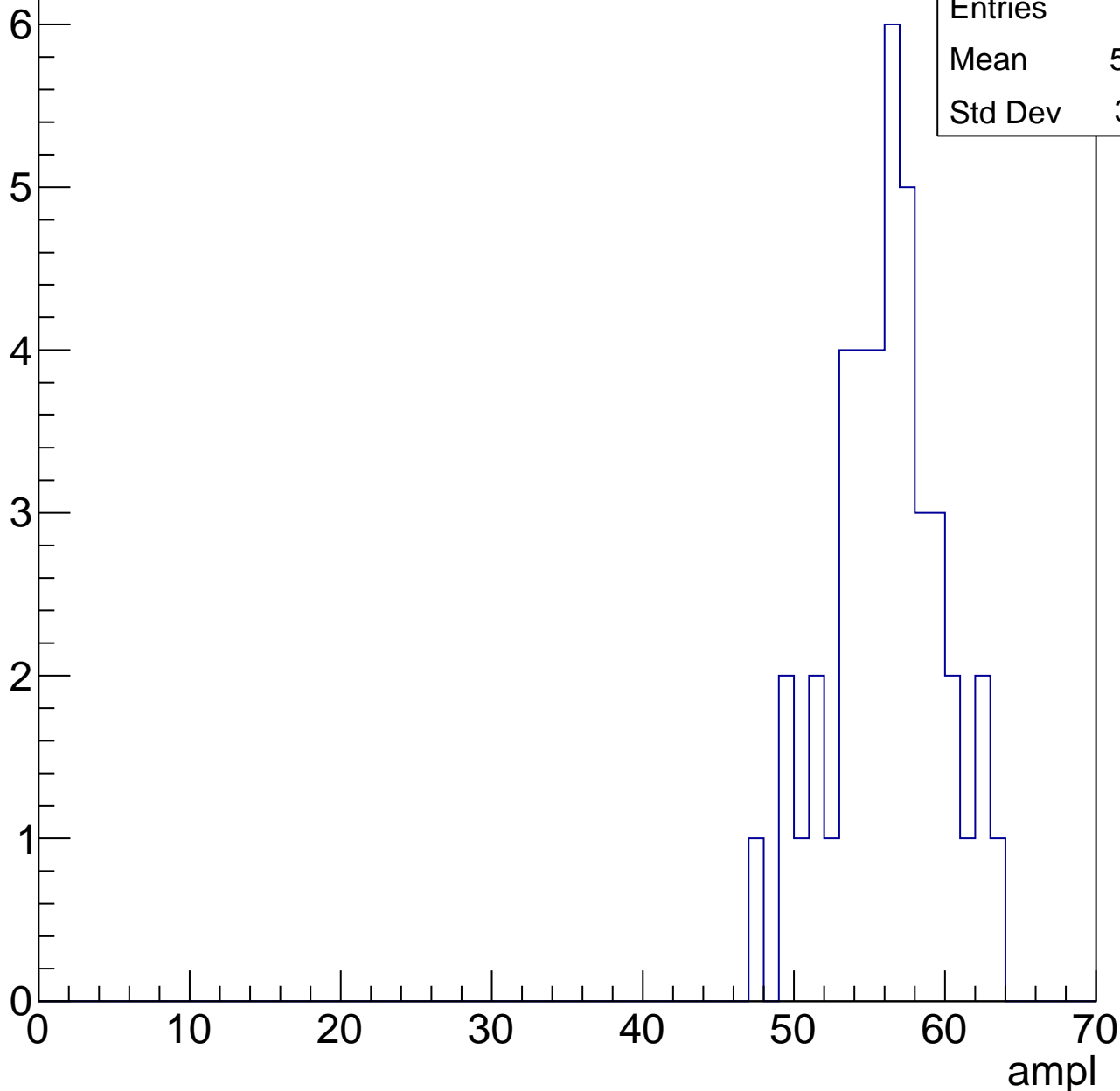
Entries	85
Mean	49.28
Std Dev	3.446

B0L001S, U14-ch14, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	55.64
Std Dev	3.631

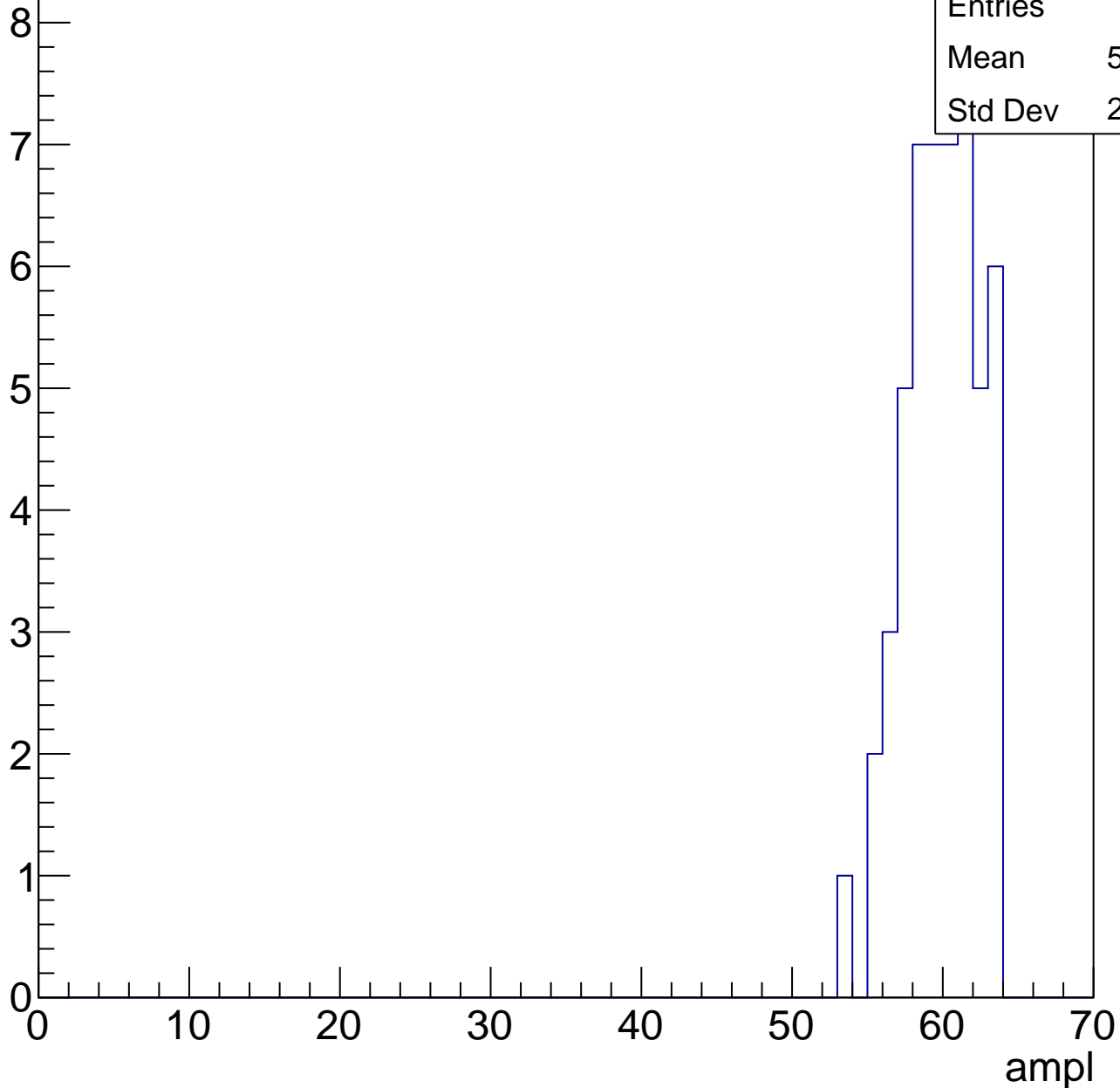


B0L001S, U14-ch14, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

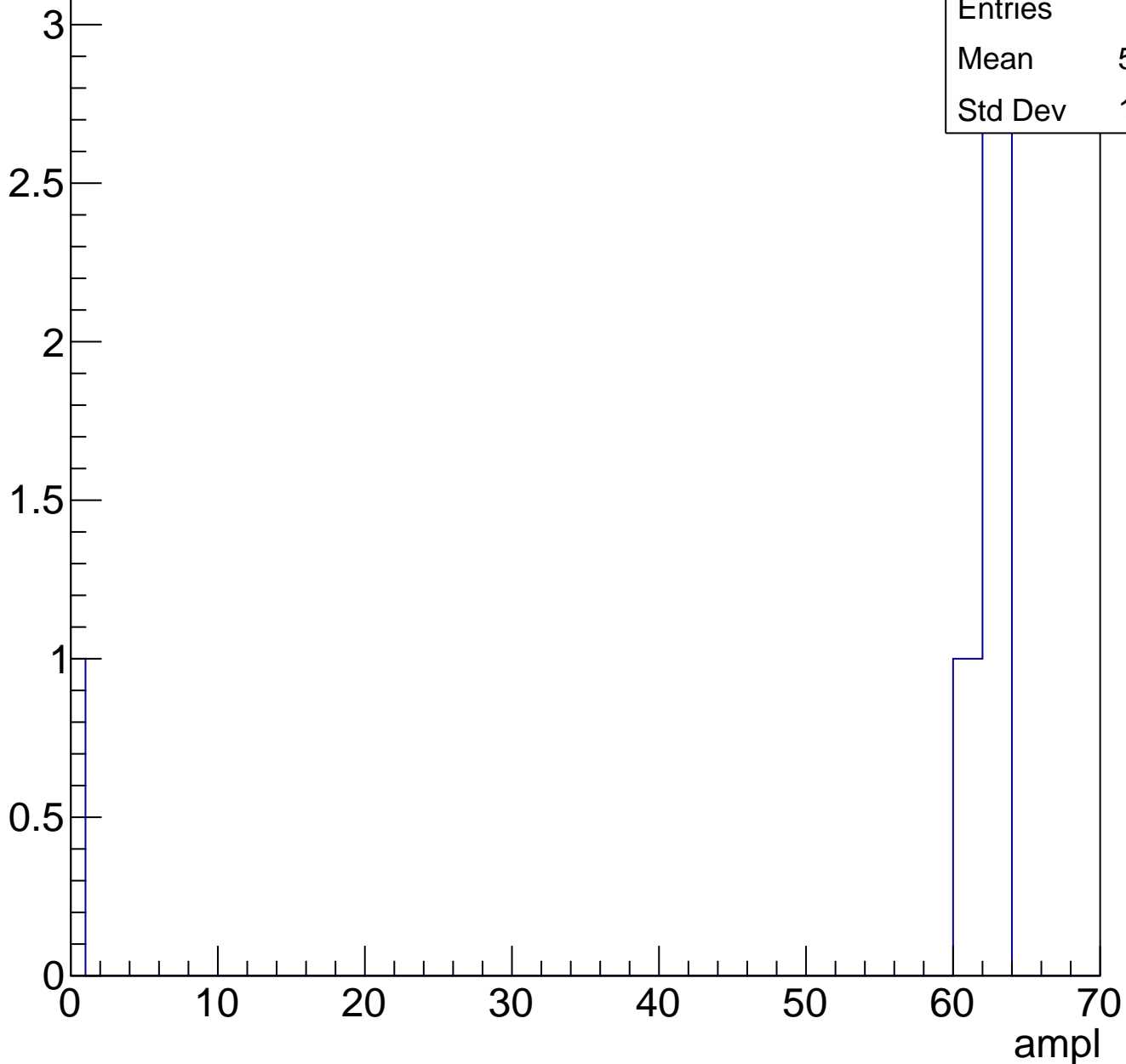
Entries	51
Mean	59.43
Std Dev	2.395



B0L001S, U14-ch14, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch14, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch15, adc0

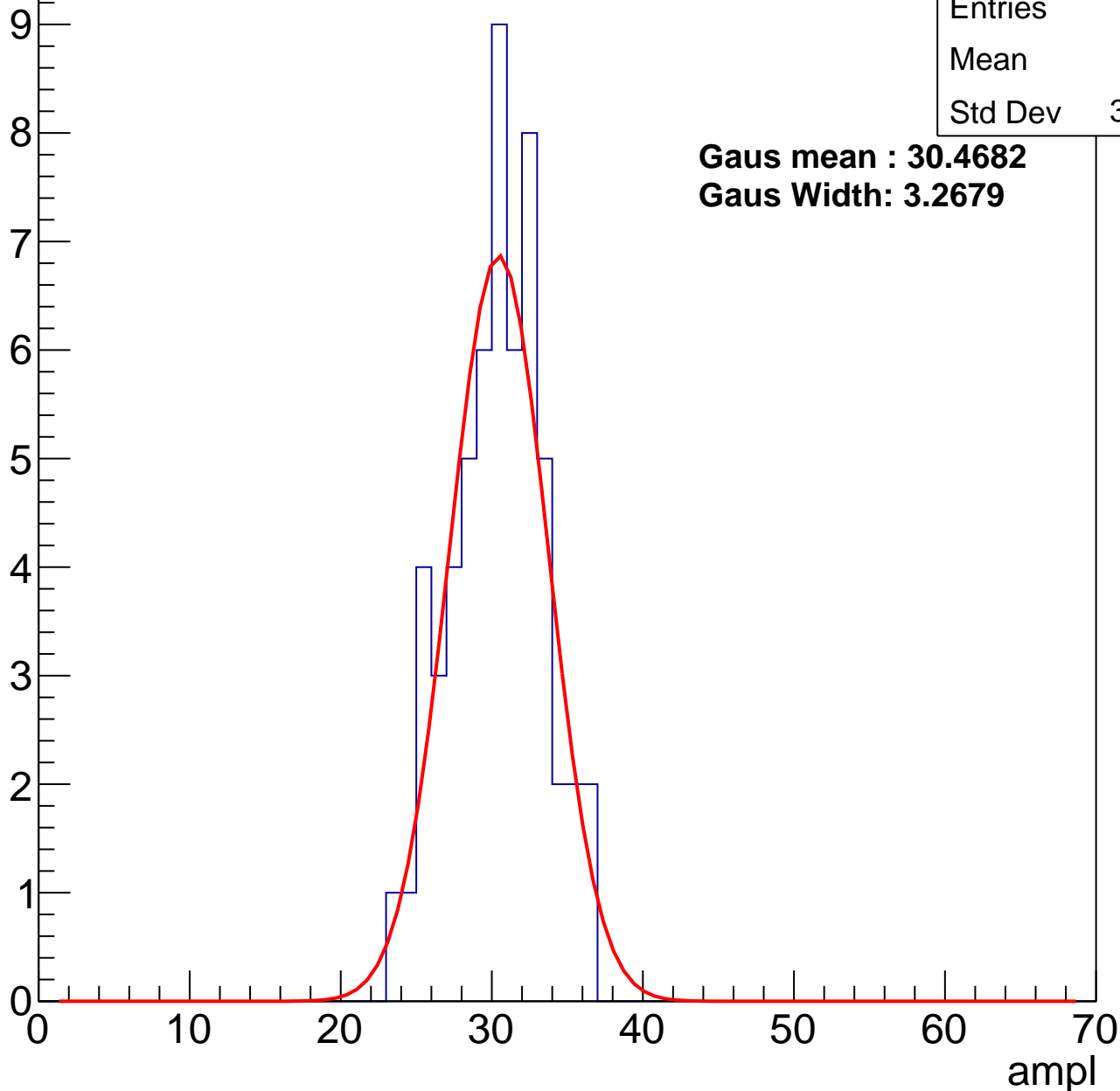
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	29.9
Std Dev	3.027

Gaus mean : 30.4682

Gaus Width: 3.2679



B0L001S, U14-ch15, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	37.22
Std Dev	3.645

Gaus mean : 37.2348

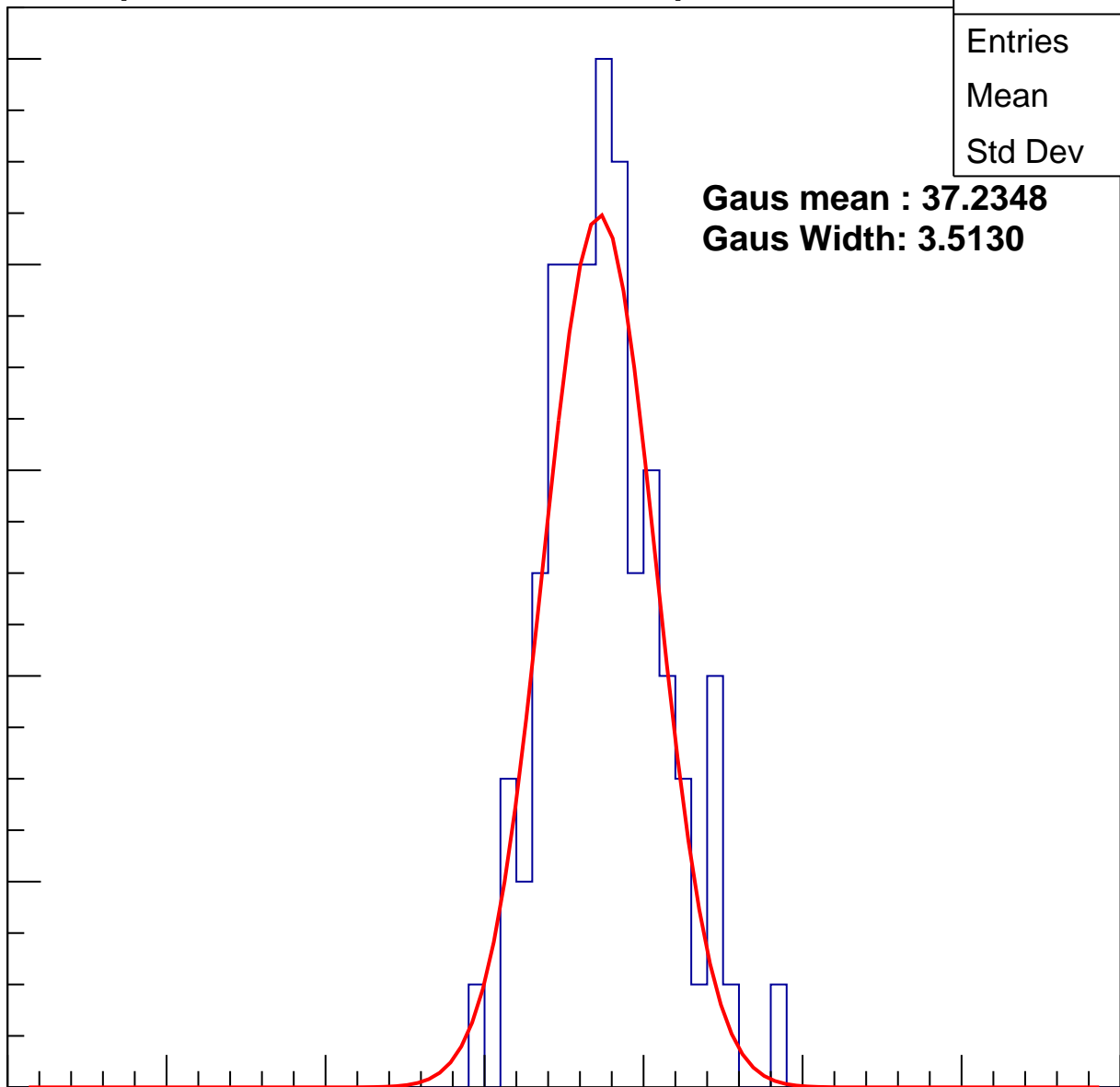
Gaus Width: 3.5130

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch15, adc2

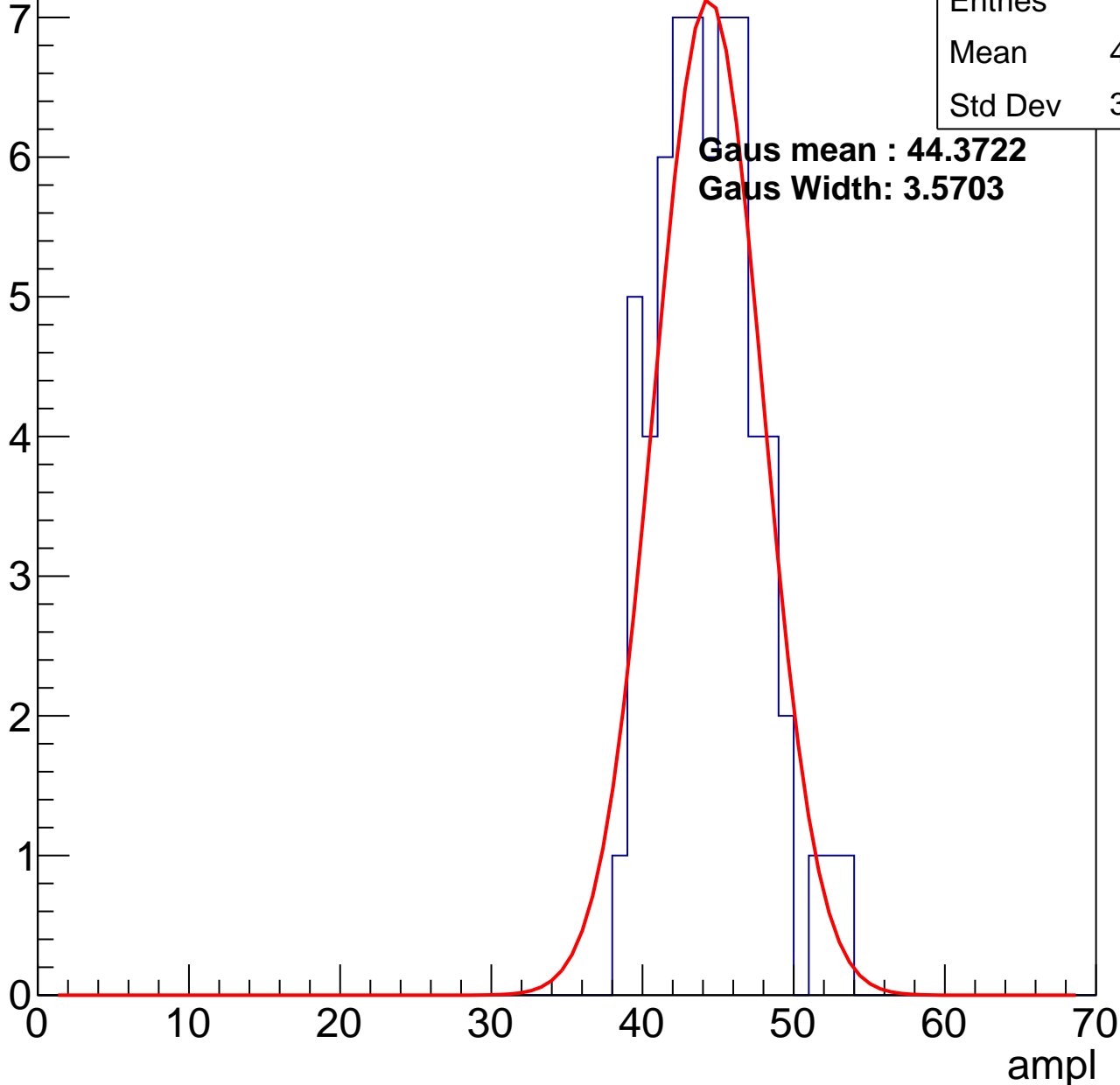
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.95
Std Dev	3.316

Gaus mean : 44.3722

Gaus Width: 3.5703

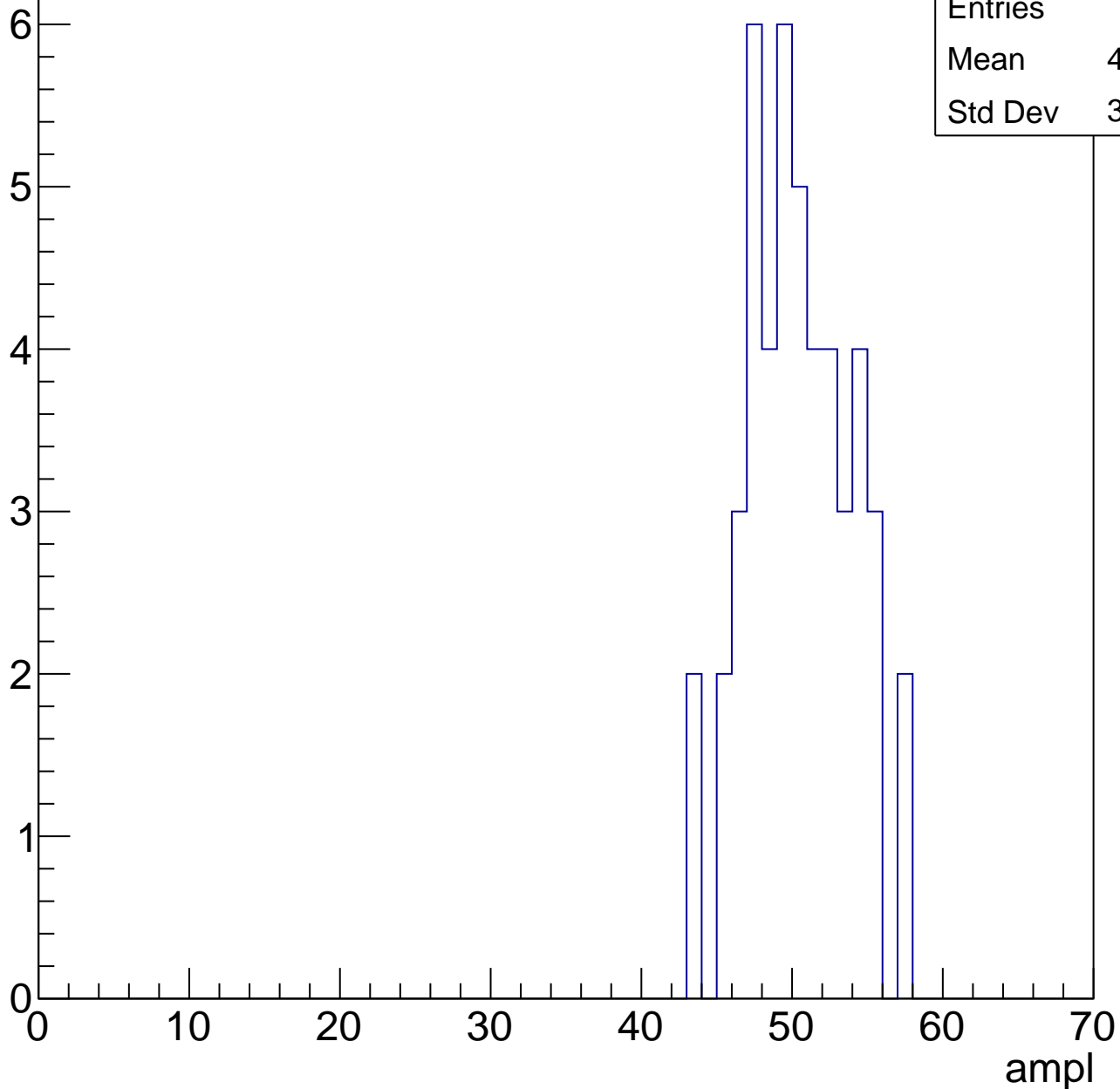


B0L001S, U14-ch15, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	49.96
Std Dev	3.403

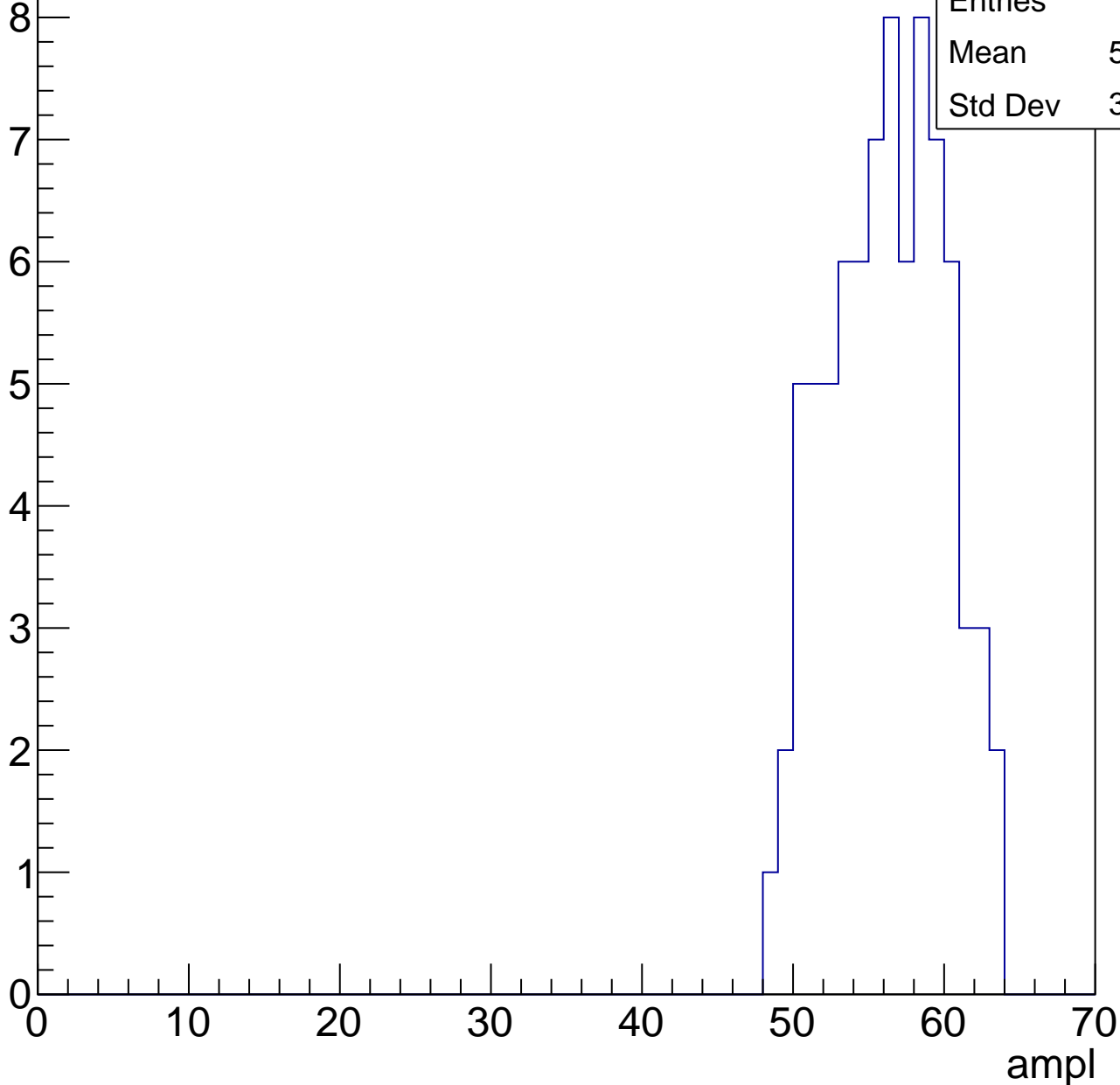


B0L001S, U14-ch15, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	55.75
Std Dev	3.717

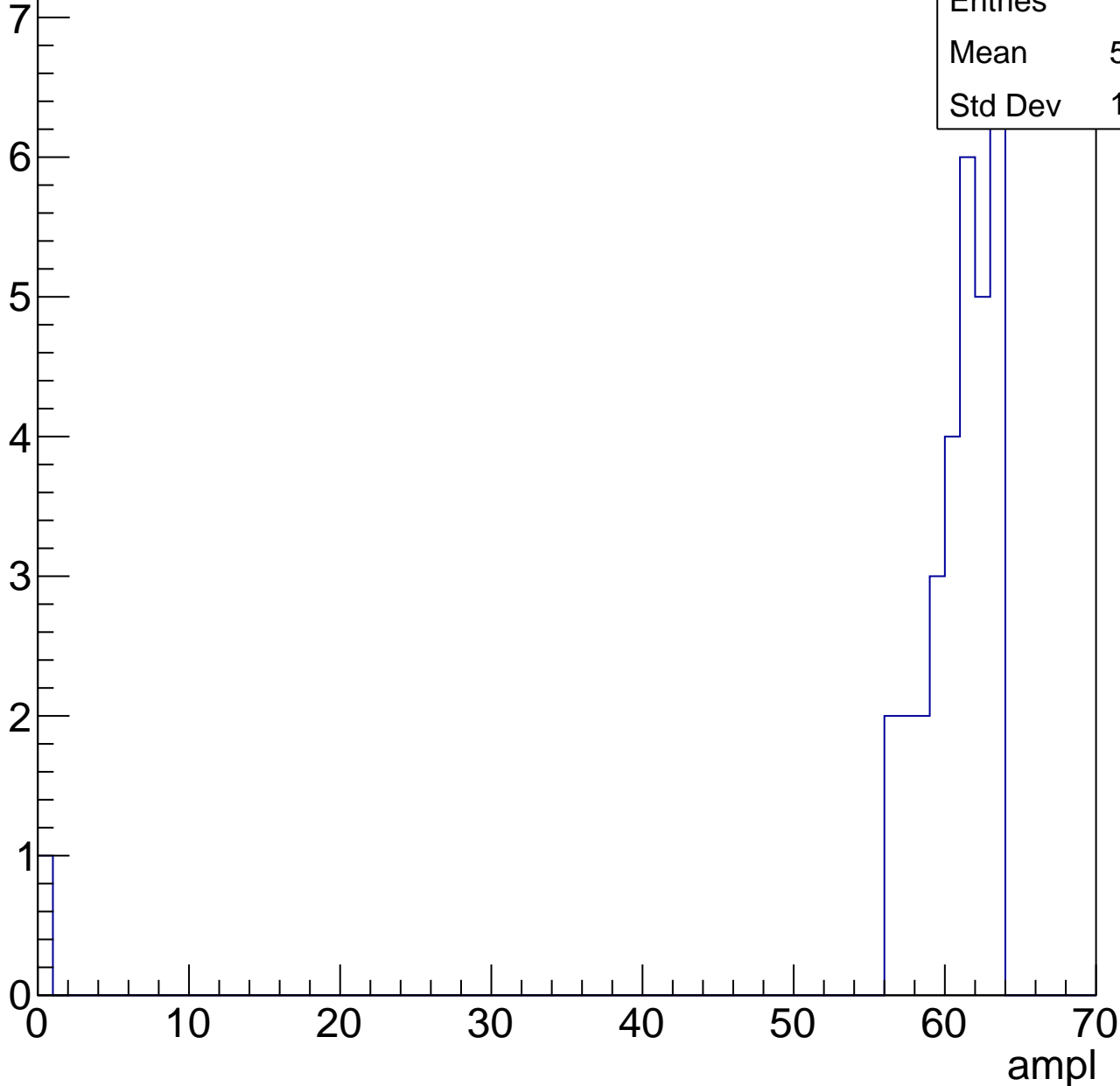


B0L001S, U14-ch15, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	32
Mean	58.62
Std Dev	10.74



B0L001S, U14-ch15, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch15, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch16, adc0

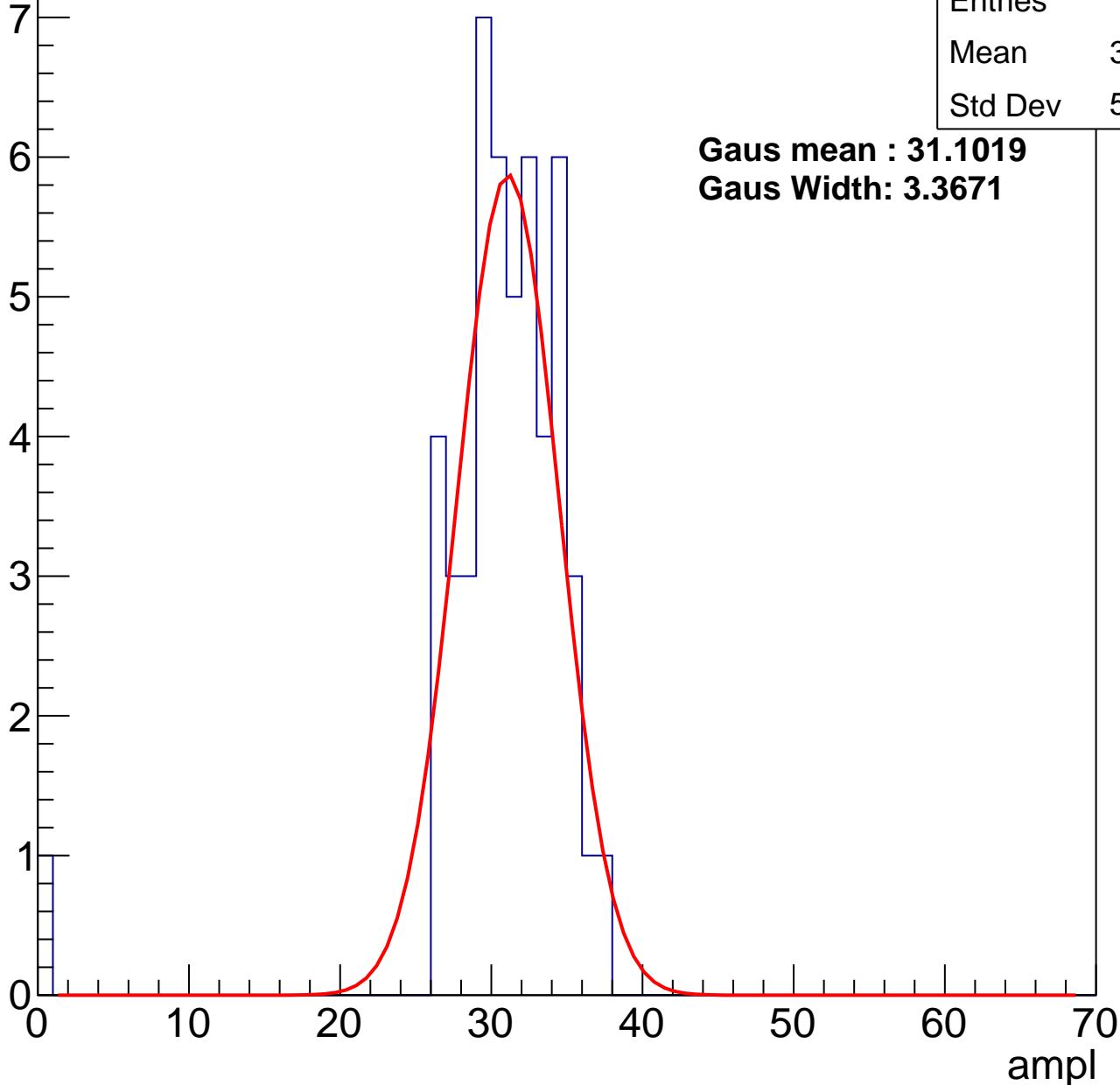
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	30.26
Std Dev	5.153

Gaus mean : 31.1019

Gaus Width: 3.3671



B0L001S, U14-ch16, adc1

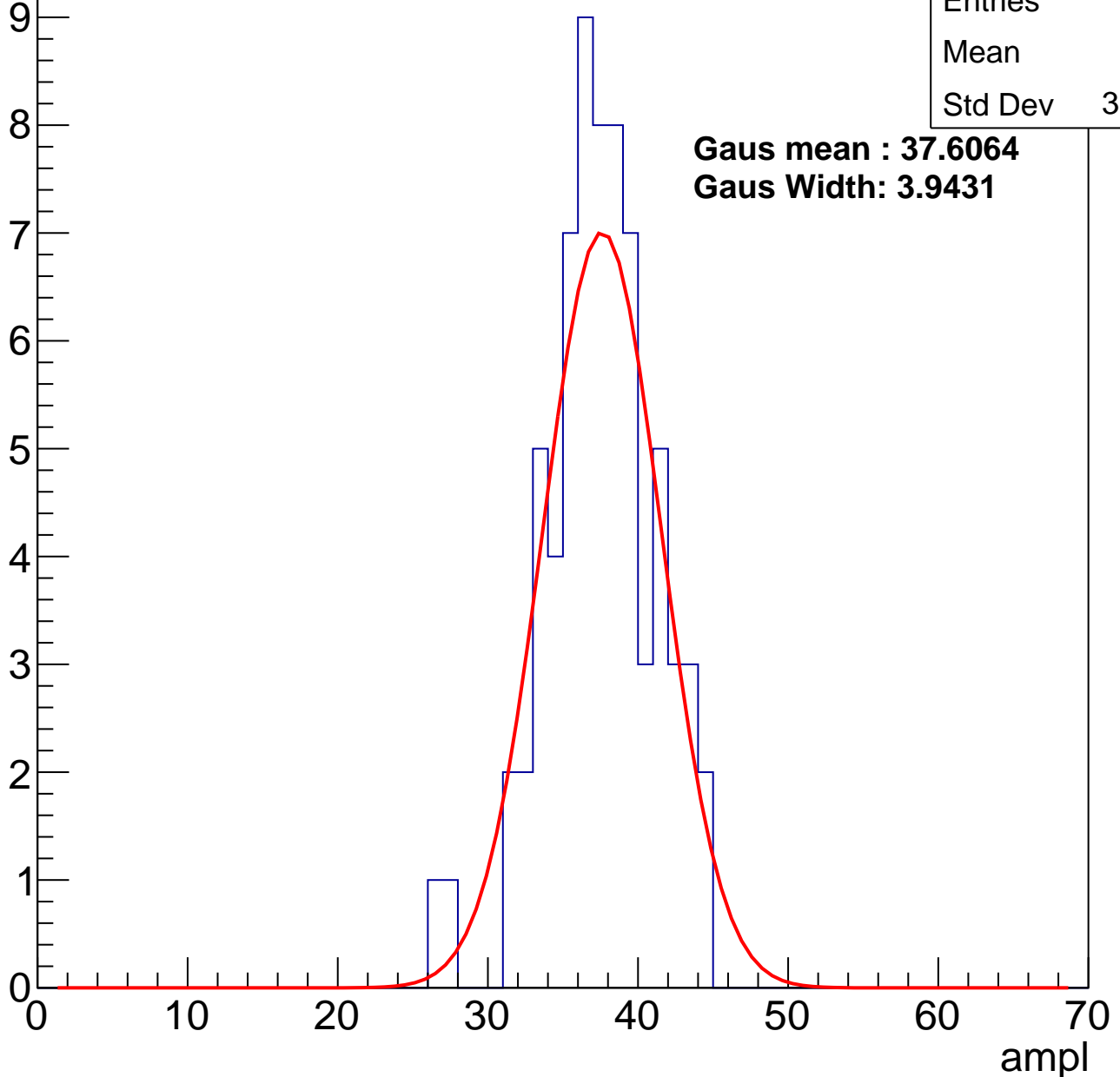
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37
Std Dev	3.625

Gaus mean : 37.6064

Gaus Width: 3.9431



B0L001S, U14-ch16, adc2

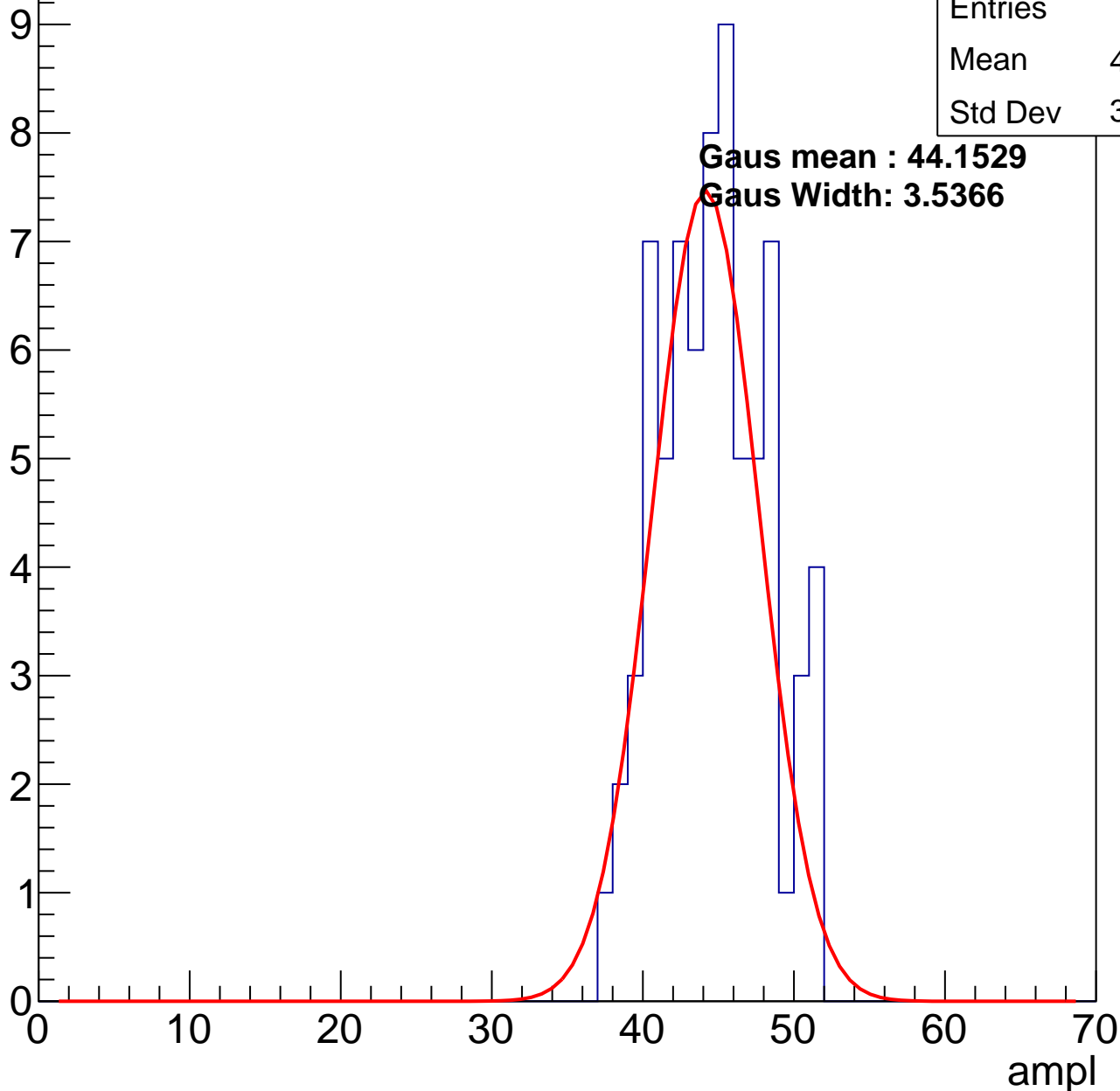
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.22
Std Dev	3.508

Gaus mean : 44.1529

Gaus Width: 3.5366

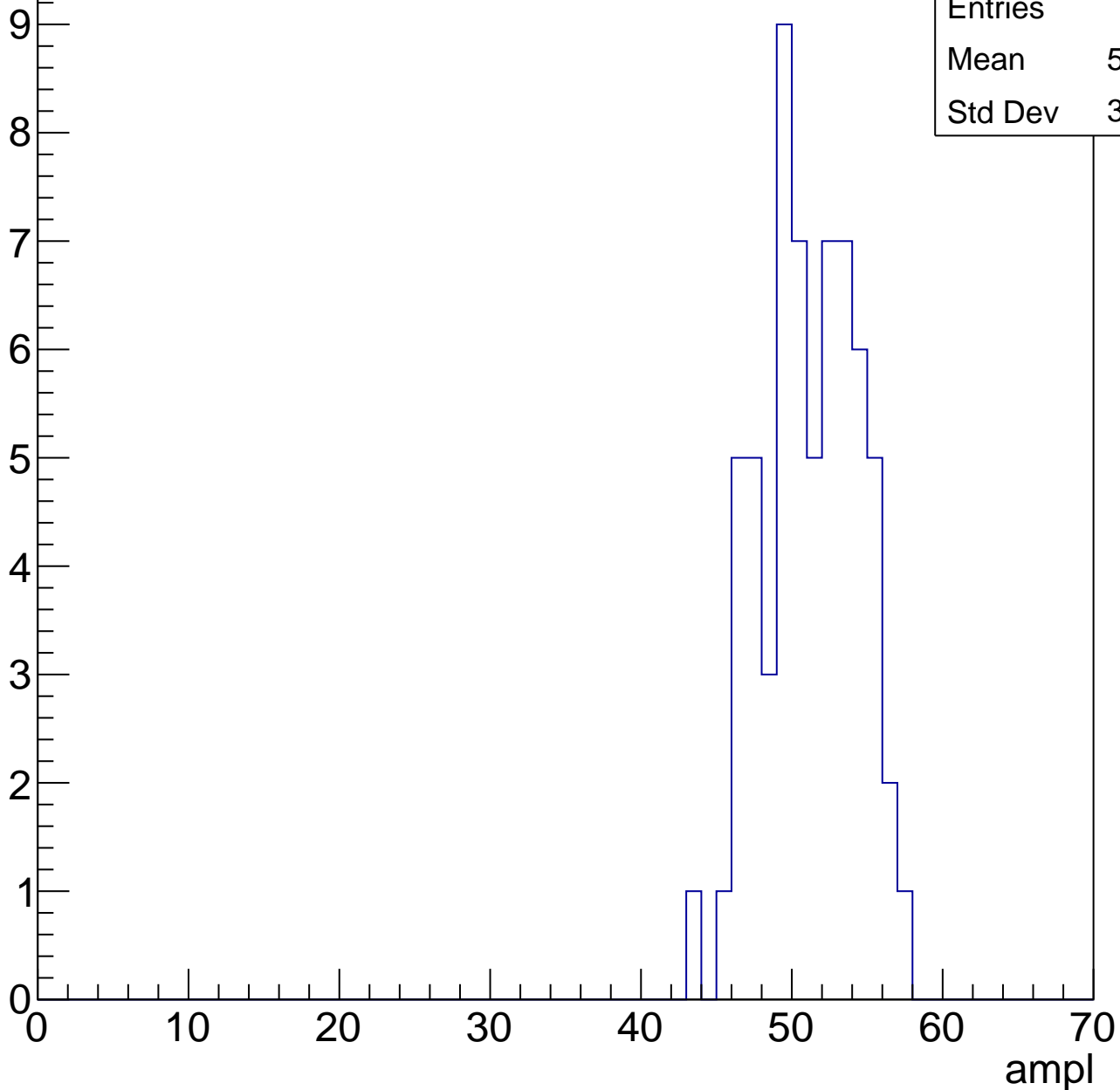


B0L001S, U14-ch16, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	50.72
Std Dev	3.125

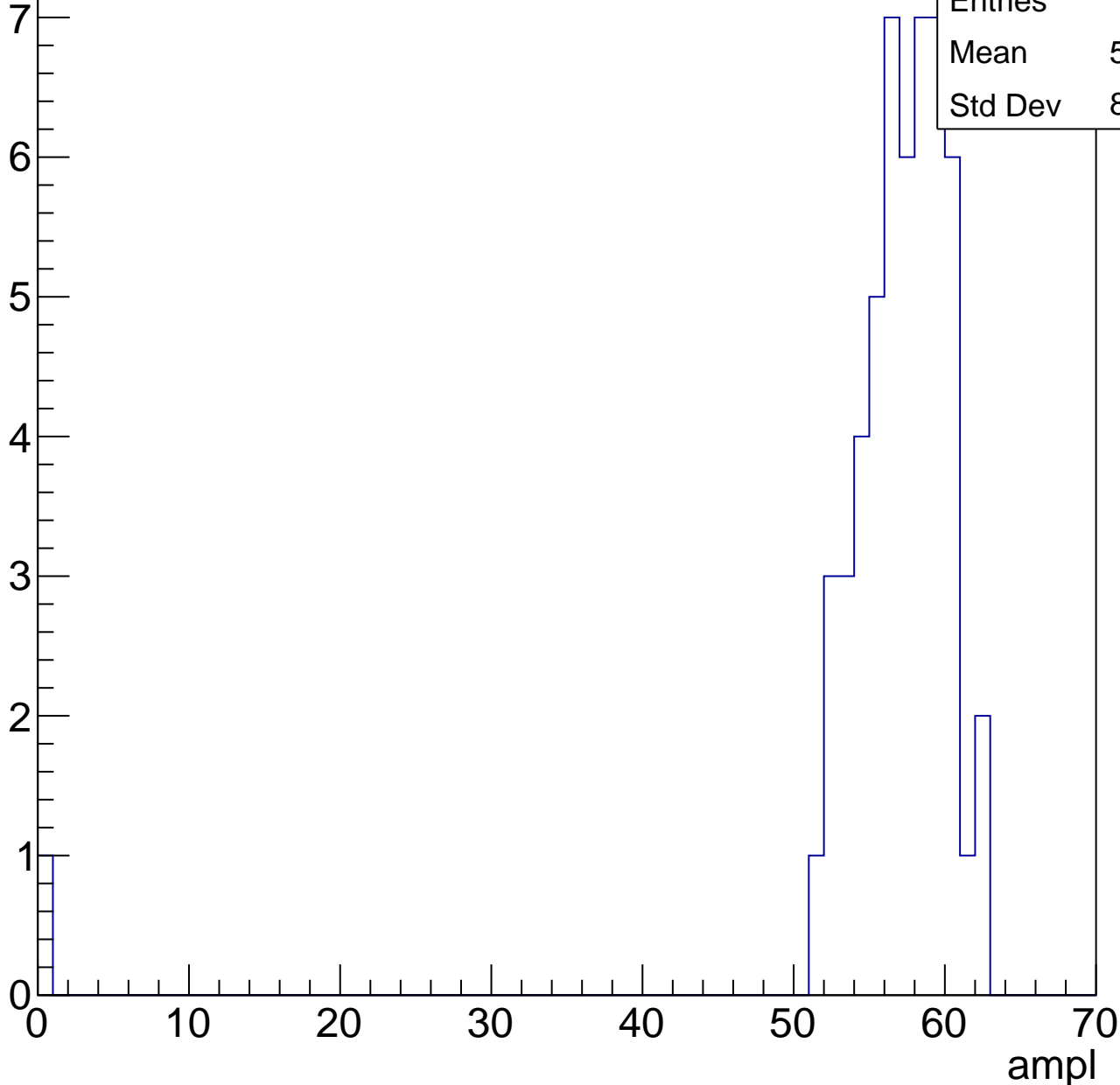


B0L001S, U14-ch16, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

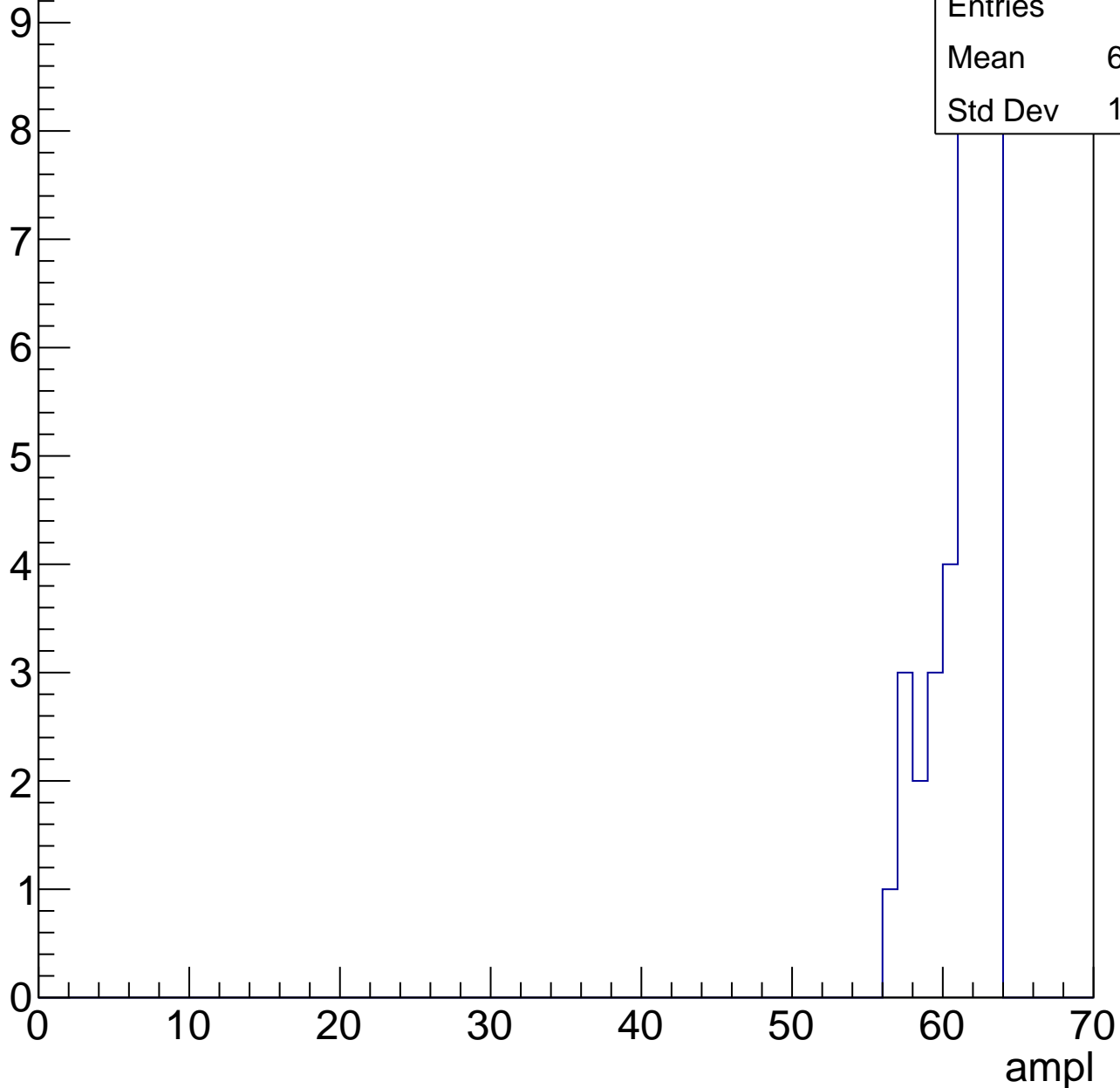
Entries	53
Mean	55.75
Std Dev	8.177



B0L001S, U14-ch16, adc5

calib_packv5_042523_0143.root, FC#9, port A1

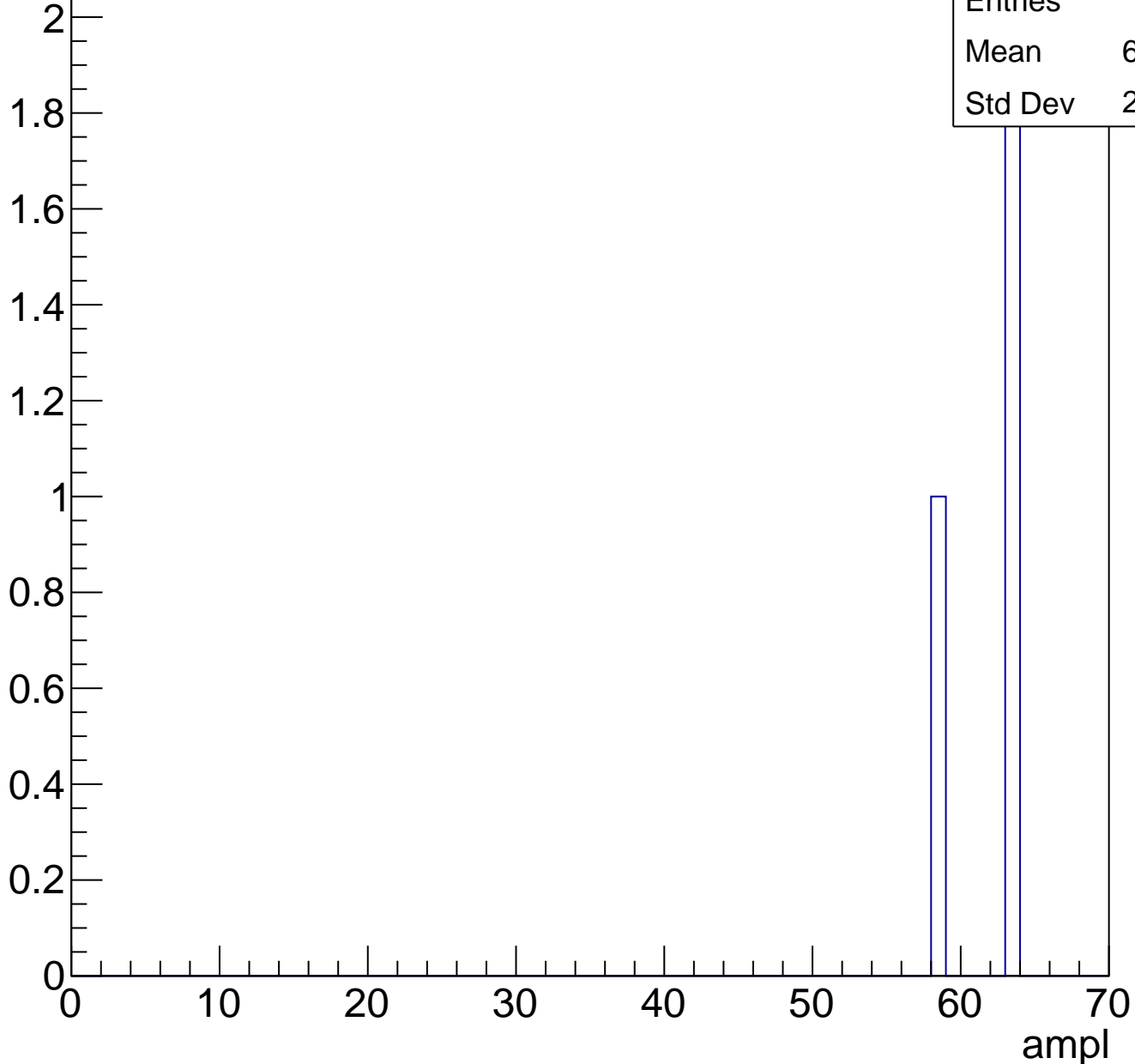
Entry



B0L001S, U14-ch16, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch16, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch17, adc0

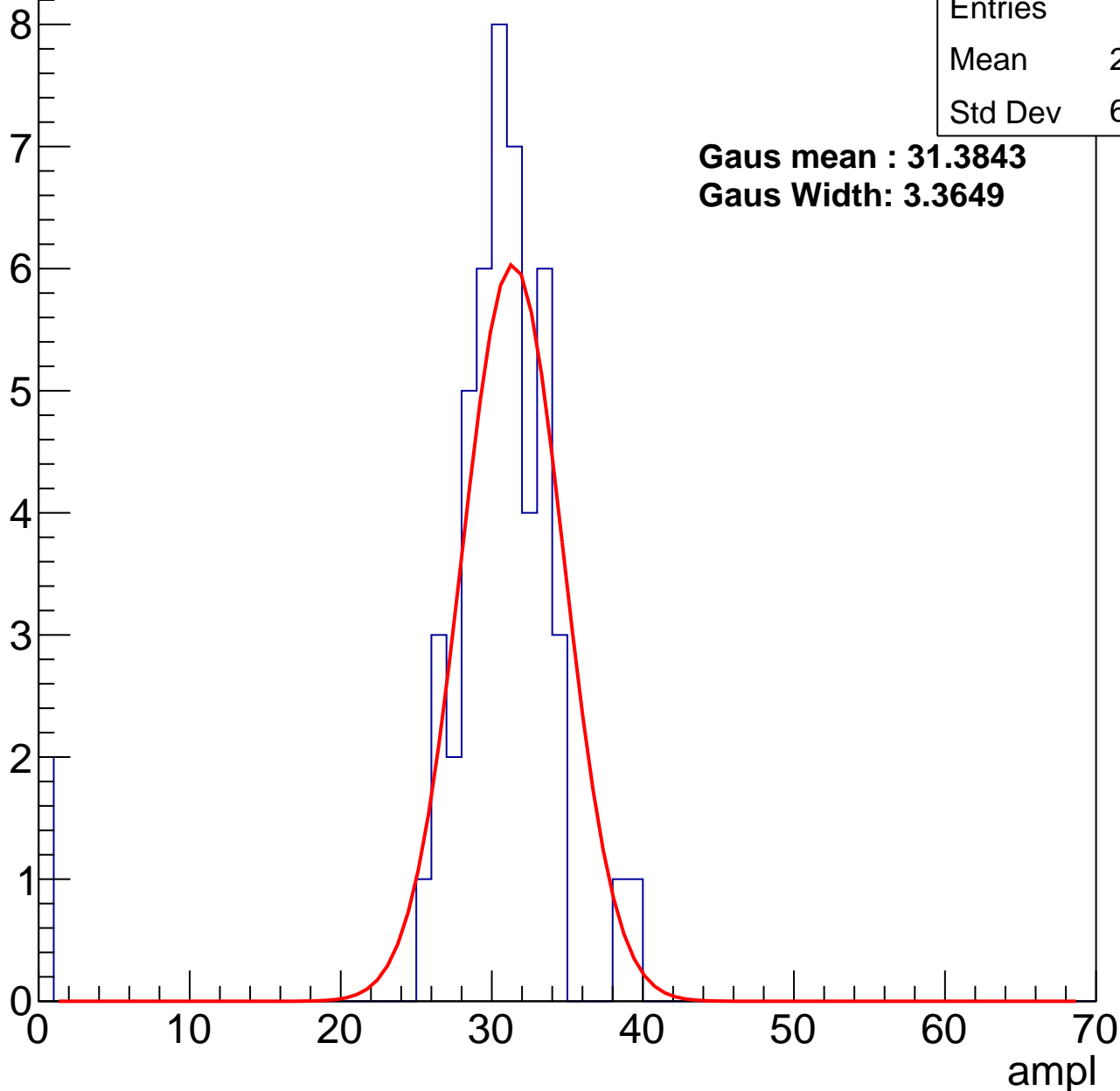
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	29.24
Std Dev	6.638

Gaus mean : 31.3843

Gaus Width: 3.3649



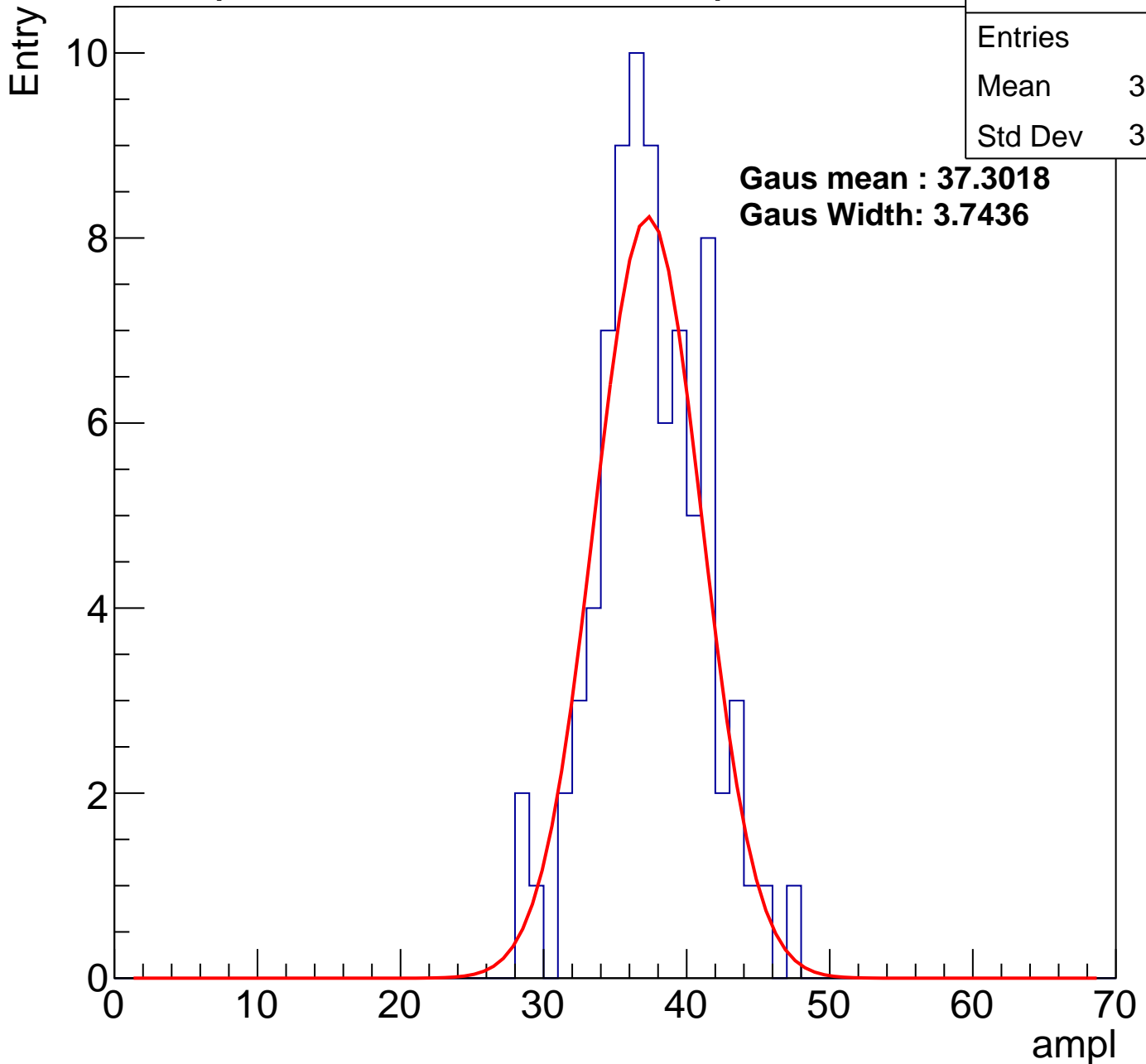
B0L001S, U14-ch17, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	37.02
Std Dev	3.725

Gaus mean : 37.3018

Gaus Width: 3.7436



B0L001S, U14-ch17, adc2

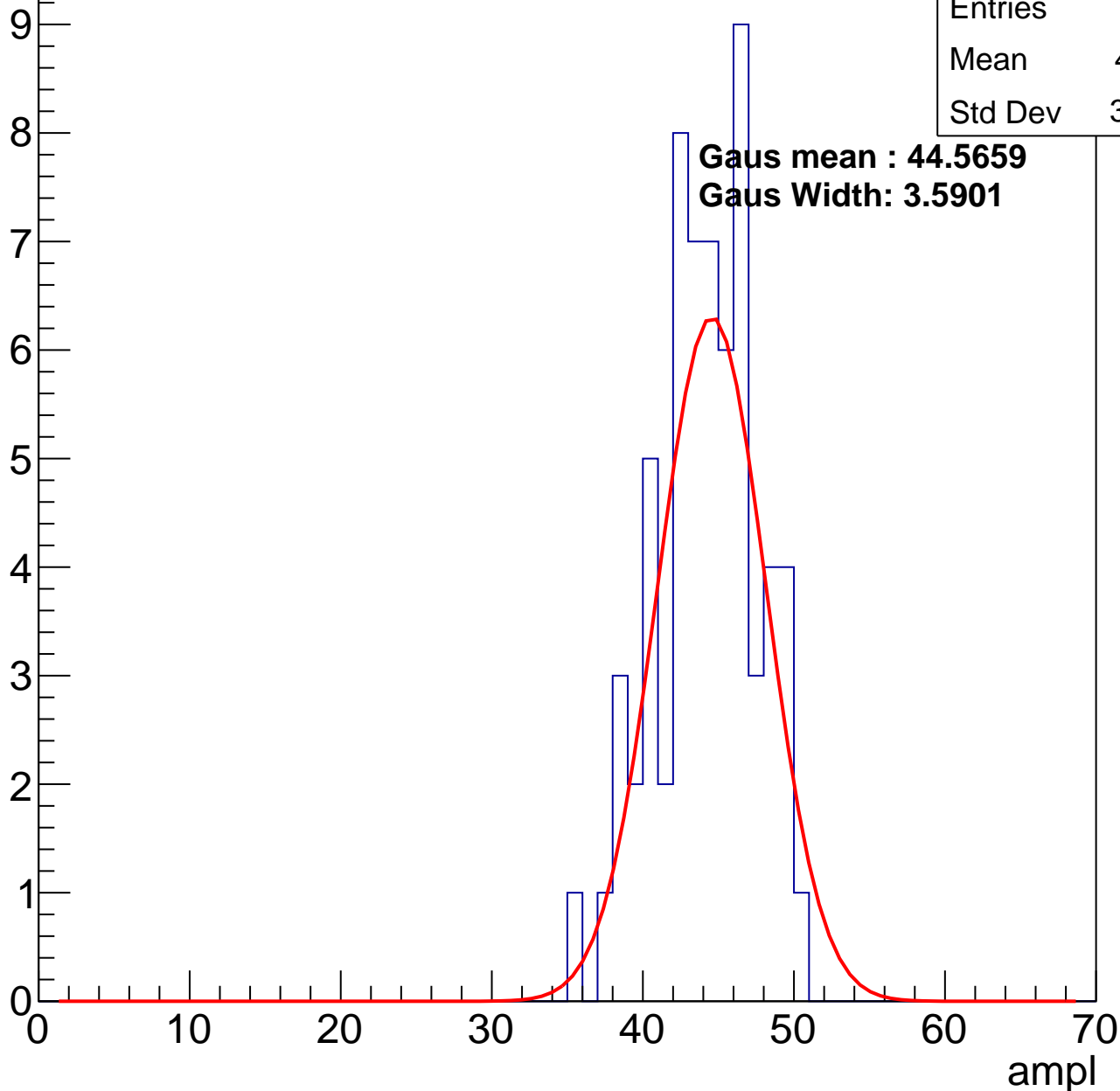
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.71
Std Dev	3.307

Gaus mean : 44.5659

Gaus Width: 3.5901

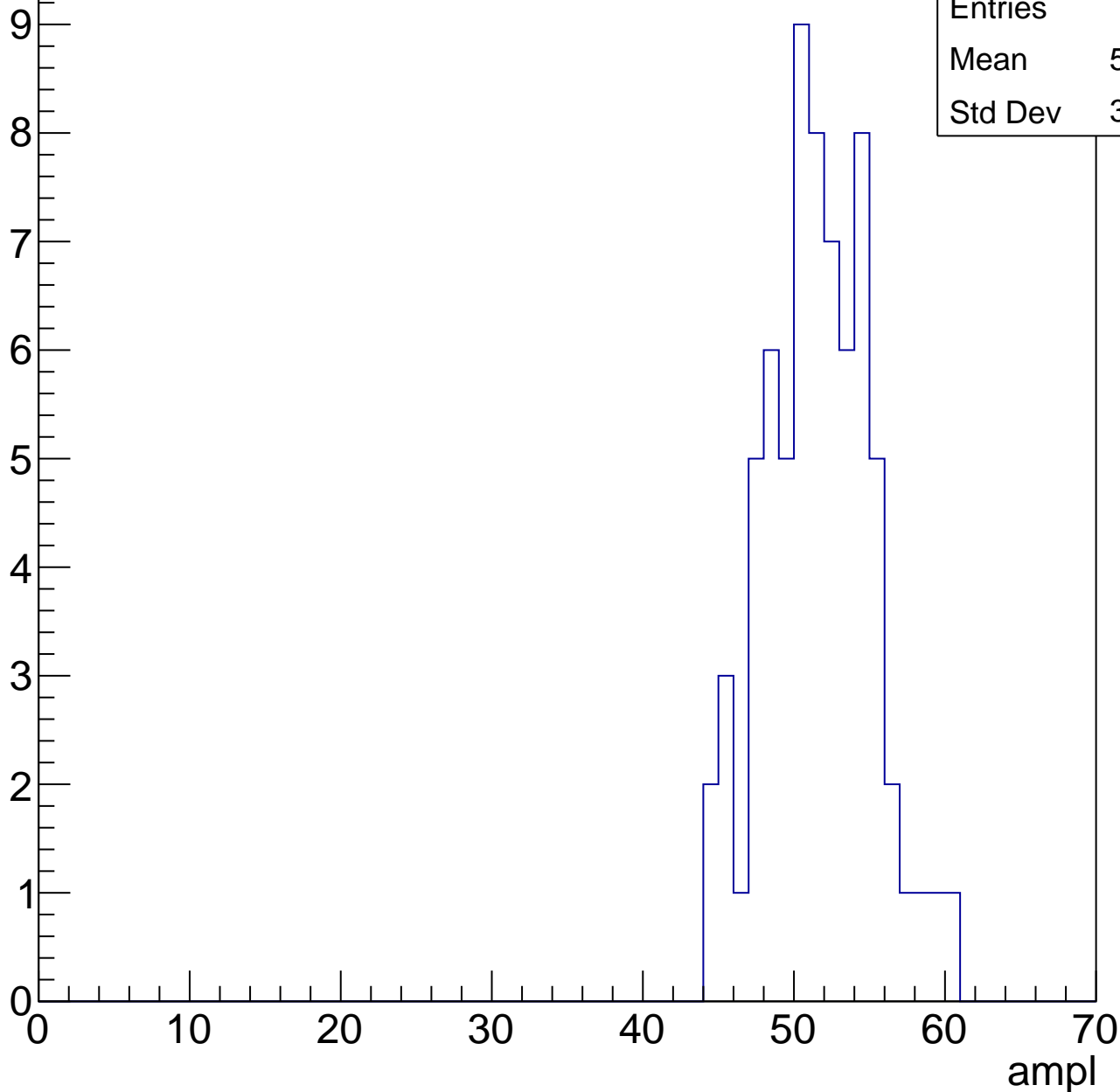


B0L001S, U14-ch17, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.13
Std Dev	3.472

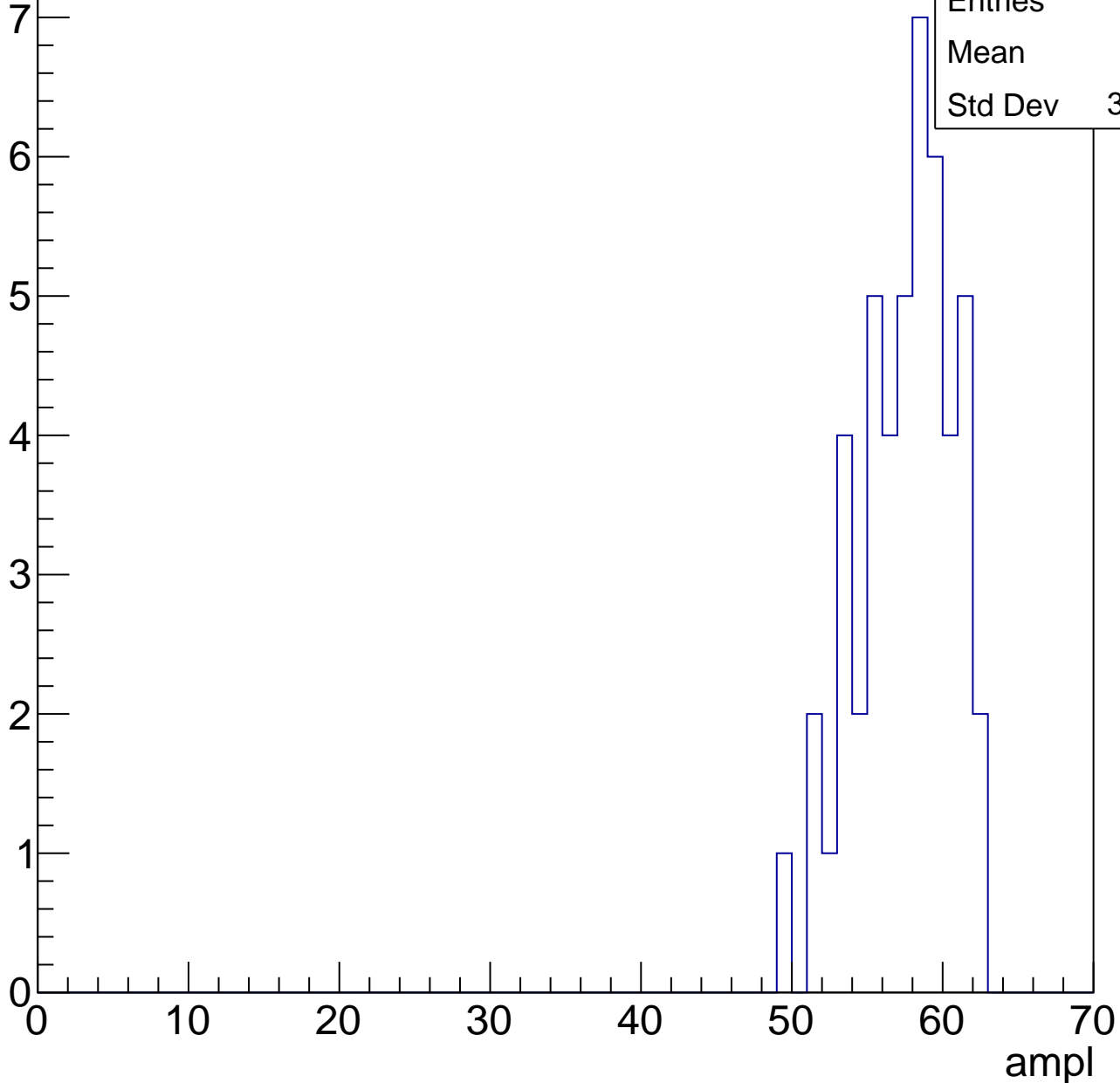


B0L001S, U14-ch17, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	57
Std Dev	3.109

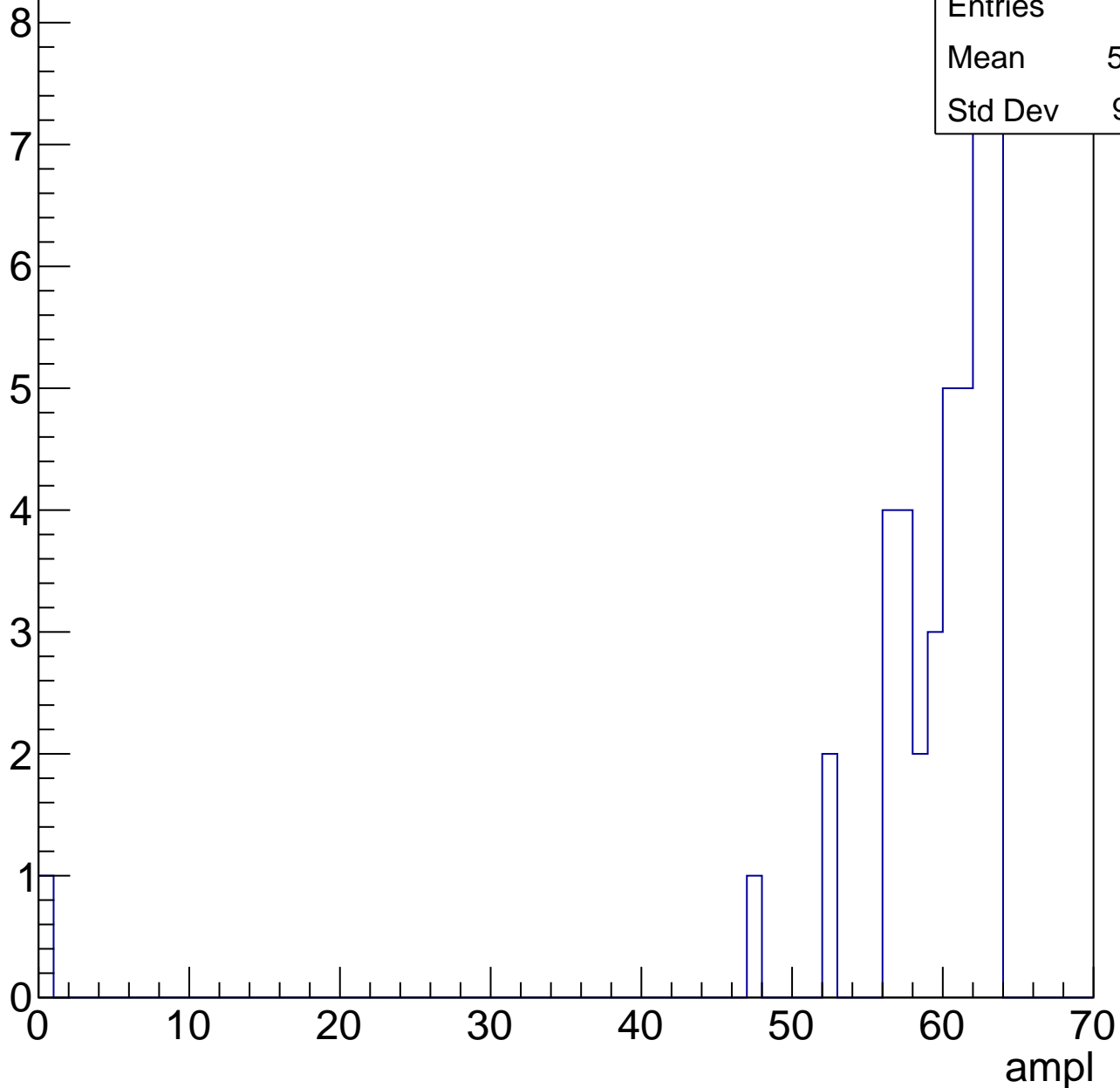


B0L001S, U14-ch17, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.16
Std Dev	9.611



B0L001S, U14-ch17, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch17, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch18, adc0

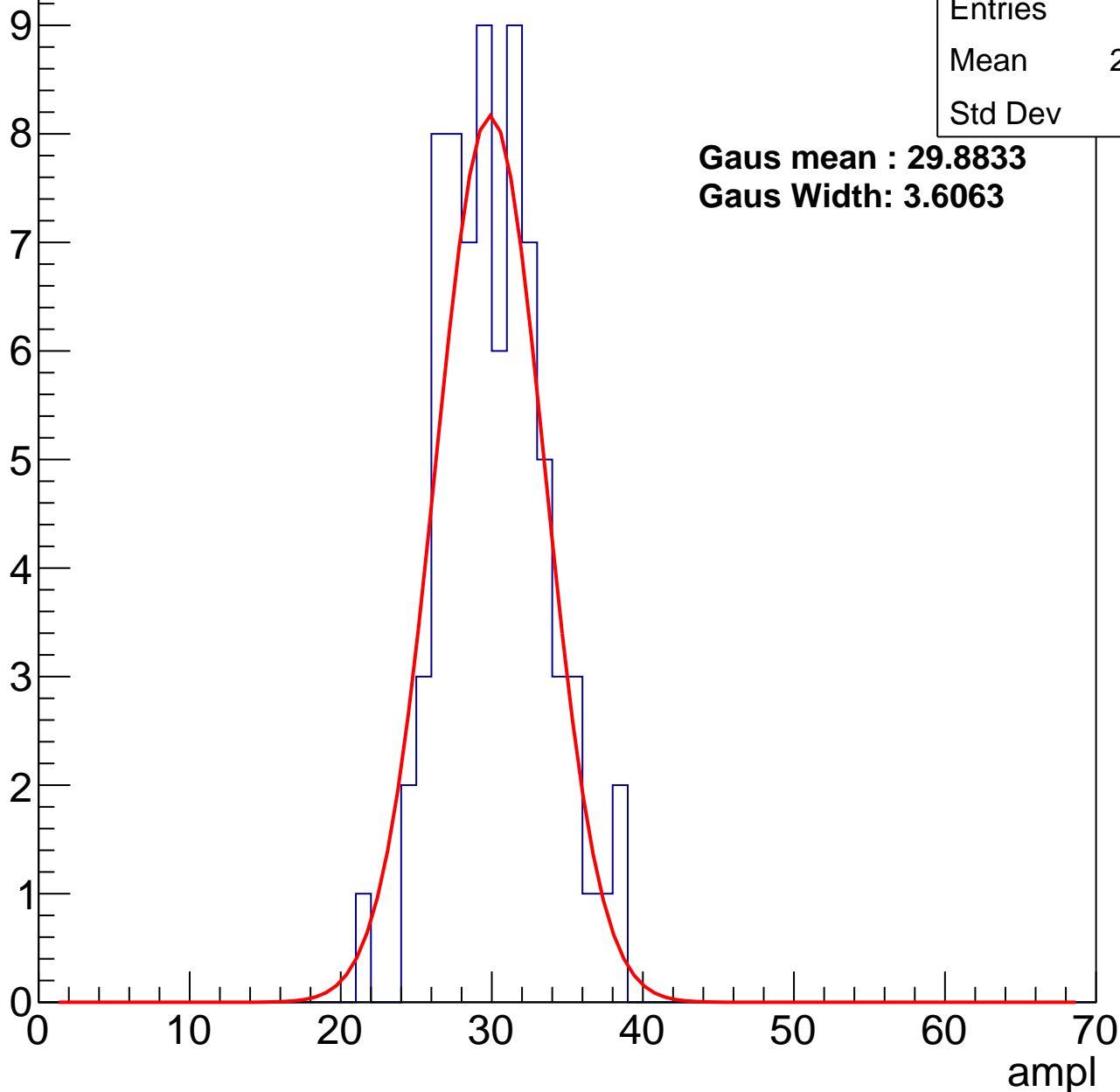
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.72
Std Dev	3.42

Gaus mean : 29.8833

Gaus Width: 3.6063



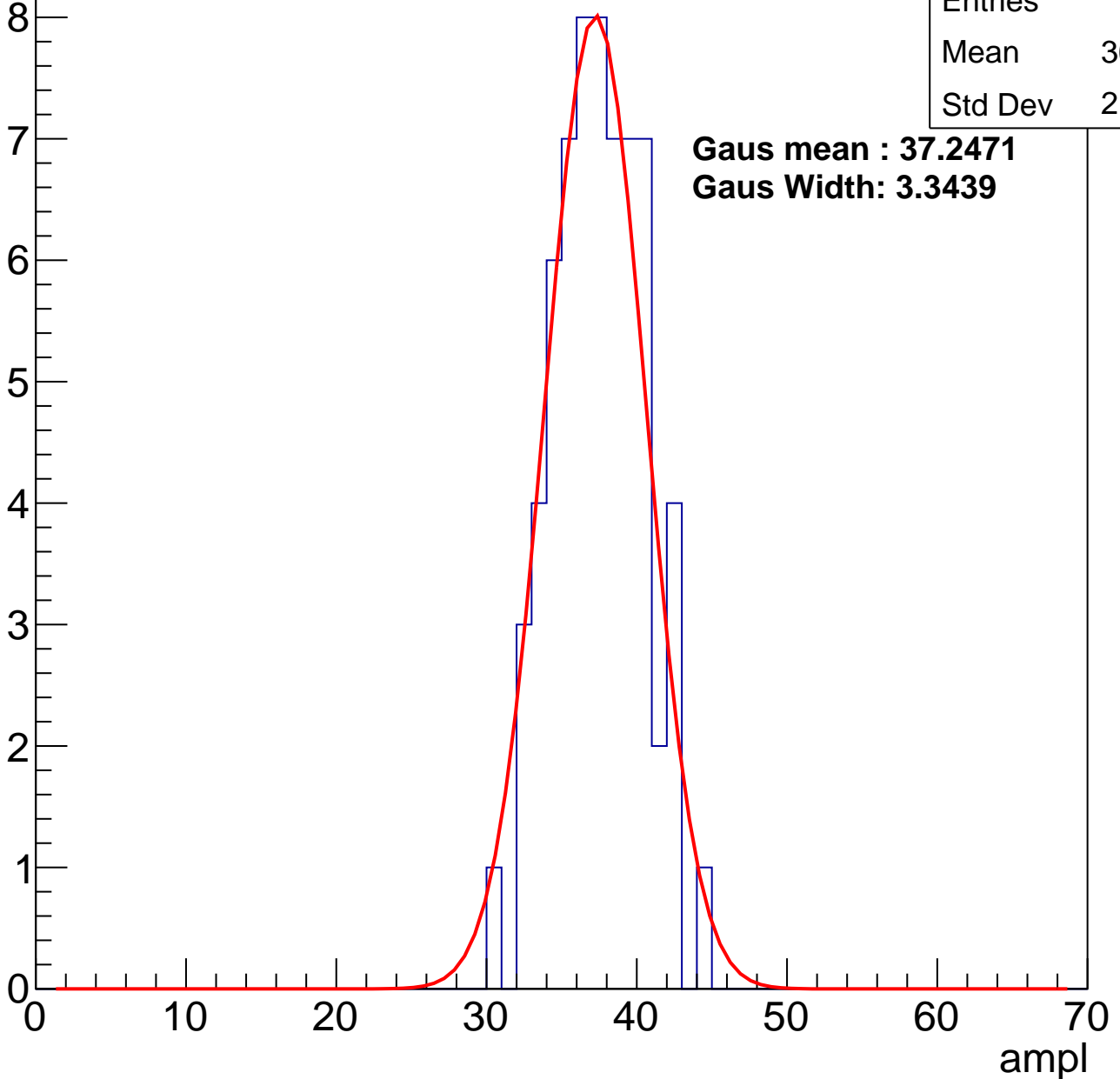
B0L001S, U14-ch18, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.98
Std Dev	2.927

Gaus mean : 37.2471
Gaus Width: 3.3439



B0L001S, U14-ch18, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	57
Mean	42.65
Std Dev	3.322

Gaus mean : 43.2819

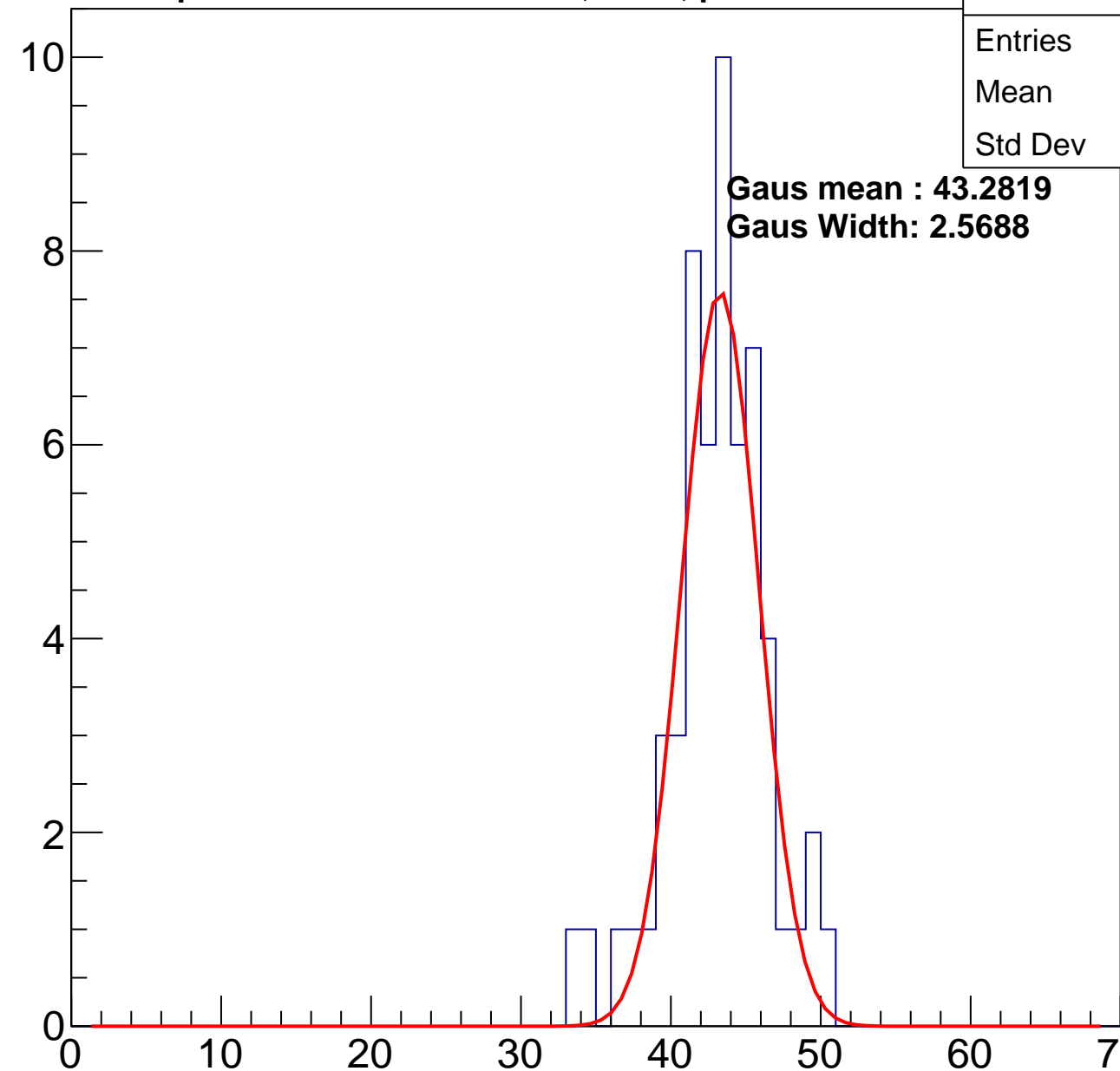
Gaus Width: 2.5688

Entry

10
8
6
4
2
0

ampl

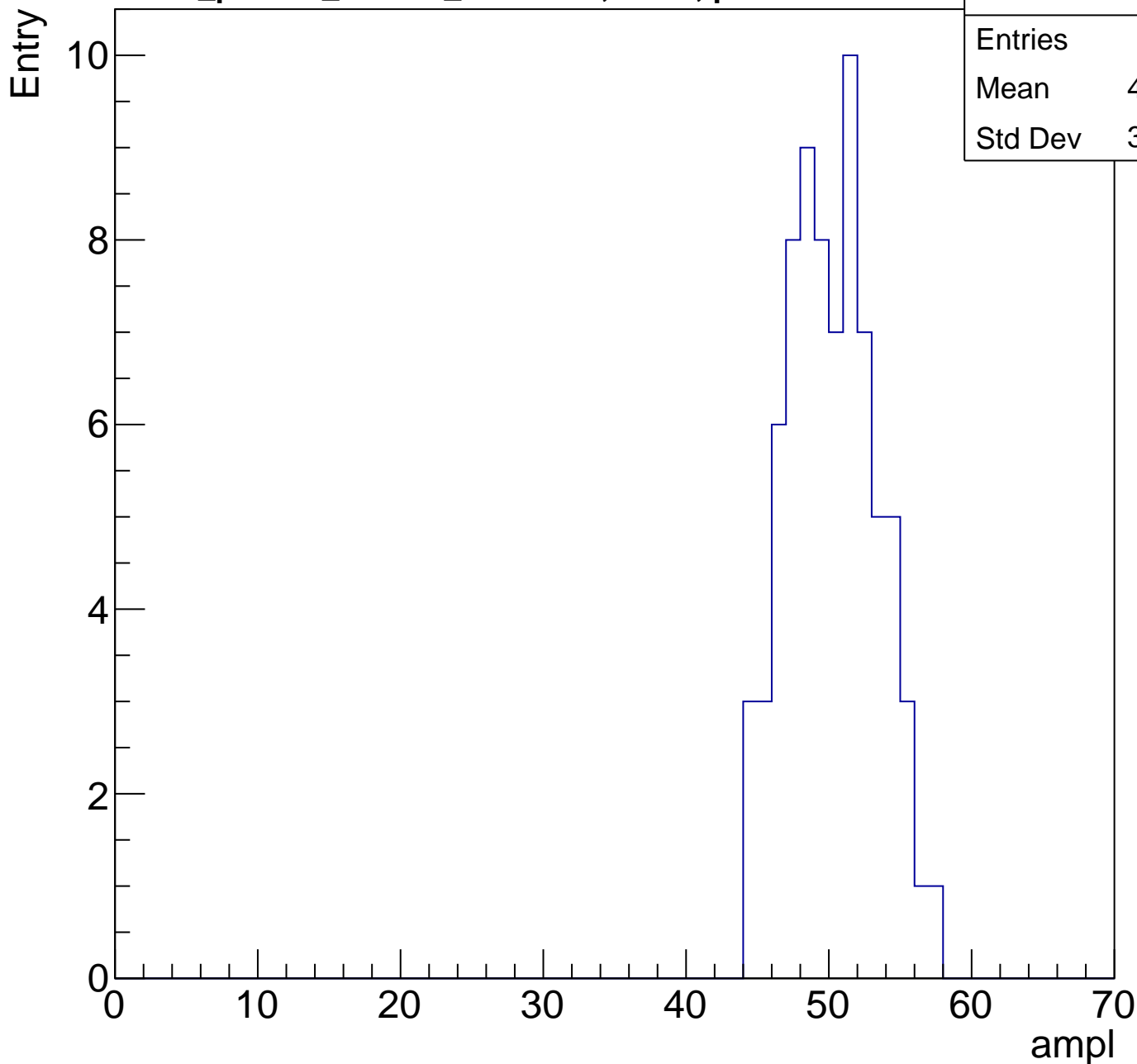
0 10 20 30 40 50 60 70



B0L001S, U14-ch18, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	49.74
Std Dev	3.062

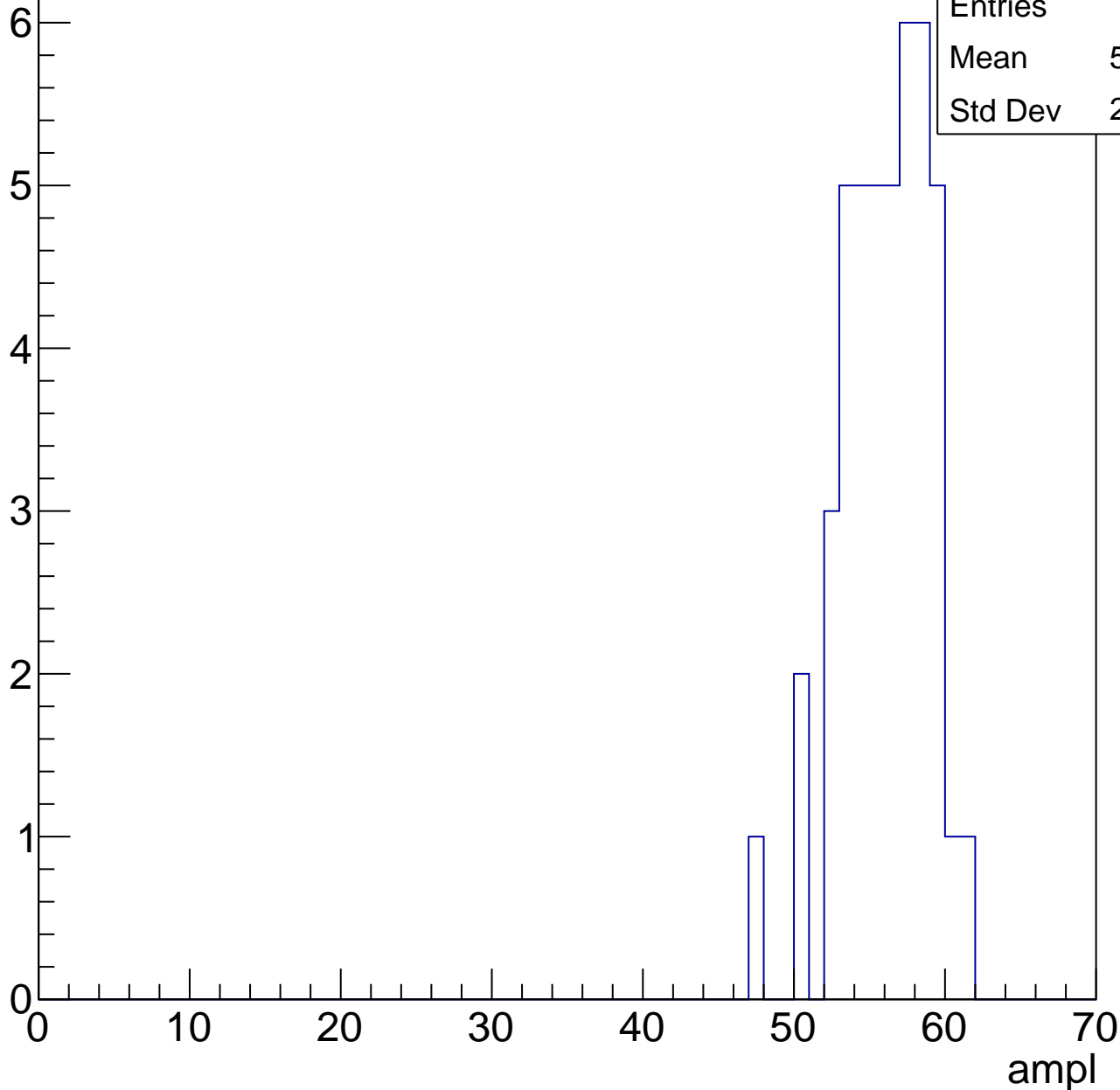


B0L001S, U14-ch18, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

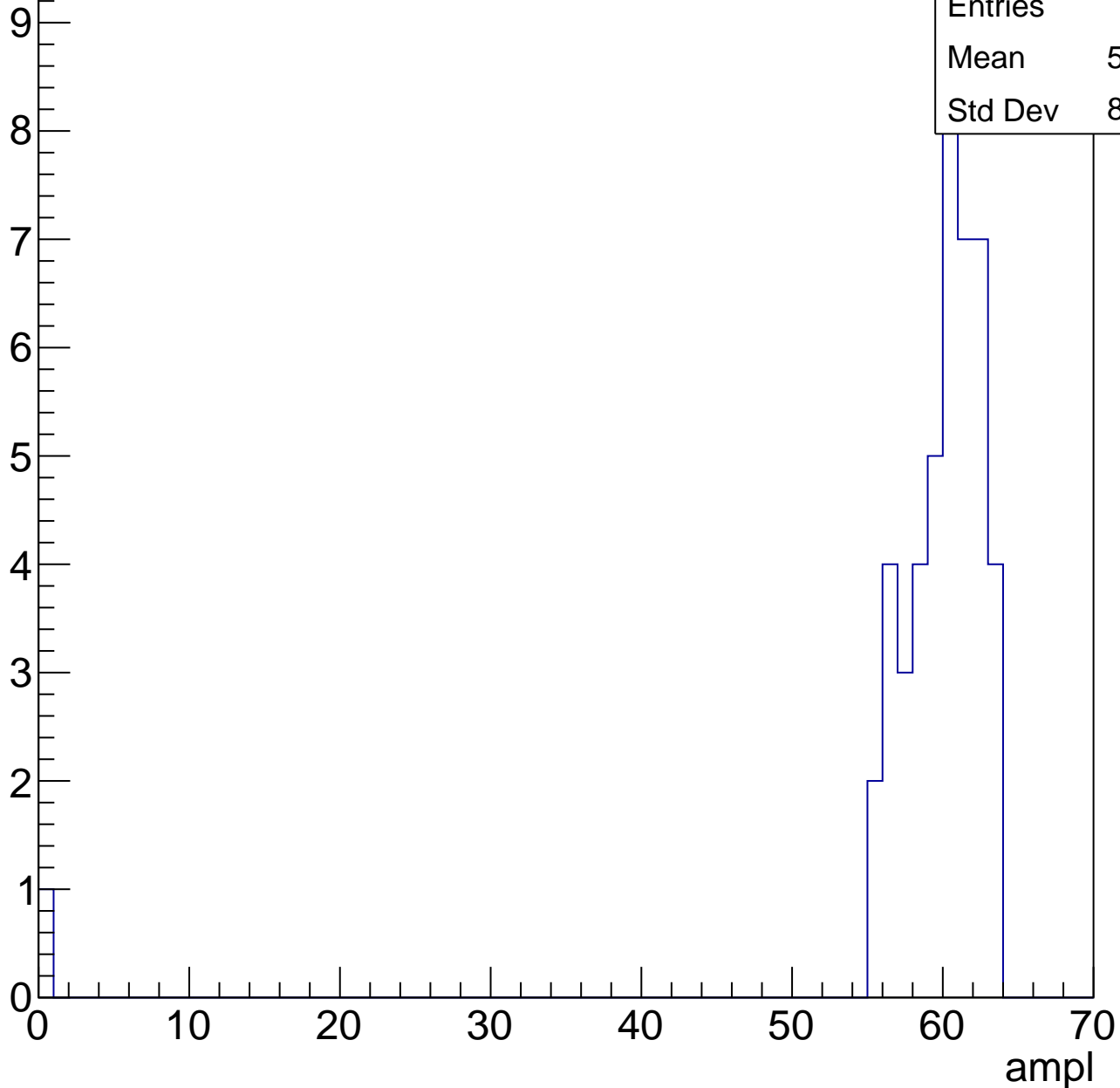
Entries	45
Mean	55.53
Std Dev	2.895



B0L001S, U14-ch18, adc5

calib_packv5_042523_0143.root, FC#9, port A1

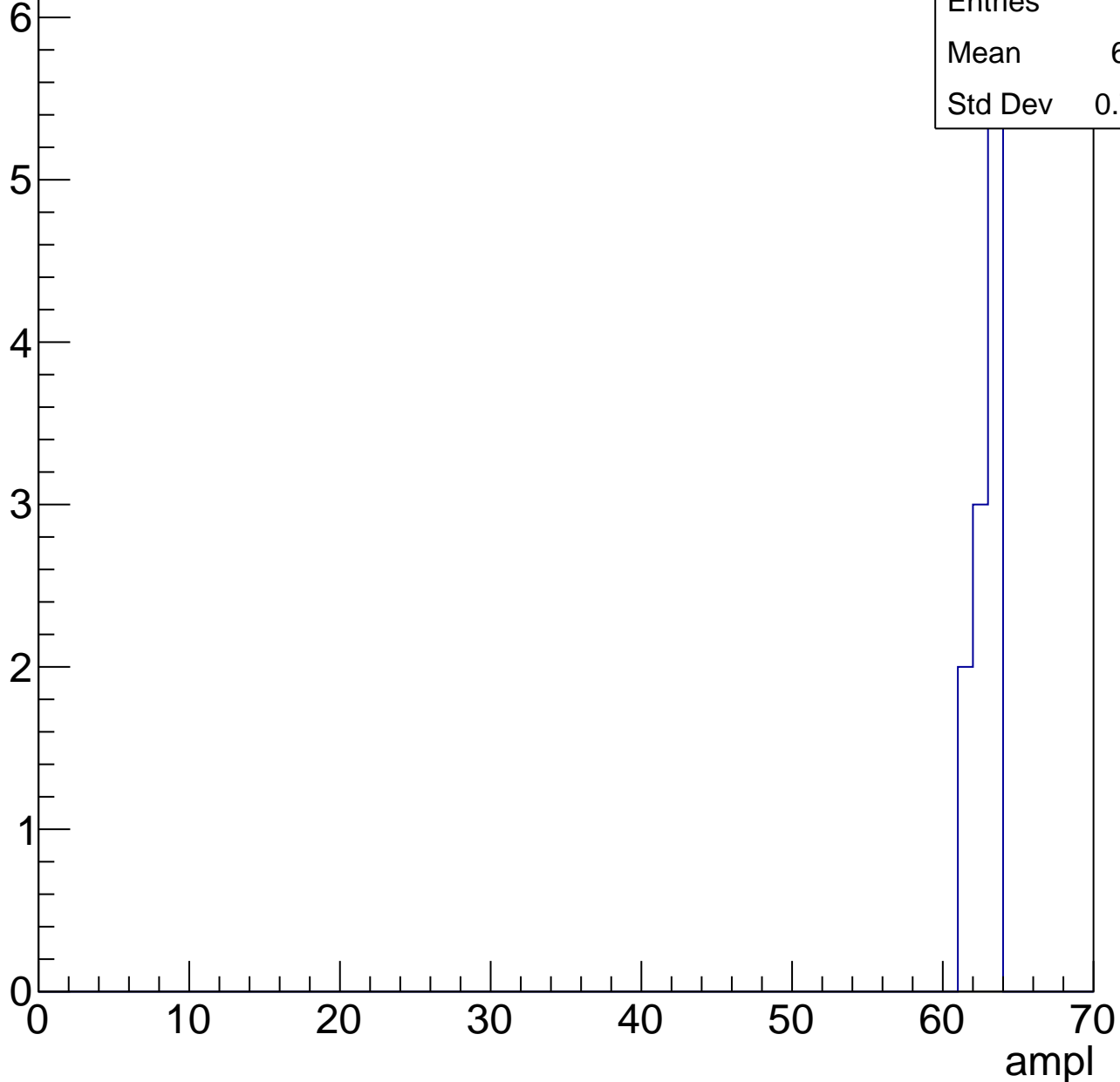
Entry



B0L001S, U14-ch18, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch18, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch19, adc0

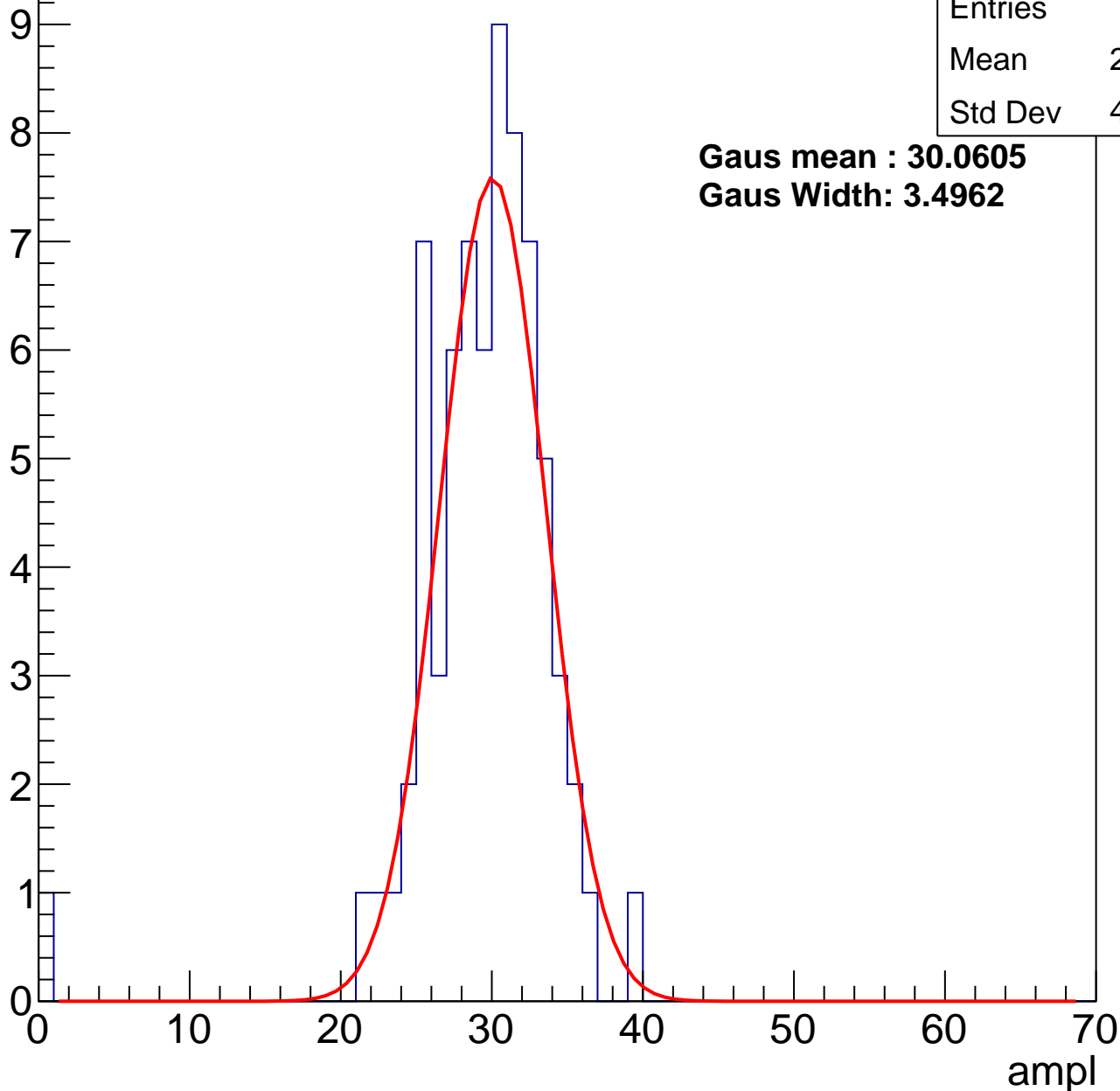
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	28.92
Std Dev	4.878

Gaus mean : 30.0605

Gaus Width: 3.4962



B0L001S, U14-ch19, adc1

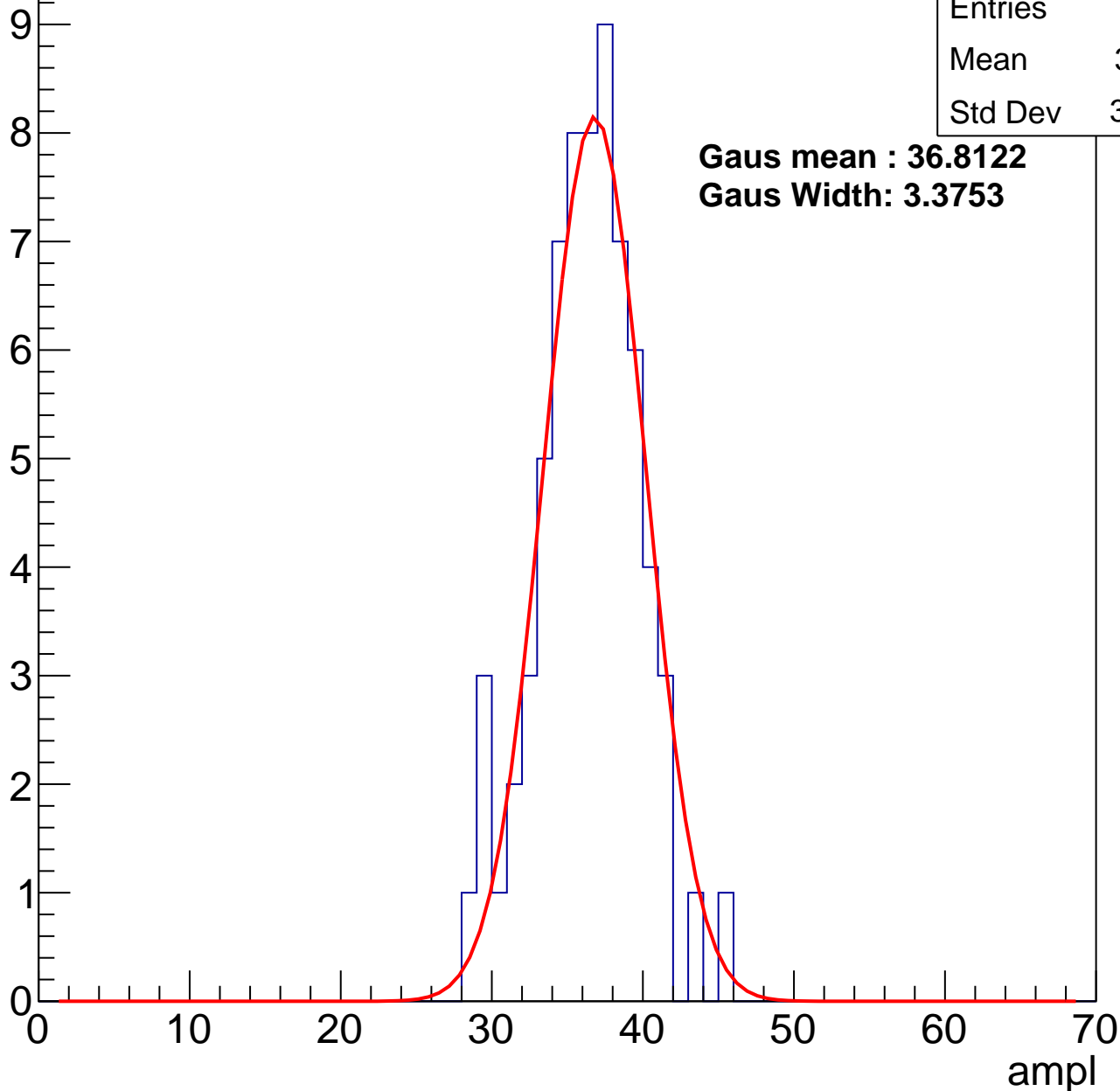
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	35.91
Std Dev	3.374

Gaus mean : 36.8122

Gaus Width: 3.3753



B0L001S, U14-ch19, adc2

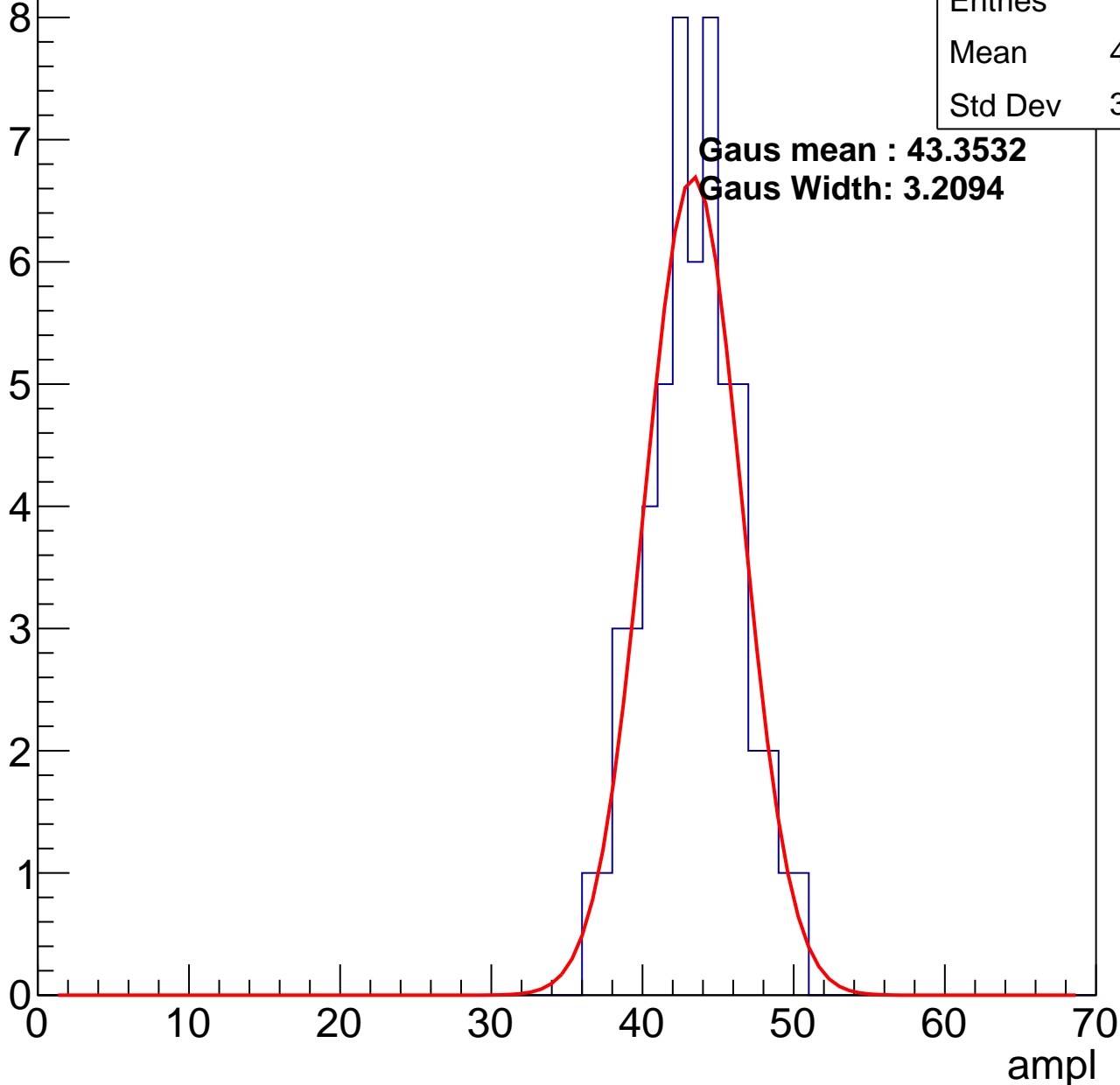
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.89
Std Dev	3.049

Gaus mean : 43.3532

Gaus Width: 3.2094

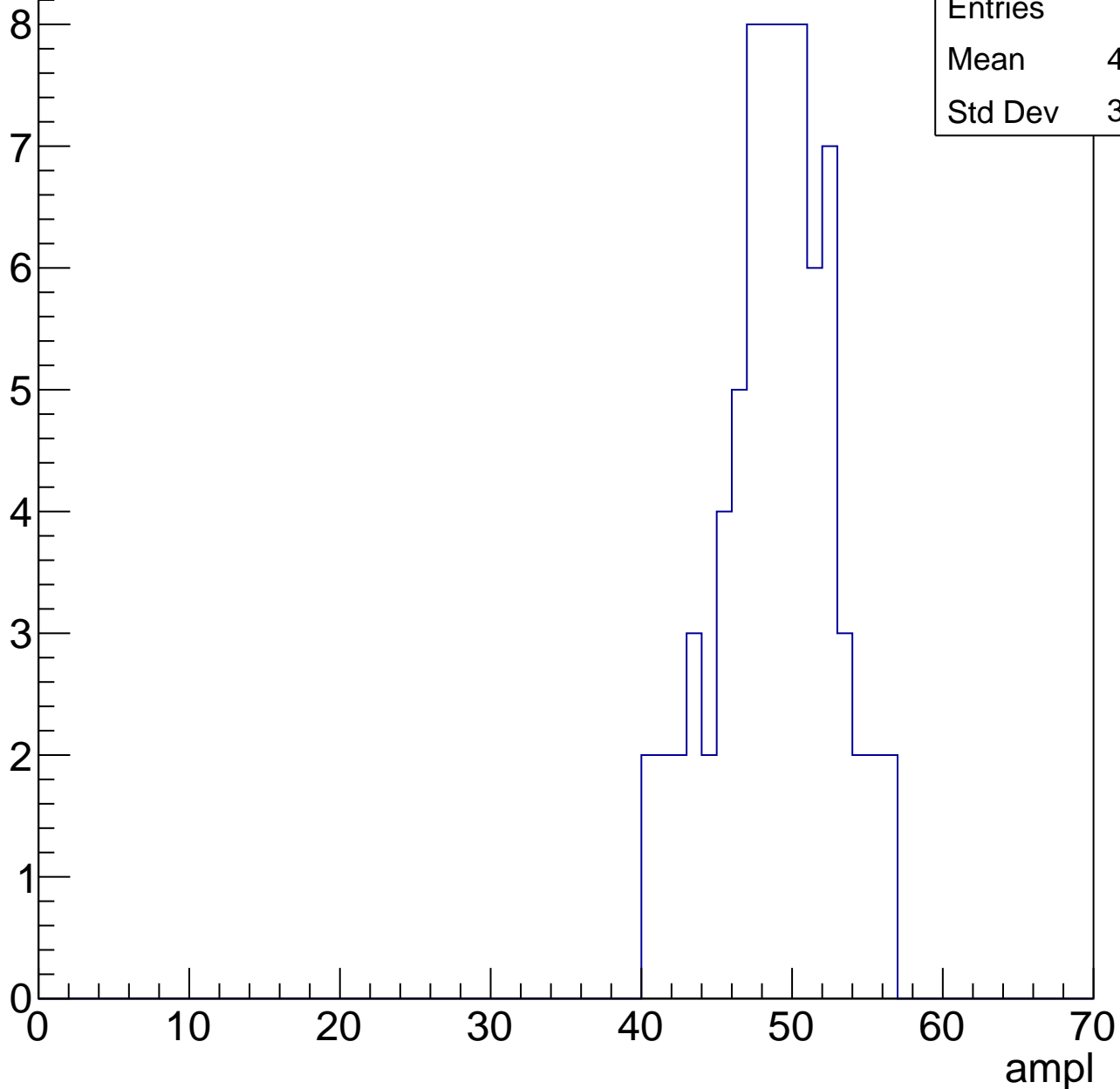


B0L001S, U14-ch19, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	48.43
Std Dev	3.738

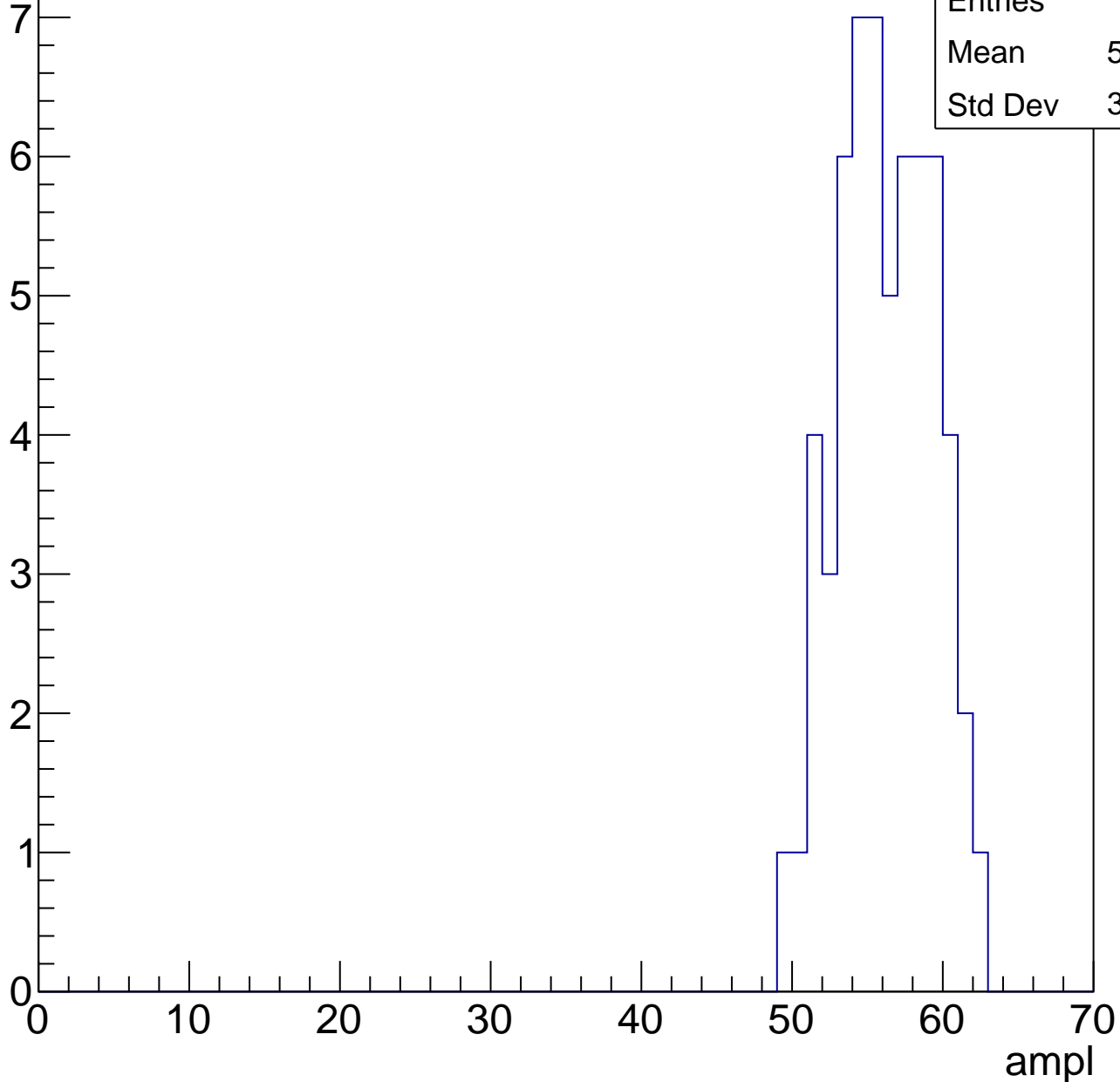


B0L001S, U14-ch19, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	55.73
Std Dev	3.058

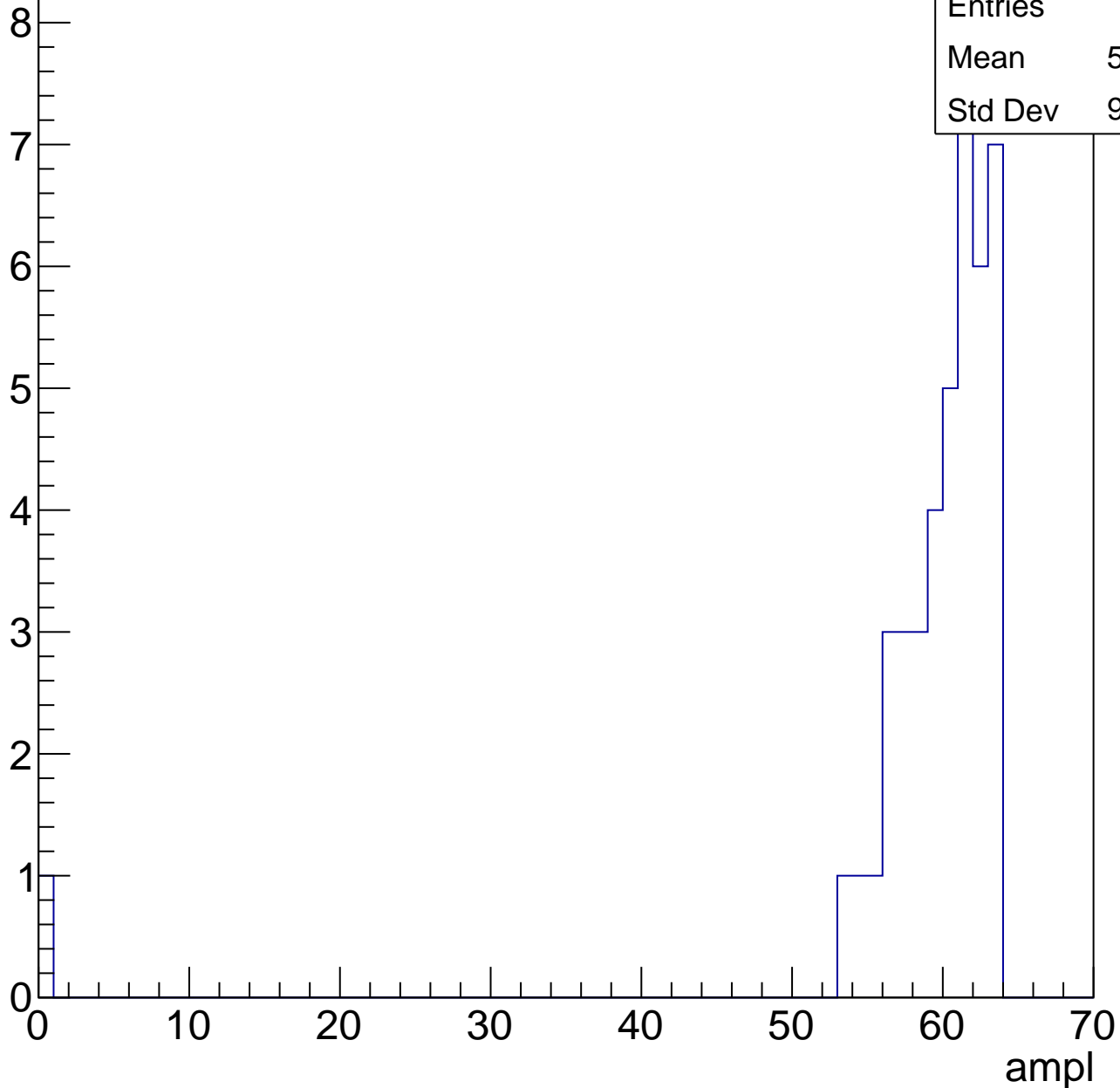


B0L001S, U14-ch19, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

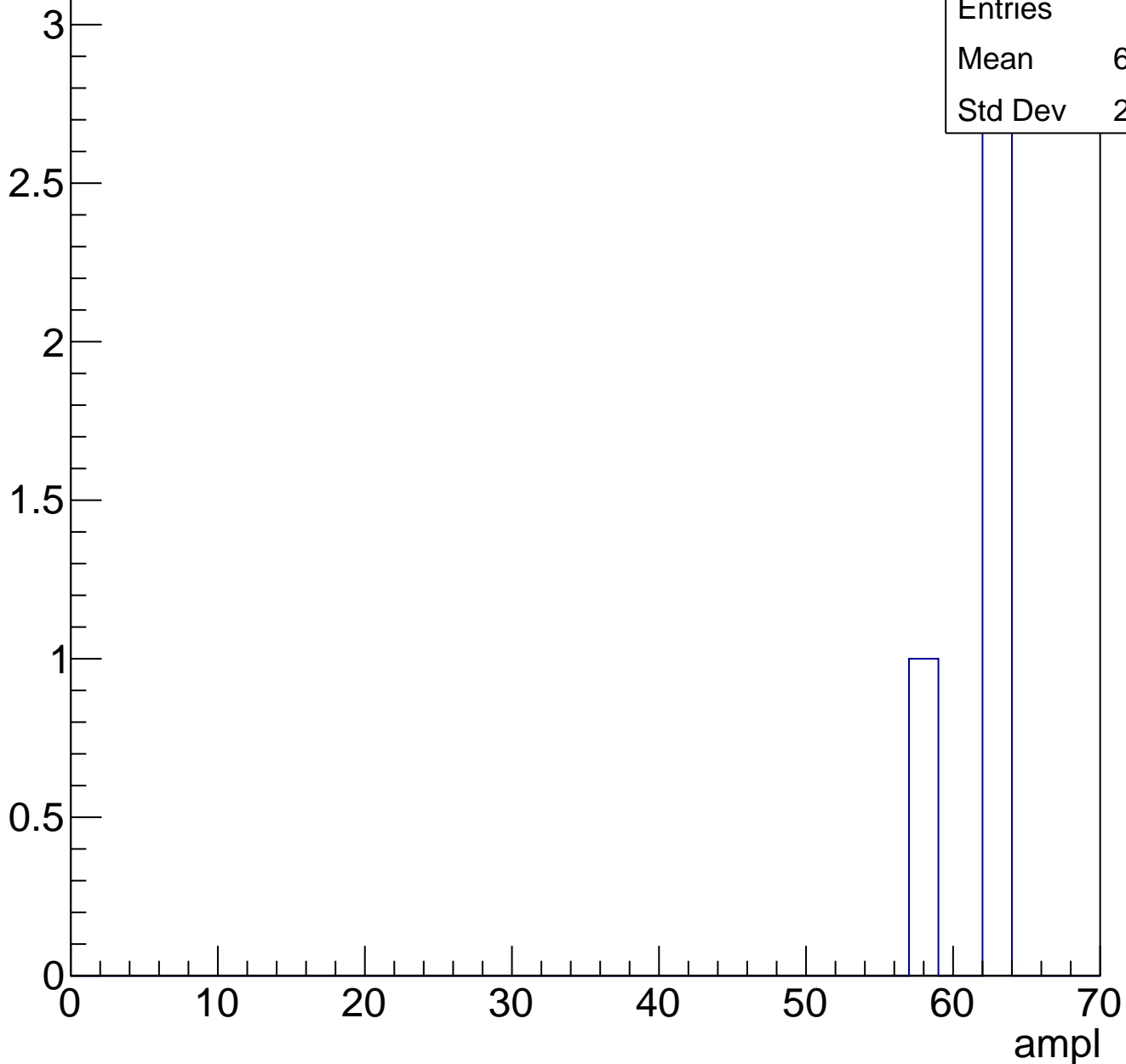
Entries	43
Mean	58.42
Std Dev	9.386



B0L001S, U14-ch19, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch19, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch20, adc0

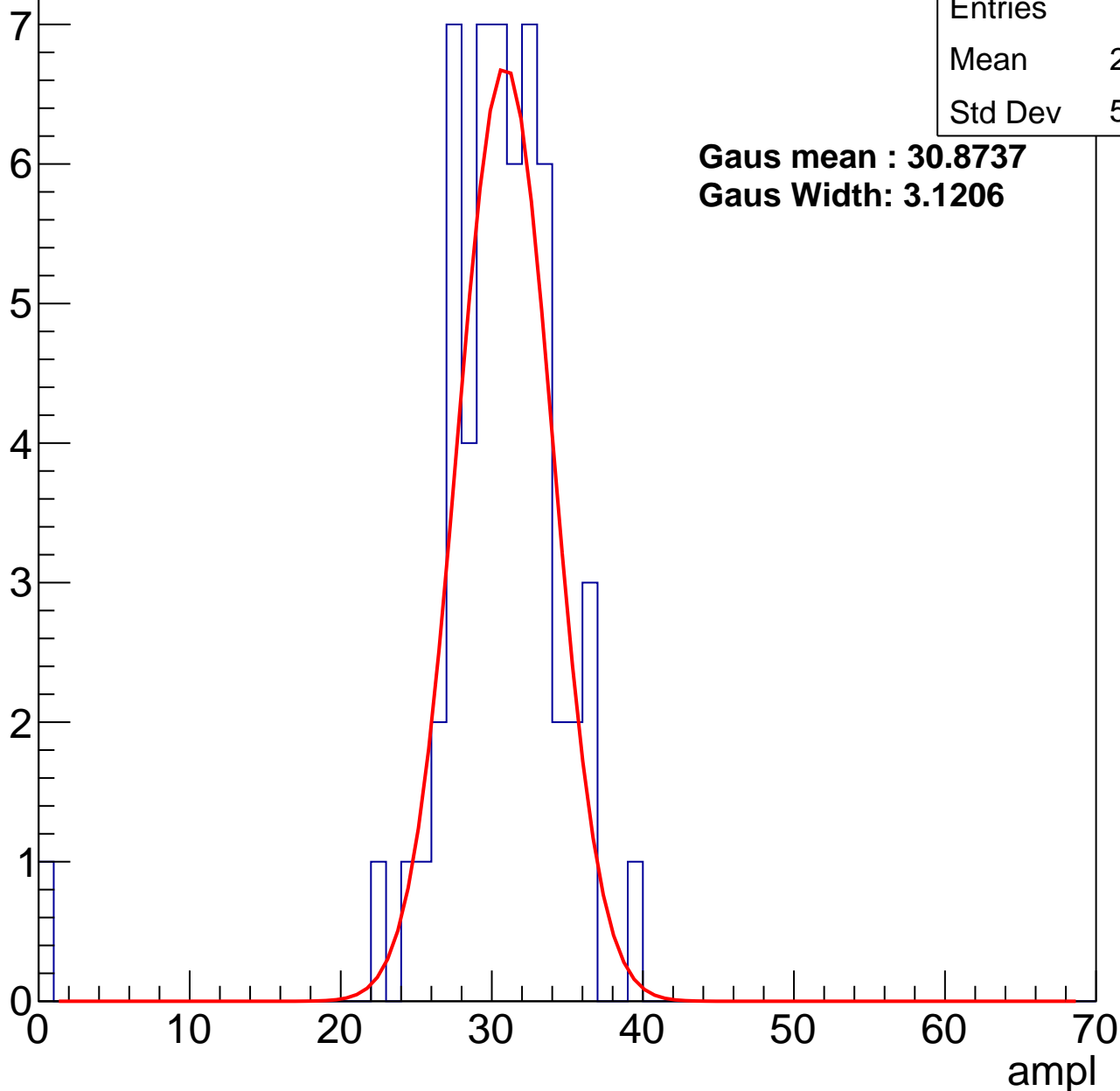
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	29.83
Std Dev	5.086

Gaus mean : 30.8737

Gaus Width: 3.1206



B0L001S, U14-ch20, adc1

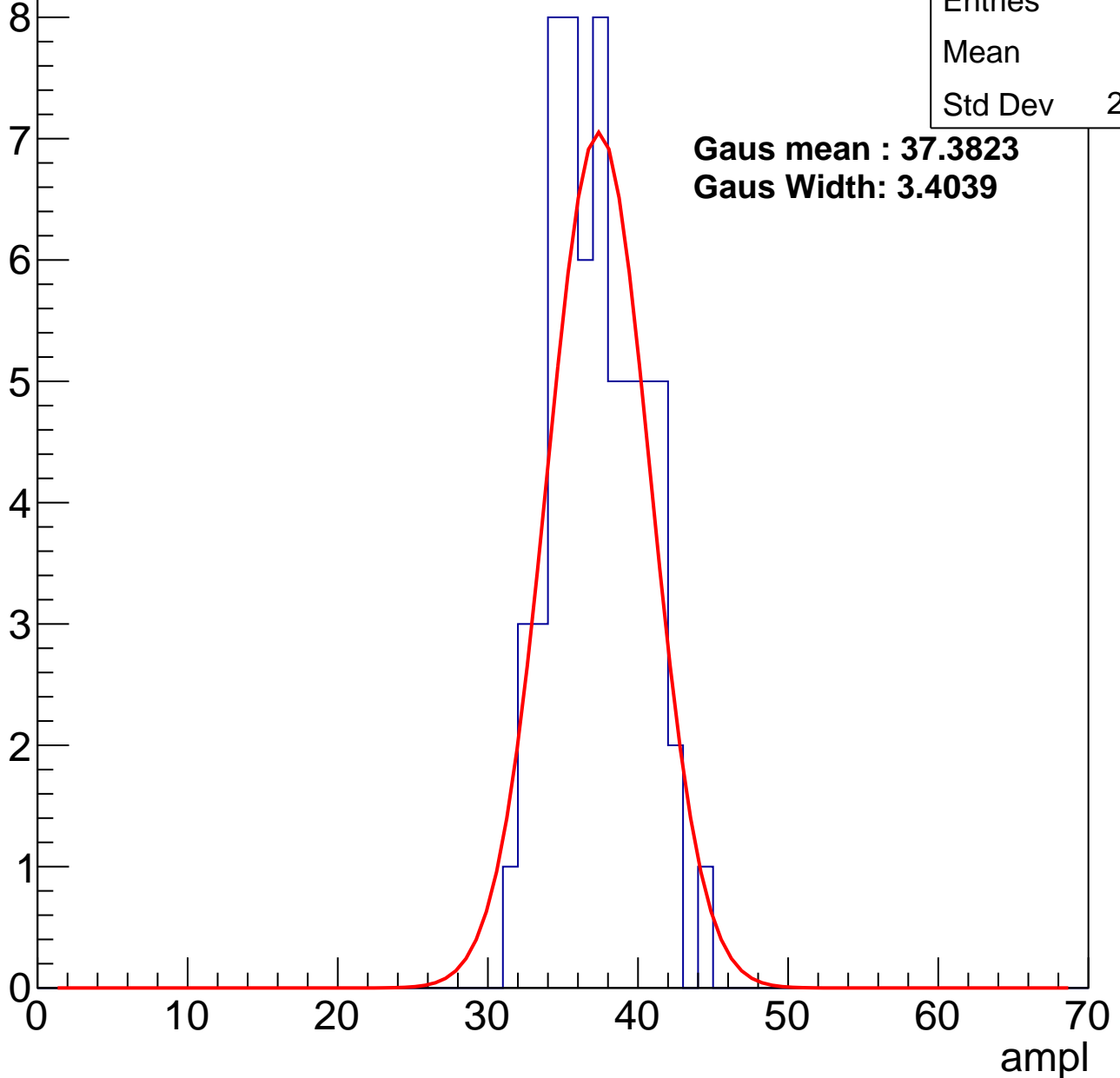
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.8
Std Dev	2.931

Gaus mean : 37.3823

Gaus Width: 3.4039



B0L001S, U14-ch20, adc2

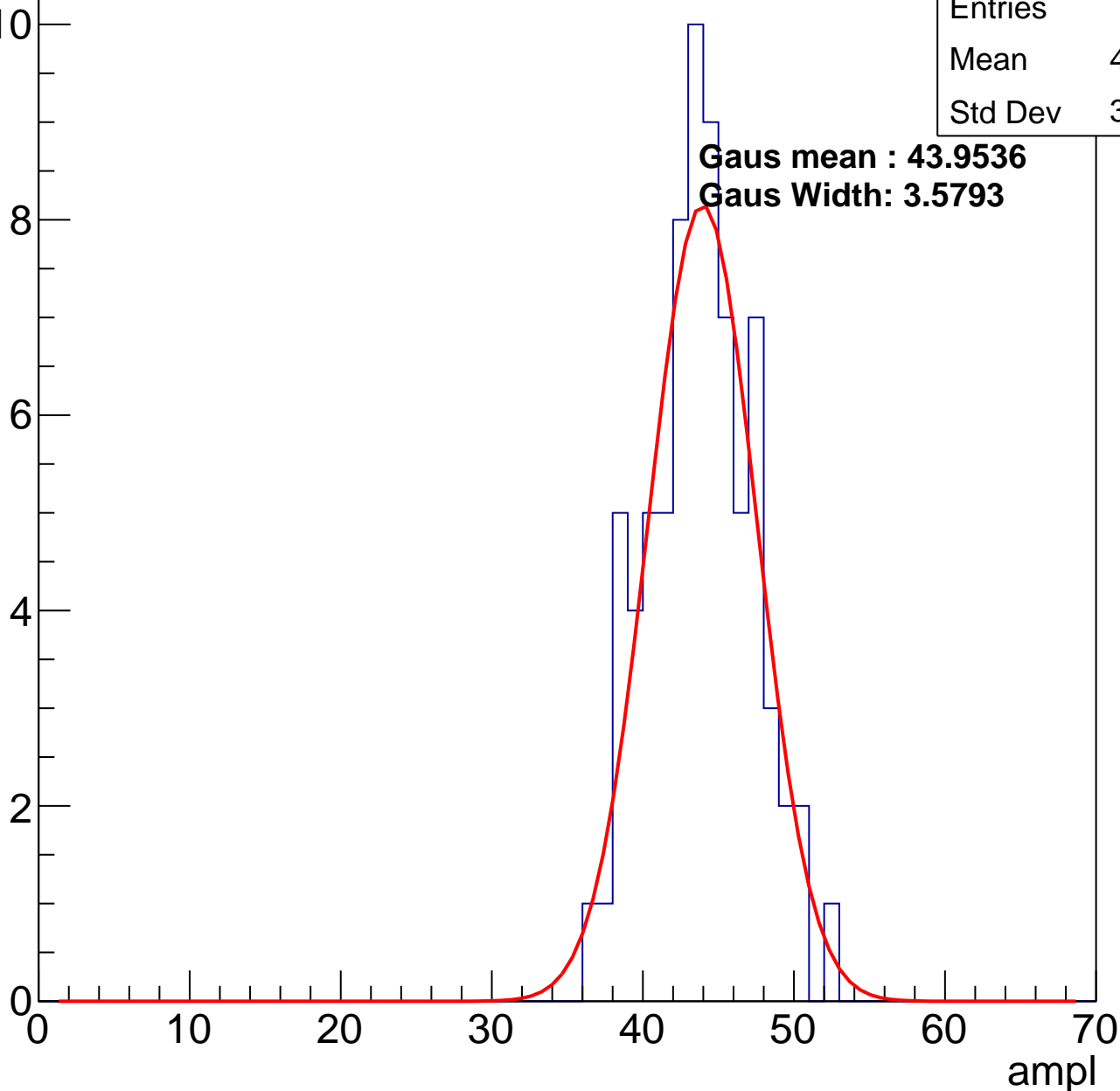
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	43.39
Std Dev	3.378

Gaus mean : 43.9536

Gaus Width: 3.5793



B0L001S, U14-ch20, adc3

calib_packv5_042523_0143.root, FC#9, port A1

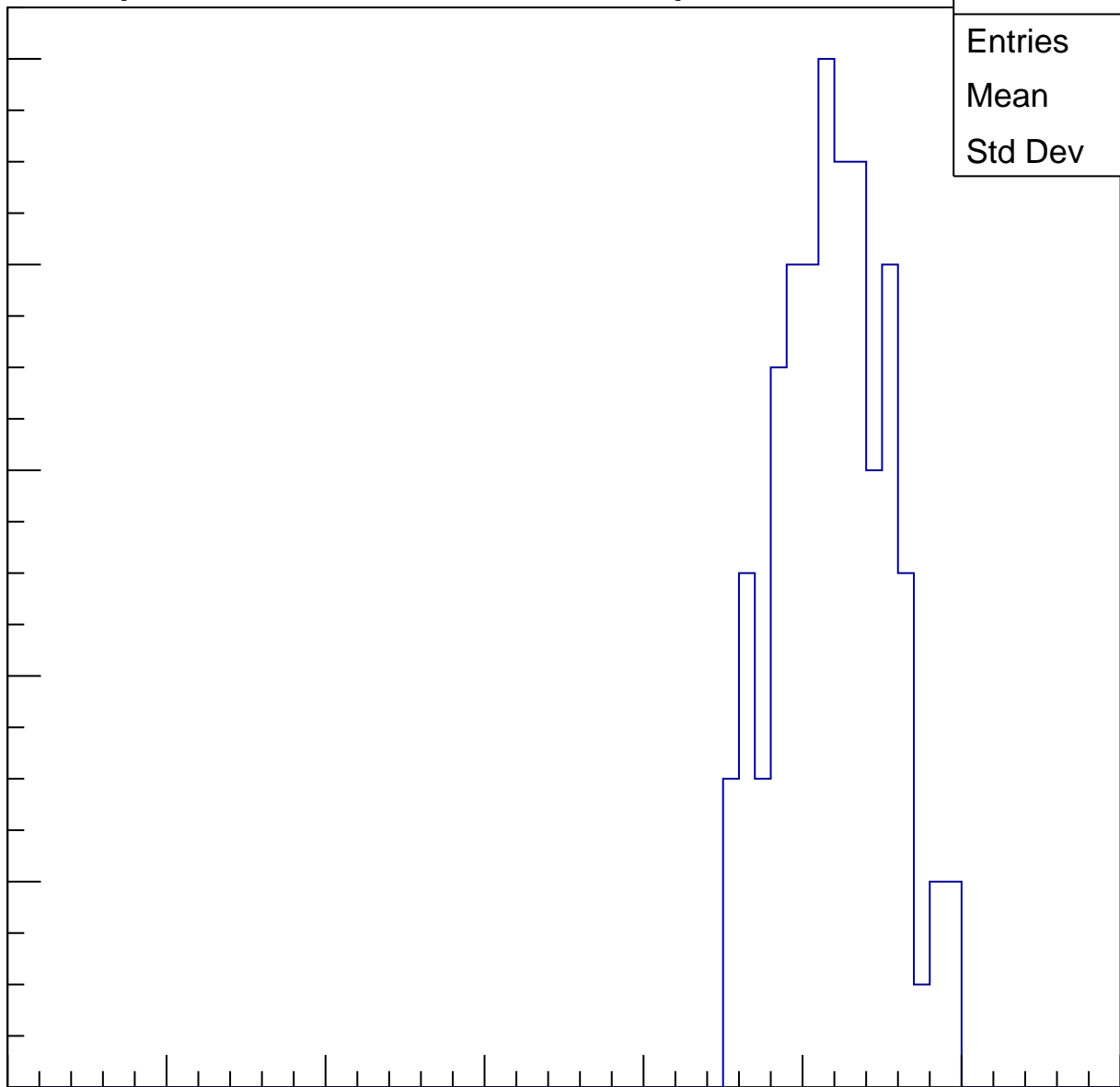
Entries	86
Mean	51.44
Std Dev	3.378

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

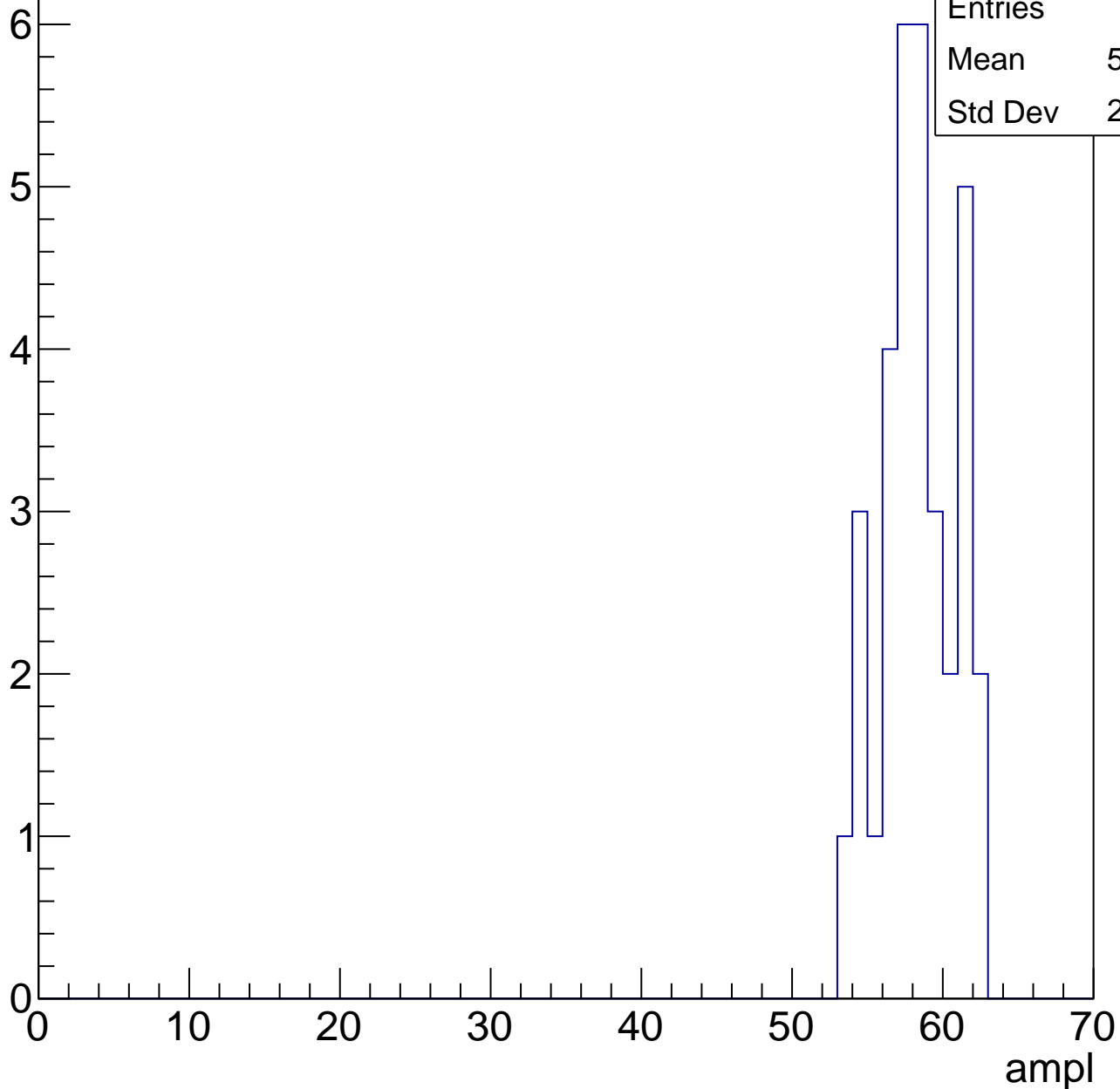


B0L001S, U14-ch20, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

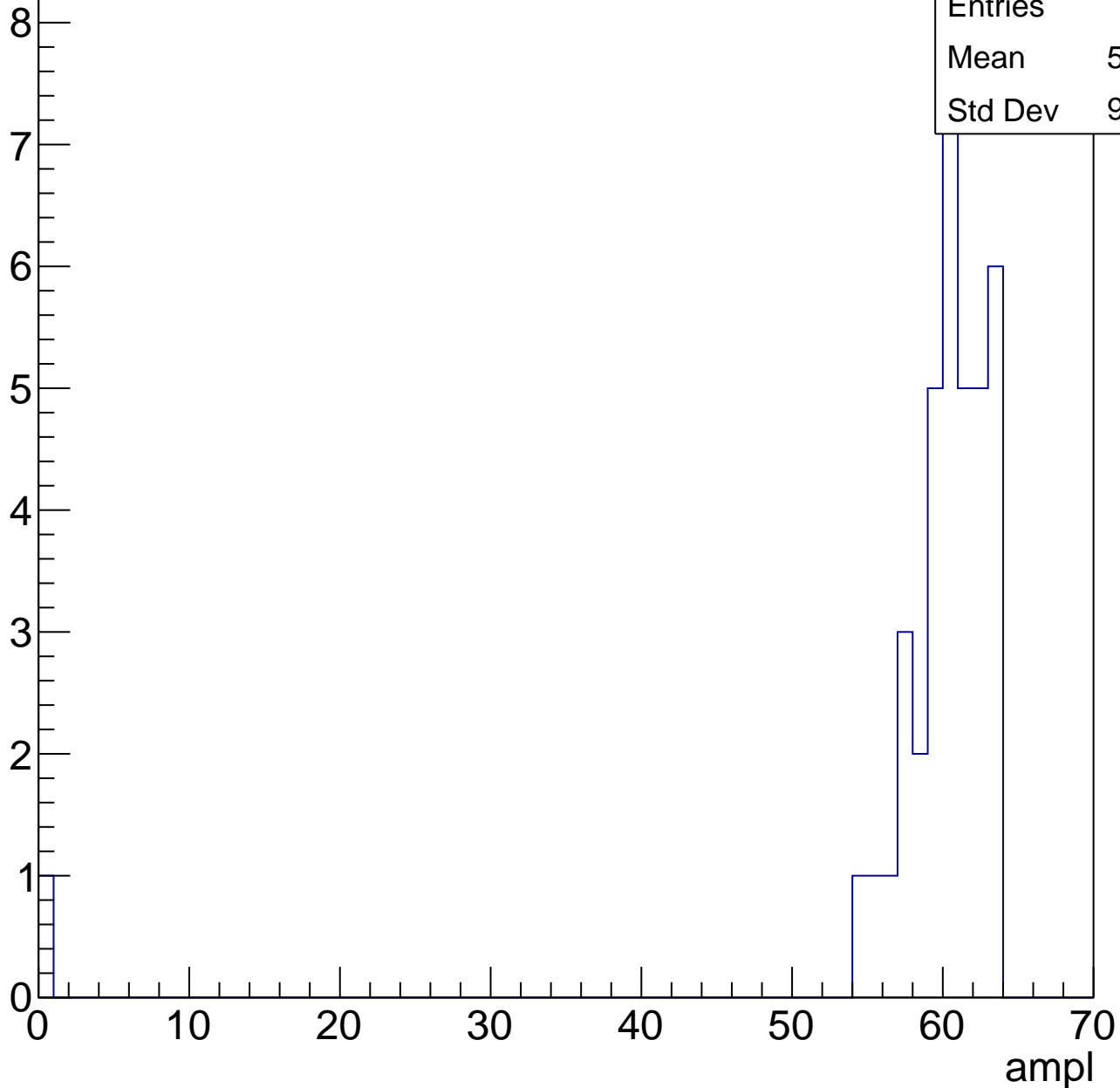
Entries	33
Mean	57.88
Std Dev	2.409



B0L001S, U14-ch20, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch20, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch20, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch21, adc0

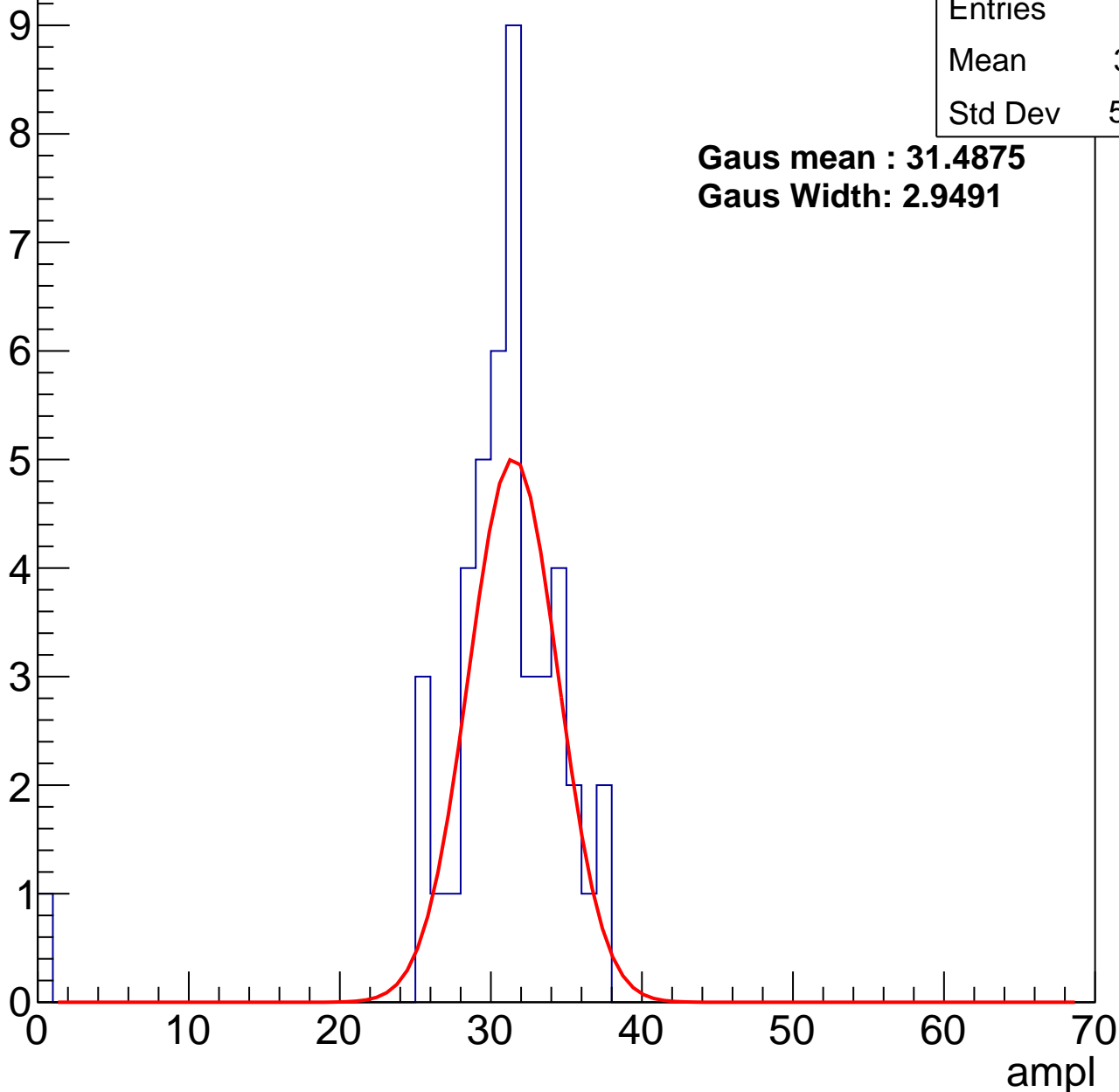
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	30.11
Std Dev	5.409

Gaus mean : 31.4875

Gaus Width: 2.9491



B0L001S, U14-ch21, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	37.94
Std Dev	3.501

Gaus mean : 38.1468

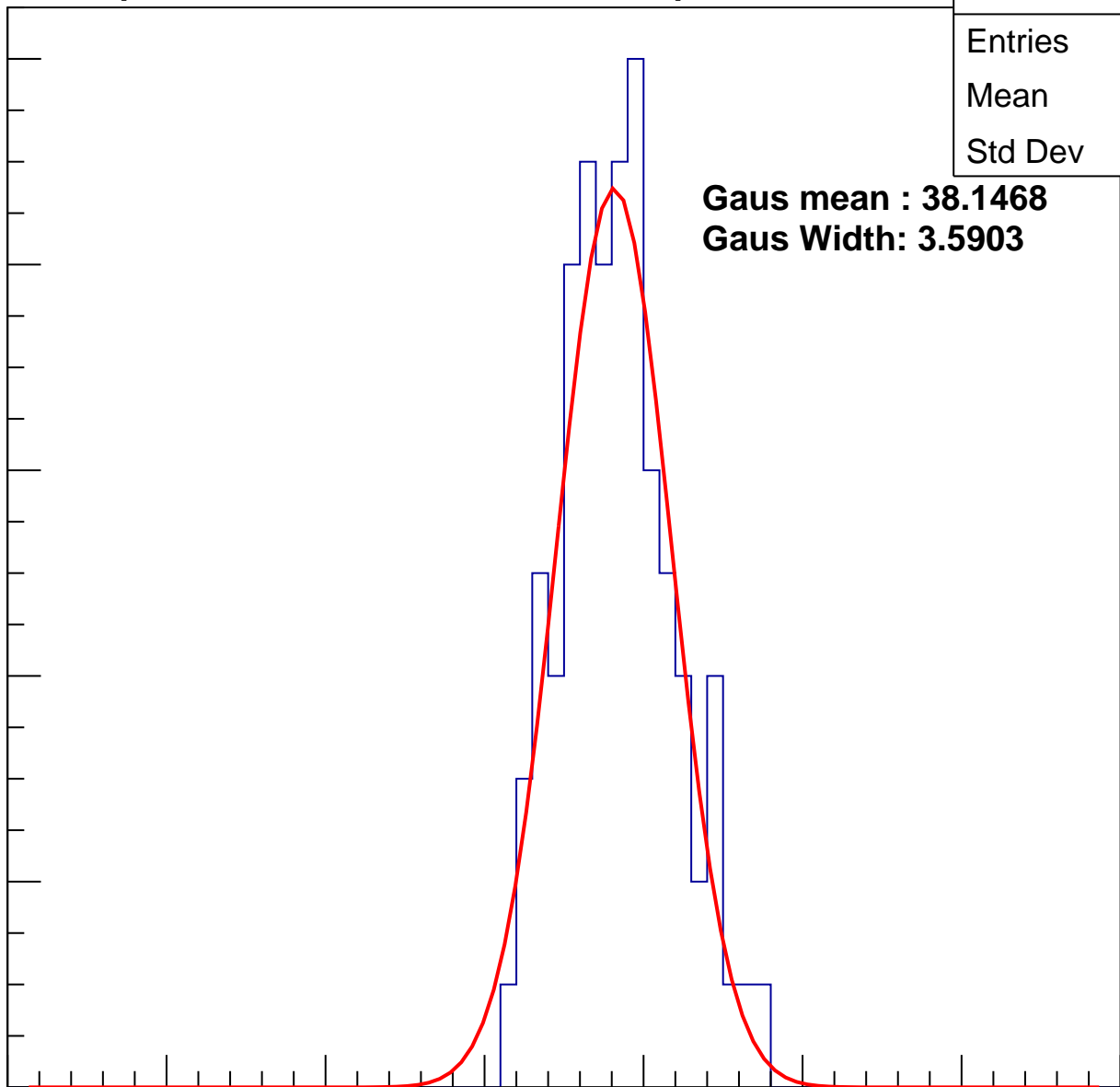
Gaus Width: 3.5903

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch21, adc2

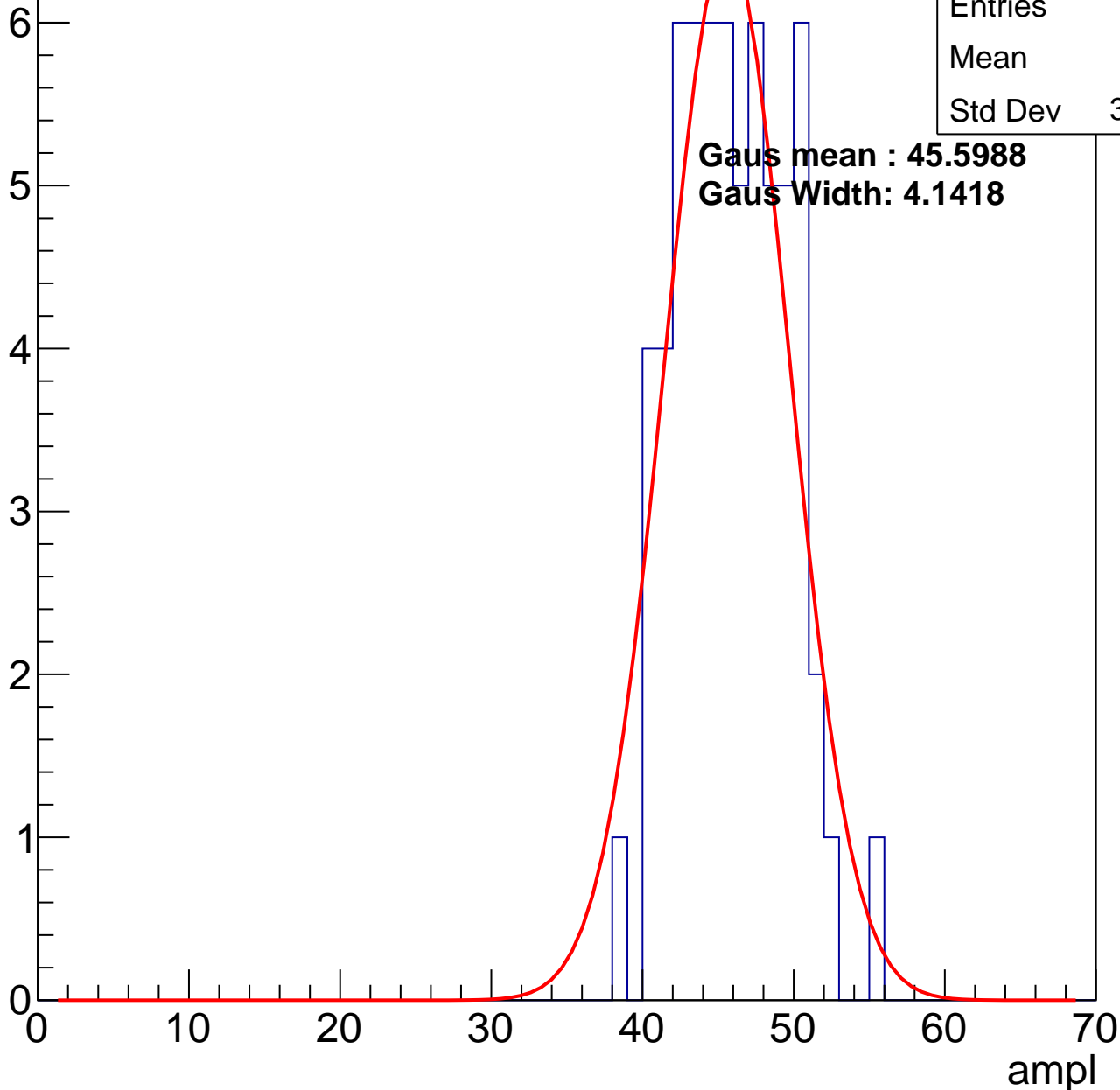
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	45.5
Std Dev	3.549

Gaus mean : 45.5988

Gaus Width: 4.1418

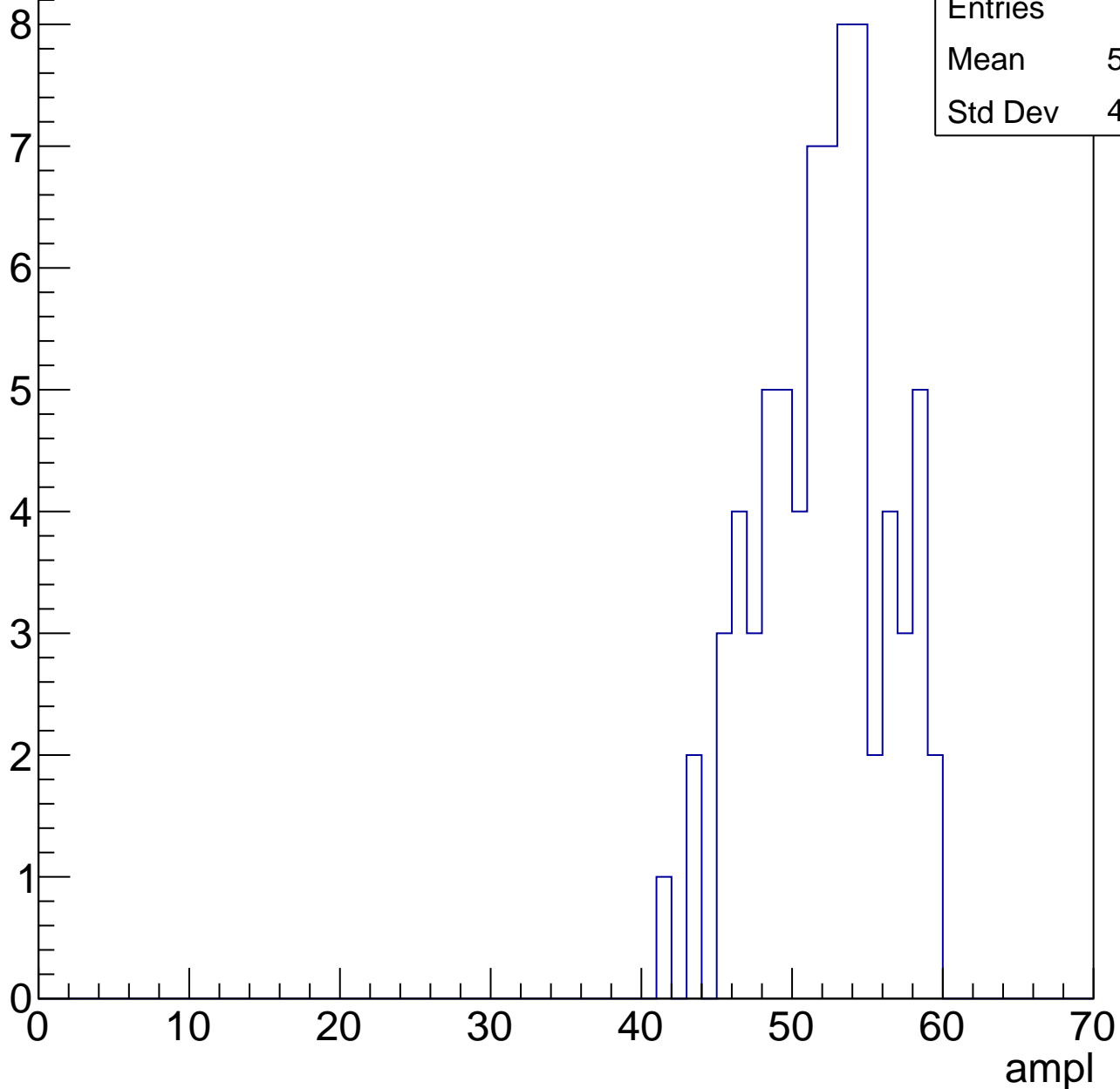


B0L001S, U14-ch21, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

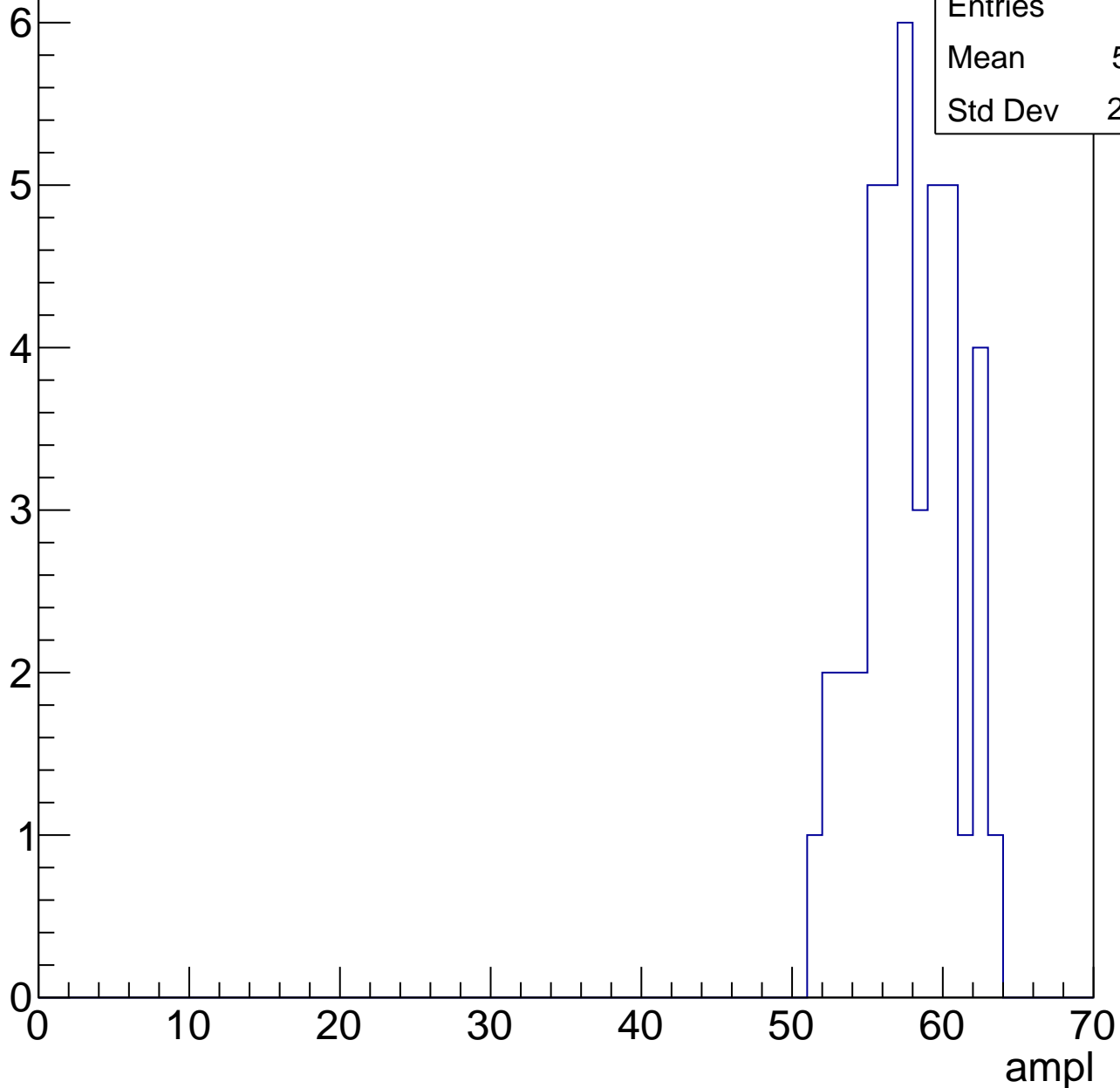
Entries	73
Mean	51.53
Std Dev	4.148



B0L001S, U14-ch21, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



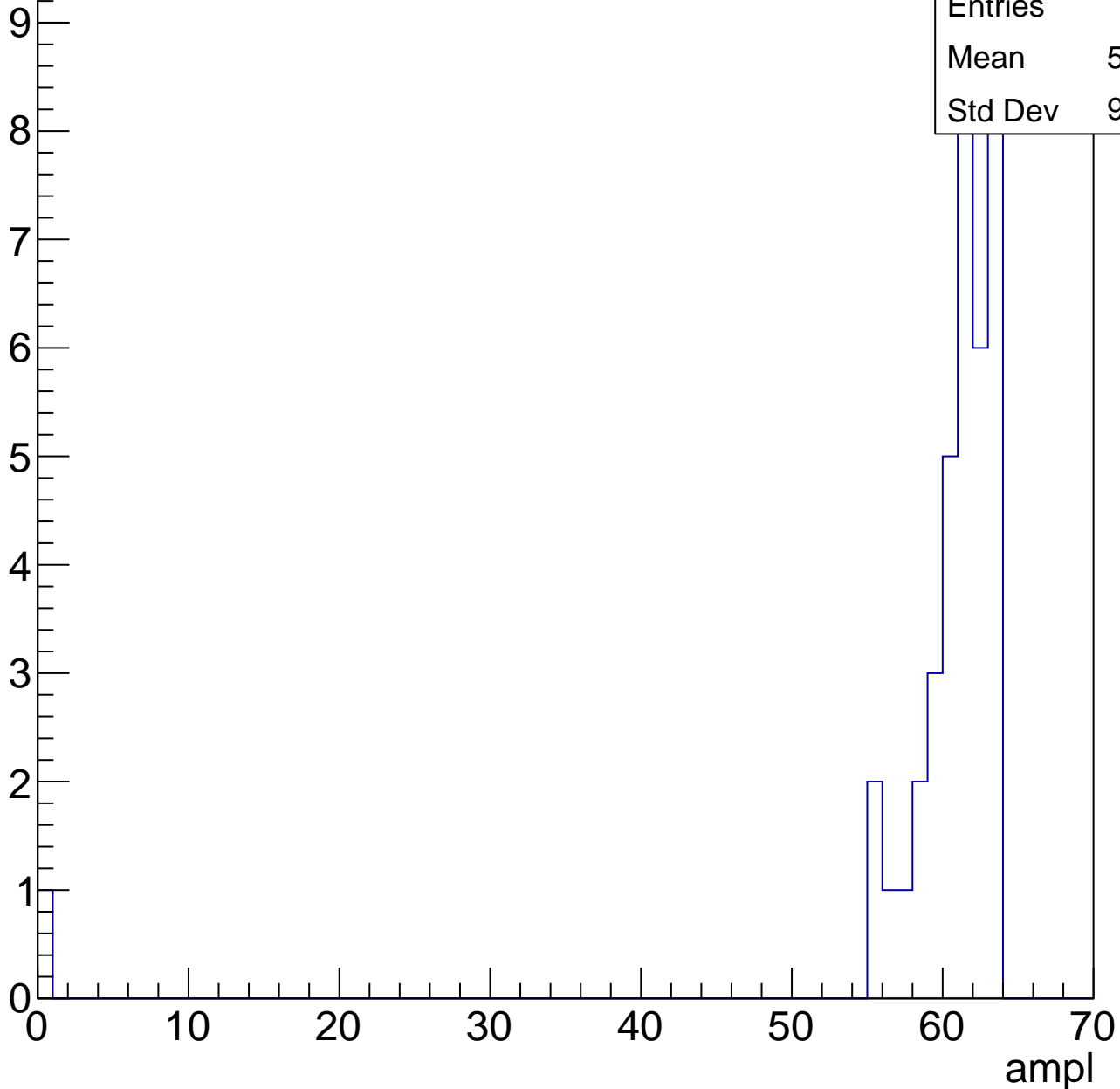
Entries	42
Mean	57.31
Std Dev	2.996

B0L001S, U14-ch21, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	39
Mean	59.08
Std Dev	9.828



B0L001S, U14-ch21, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch21, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch22, adc0

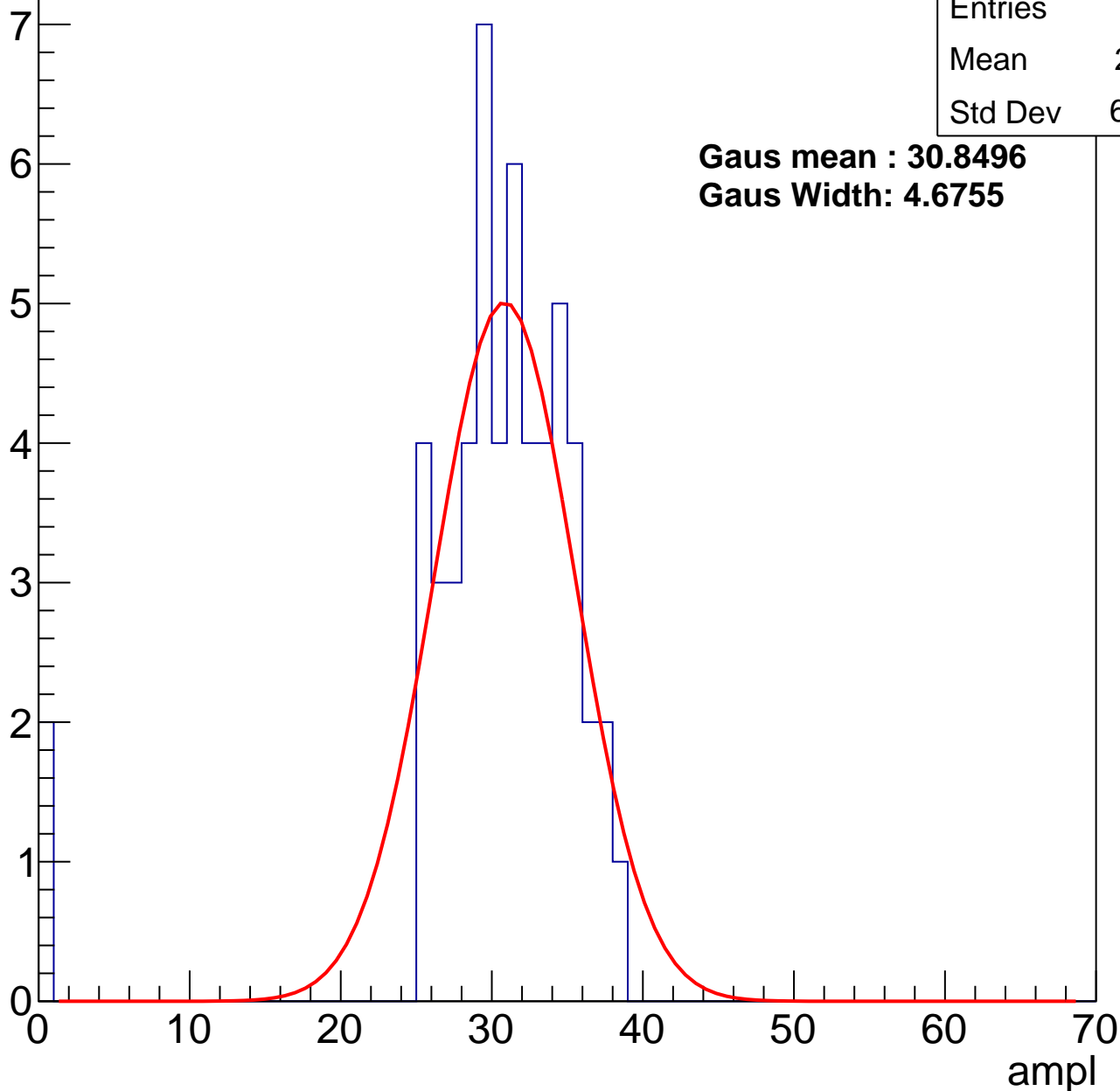
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.71
Std Dev	6.695

Gaus mean : 30.8496

Gaus Width: 4.6755



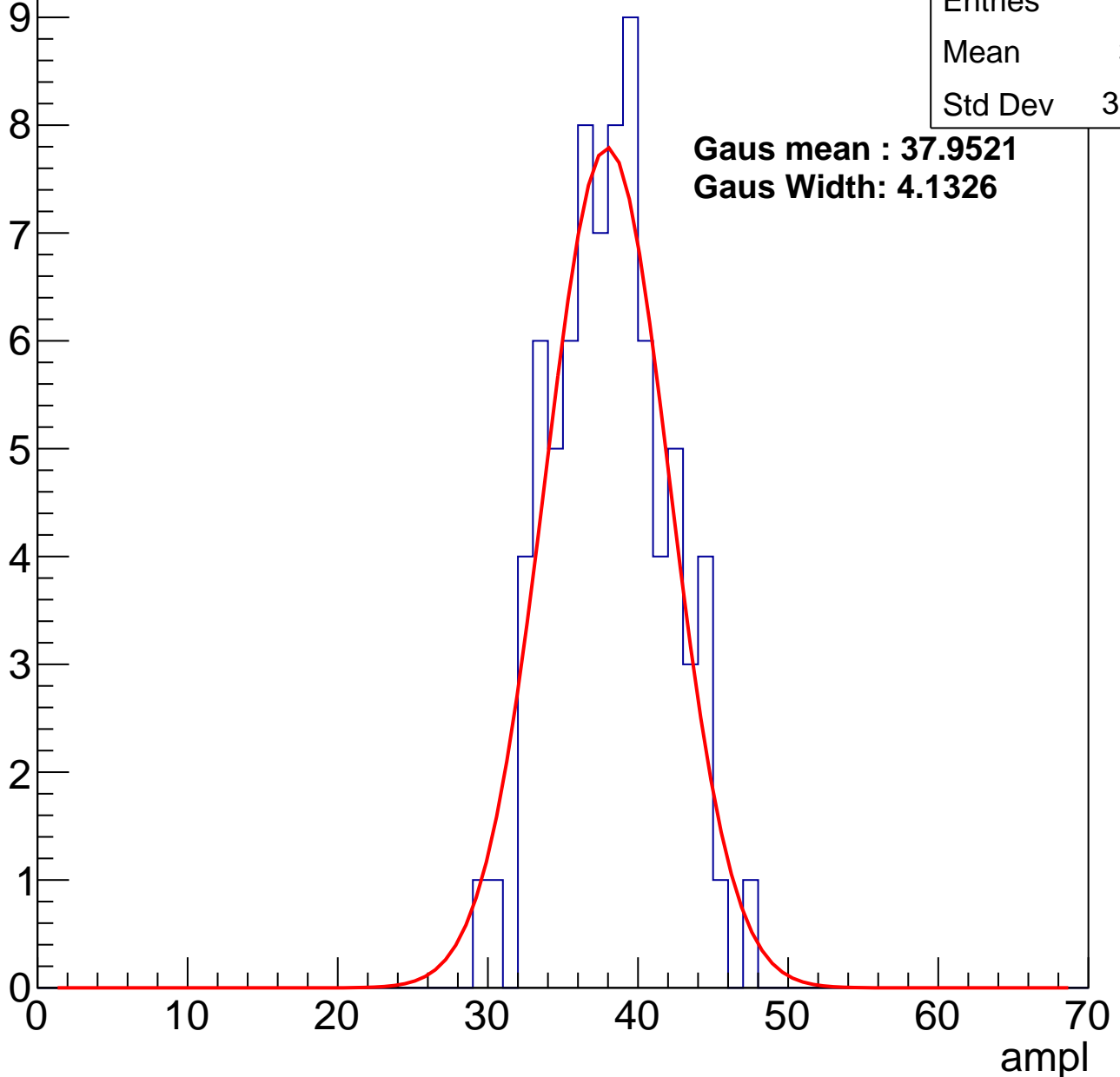
B0L001S, U14-ch22, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	37.7
Std Dev	3.736

Gaus mean : 37.9521
Gaus Width: 4.1326



B0L001S, U14-ch22, adc2

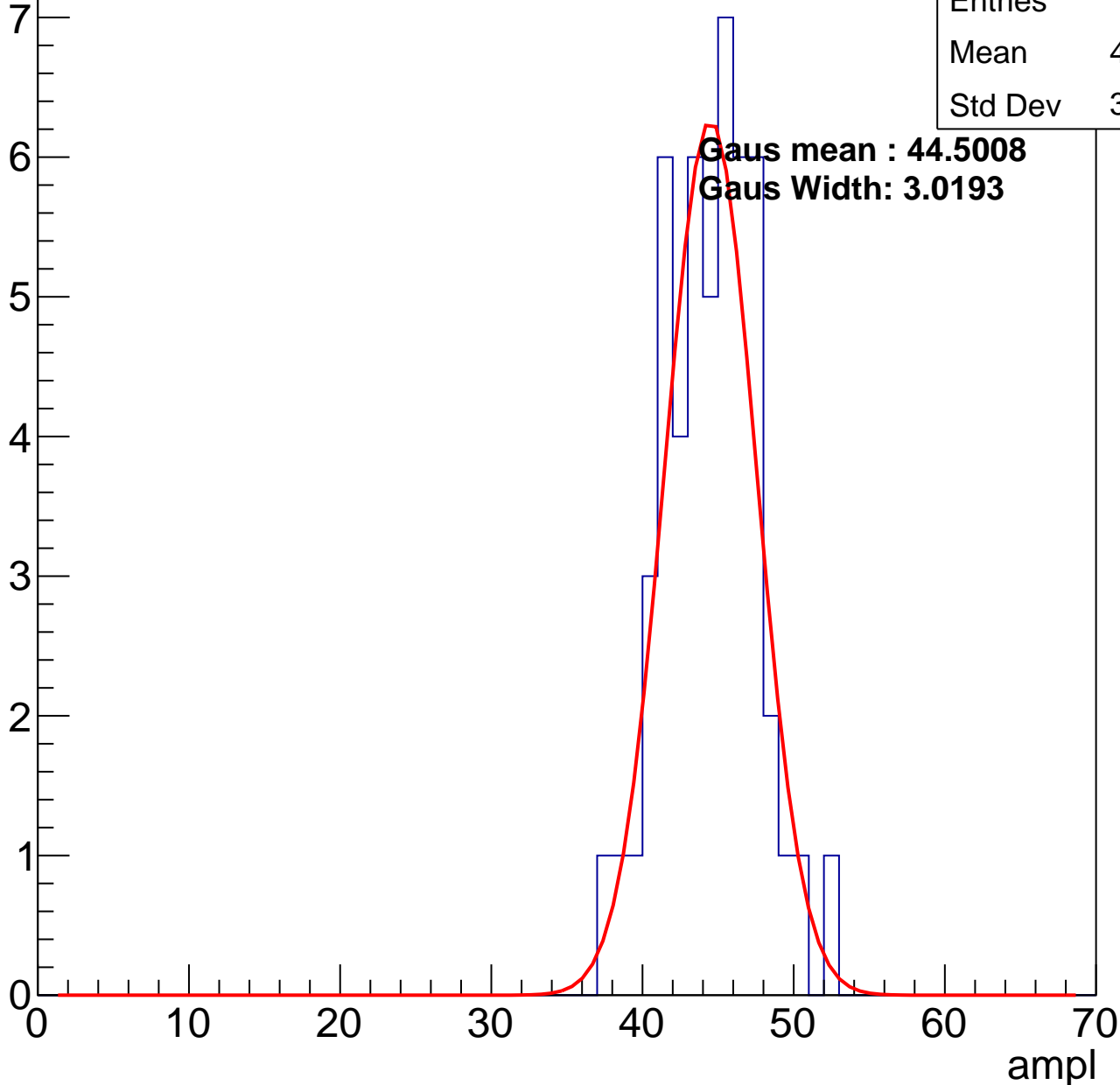
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.04
Std Dev	3.055

Gaus mean : 44.5008

Gaus Width: 3.0193

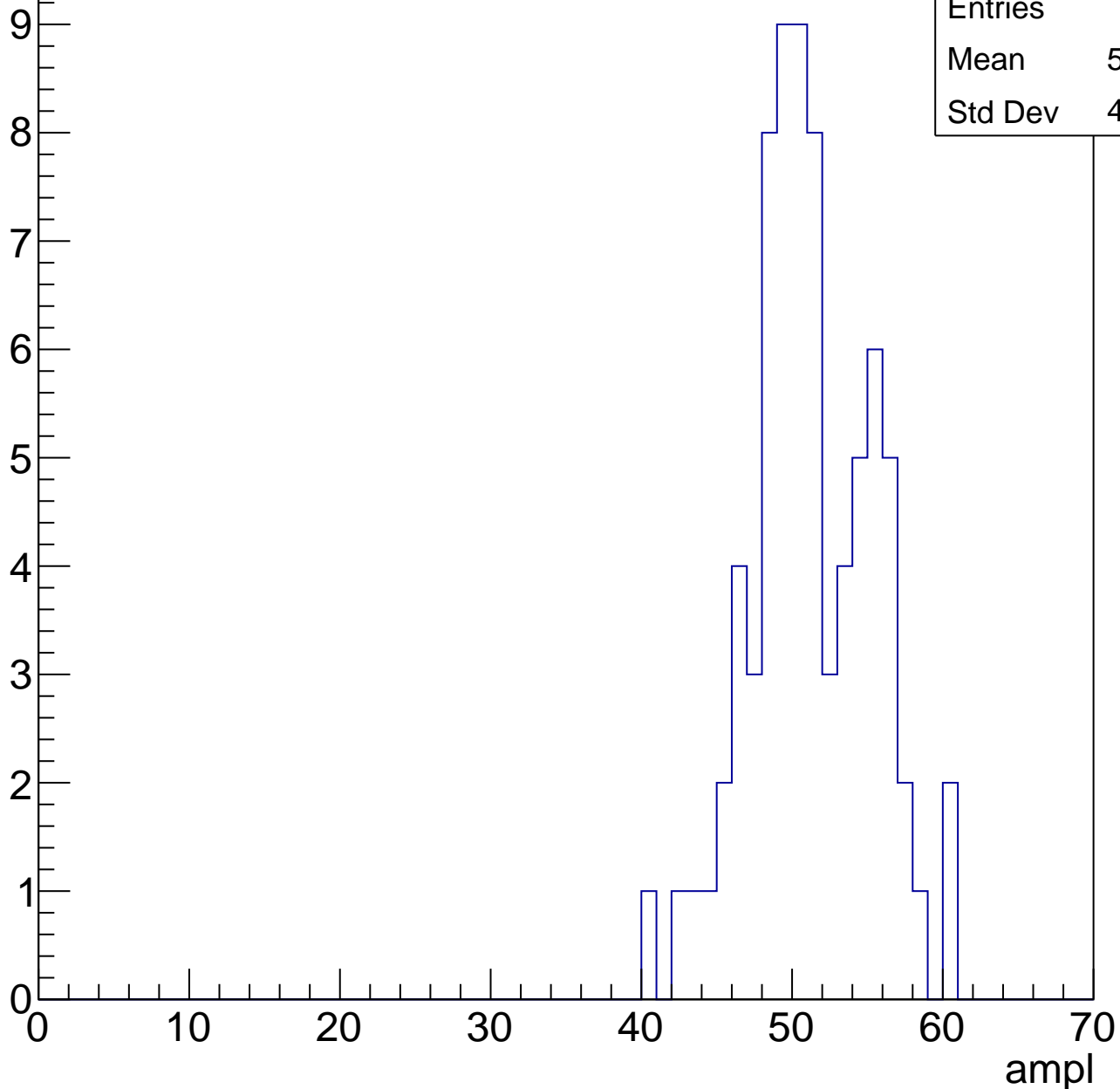


B0L001S, U14-ch22, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	50.76
Std Dev	4.026

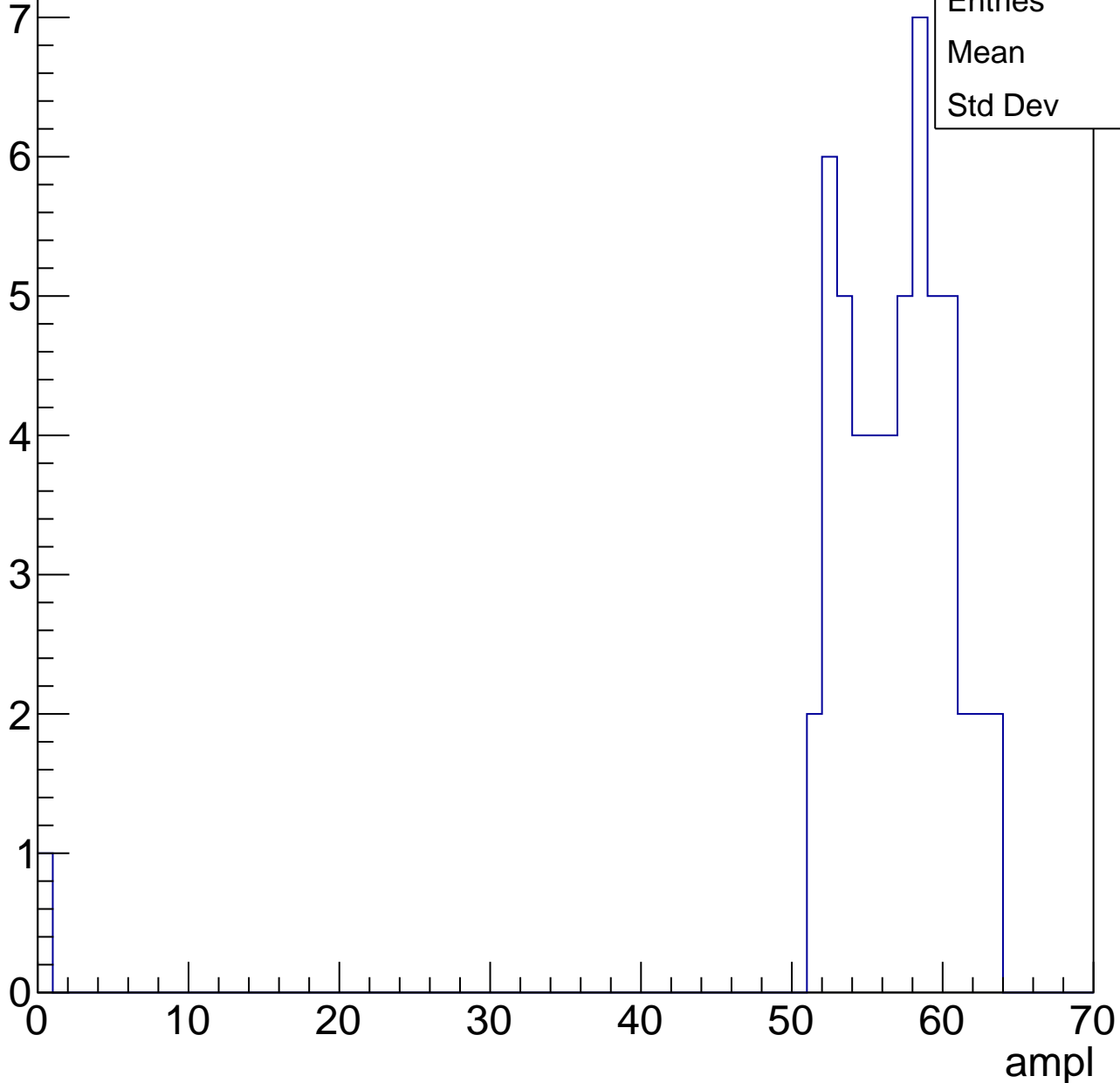


B0L001S, U14-ch22, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.5
Std Dev	8.29

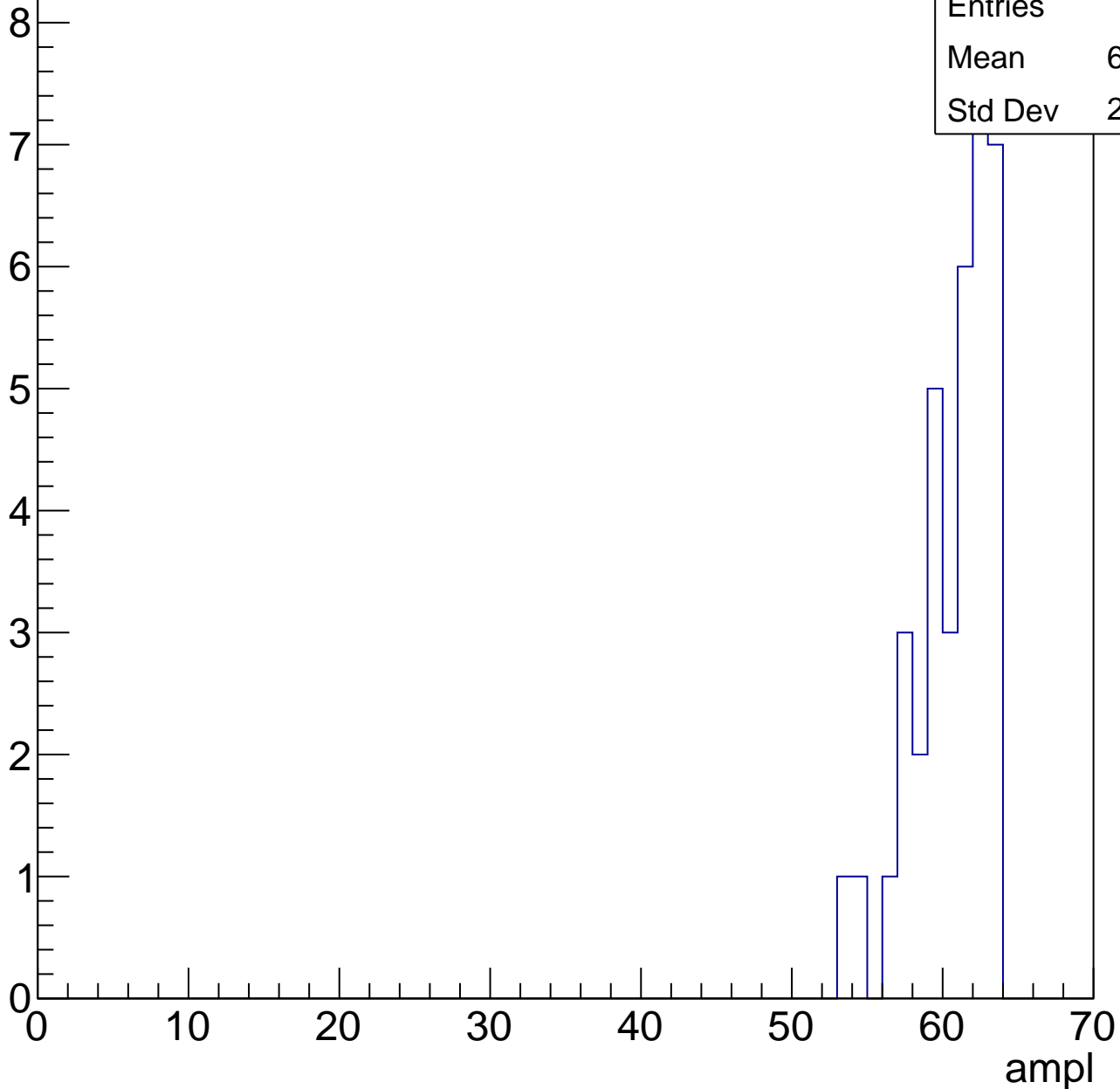


B0L001S, U14-ch22, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	60.22
Std Dev	2.548



B0L001S, U14-ch22, adc6

calib_packv5_042523_0143.root, FC#9, port A1

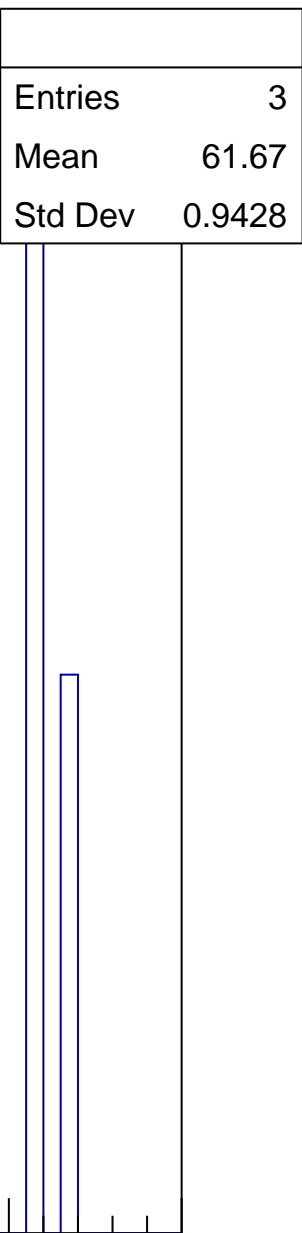
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch22, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch23, adc0

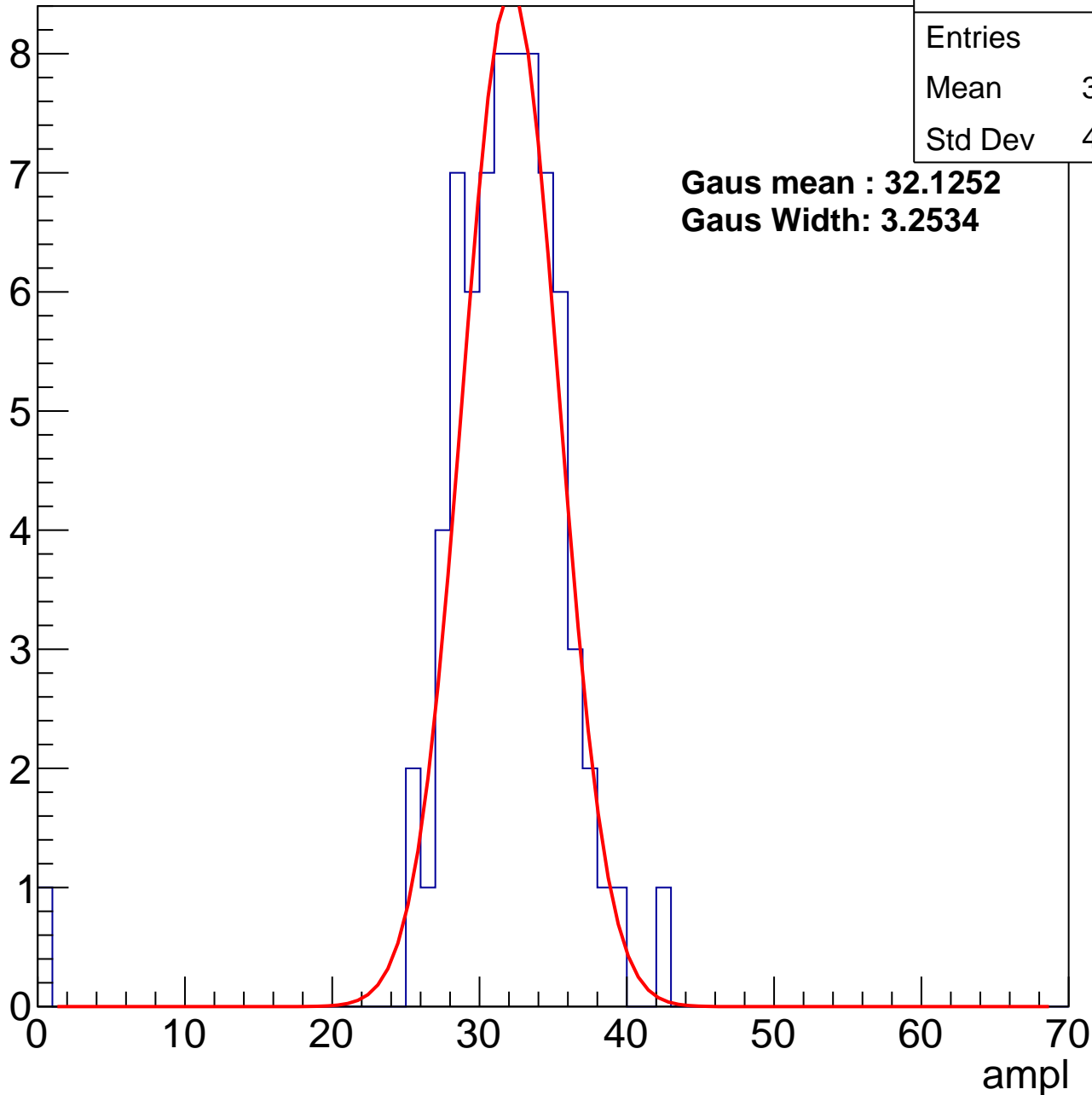
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	31.25
Std Dev	4.954

Gaus mean : 32.1252

Gaus Width: 3.2534



B0L001S, U14-ch23, adc1

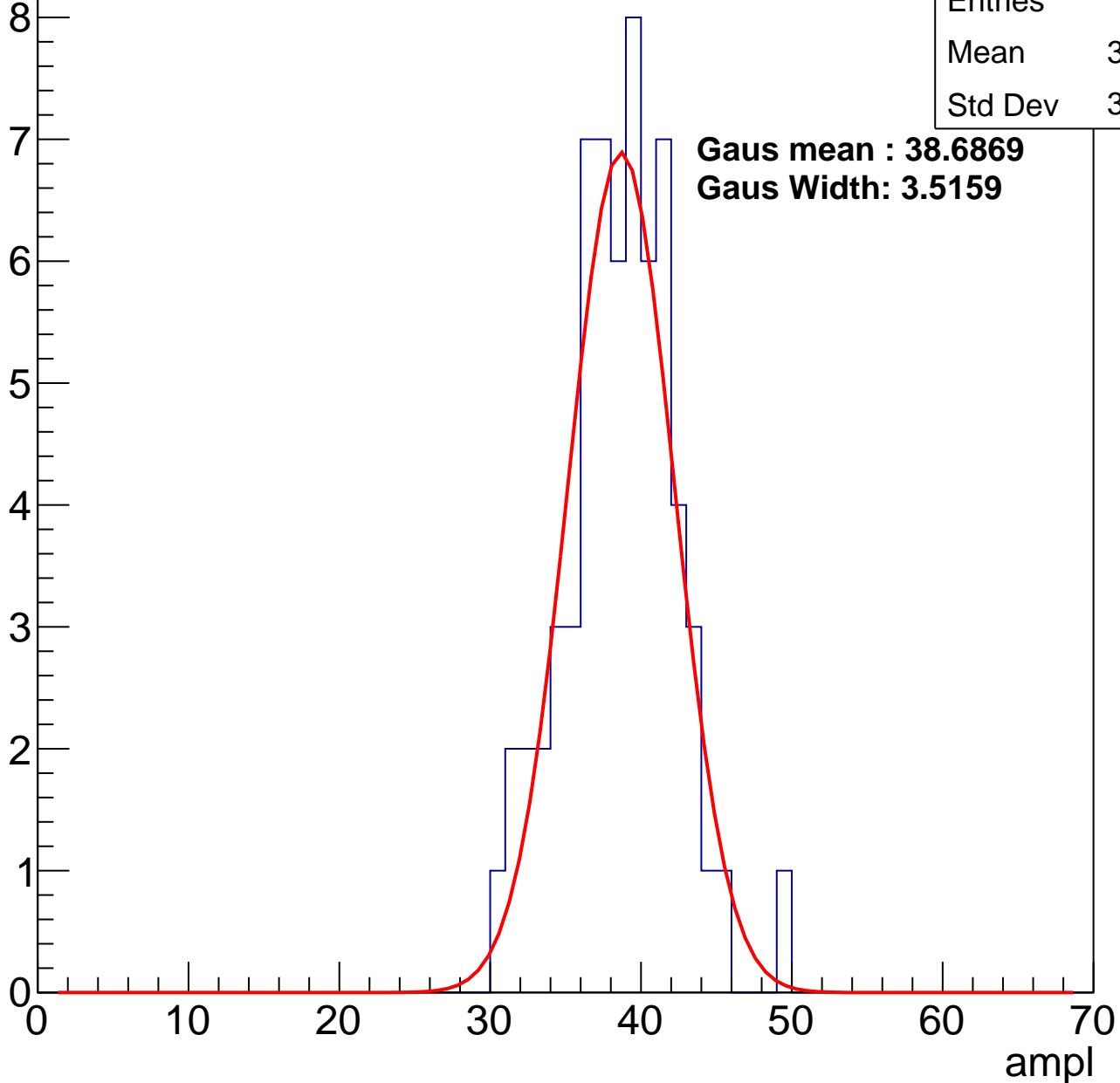
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	38.16
Std Dev	3.606

Gaus mean : 38.6869

Gaus Width: 3.5159



B0L001S, U14-ch23, adc2

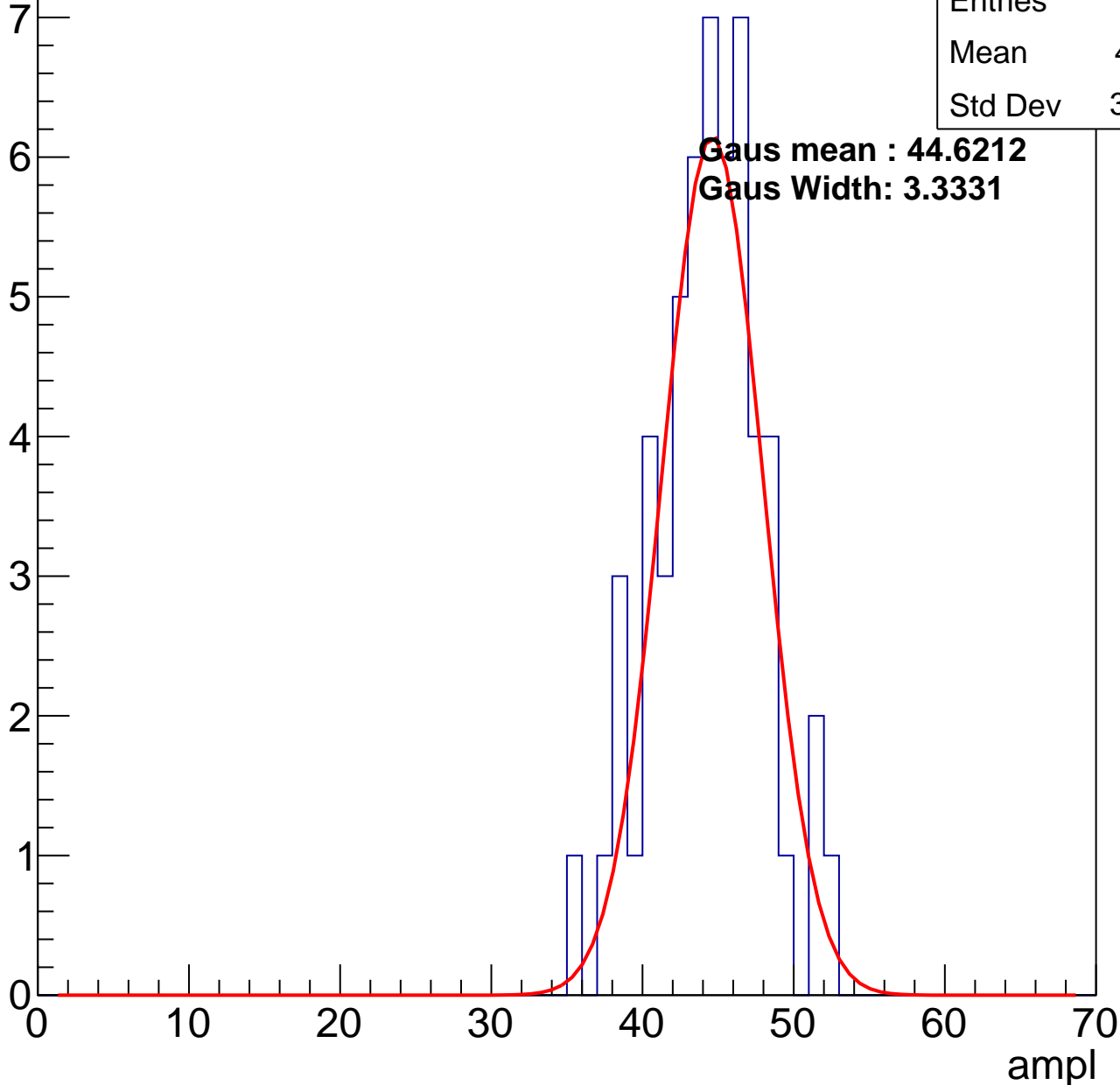
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.91
Std Dev	3.537

Gaus mean : 44.6212

Gaus Width: 3.3331

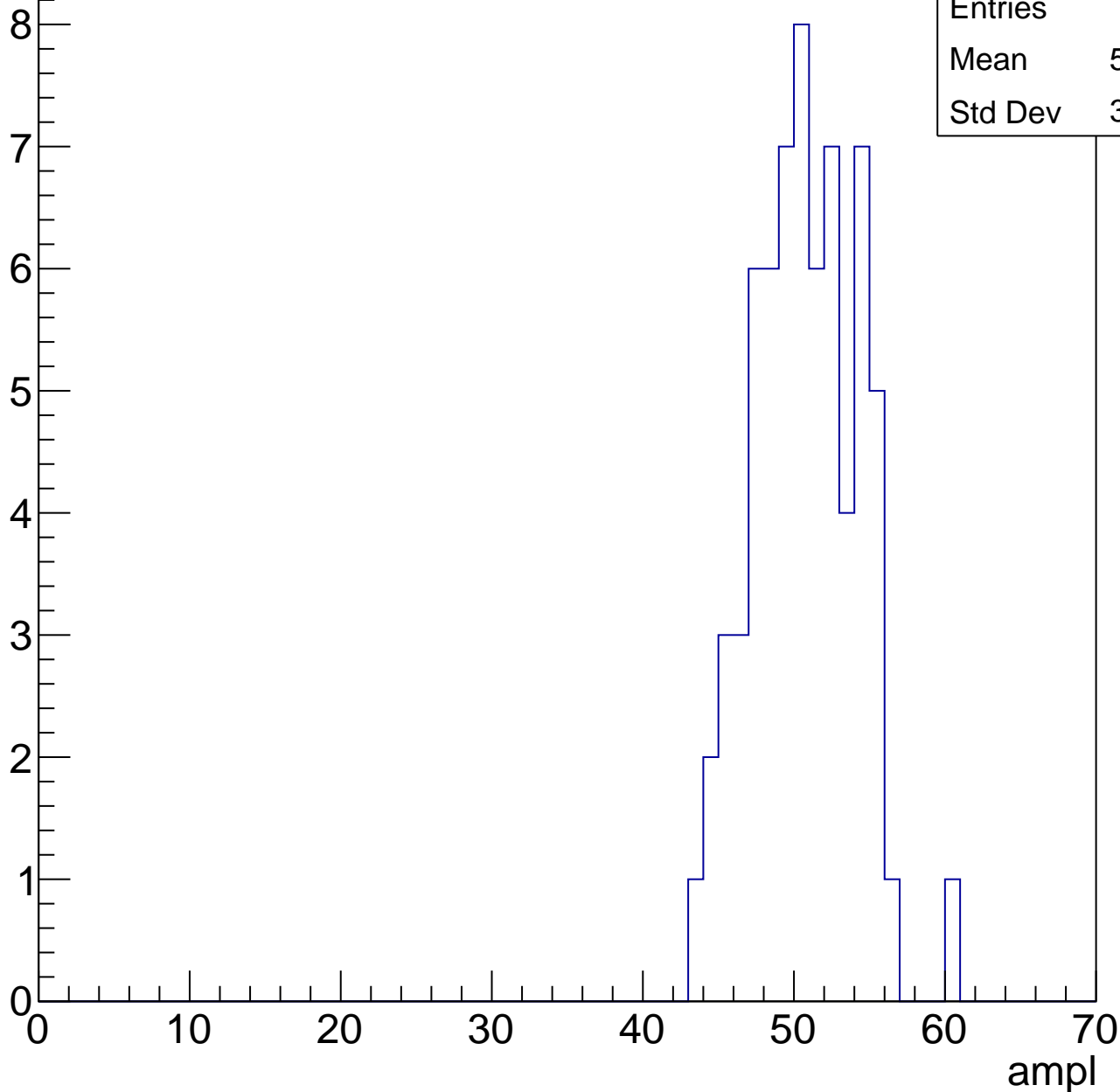


B0L001S, U14-ch23, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.27
Std Dev	3.384

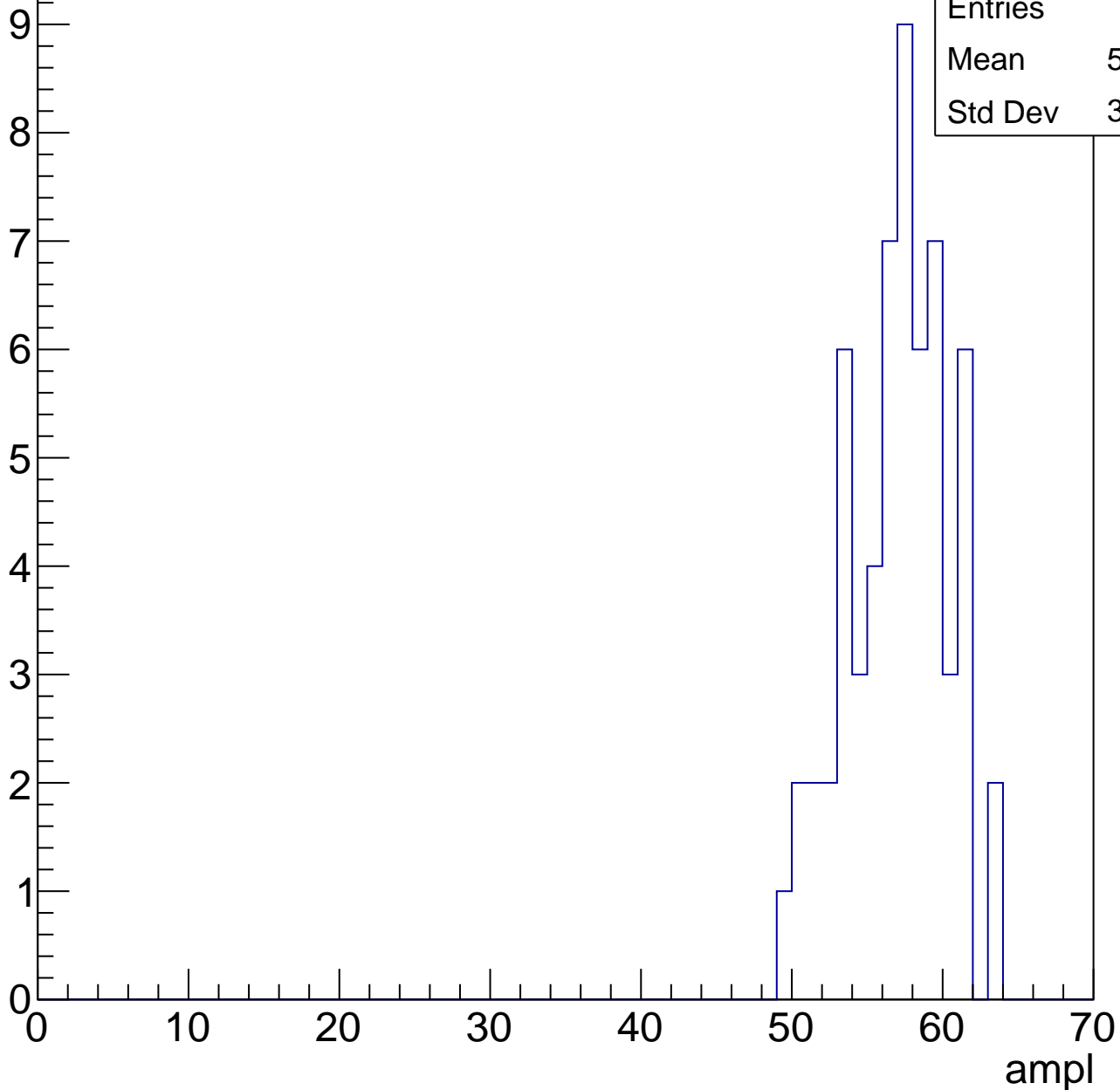


B0L001S, U14-ch23, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.55
Std Dev	3.283

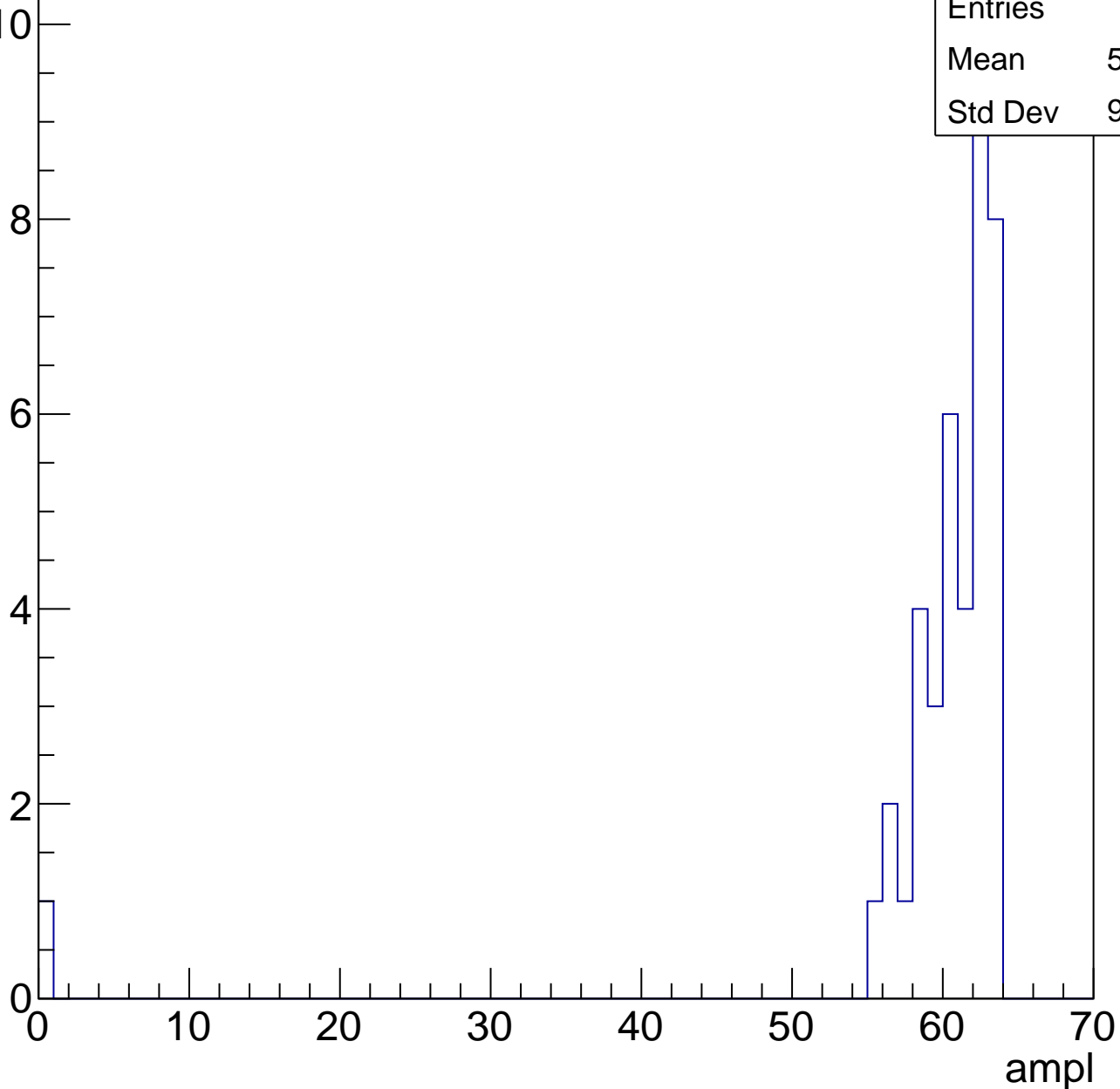


B0L001S, U14-ch23, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	40
Mean	59.02
Std Dev	9.699



B0L001S, U14-ch23, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch23, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L001S, U14-ch24, adc0

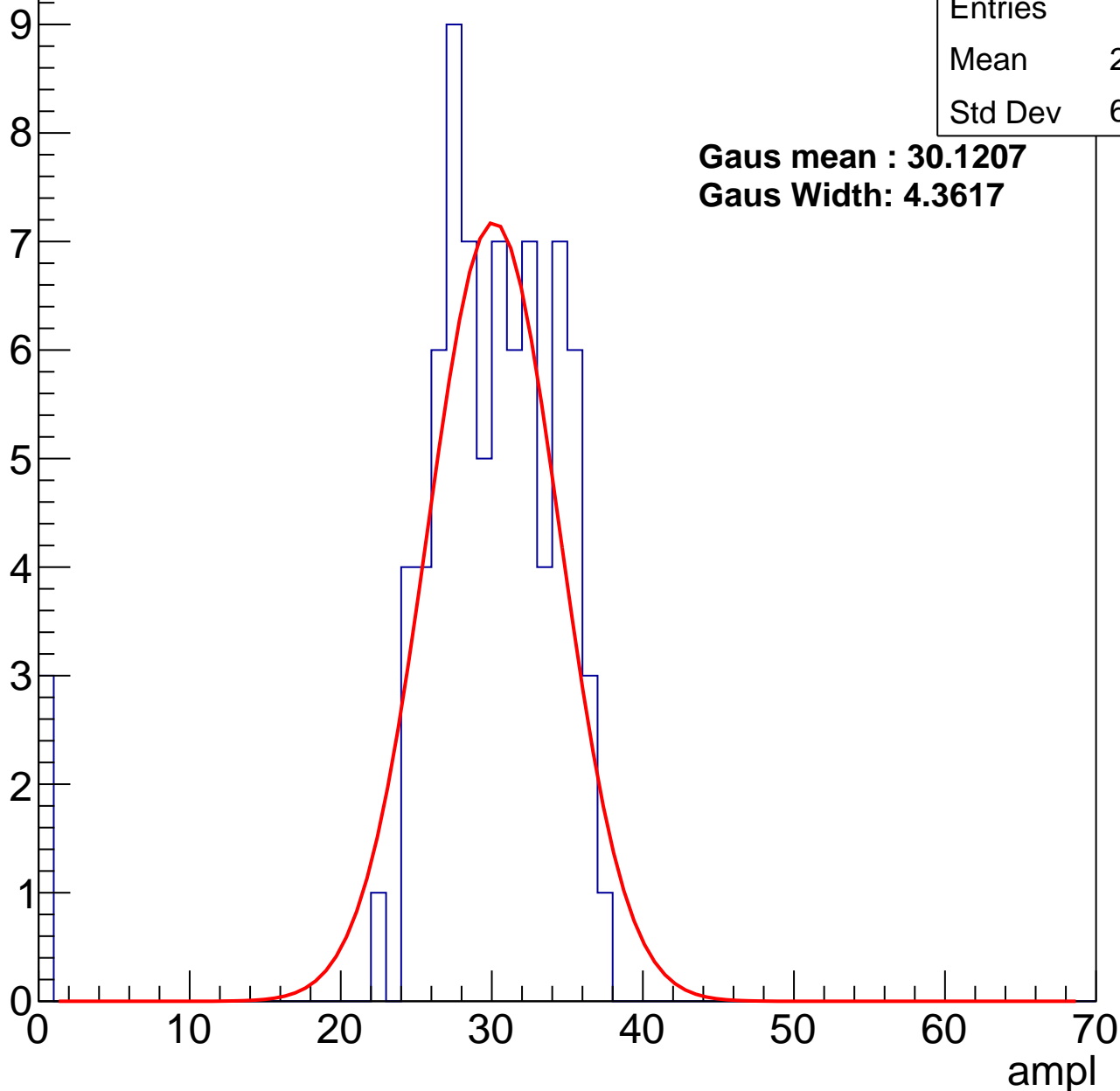
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	28.79
Std Dev	6.698

Gaus mean : 30.1207

Gaus Width: 4.3617



B0L001S, U14-ch24, adc1

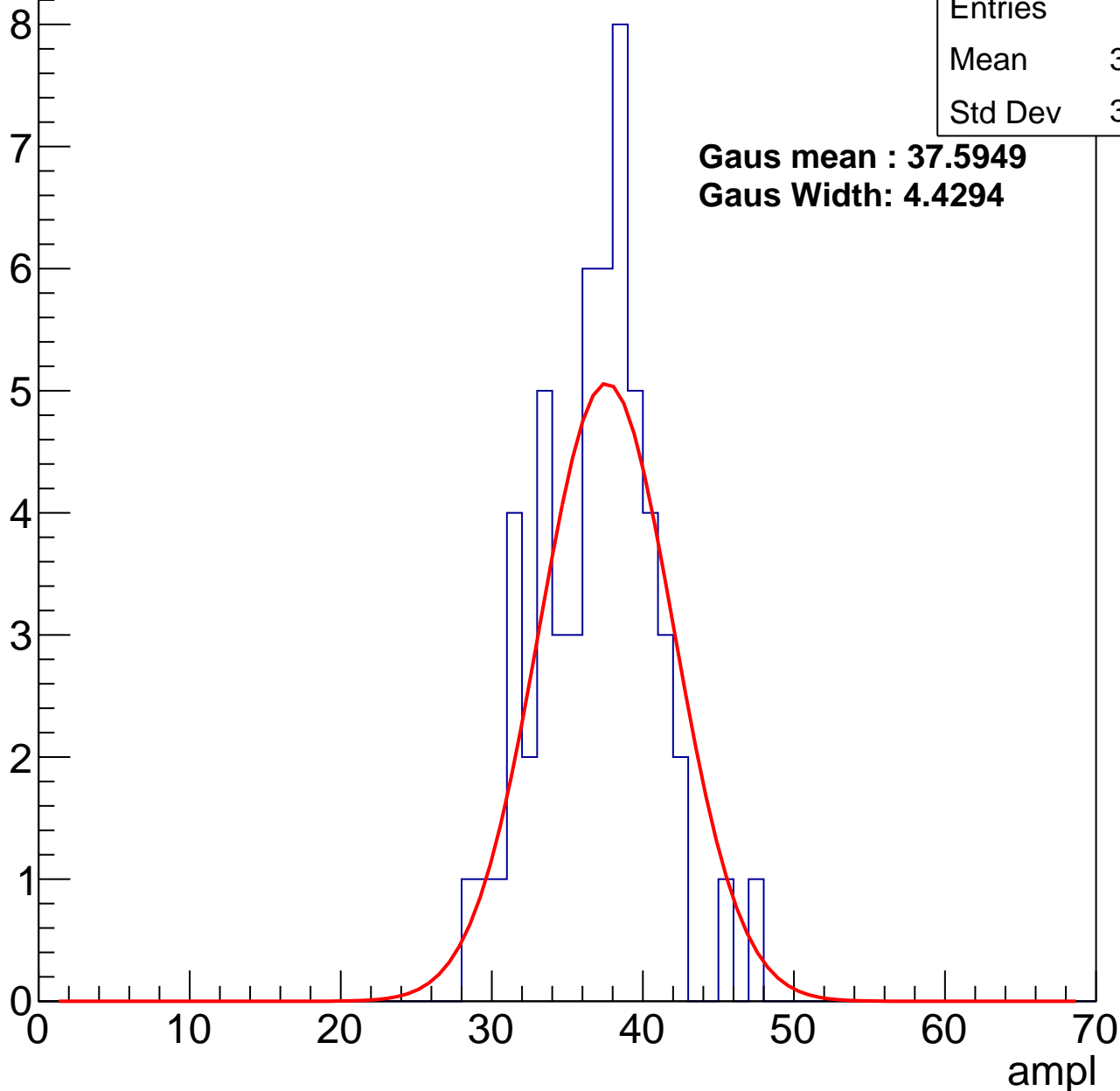
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	36.48
Std Dev	3.836

Gaus mean : 37.5949

Gaus Width: 4.4294



B0L001S, U14-ch24, adc2

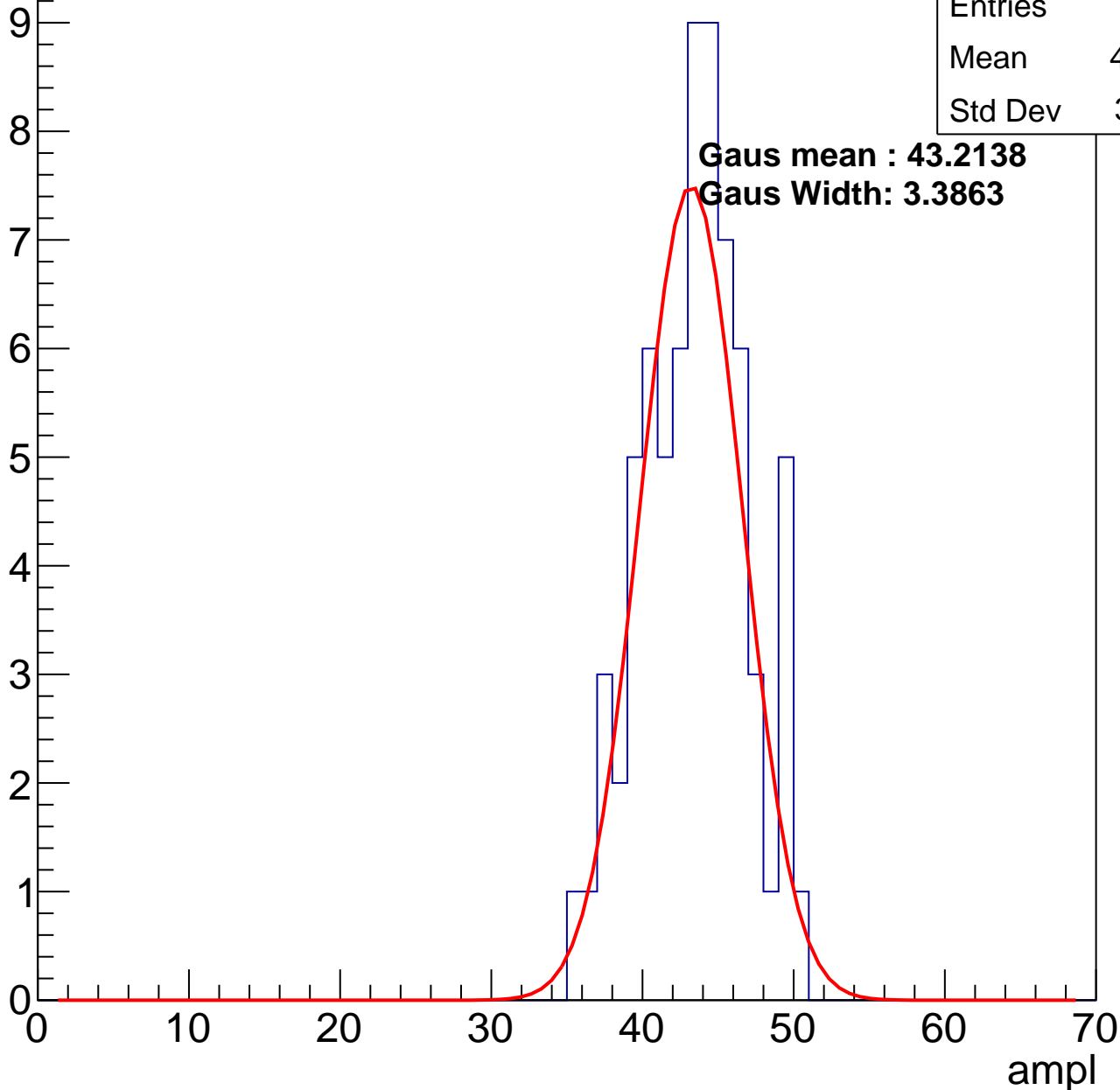
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	42.97
Std Dev	3.431

Gaus mean : 43.2138

Gaus Width: 3.3863

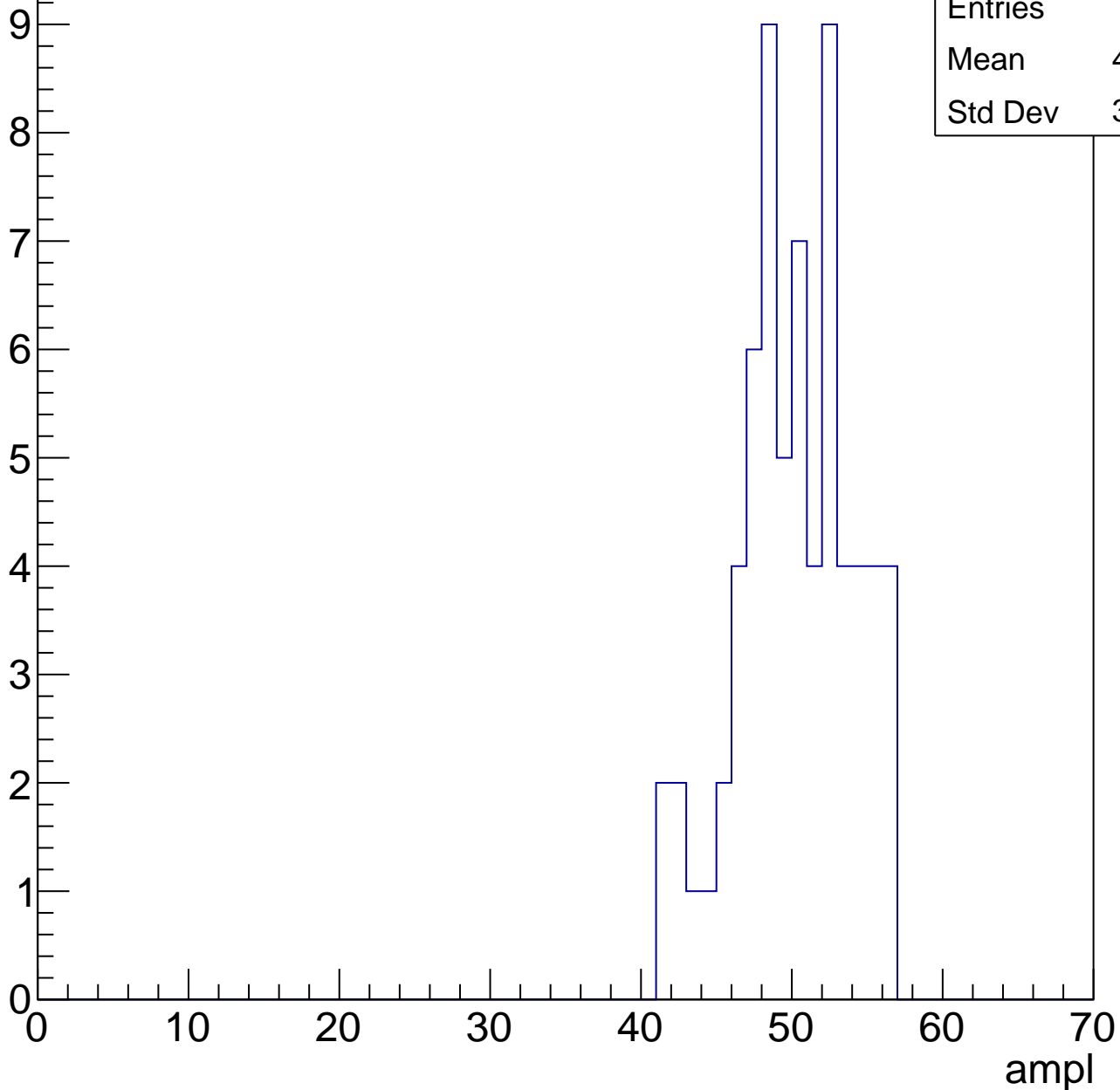


B0L001S, U14-ch24, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	49.71
Std Dev	3.761

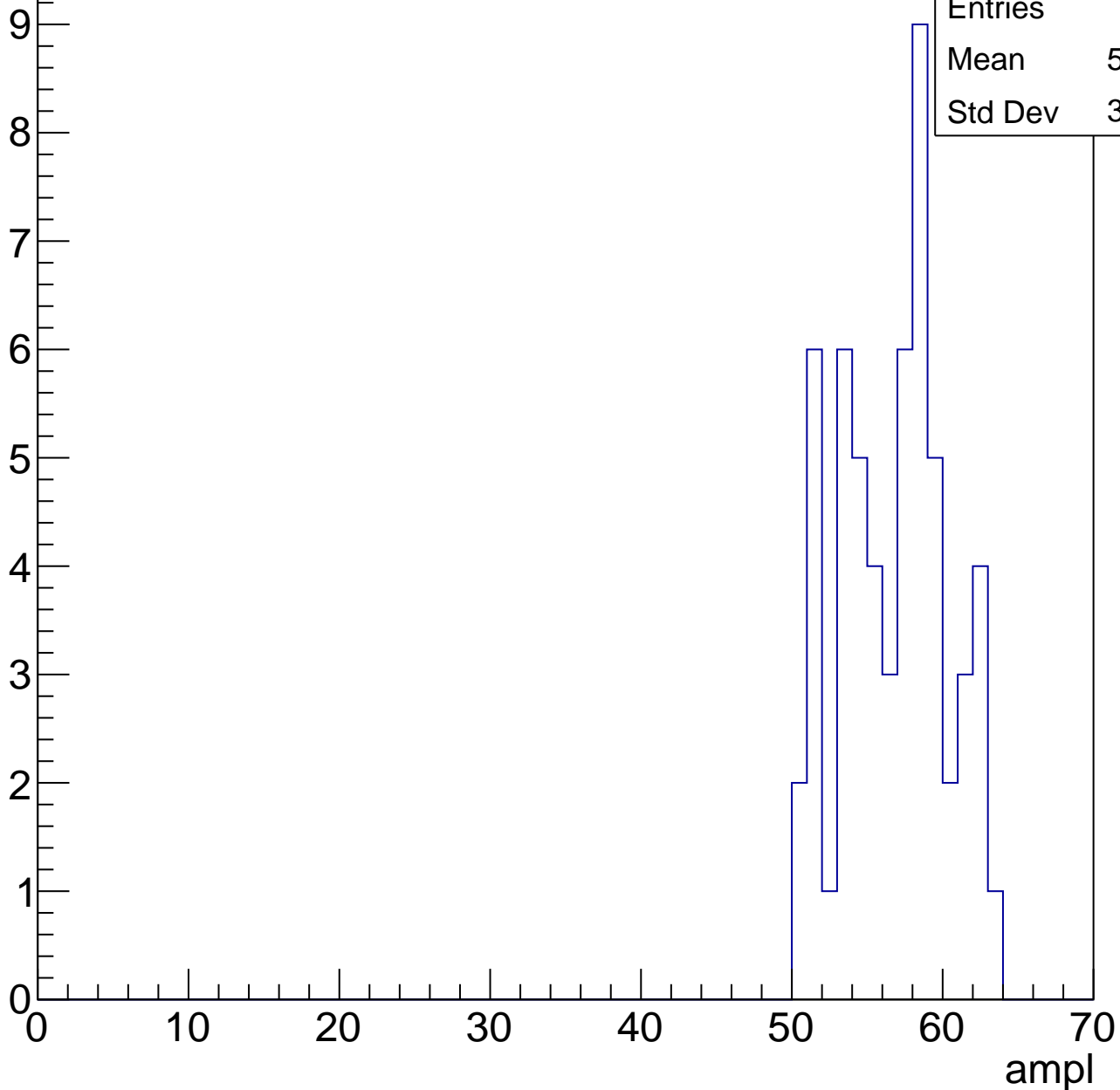


B0L001S, U14-ch24, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	56.26
Std Dev	3.502

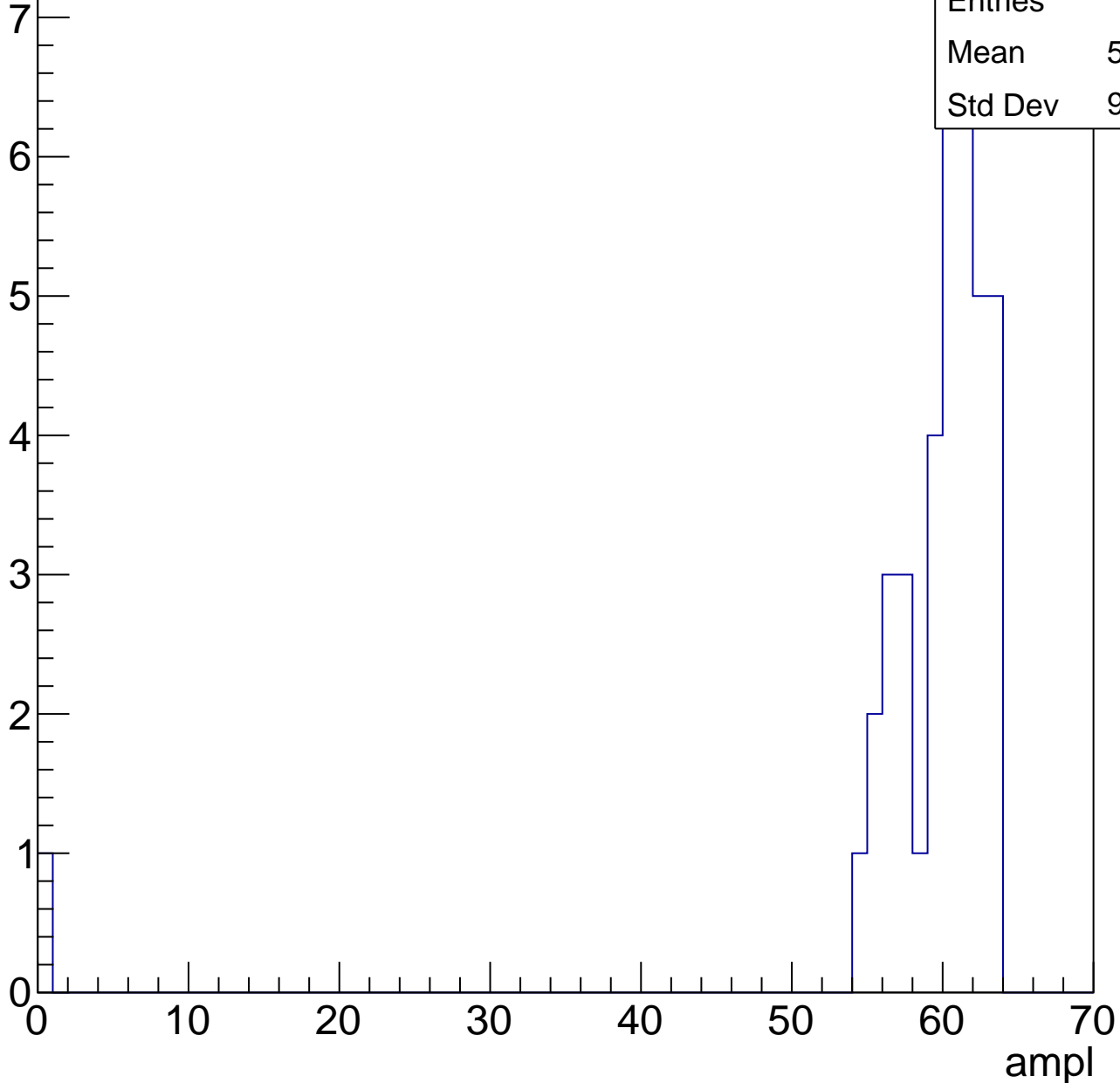


B0L001S, U14-ch24, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

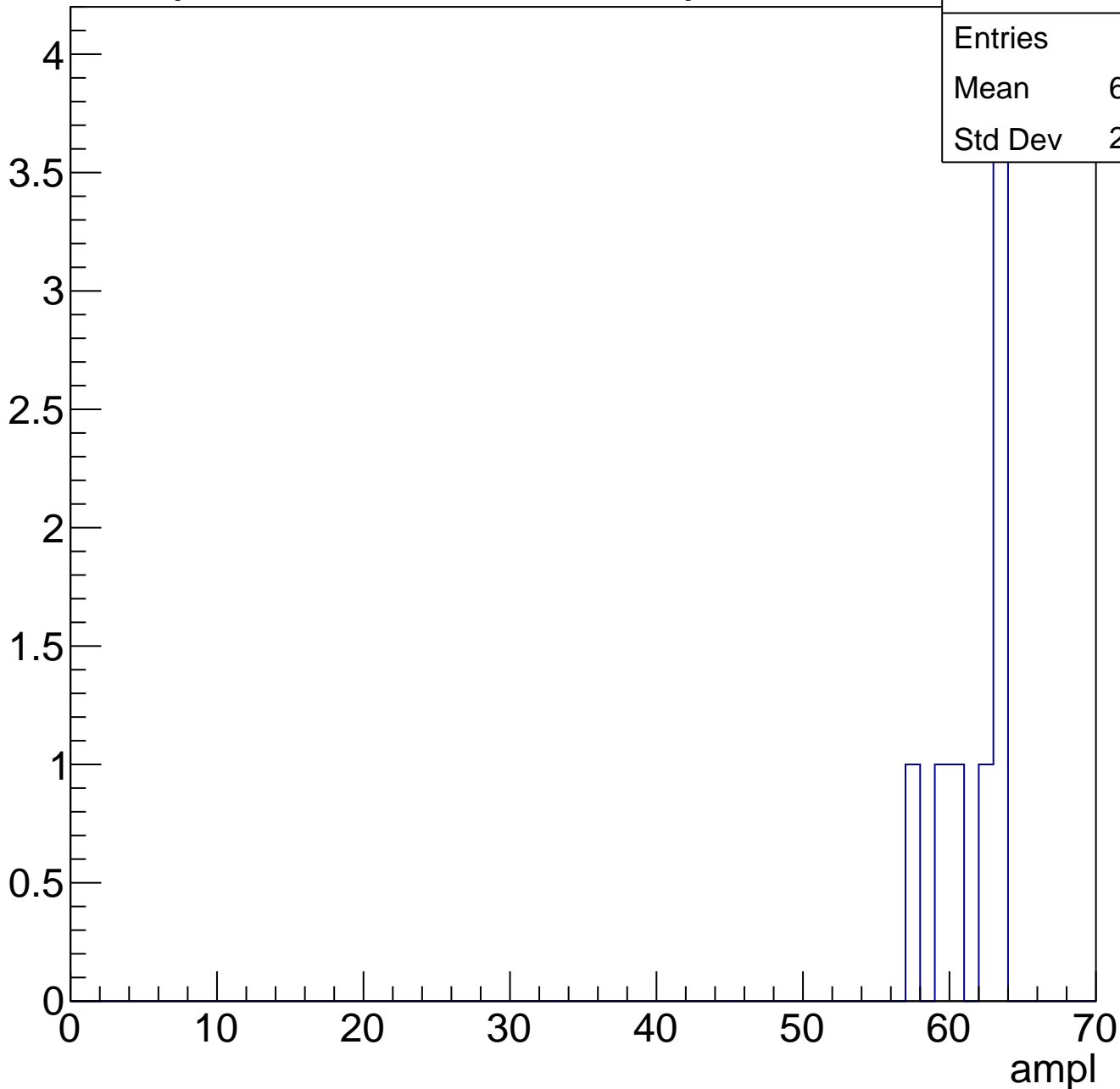
Entries	39
Mean	58.18
Std Dev	9.756



B0L001S, U14-ch24, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch24, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch25, adc0

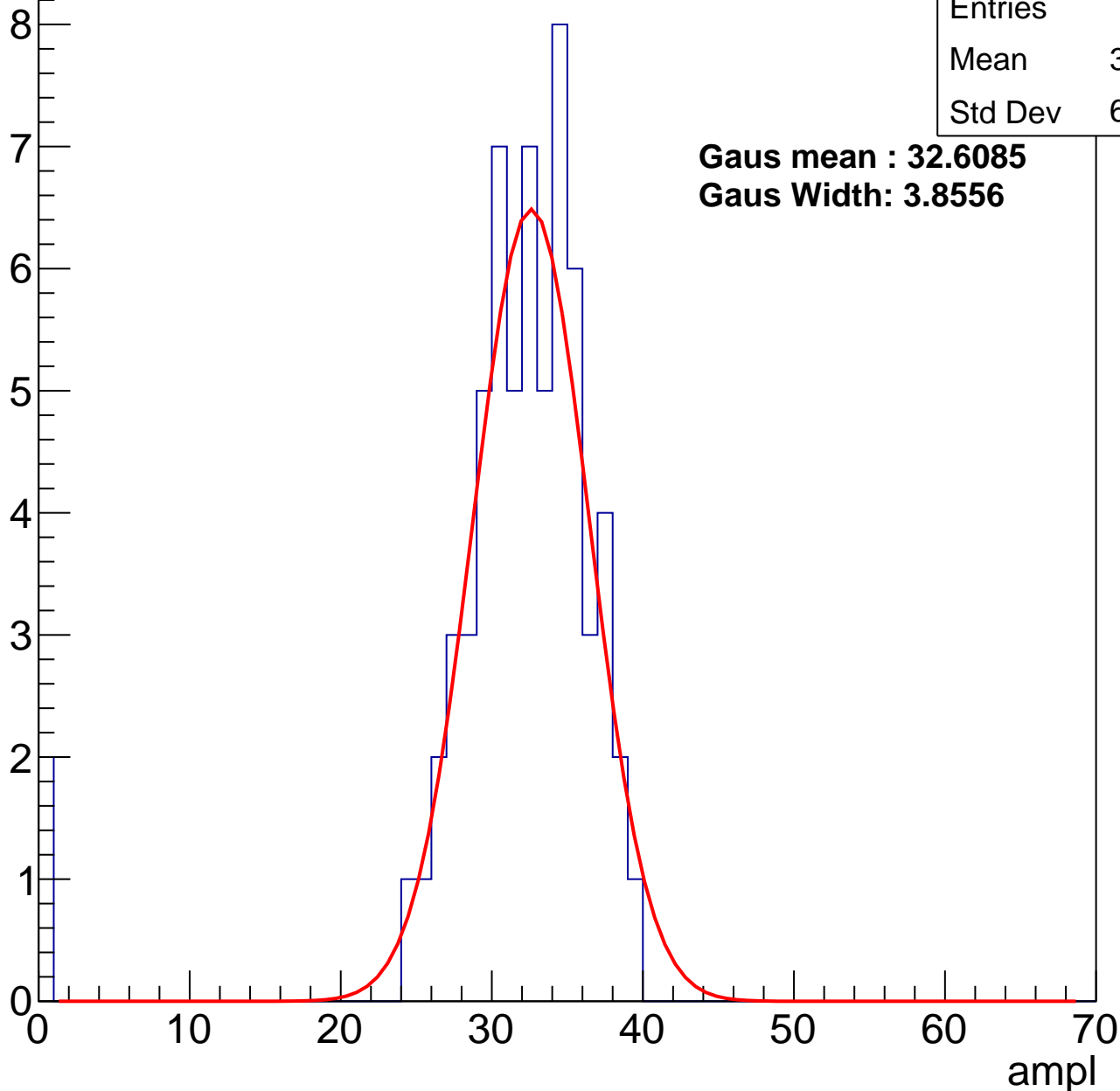
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.05
Std Dev	6.487

Gaus mean : 32.6085

Gaus Width: 3.8556



B0L001S, U14-ch25, adc1

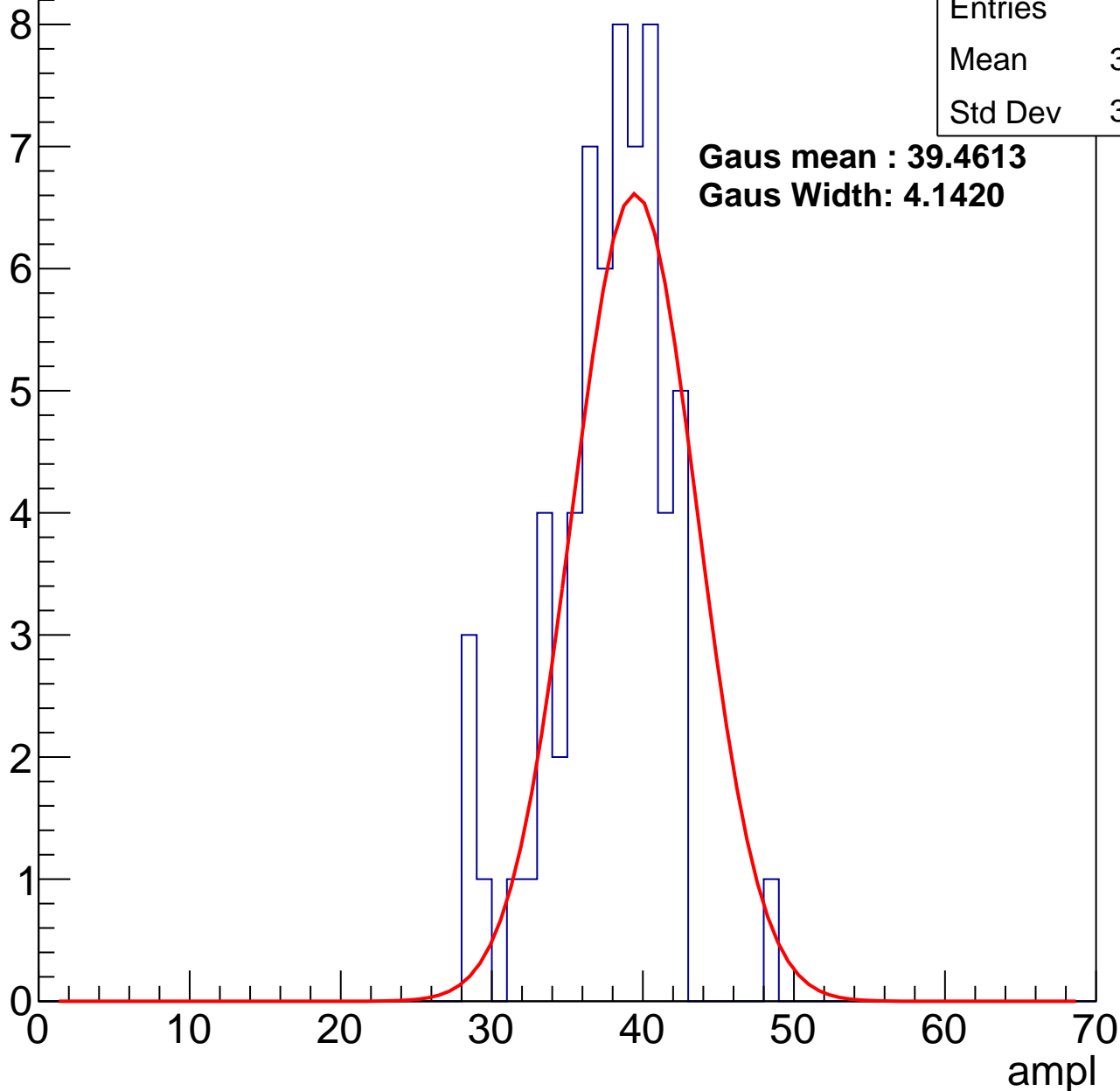
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.24
Std Dev	3.783

Gaus mean : 39.4613

Gaus Width: 4.1420



B0L001S, U14-ch25, adc2

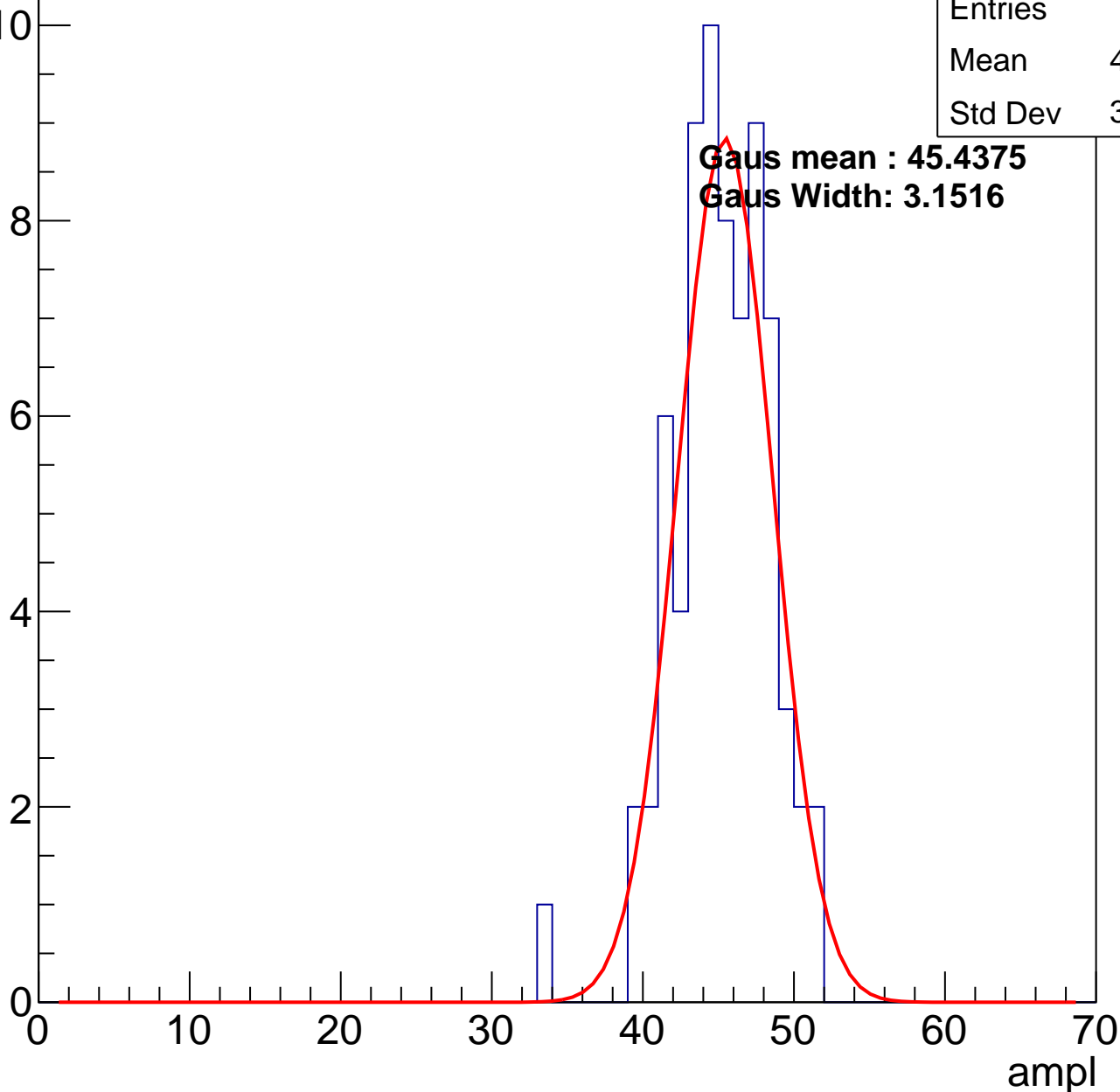
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	44.75
Std Dev	3.152

Gaus mean : 45.4375

Gaus Width: 3.1516

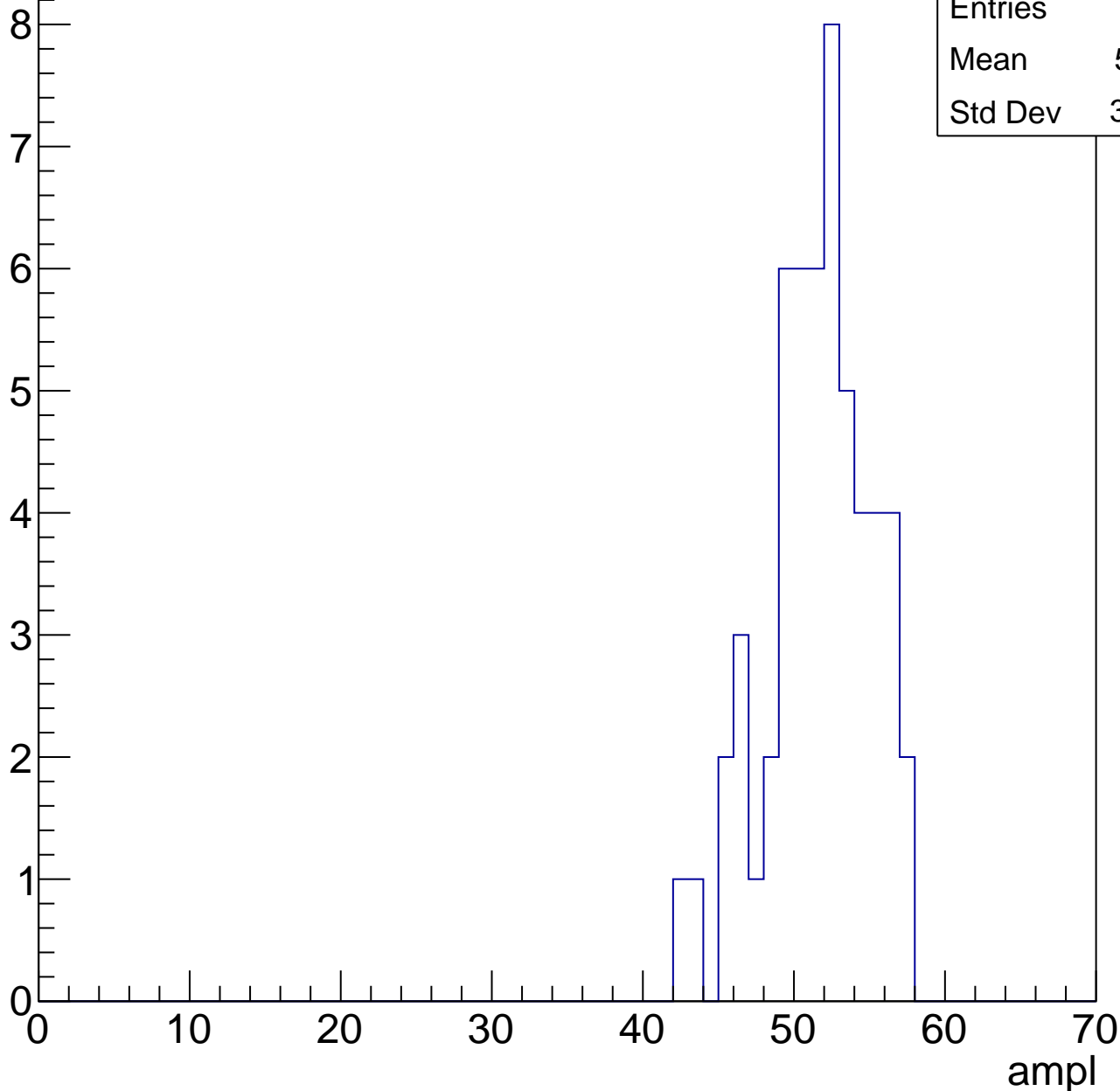


B0L001S, U14-ch25, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	51.11
Std Dev	3.452



B0L001S, U14-ch25, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	63
Mean	56.7
Std Dev	3.402

Entry

10

8

6

4

2

0

0

10

20

30

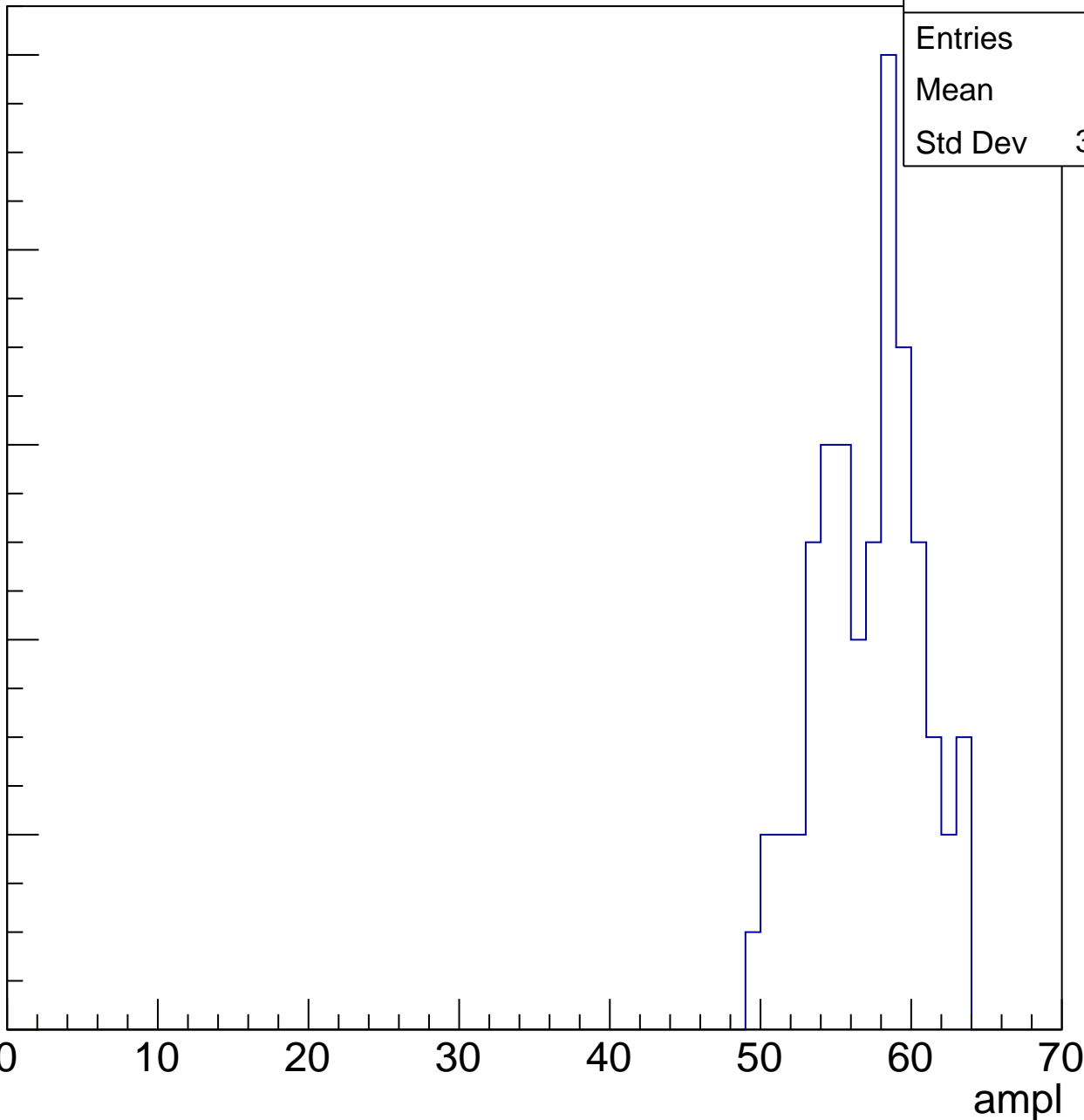
40

50

60

70

ampl

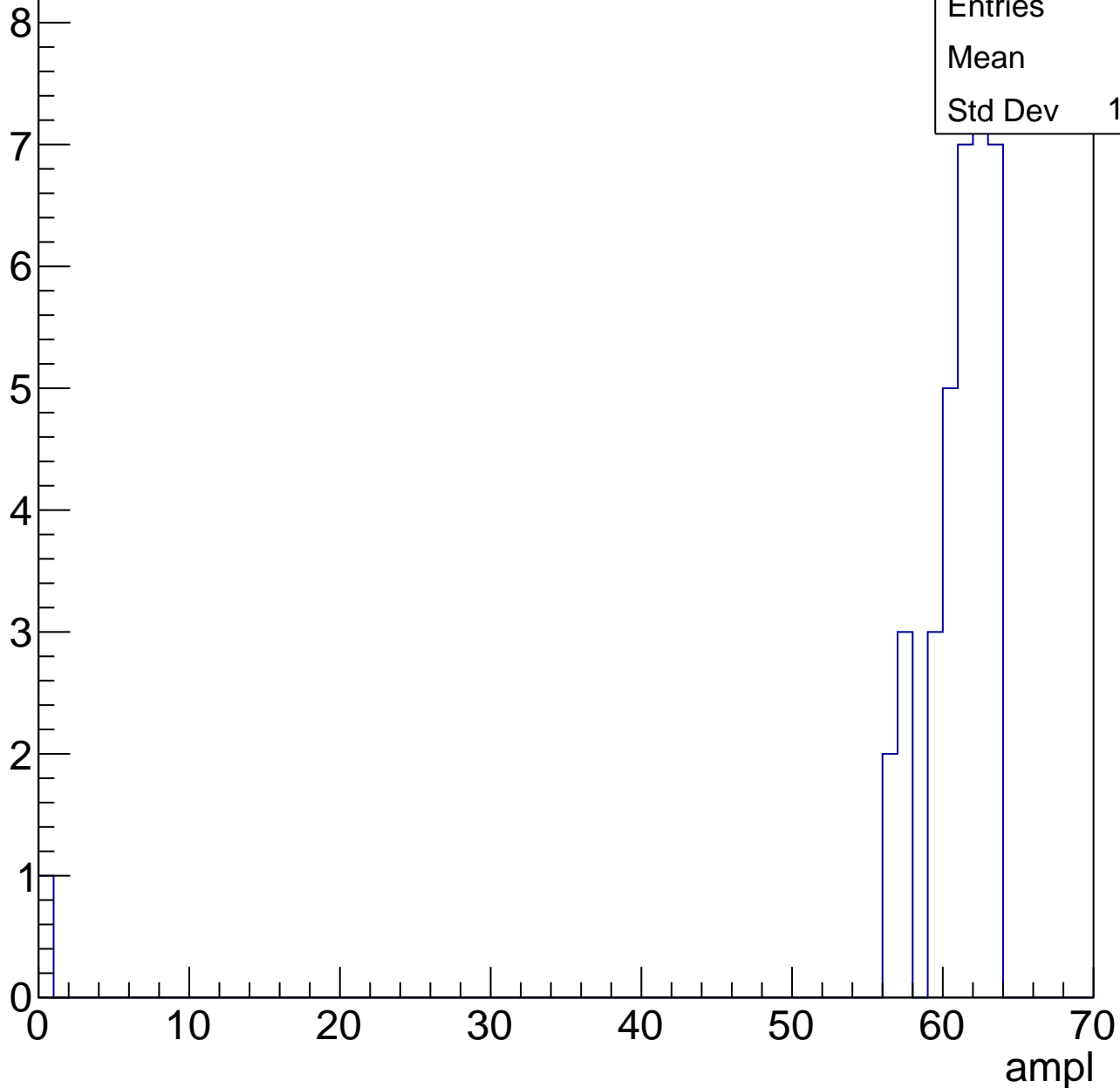


B0L001S, U14-ch25, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	59
Std Dev	10.18



B0L001S, U14-ch25, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch25, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch26, adc0

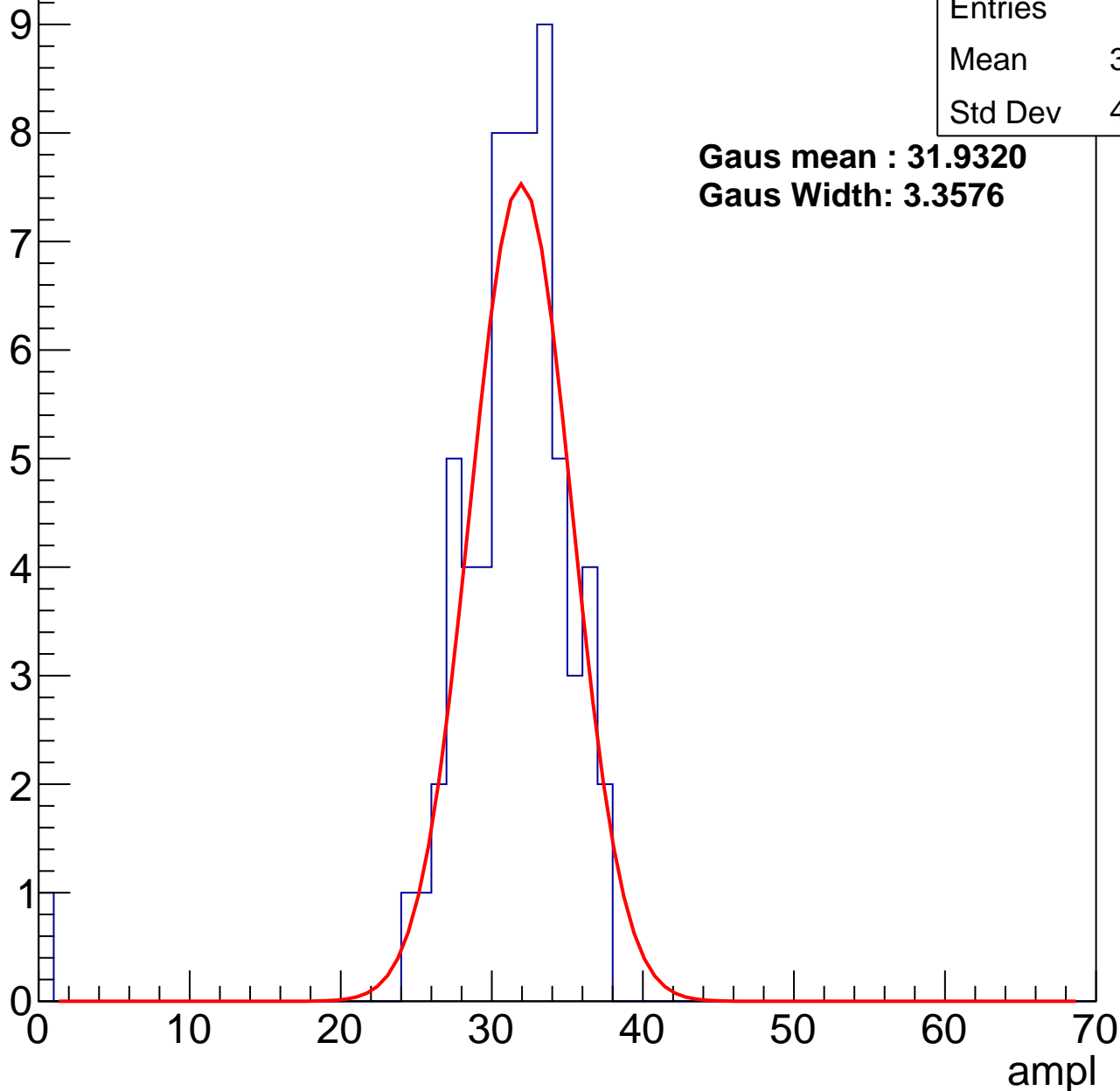
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	30.74
Std Dev	4.868

Gaus mean : 31.9320

Gaus Width: 3.3576



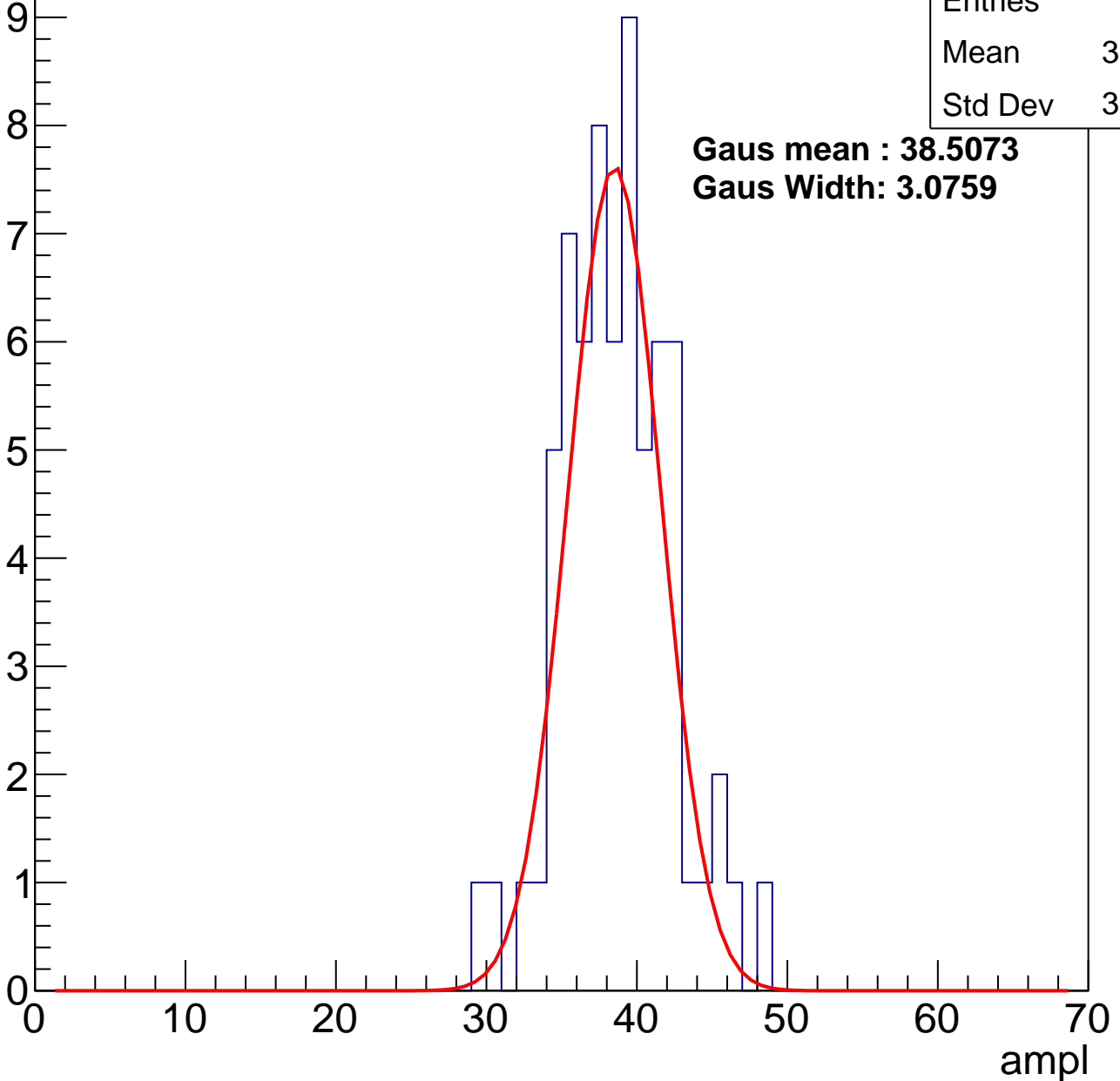
B0L001S, U14-ch26, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.22
Std Dev	3.597

Gaus mean : 38.5073
Gaus Width: 3.0759



B0L001S, U14-ch26, adc2

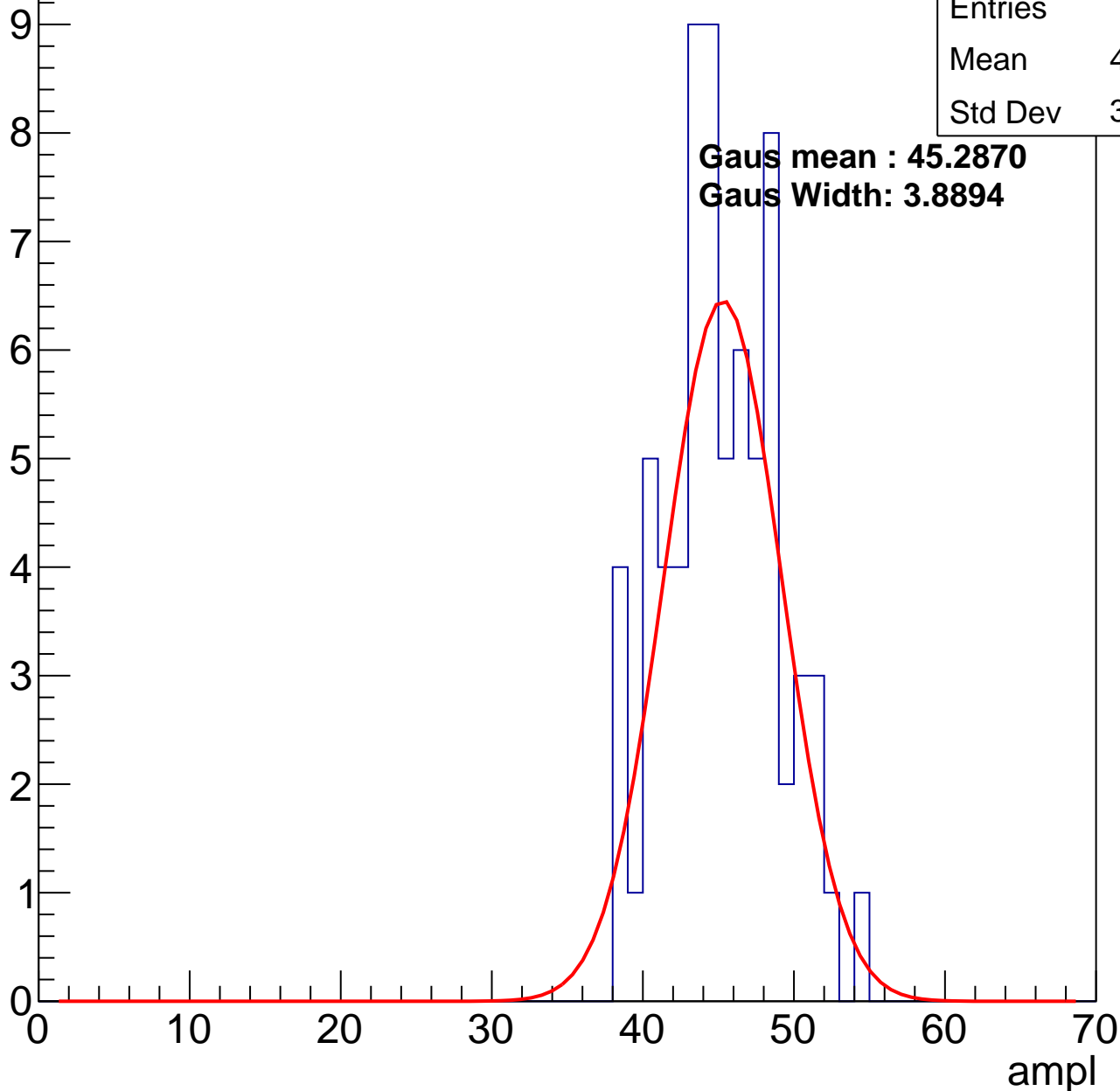
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	44.76
Std Dev	3.666

Gaus mean : 45.2870

Gaus Width: 3.8894

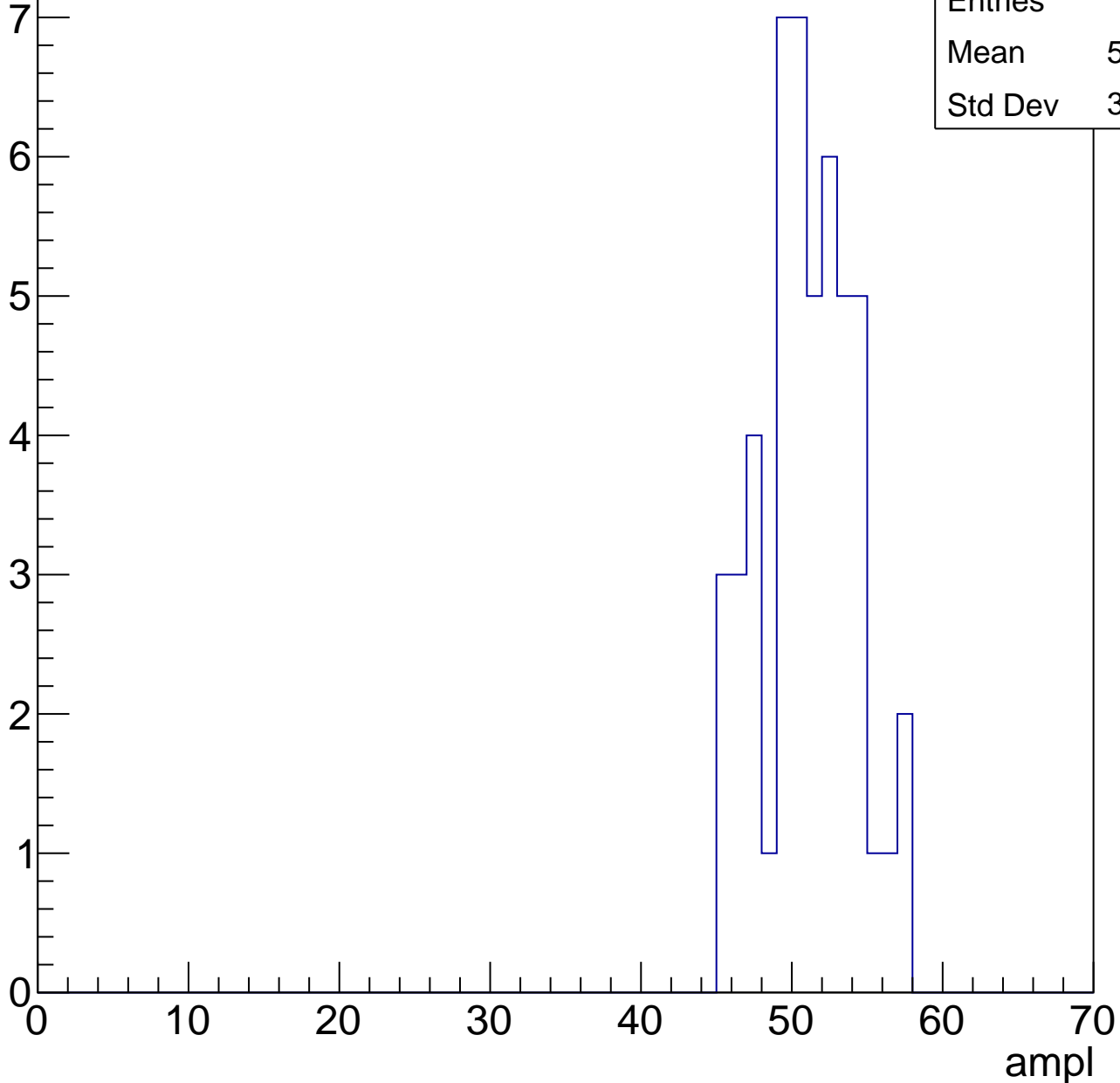


B0L001S, U14-ch26, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	50.58
Std Dev	3.053

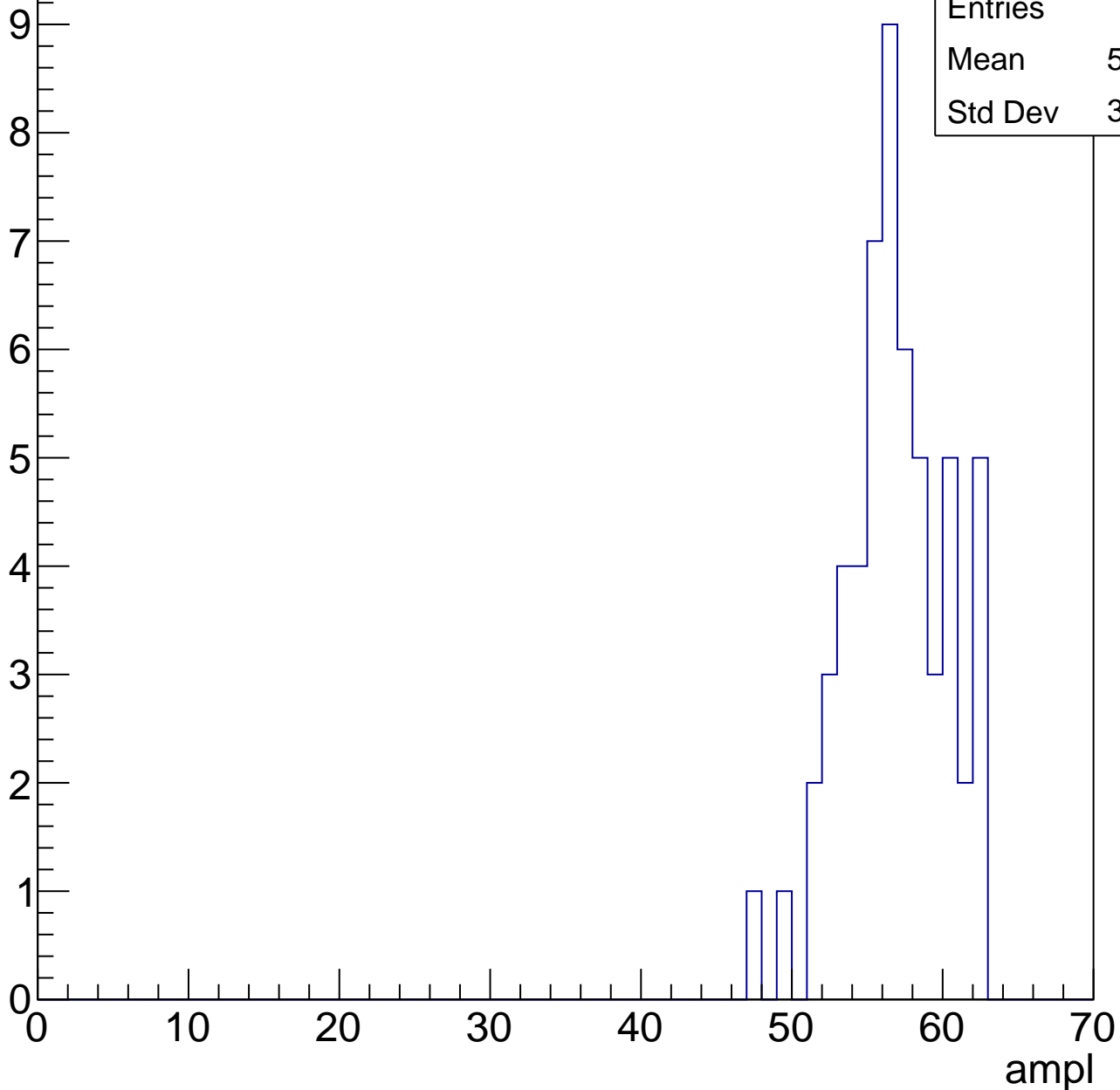


B0L001S, U14-ch26, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

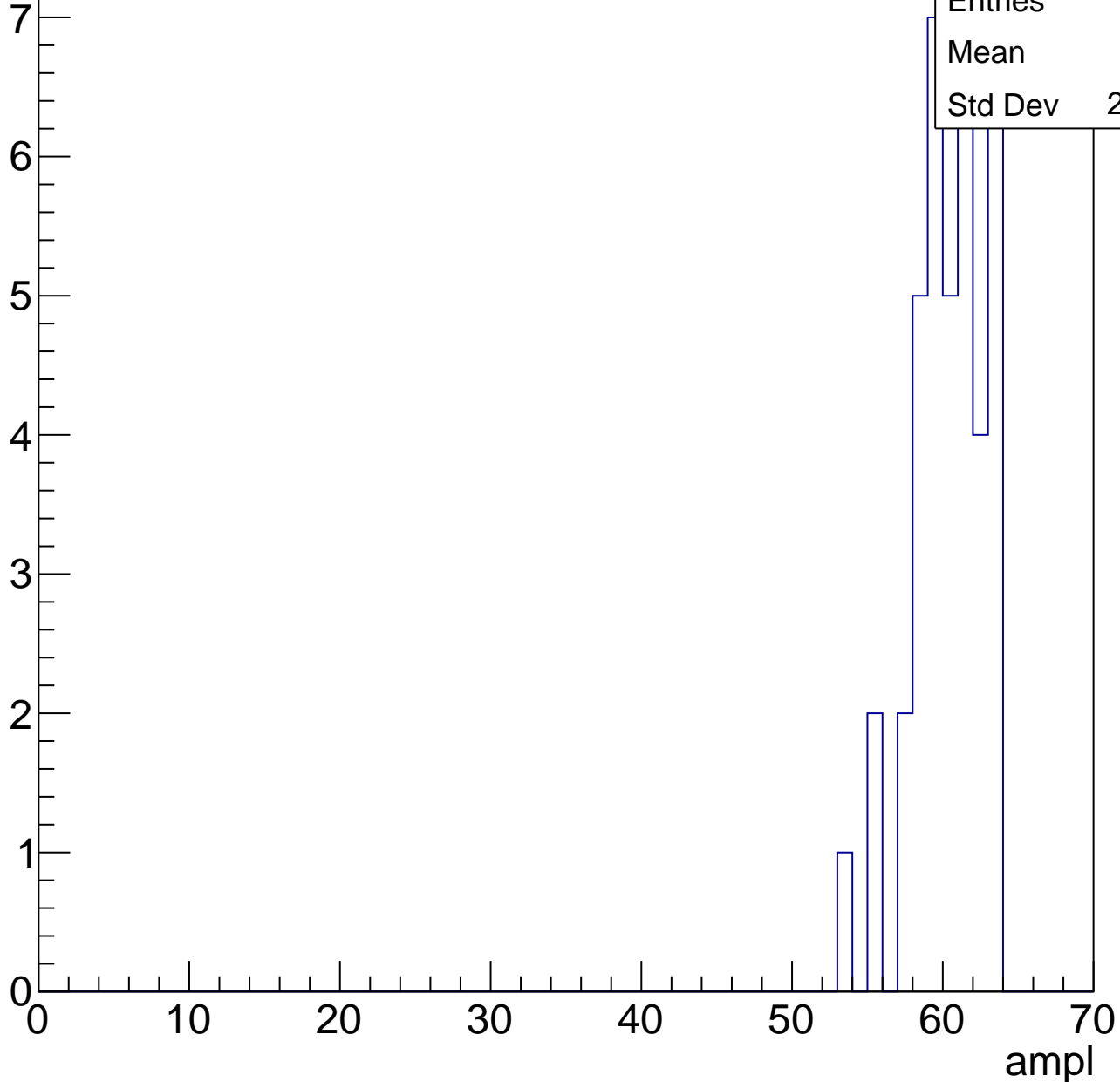
Entries	57
Mean	56.35
Std Dev	3.359



B0L001S, U14-ch26, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch26, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch26, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch27, adc0

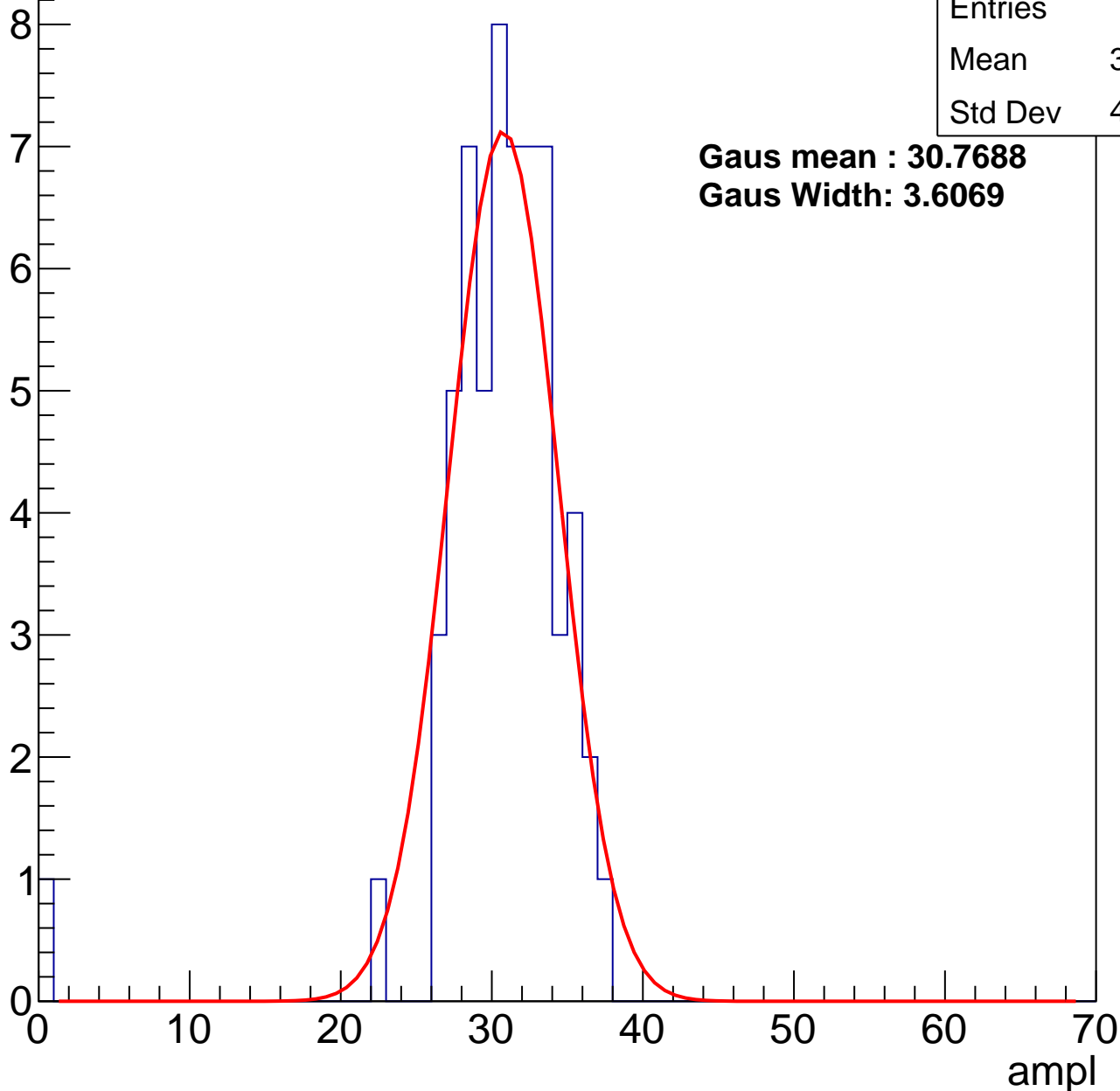
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	30.15
Std Dev	4.885

Gaus mean : 30.7688

Gaus Width: 3.6069



B0L001S, U14-ch27, adc1

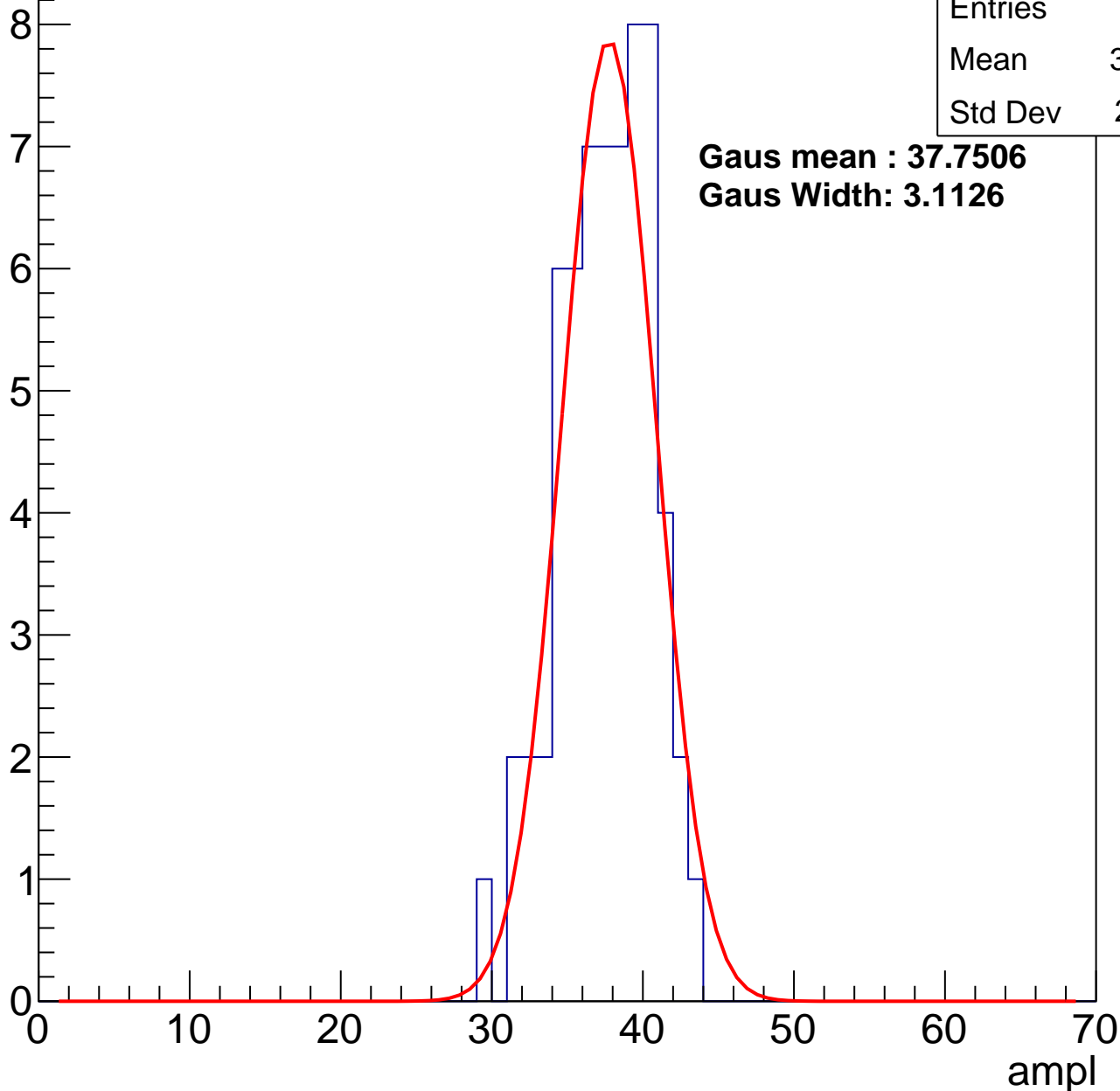
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.06
Std Dev	2.991

Gaus mean : 37.7506

Gaus Width: 3.1126



B0L001S, U14-ch27, adc2

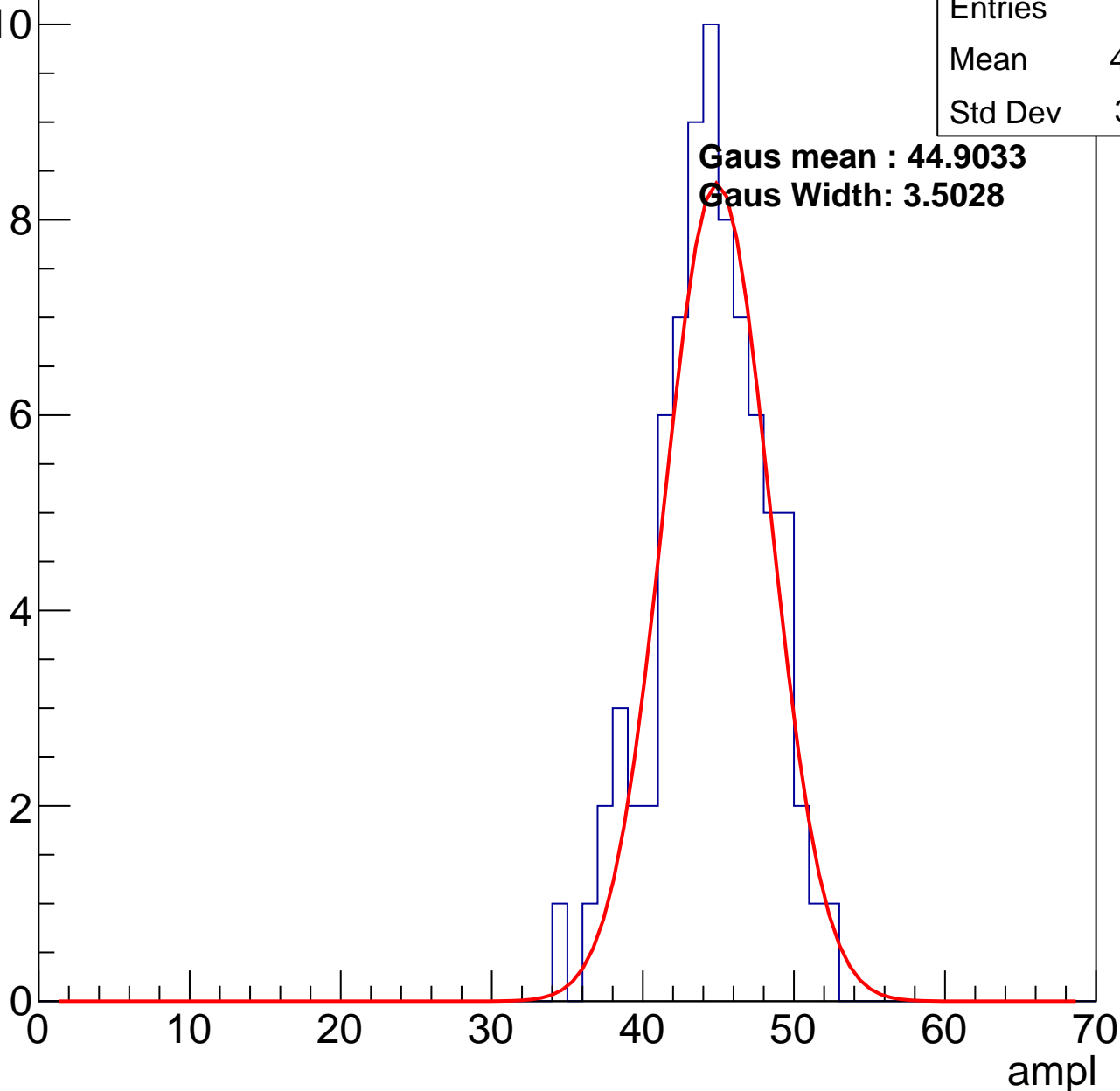
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	44.04
Std Dev	3.621

Gaus mean : 44.9033

Gaus Width: 3.5028

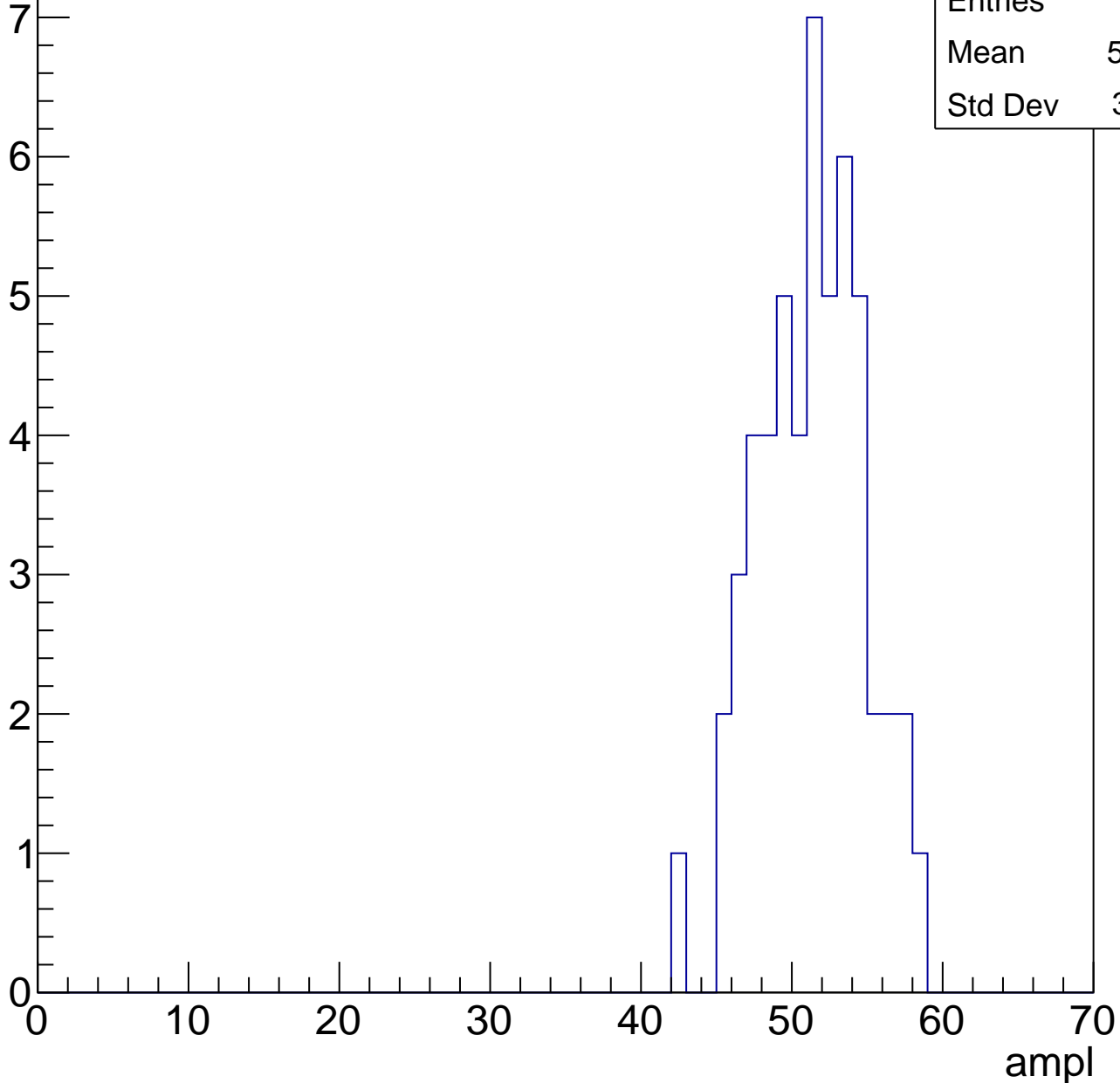


B0L001S, U14-ch27, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.83
Std Dev	3.441

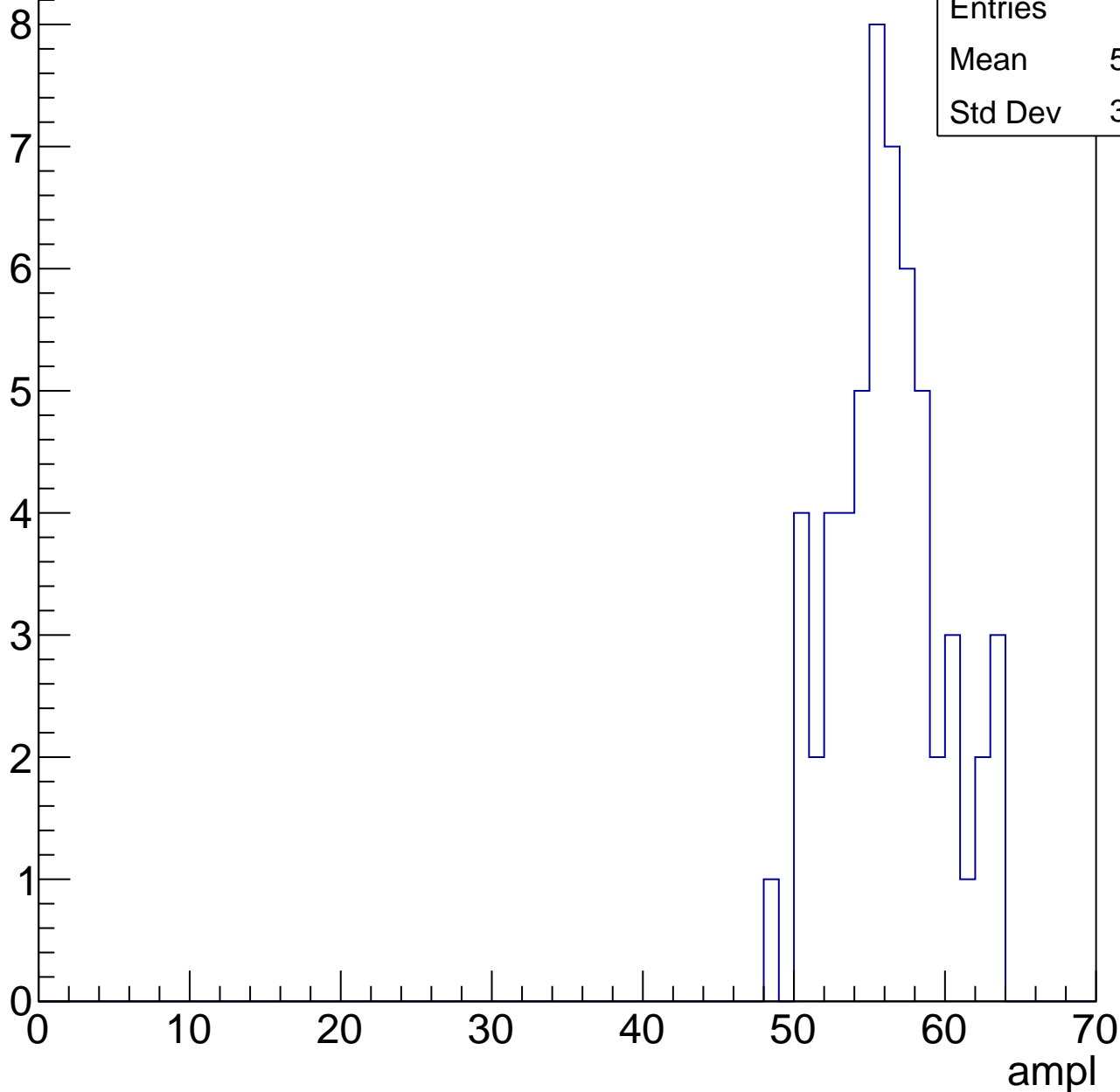


B0L001S, U14-ch27, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

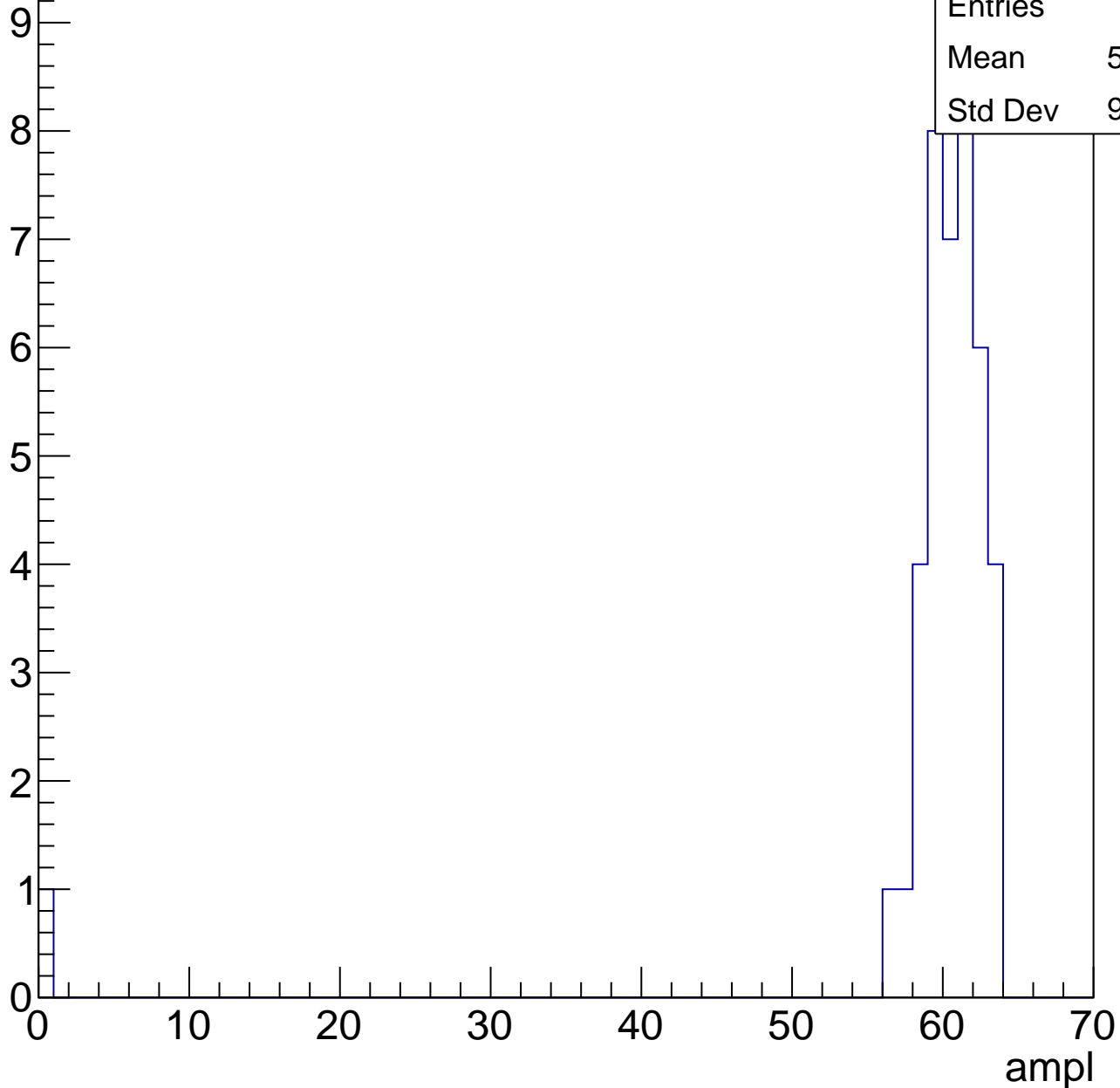
Entries	57
Mean	55.72
Std Dev	3.548



B0L001S, U14-ch27, adc5

calib_packv5_042523_0143.root, FC#9, port A1

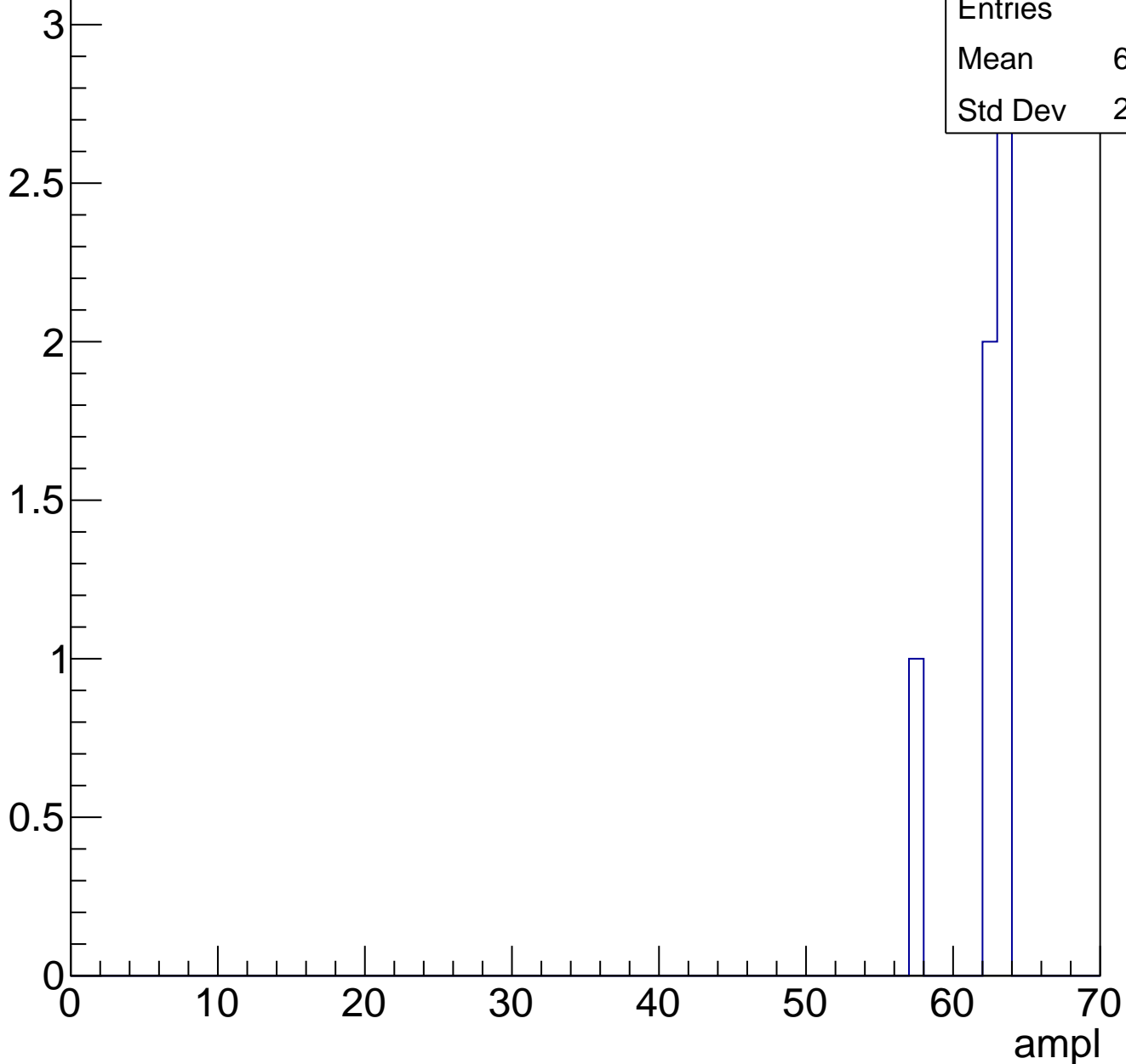
Entry



B0L001S, U14-ch27, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch27, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch28, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	30.36
Std Dev	3.443

Gaus mean : 31.0687

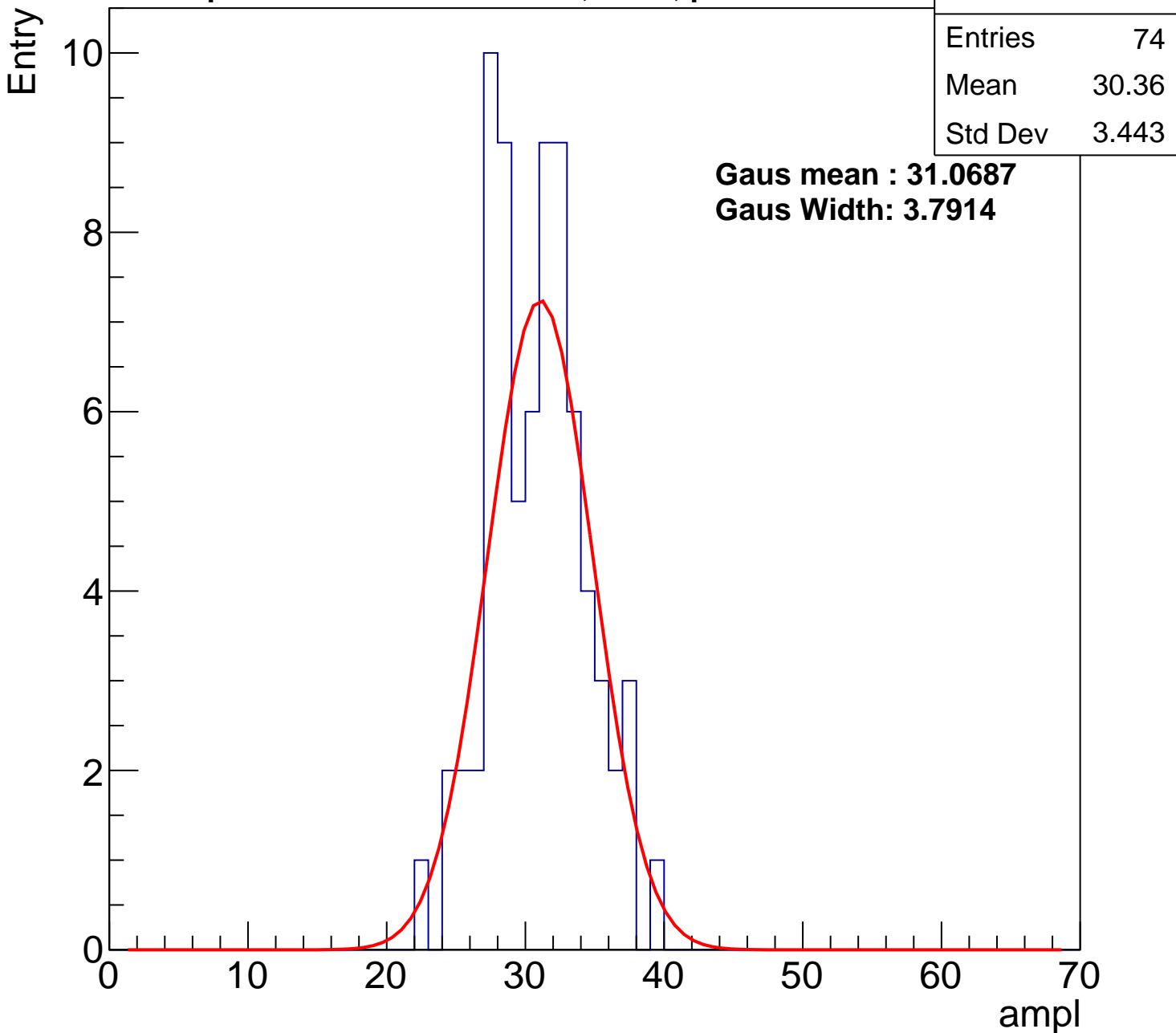
Gaus Width: 3.7914

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



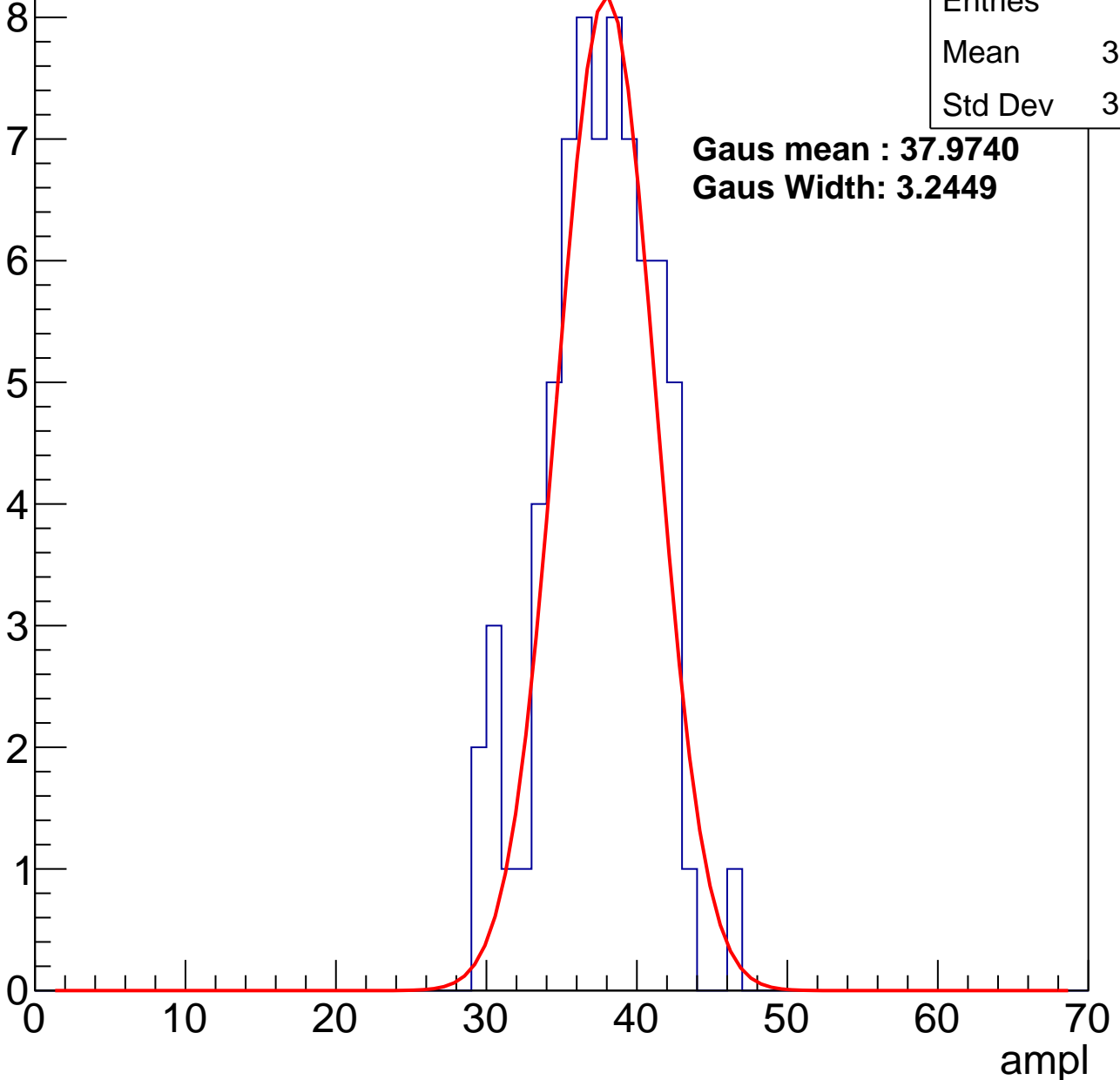
B0L001S, U14-ch28, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	37.04
Std Dev	3.553

Gaus mean : 37.9740
Gaus Width: 3.2449



B0L001S, U14-ch28, adc2

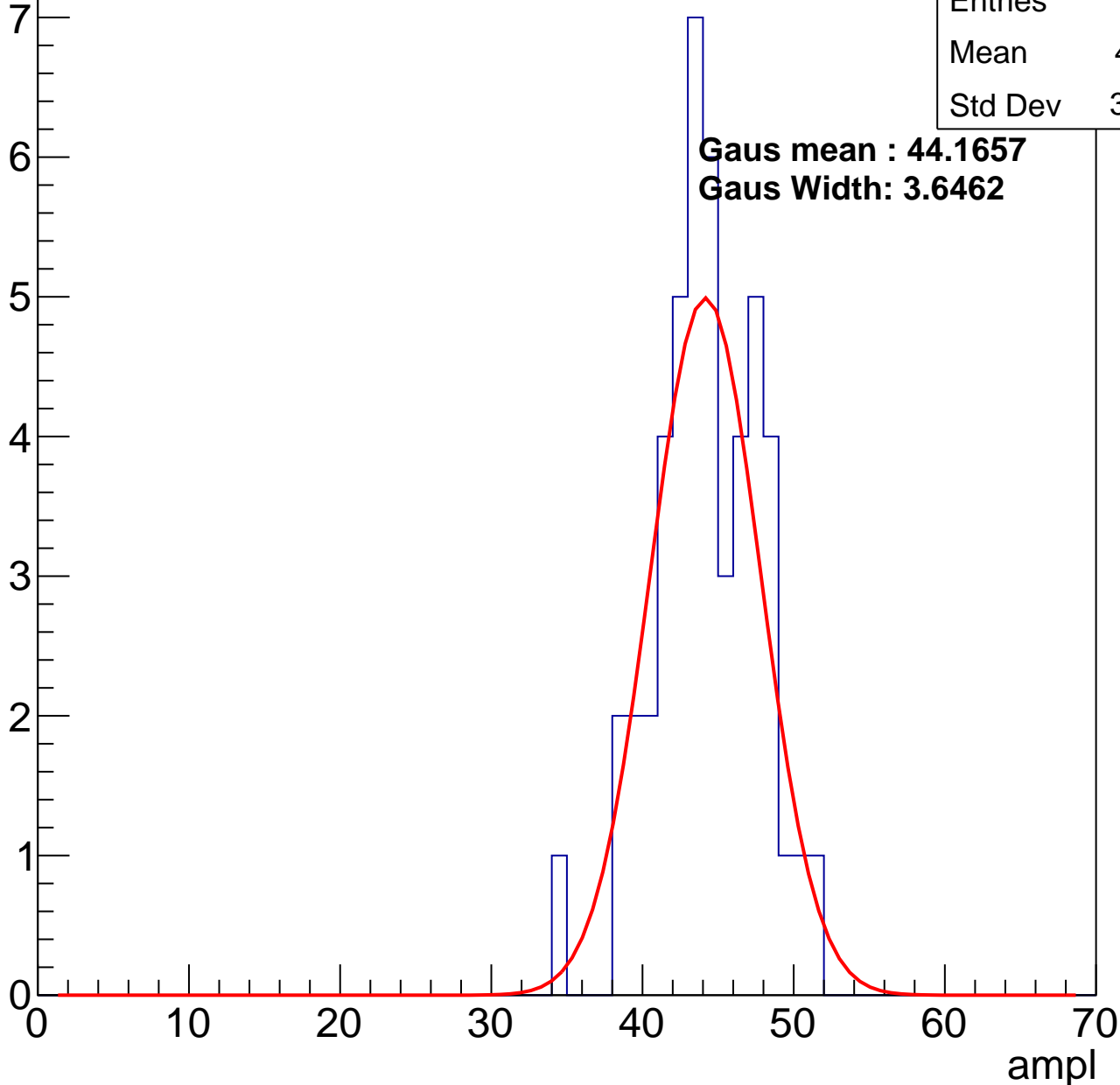
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	43.81
Std Dev	3.395

Gaus mean : 44.1657

Gaus Width: 3.6462

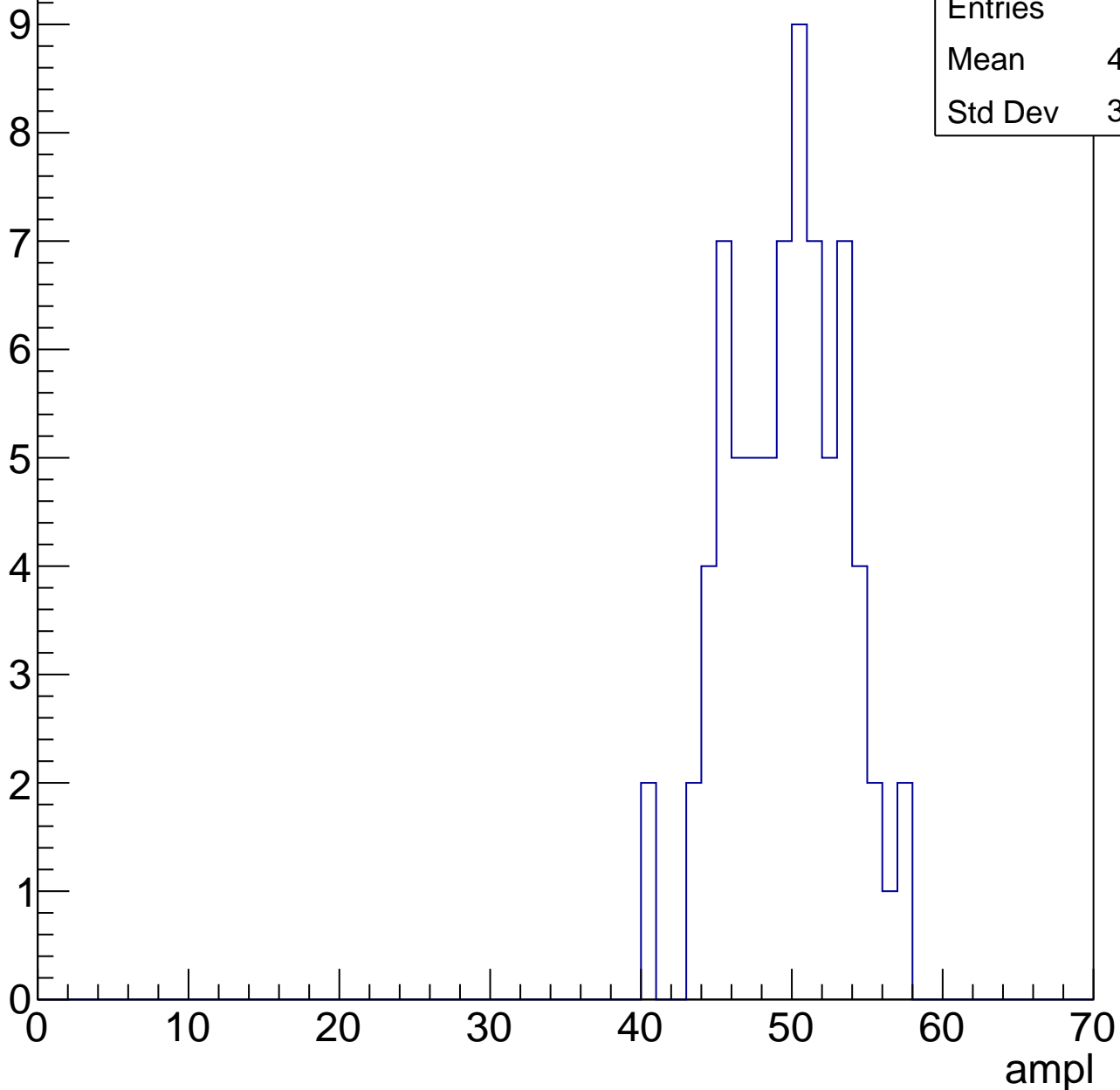


B0L001S, U14-ch28, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	49.18
Std Dev	3.779

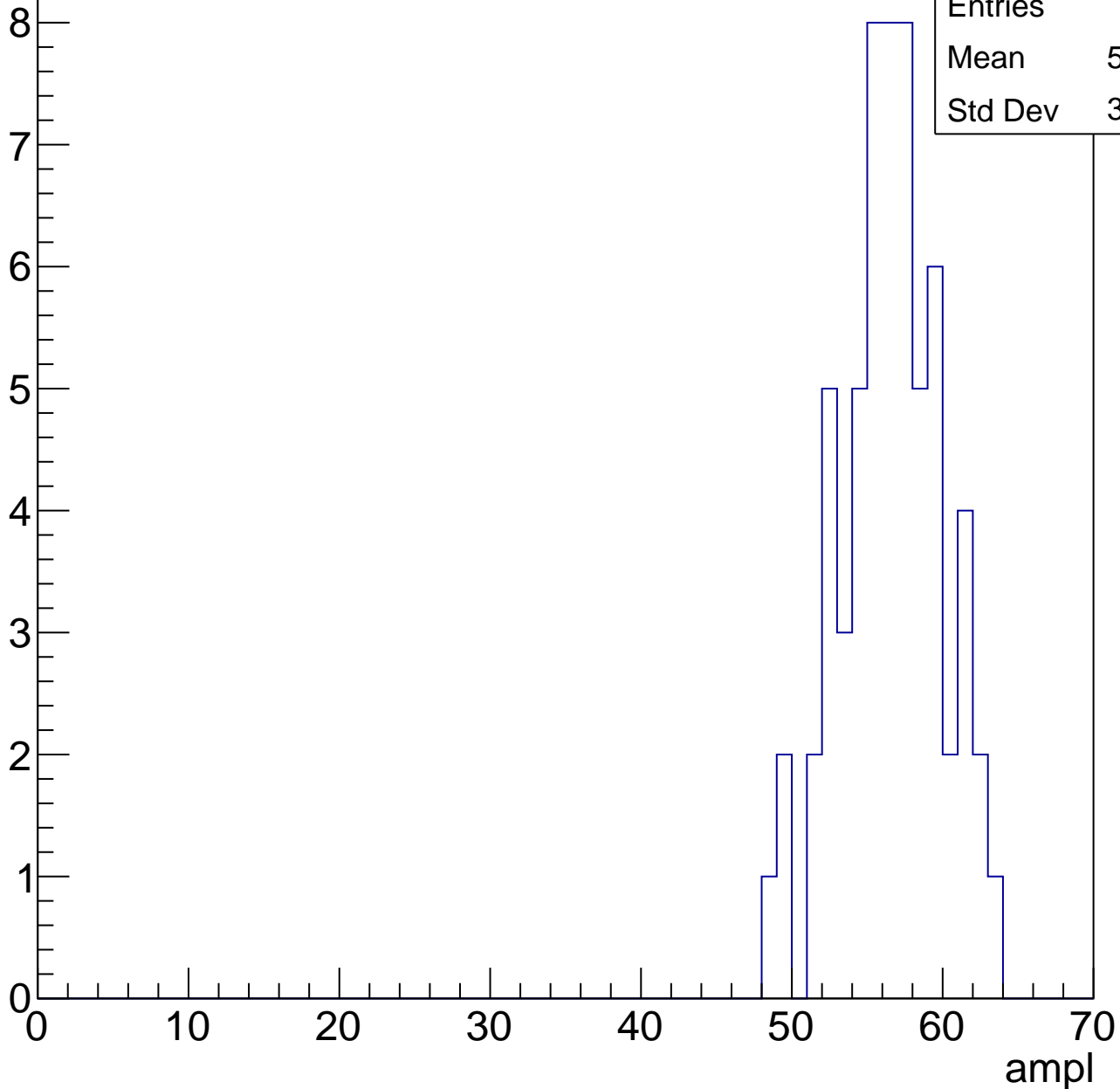


B0L001S, U14-ch28, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.06
Std Dev	3.316



B0L001S, U14-ch28, adc5

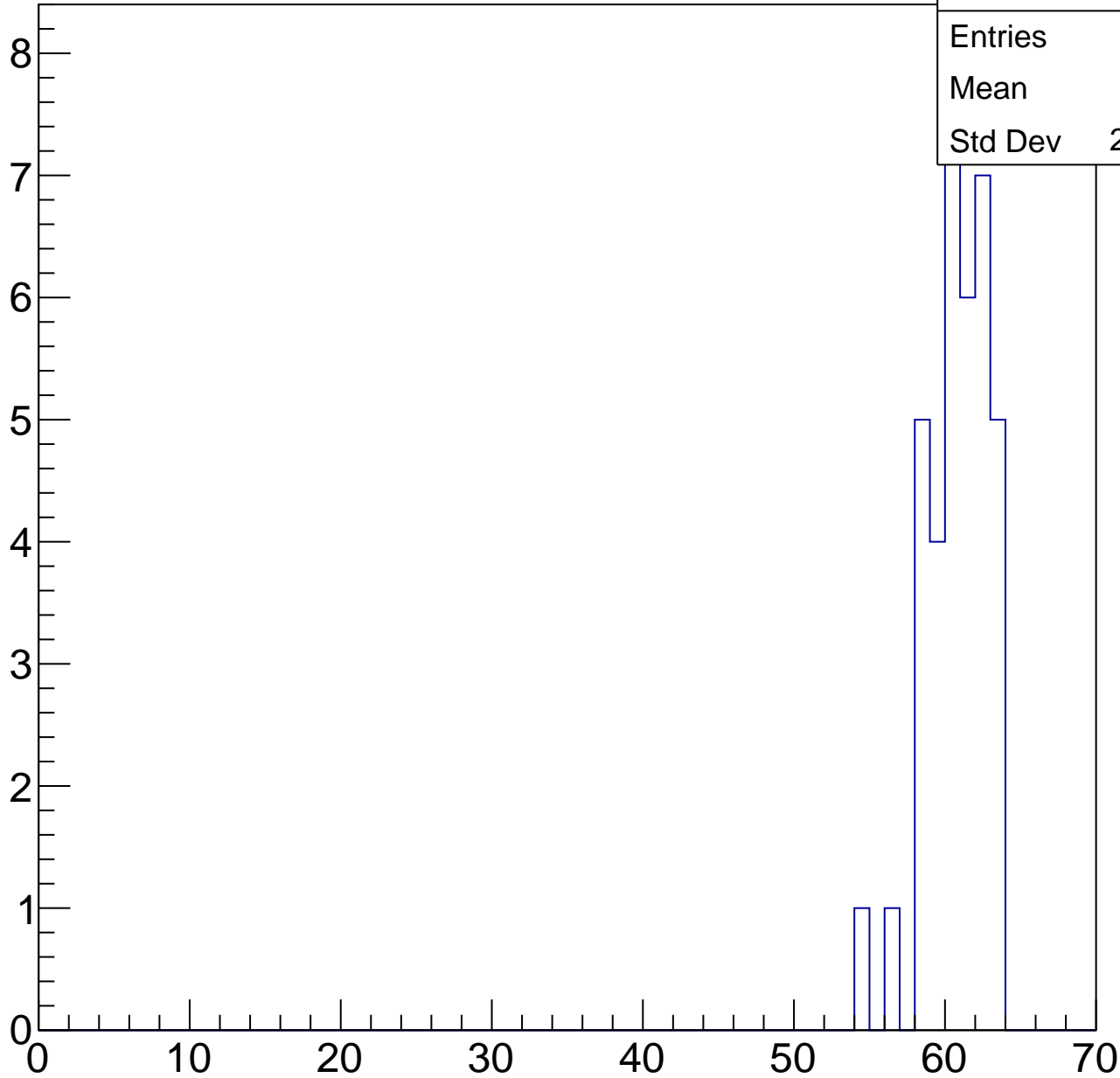
calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	37
Mean	60.3
Std Dev	2.025

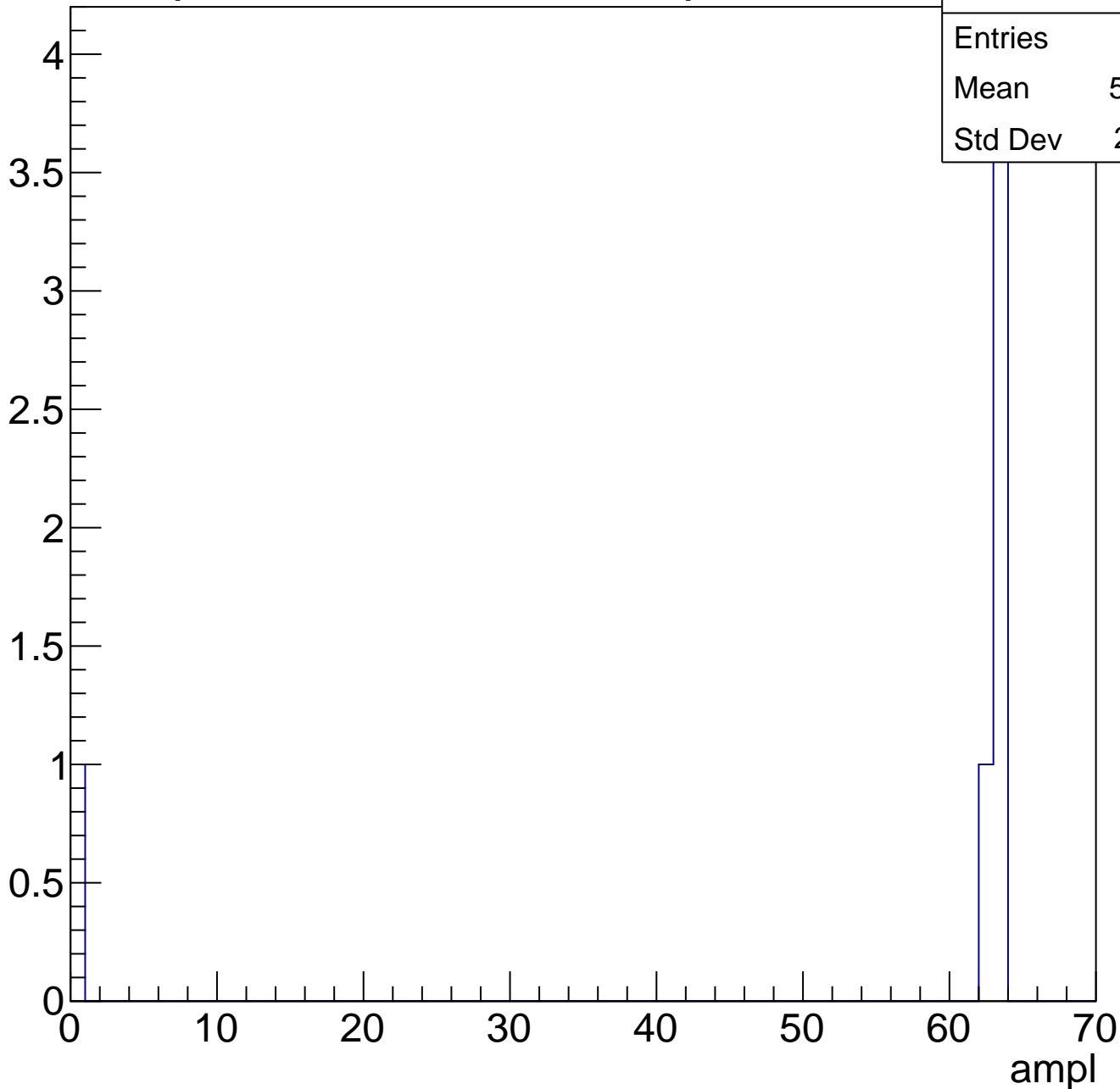
ampl



B0L001S, U14-ch28, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	6
Mean	52.33
Std Dev	23.41

B0L001S, U14-ch28, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch29, adc0

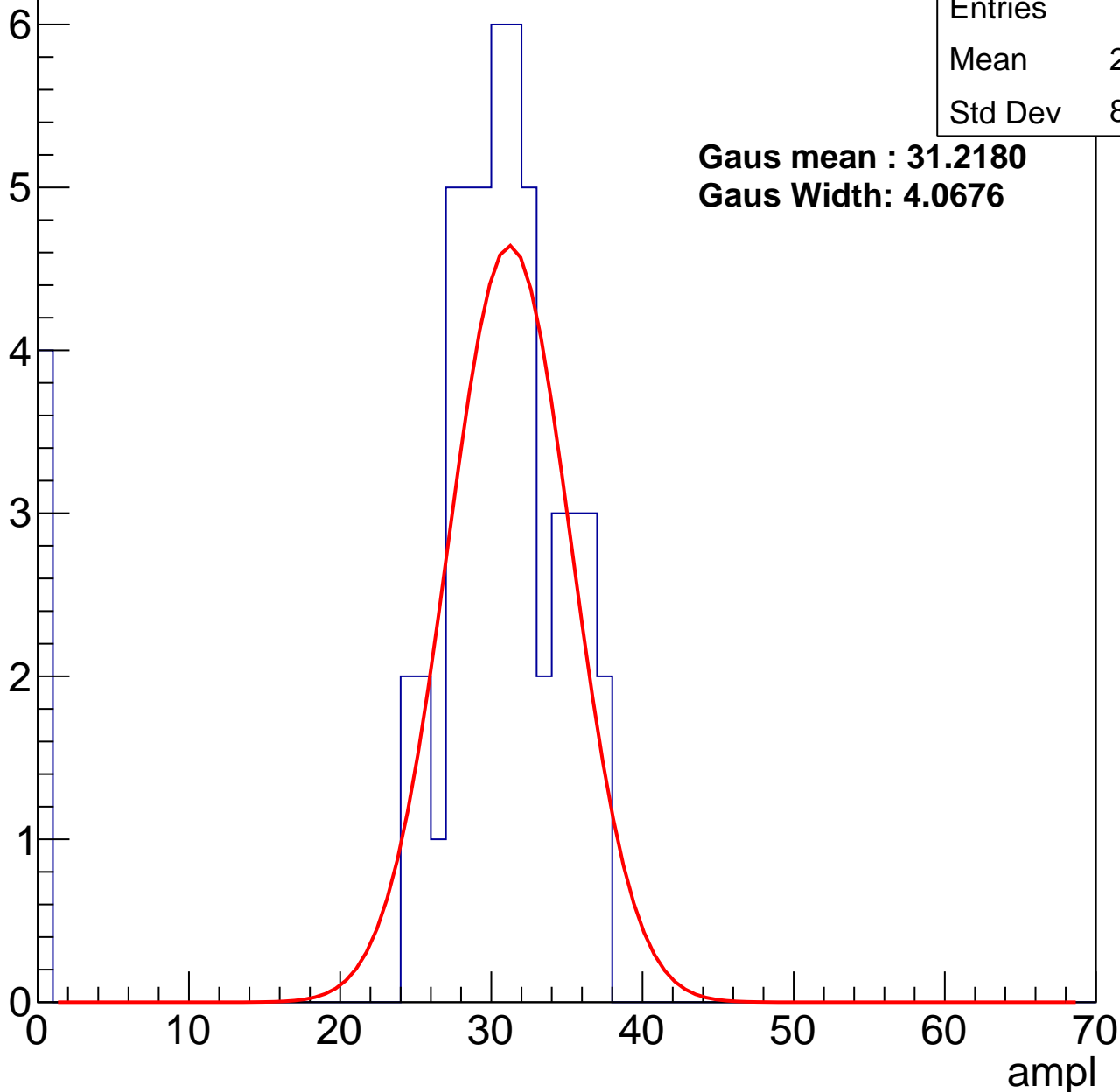
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	28.24
Std Dev	8.622

Gaus mean : 31.2180

Gaus Width: 4.0676



B0L001S, U14-ch29, adc1

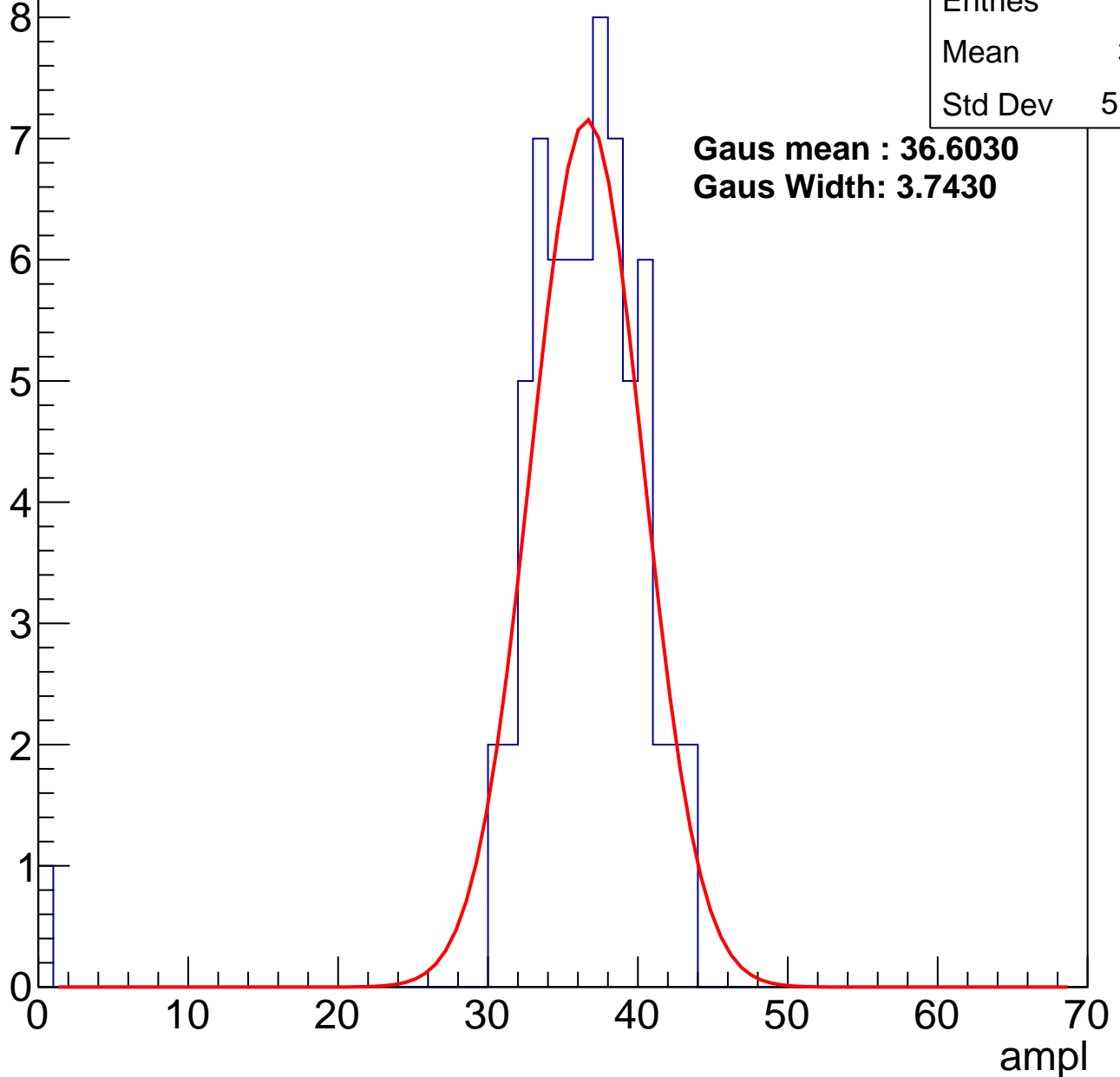
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	35.7
Std Dev	5.439

Gaus mean : 36.6030

Gaus Width: 3.7430



B0L001S, U14-ch29, adc2

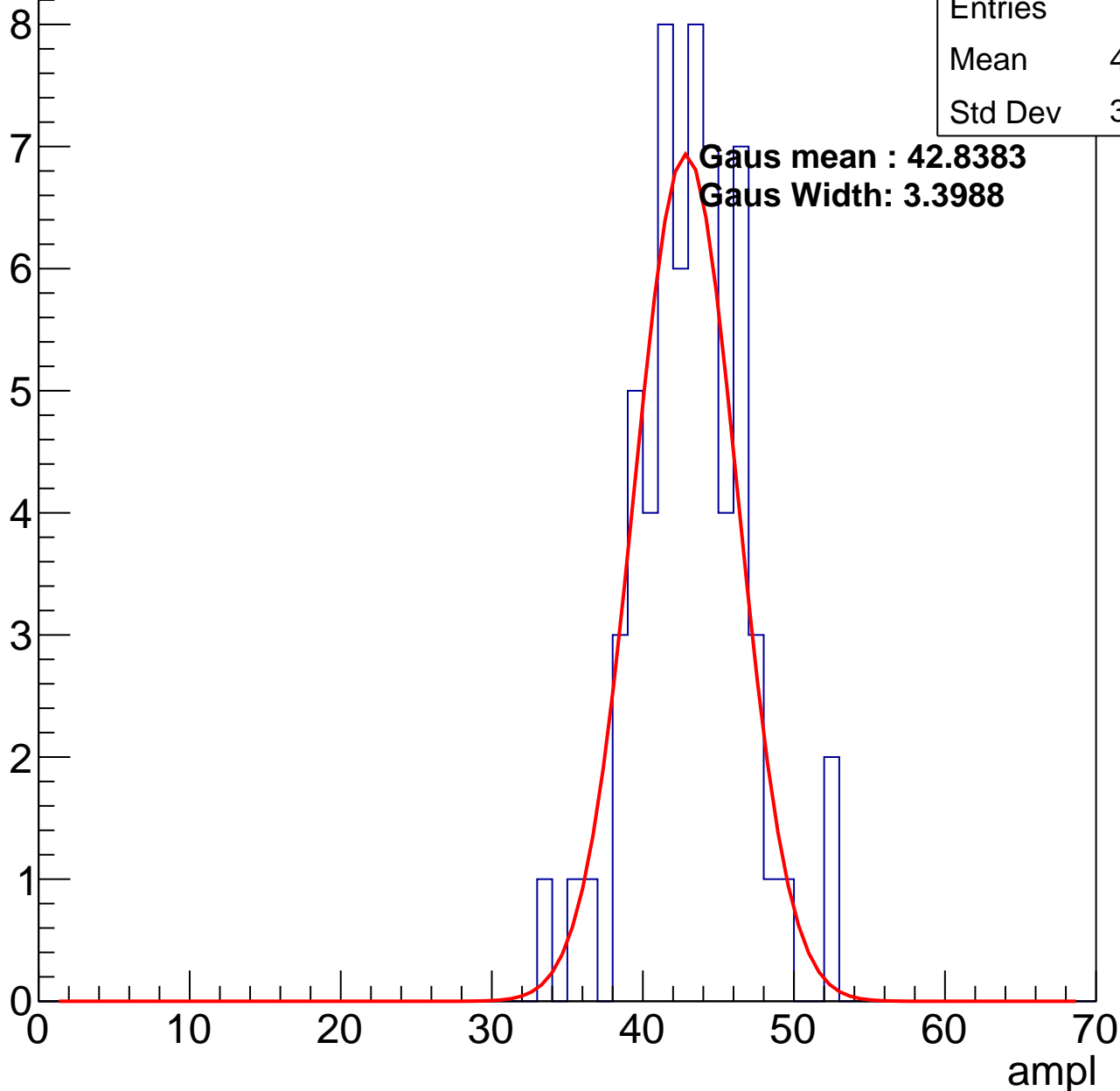
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	42.73
Std Dev	3.579

Gaus mean : 42.8383

Gaus Width: 3.3988

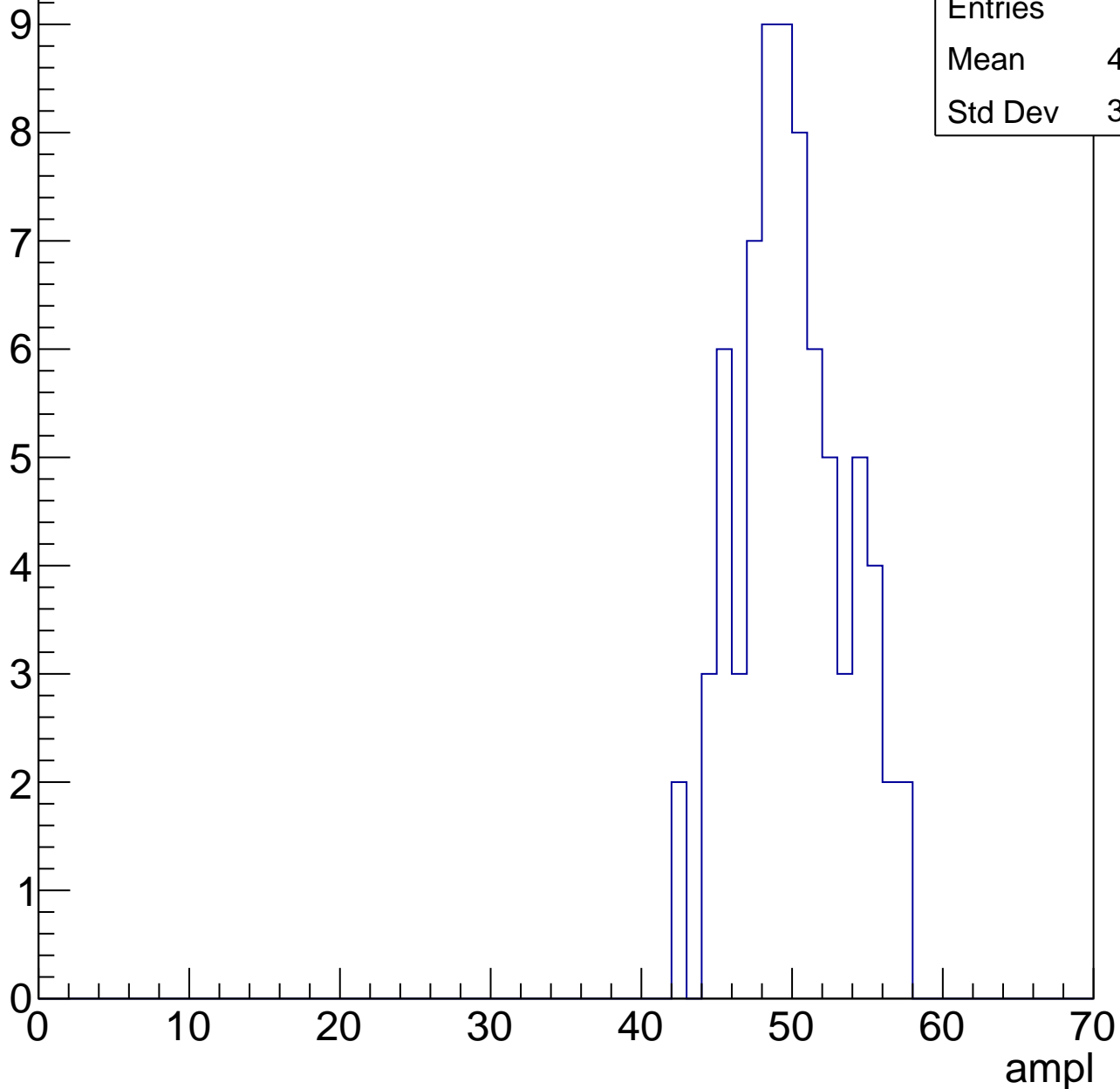


B0L001S, U14-ch29, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	49.55
Std Dev	3.546

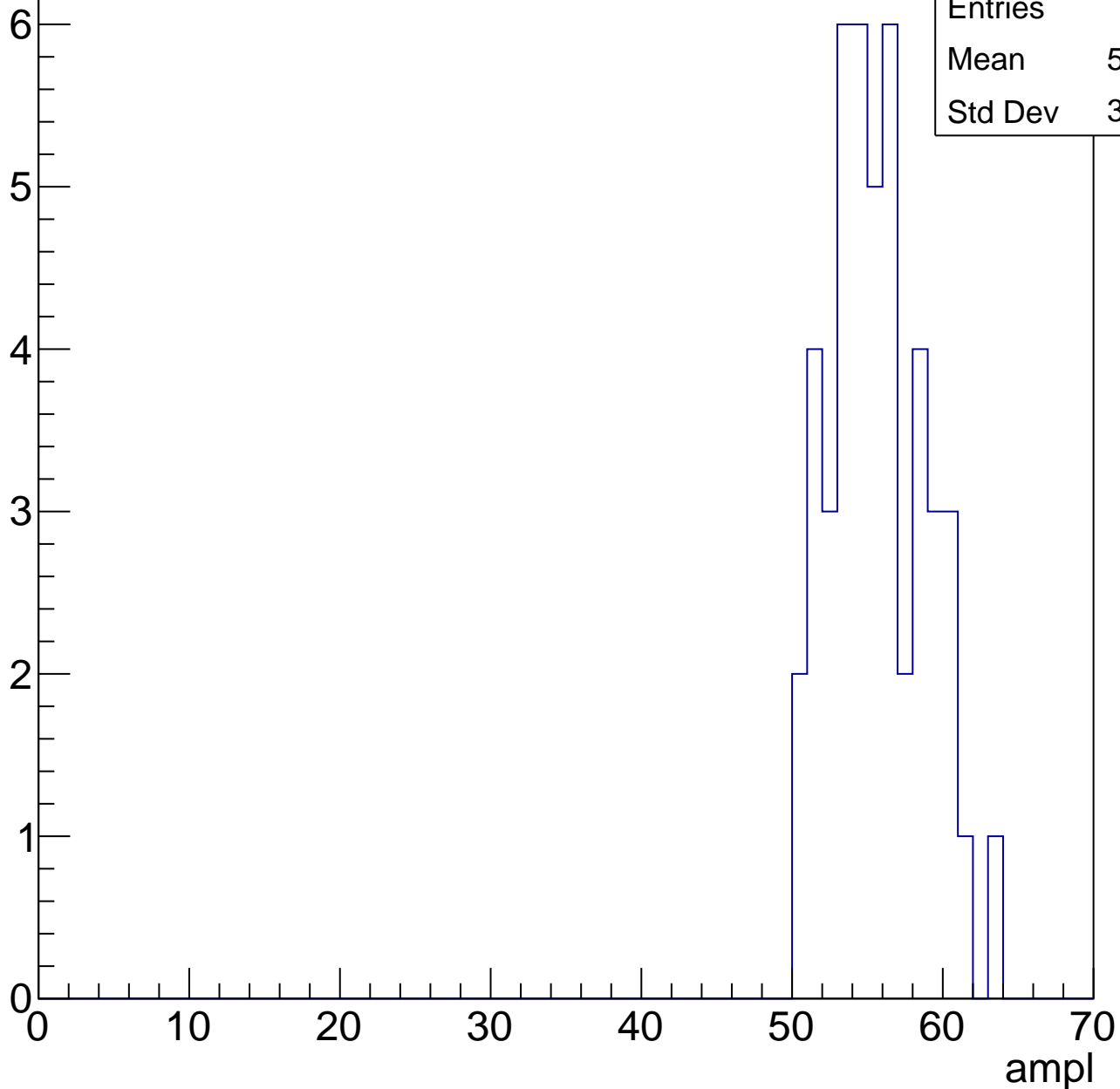


B0L001S, U14-ch29, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	55.22
Std Dev	3.099

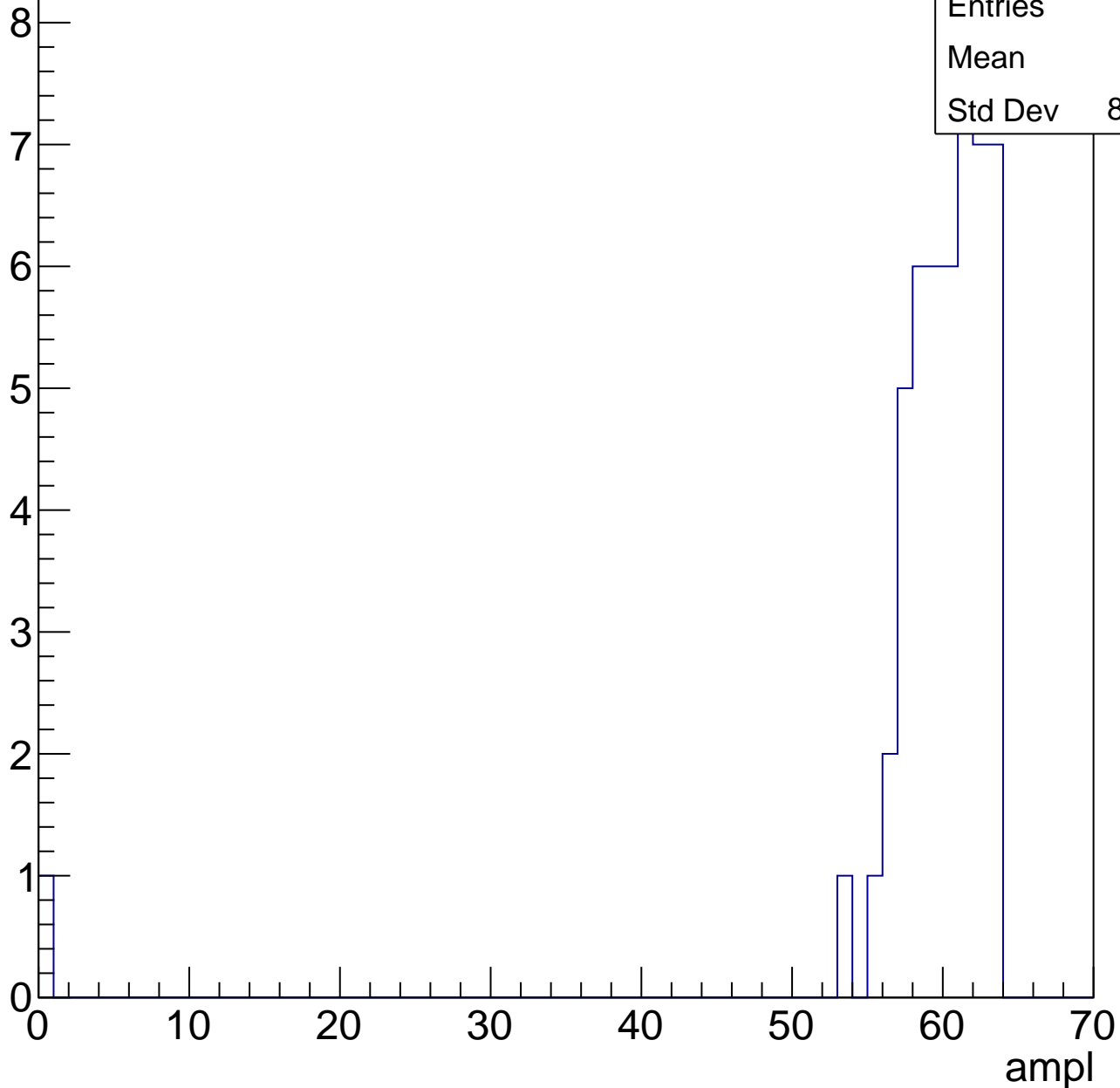


B0L001S, U14-ch29, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

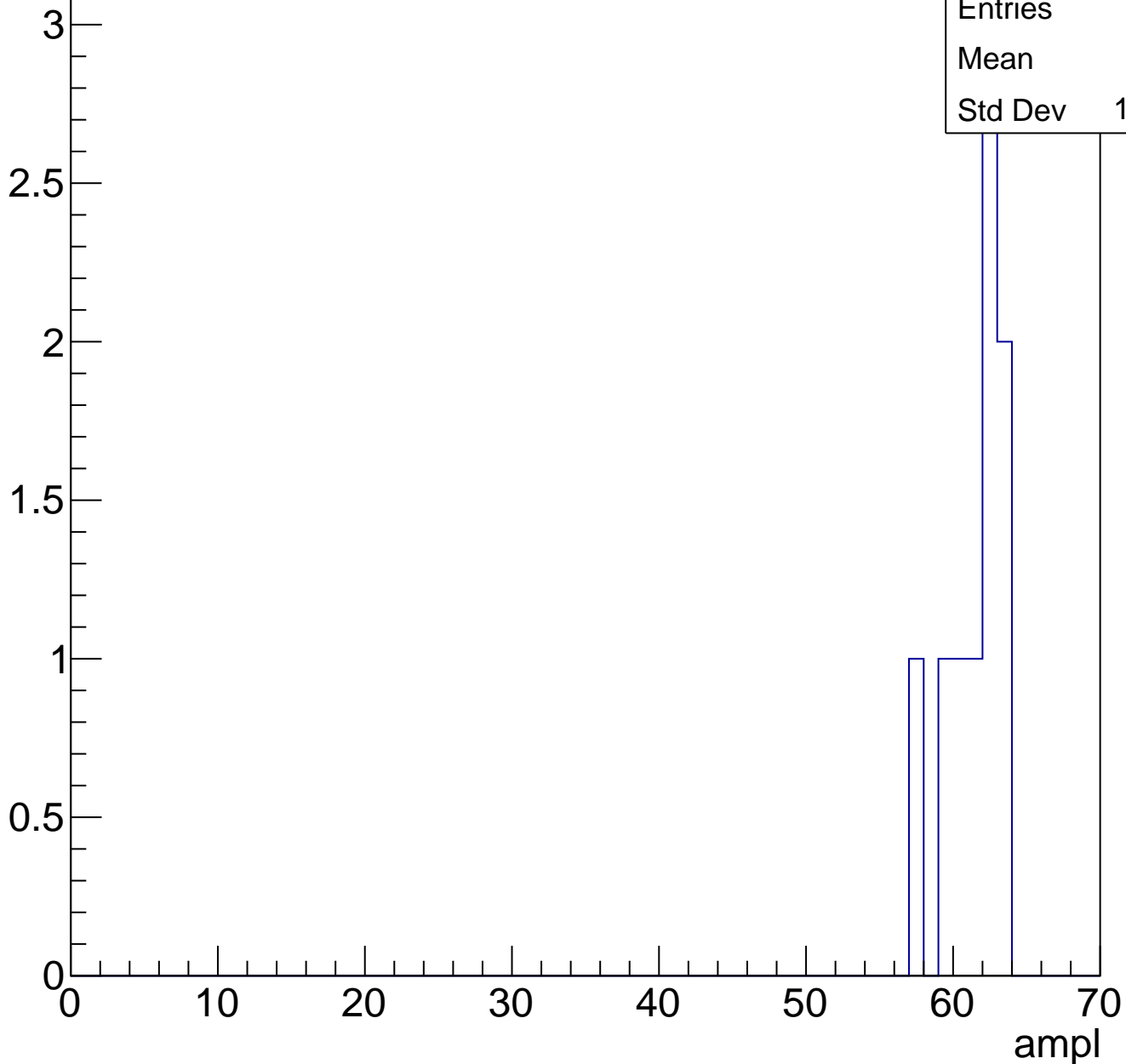
Entries	50
Mean	58.6
Std Dev	8.697



B0L001S, U14-ch29, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch29, adc7

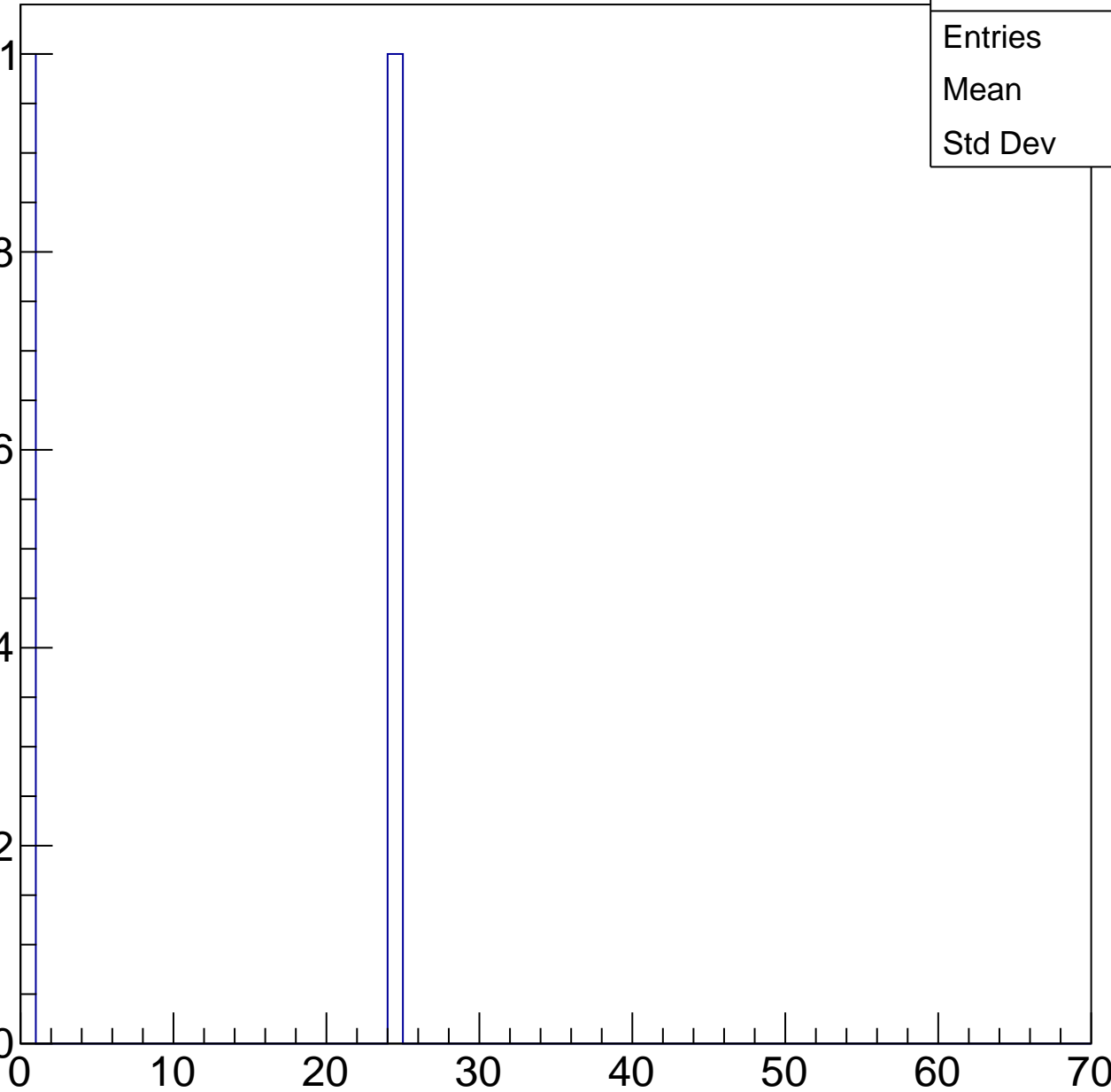
calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl



B0L001S, U14-ch30, adc0

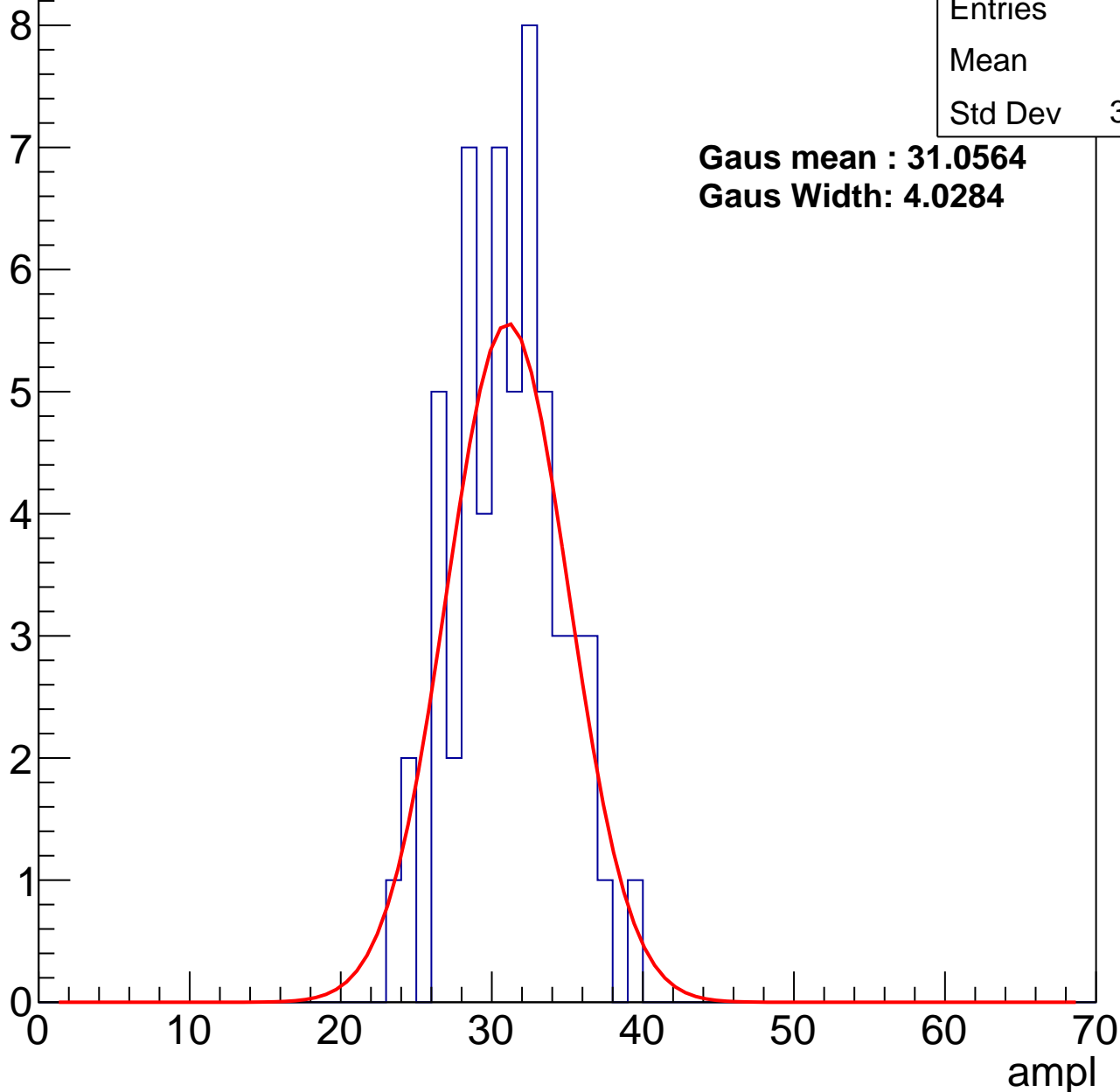
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	30.6
Std Dev	3.438

Gaus mean : 31.0564

Gaus Width: 4.0284



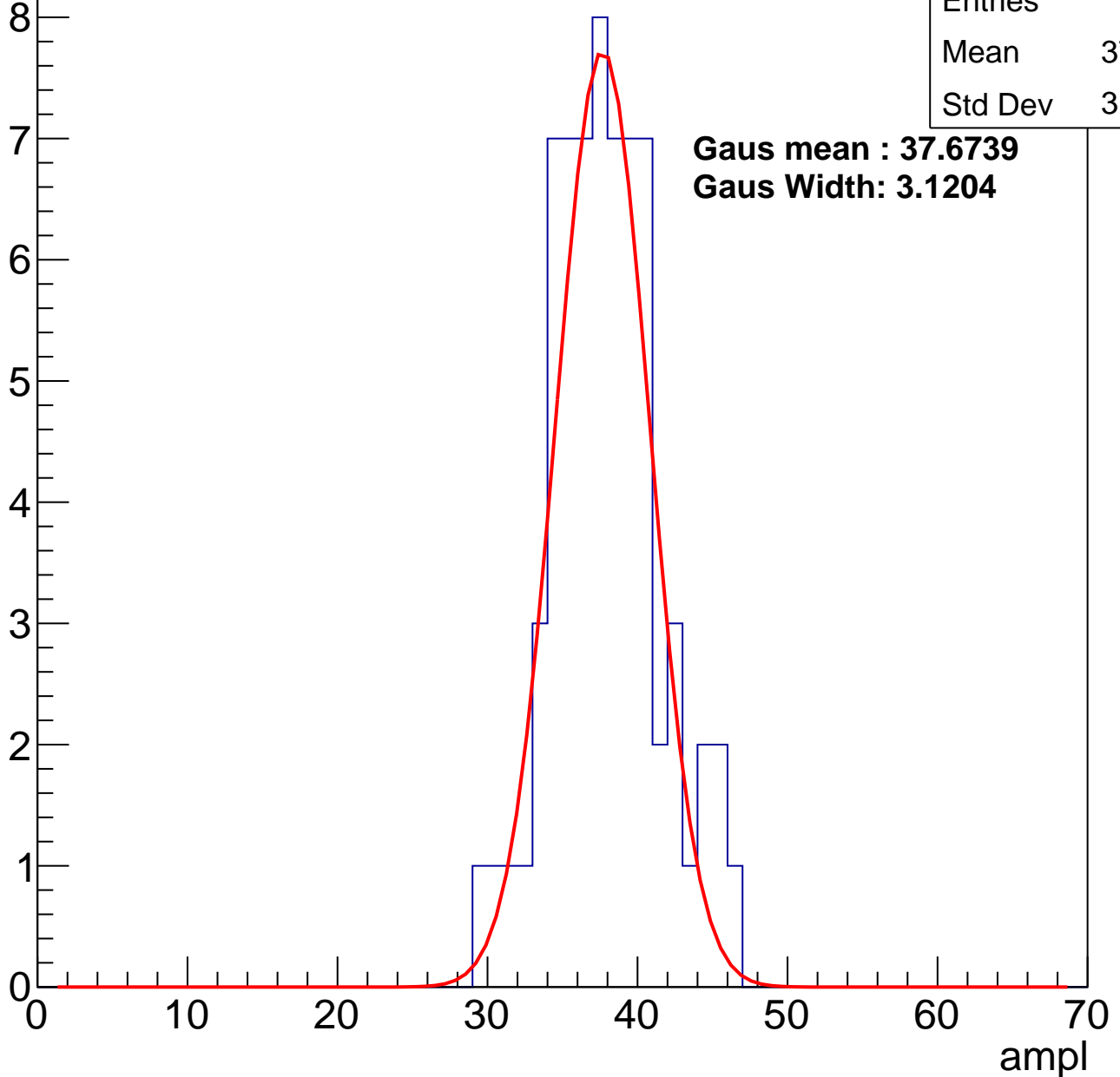
B0L001S, U14-ch30, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.44
Std Dev	3.545

Gaus mean : 37.6739
Gaus Width: 3.1204



B0L001S, U14-ch30, adc2

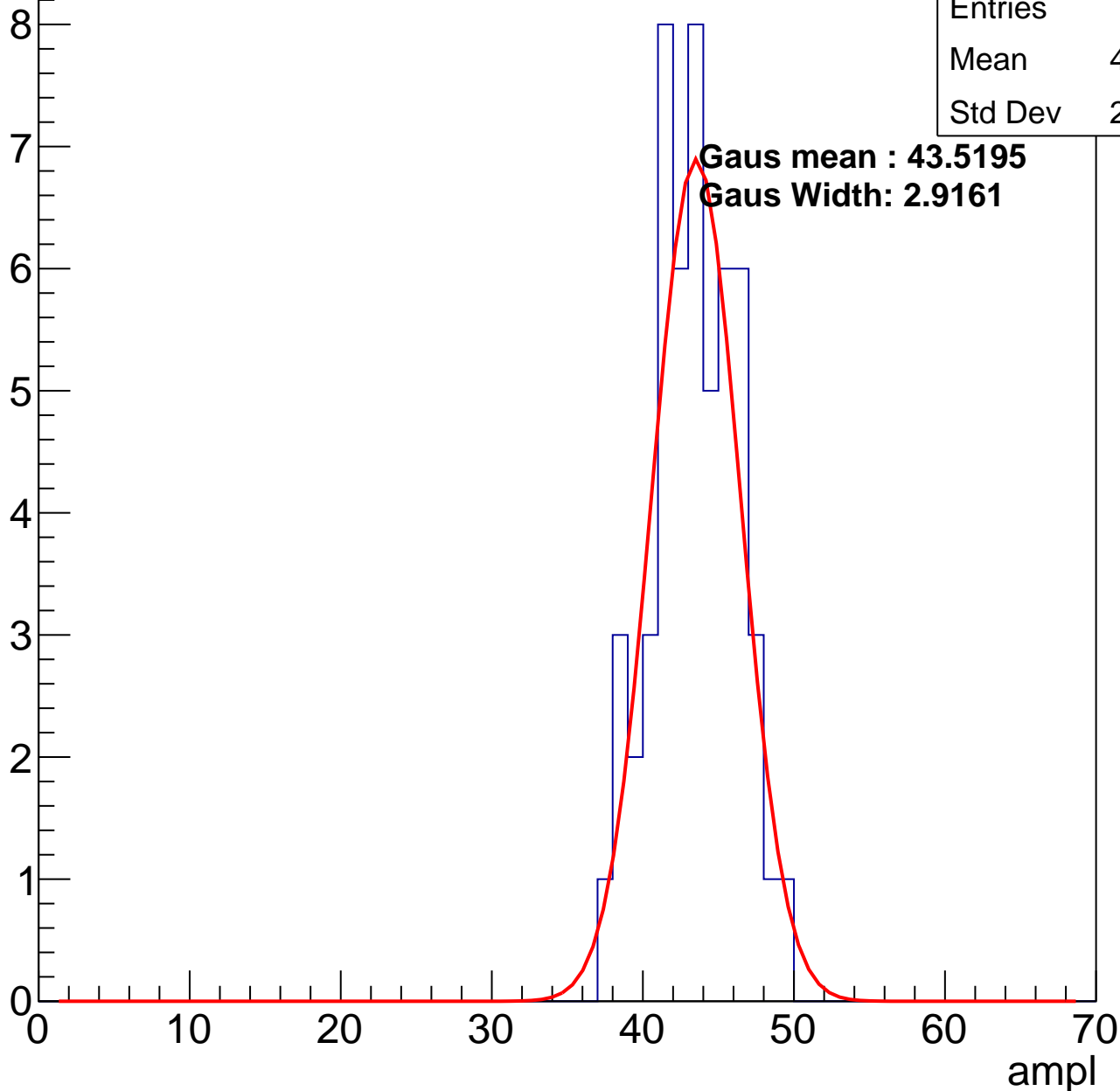
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.96
Std Dev	2.747

Gaus mean : 43.5195

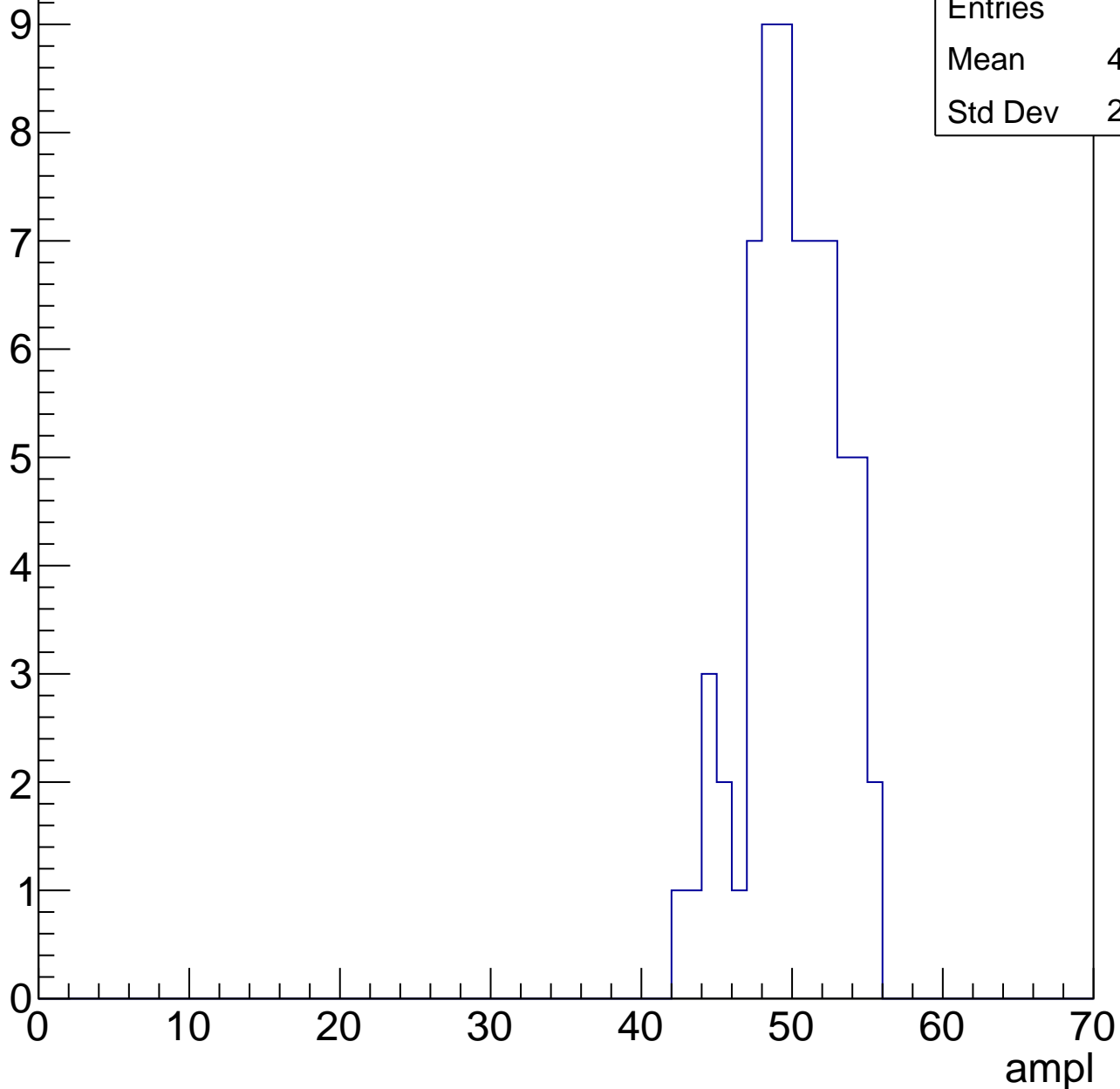
Gaus Width: 2.9161



B0L001S, U14-ch30, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



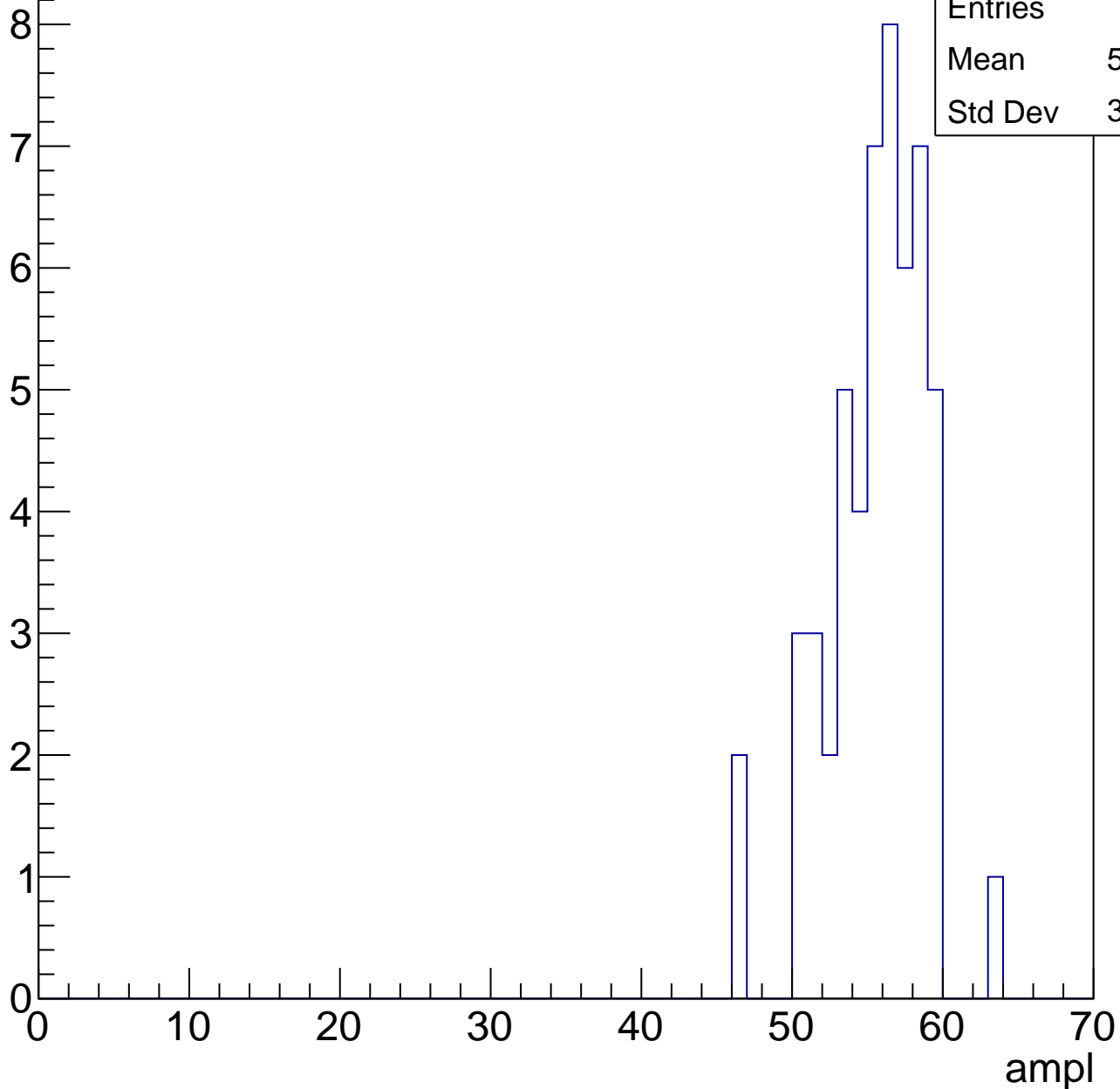
Entries	66
Mean	49.56
Std Dev	2.996

B0L001S, U14-ch30, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.08
Std Dev	3.267

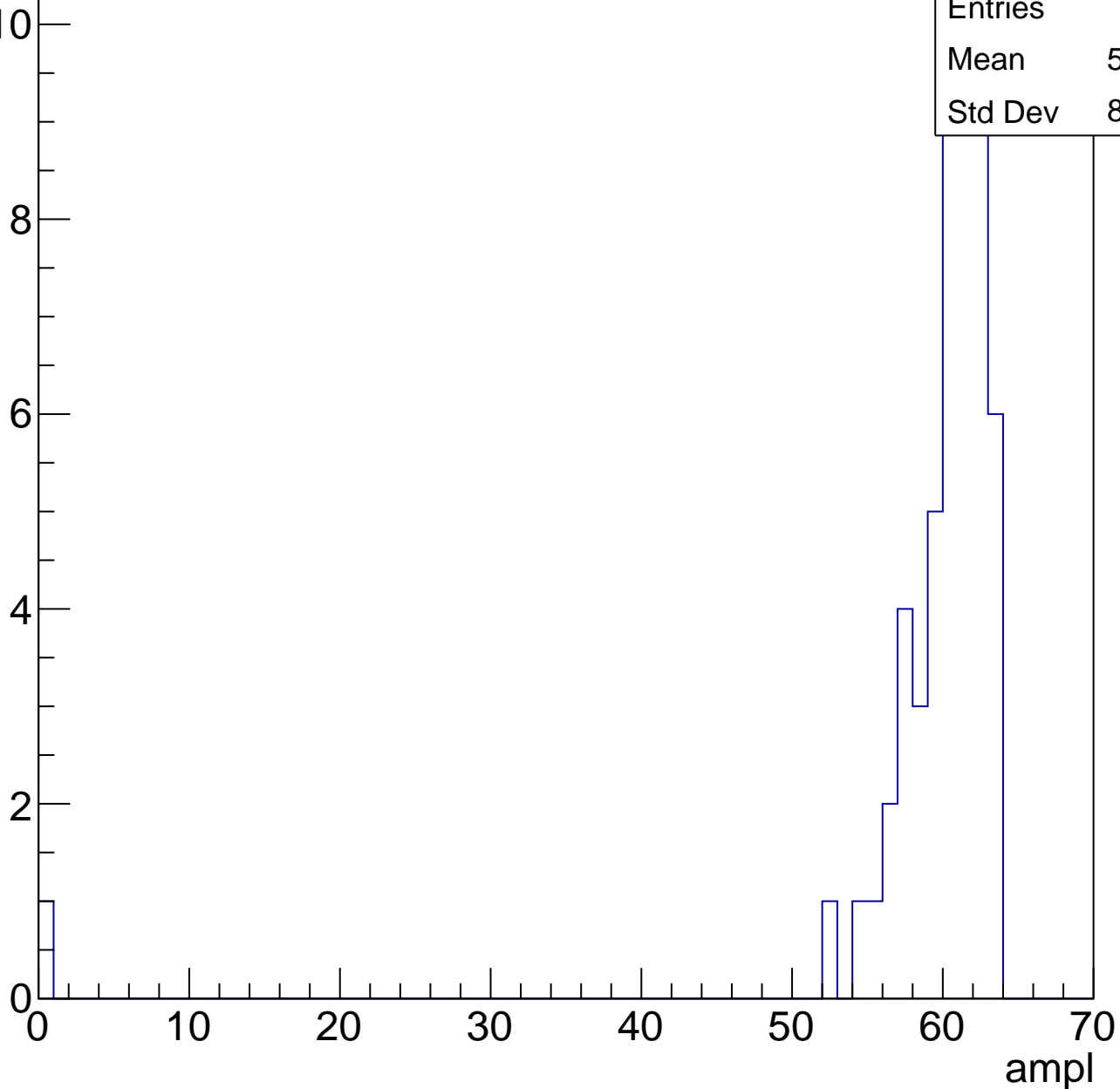


B0L001S, U14-ch30, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.79
Std Dev	8.504



B0L001S, U14-ch30, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch30, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch31, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	31.31
Std Dev	6.02

Gaus mean : 32.4685

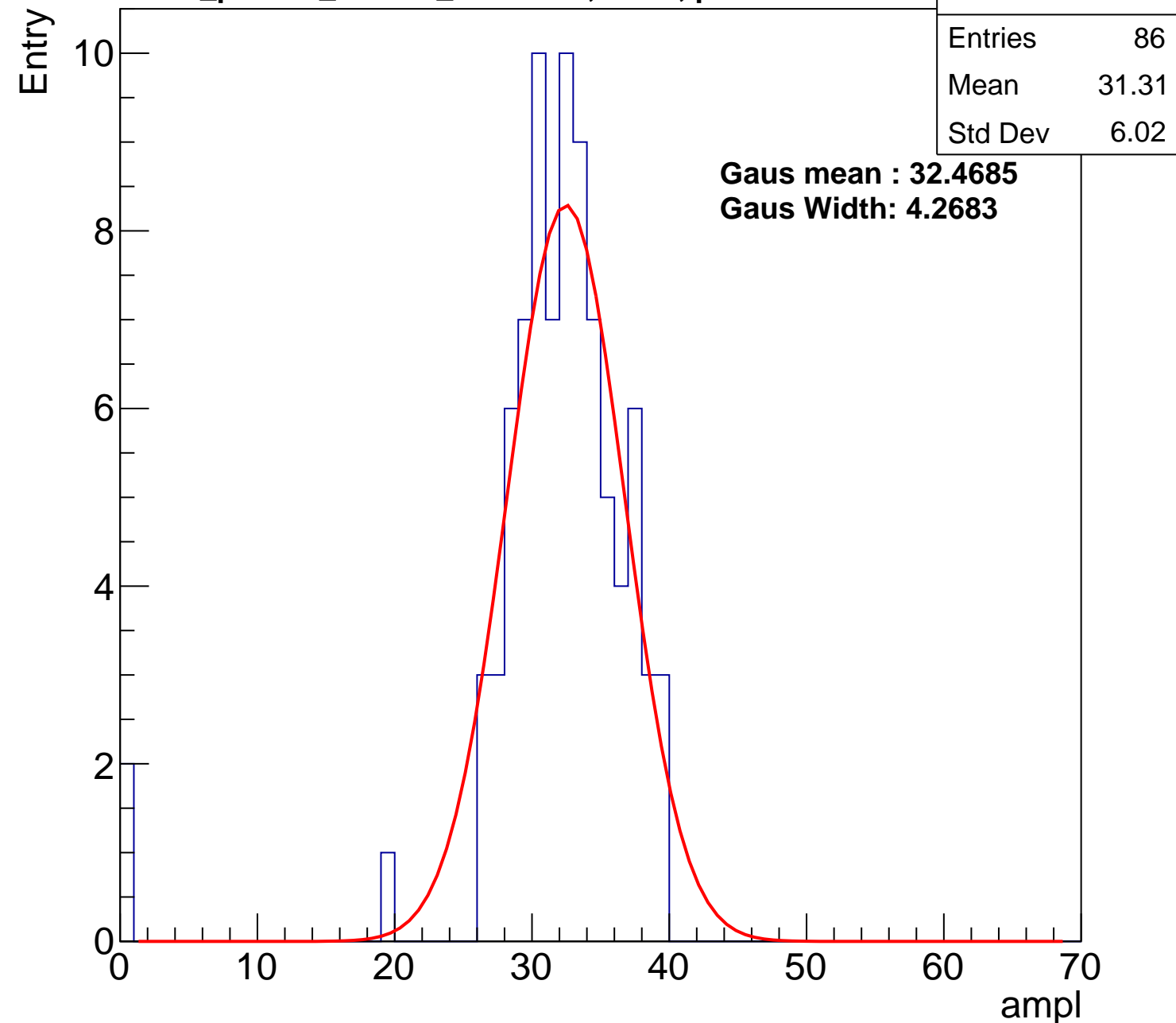
Gaus Width: 4.2683

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch31, adc1

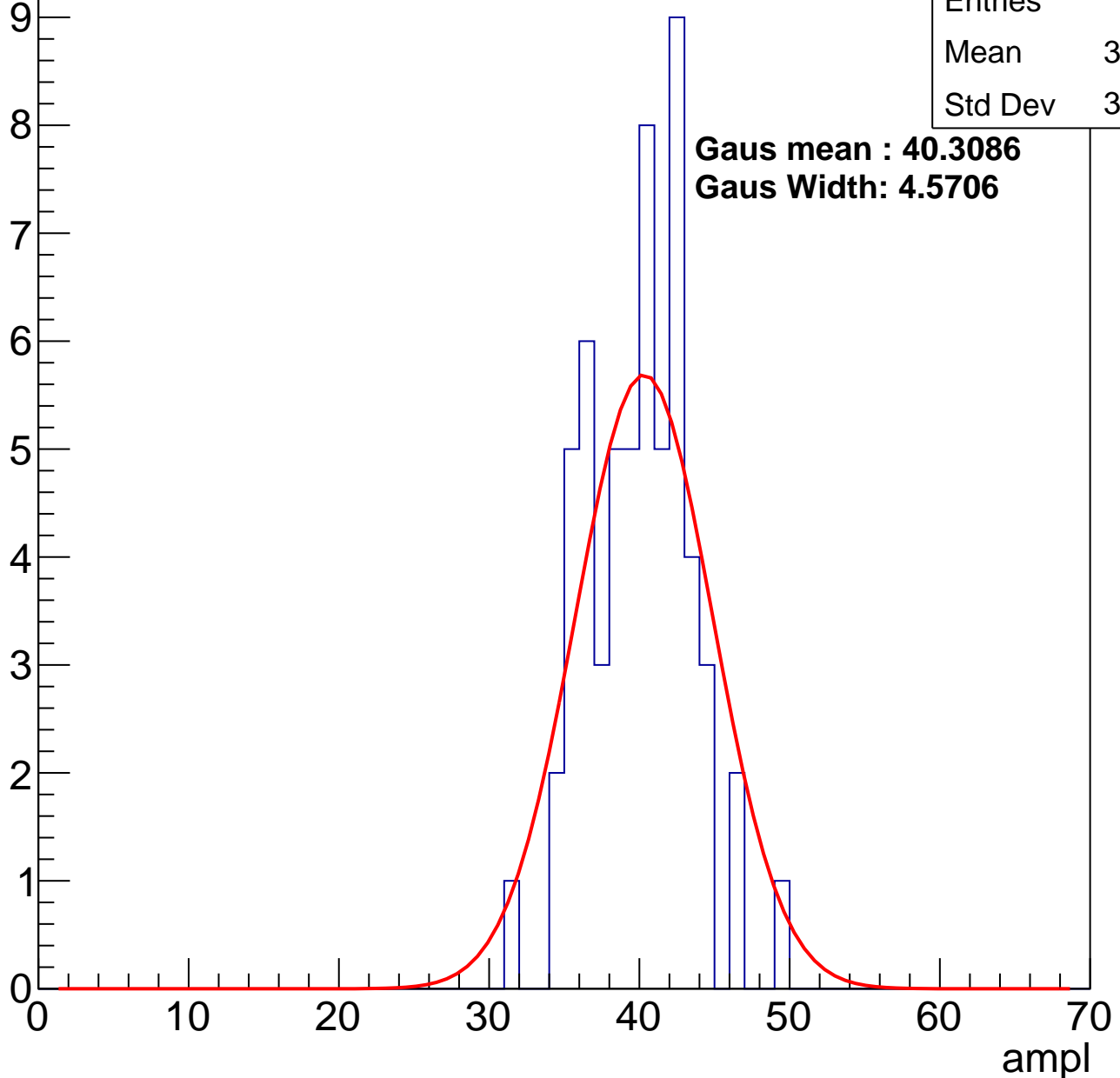
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	39.56
Std Dev	3.426

Gaus mean : 40.3086

Gaus Width: 4.5706



B0L001S, U14-ch31, adc2

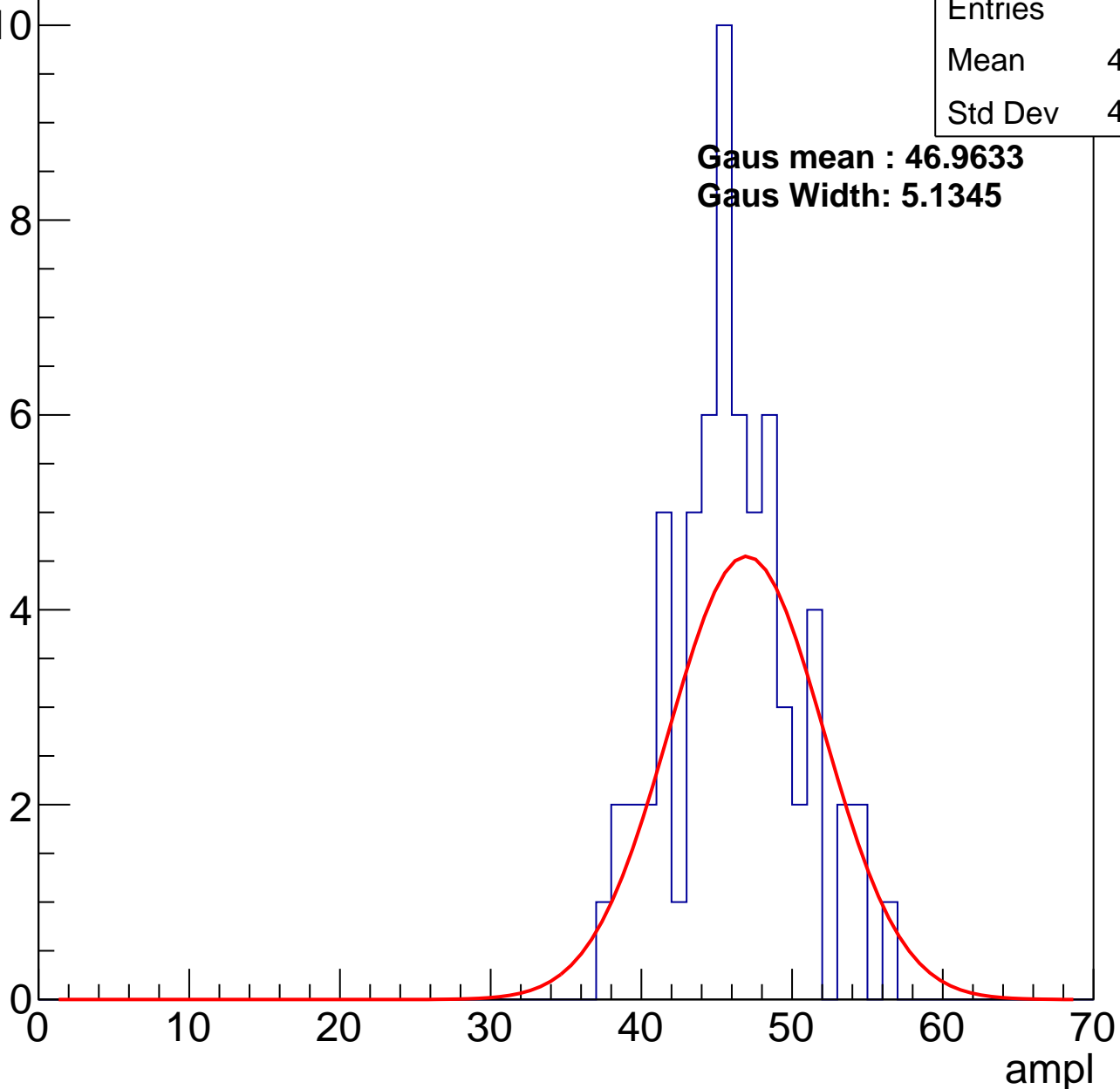
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.65
Std Dev	4.123

Gaus mean : 46.9633

Gaus Width: 5.1345

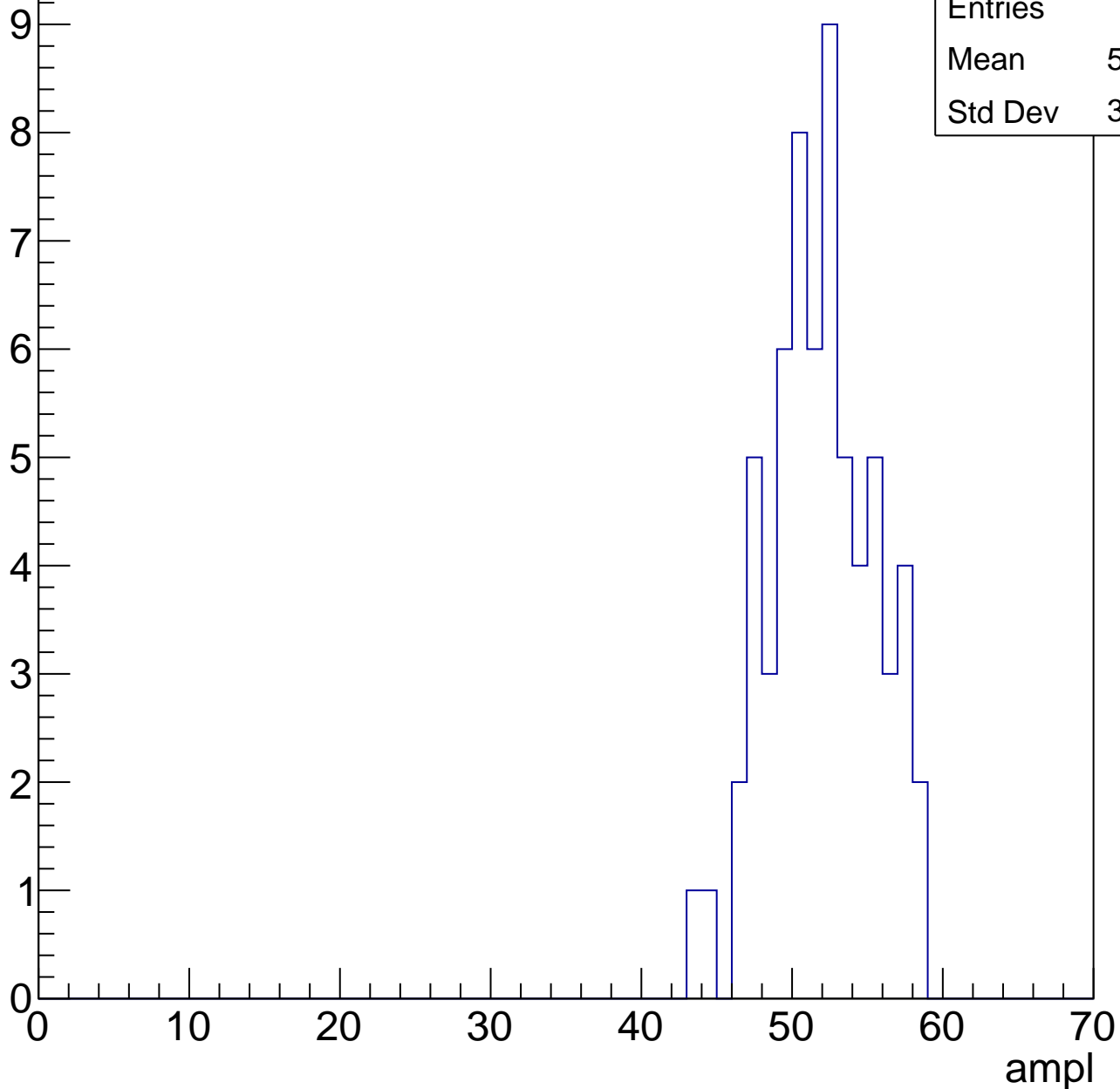


B0L001S, U14-ch31, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.47
Std Dev	3.423

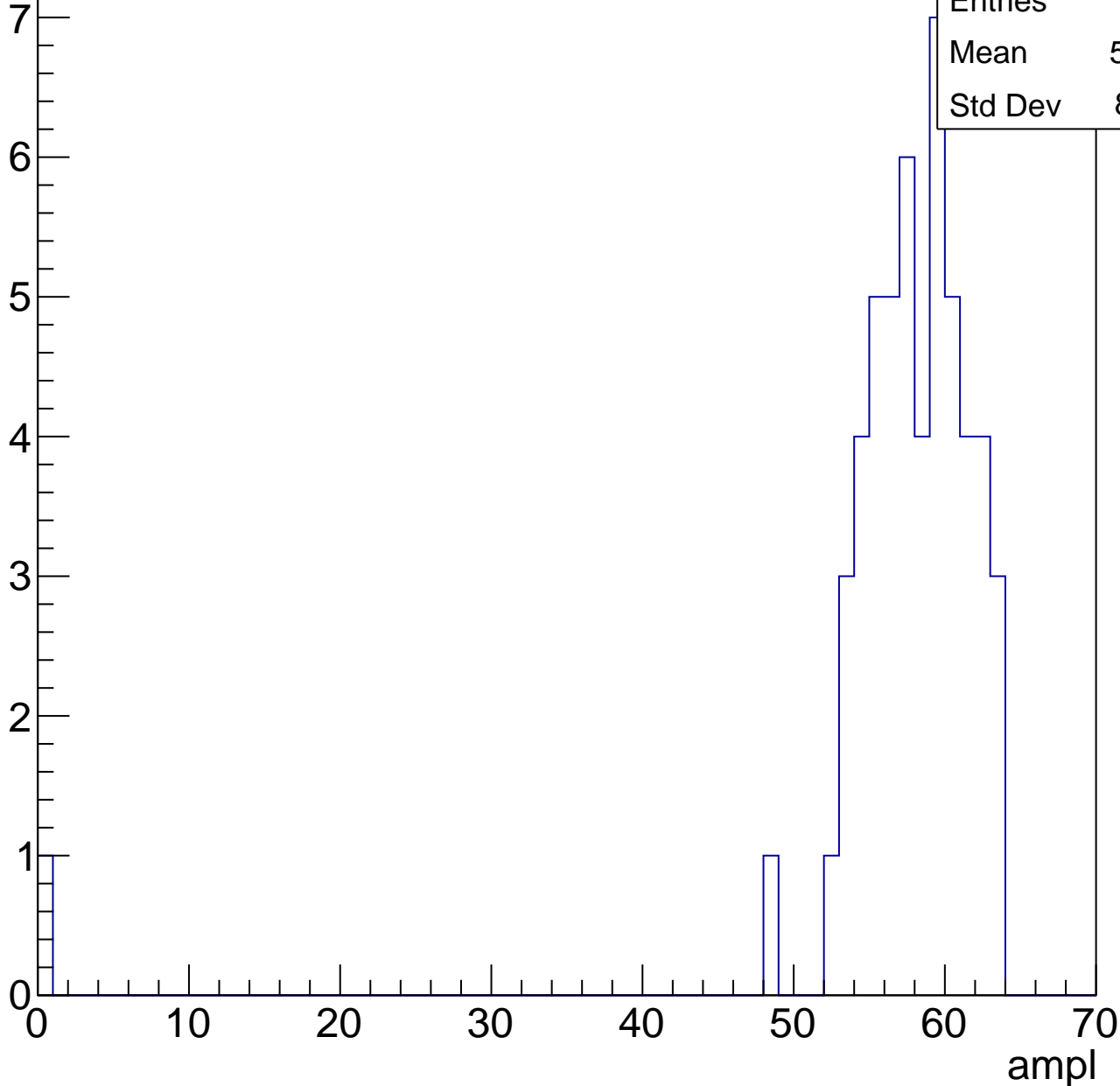


B0L001S, U14-ch31, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	56.57
Std Dev	8.471

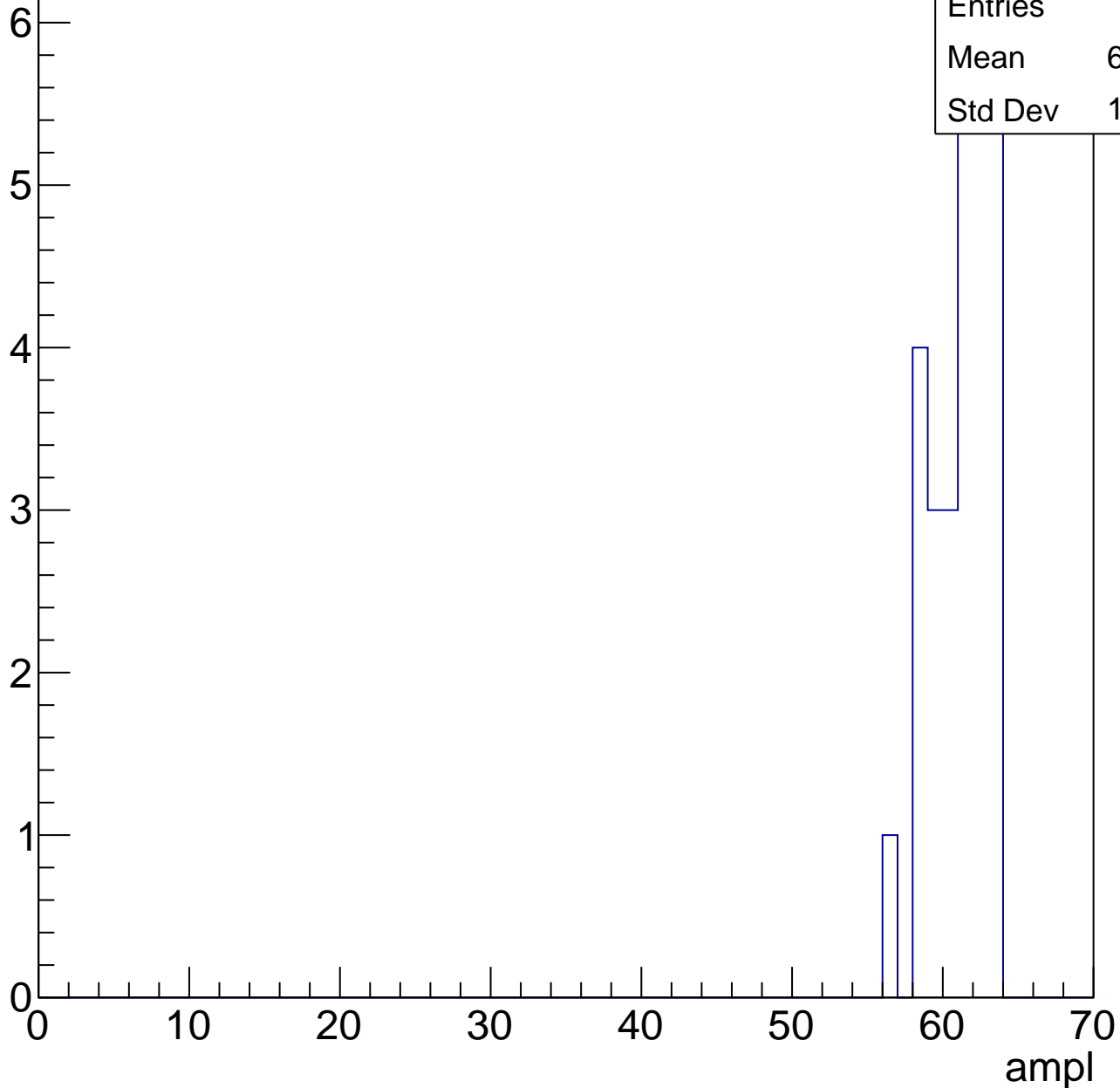


B0L001S, U14-ch31, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

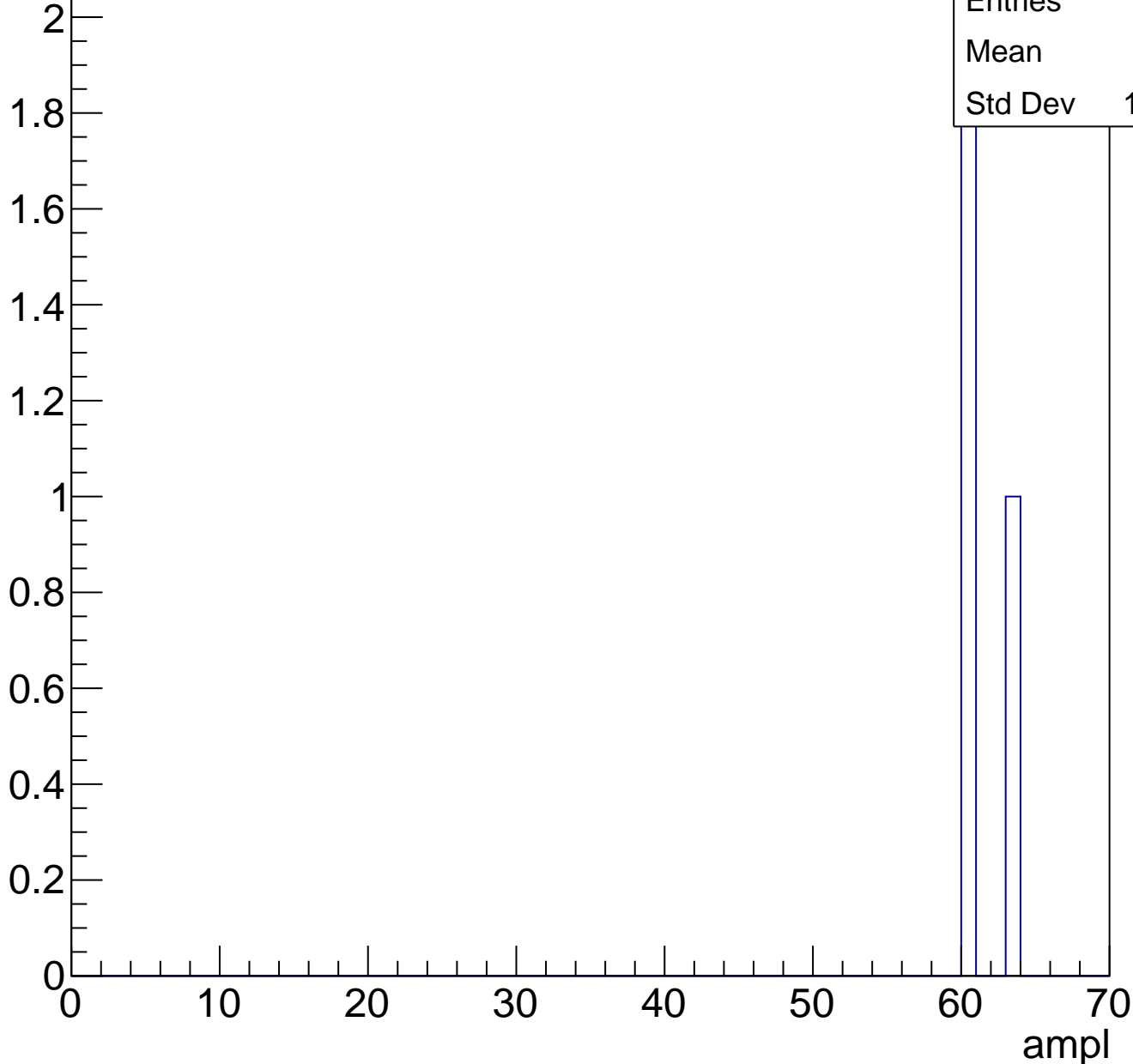
Entries	29
Mean	60.72
Std Dev	1.892



B0L001S, U14-ch31, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch31, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch32, adc0

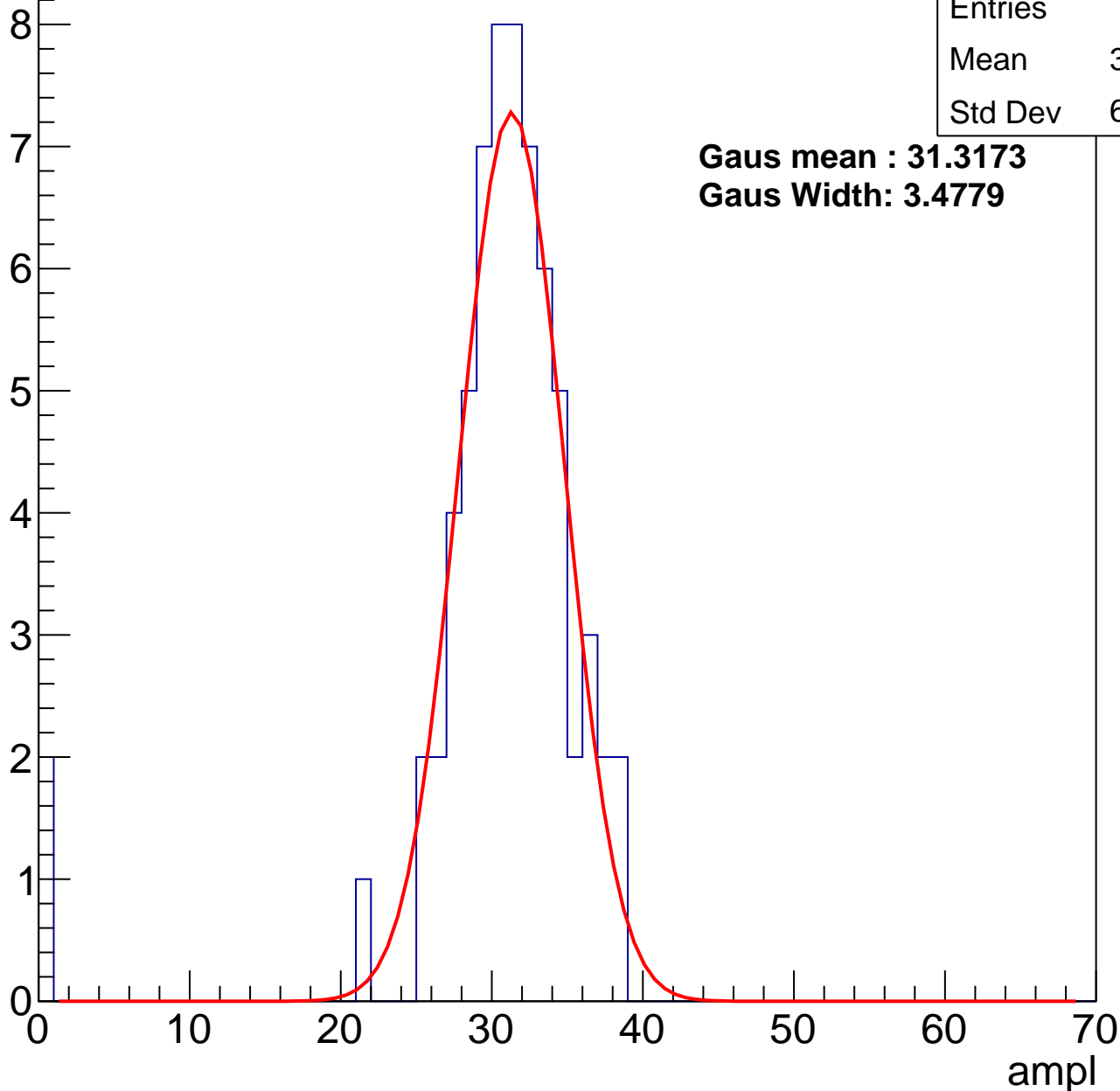
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.03
Std Dev	6.252

Gaus mean : 31.3173

Gaus Width: 3.4779



B0L001S, U14-ch32, adc1

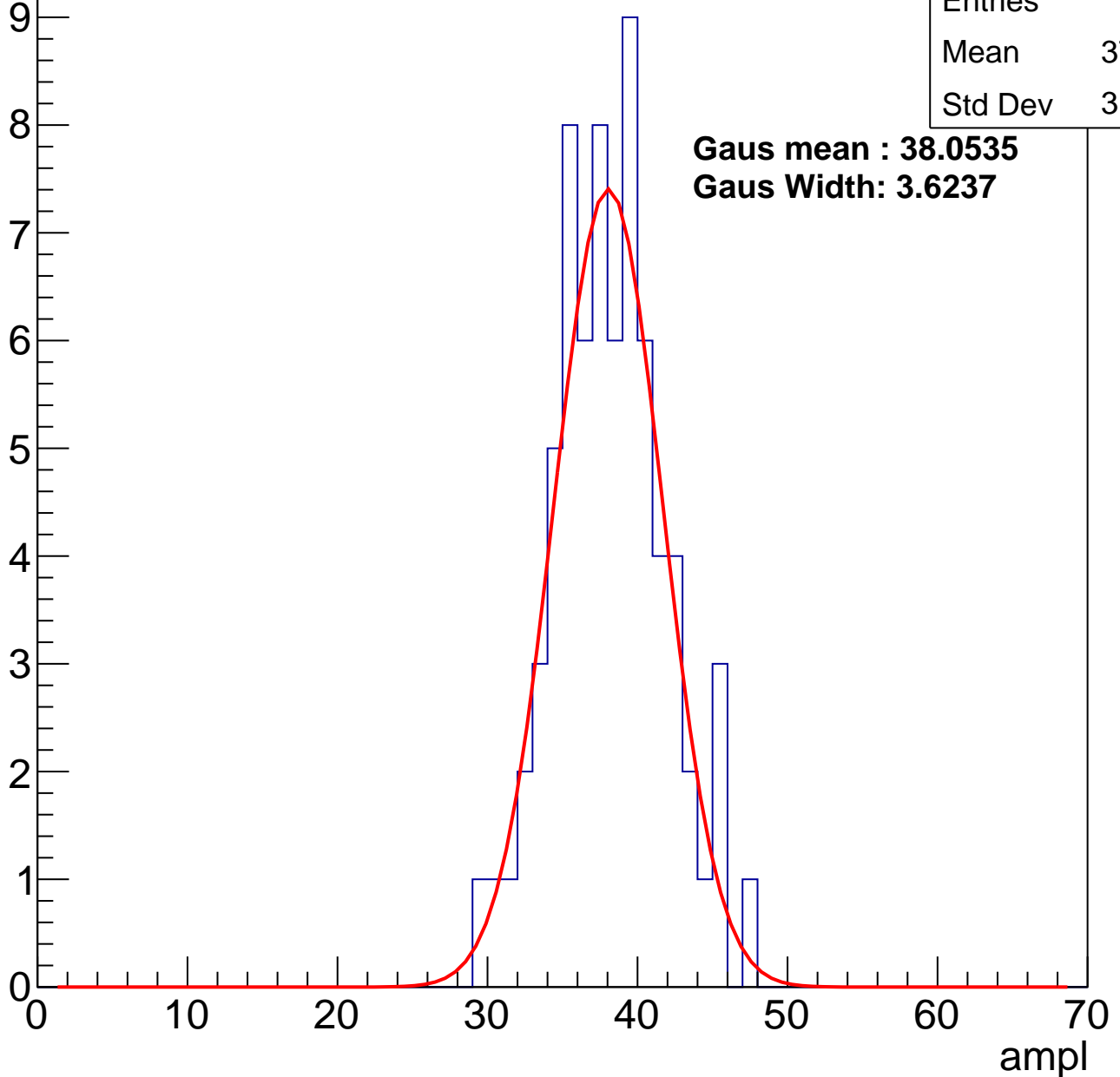
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	37.72
Std Dev	3.689

Gaus mean : 38.0535

Gaus Width: 3.6237



B0L001S, U14-ch32, adc2

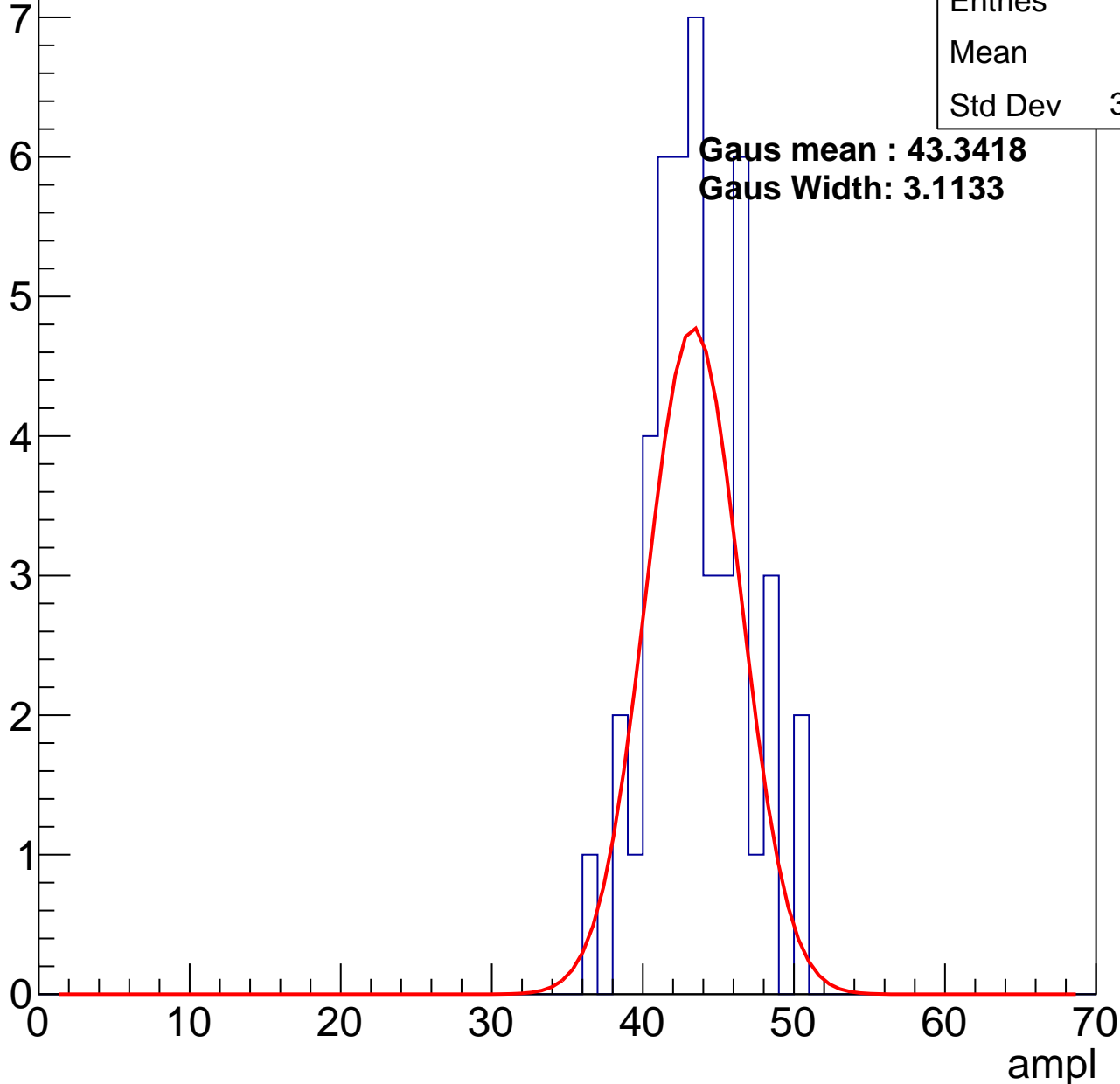
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	43.2
Std Dev	3.117

Gaus mean : 43.3418

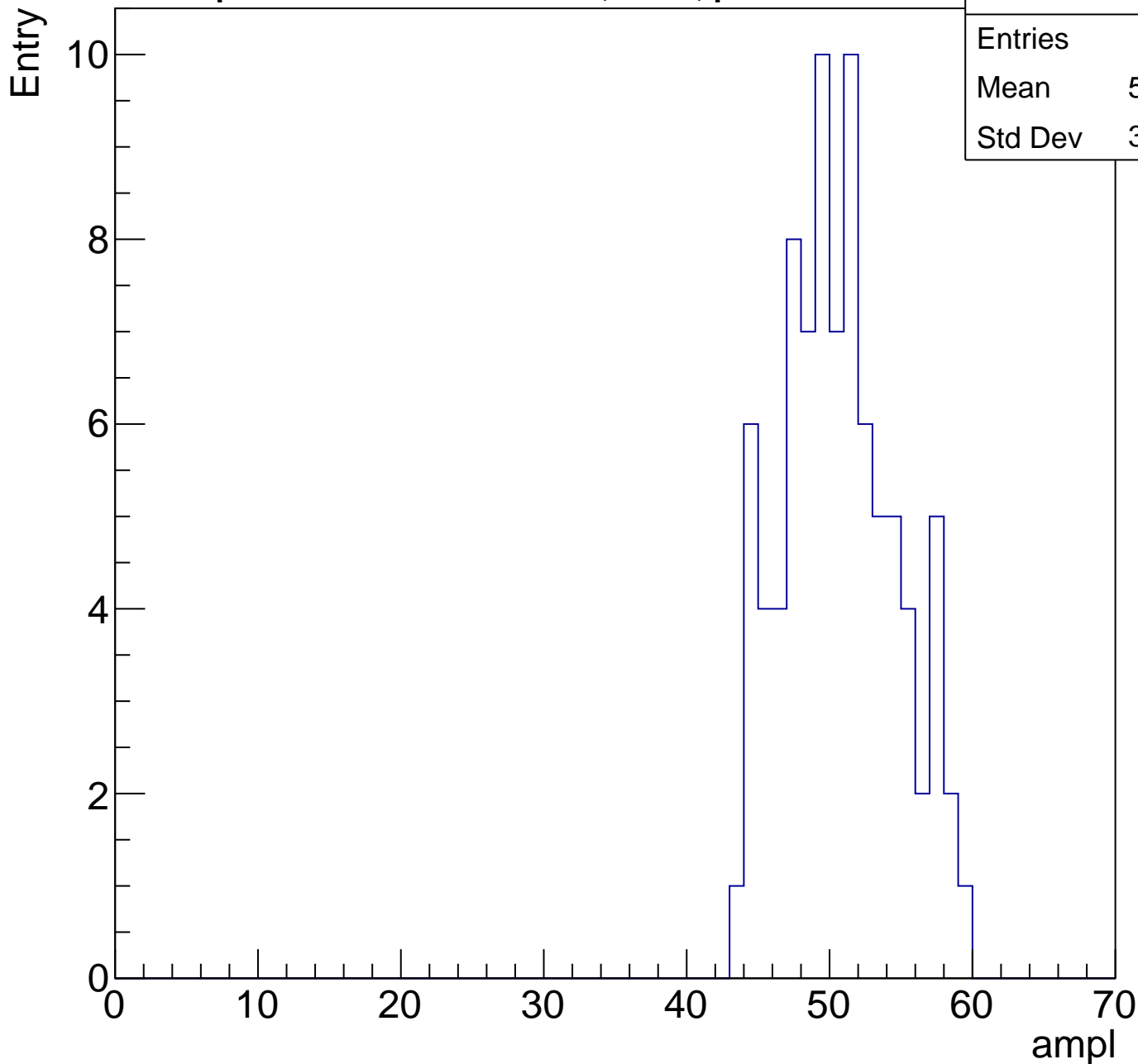
Gaus Width: 3.1133



B0L001S, U14-ch32, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	50.25
Std Dev	3.893

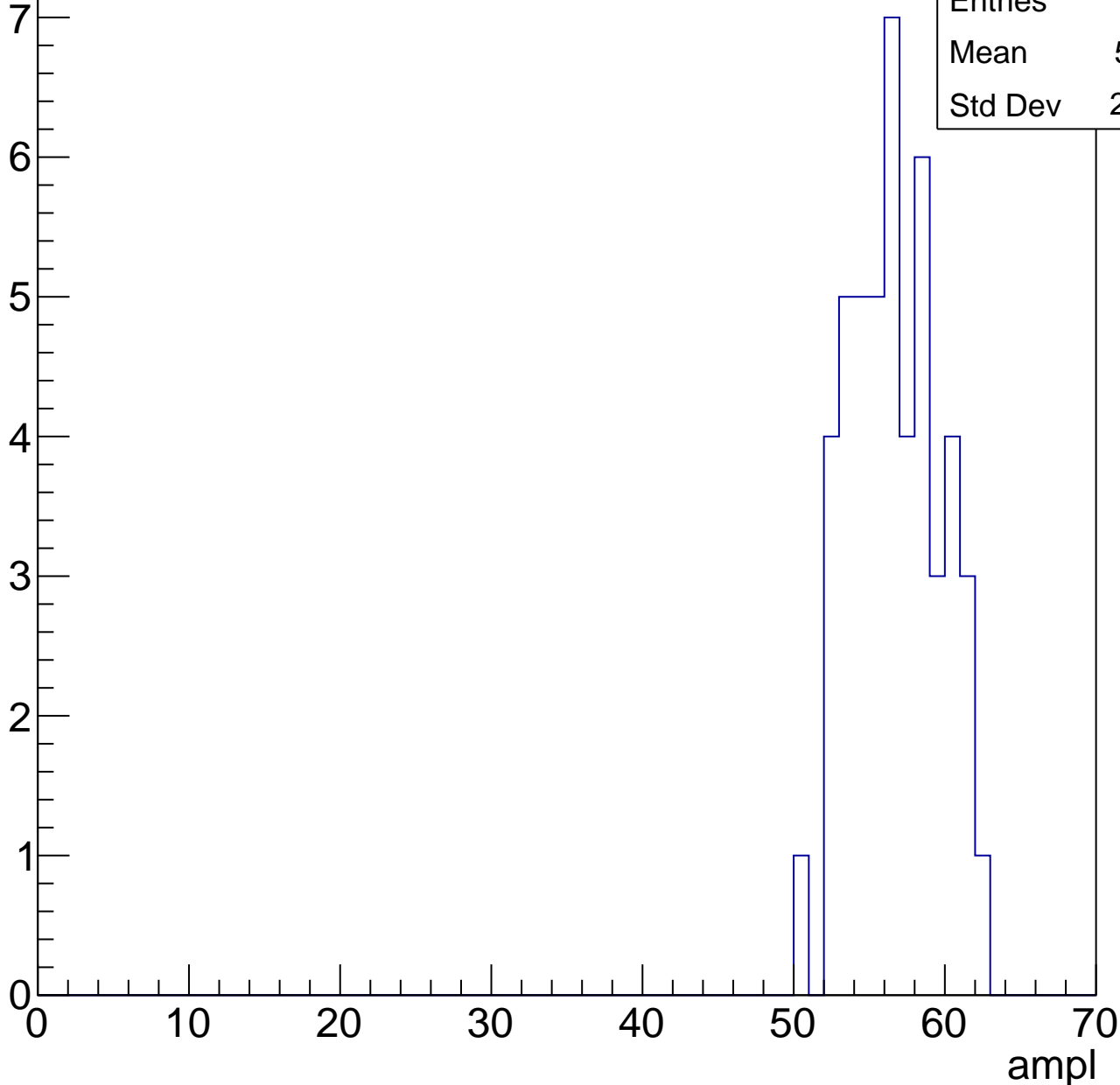


B0L001S, U14-ch32, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.21
Std Dev	2.879

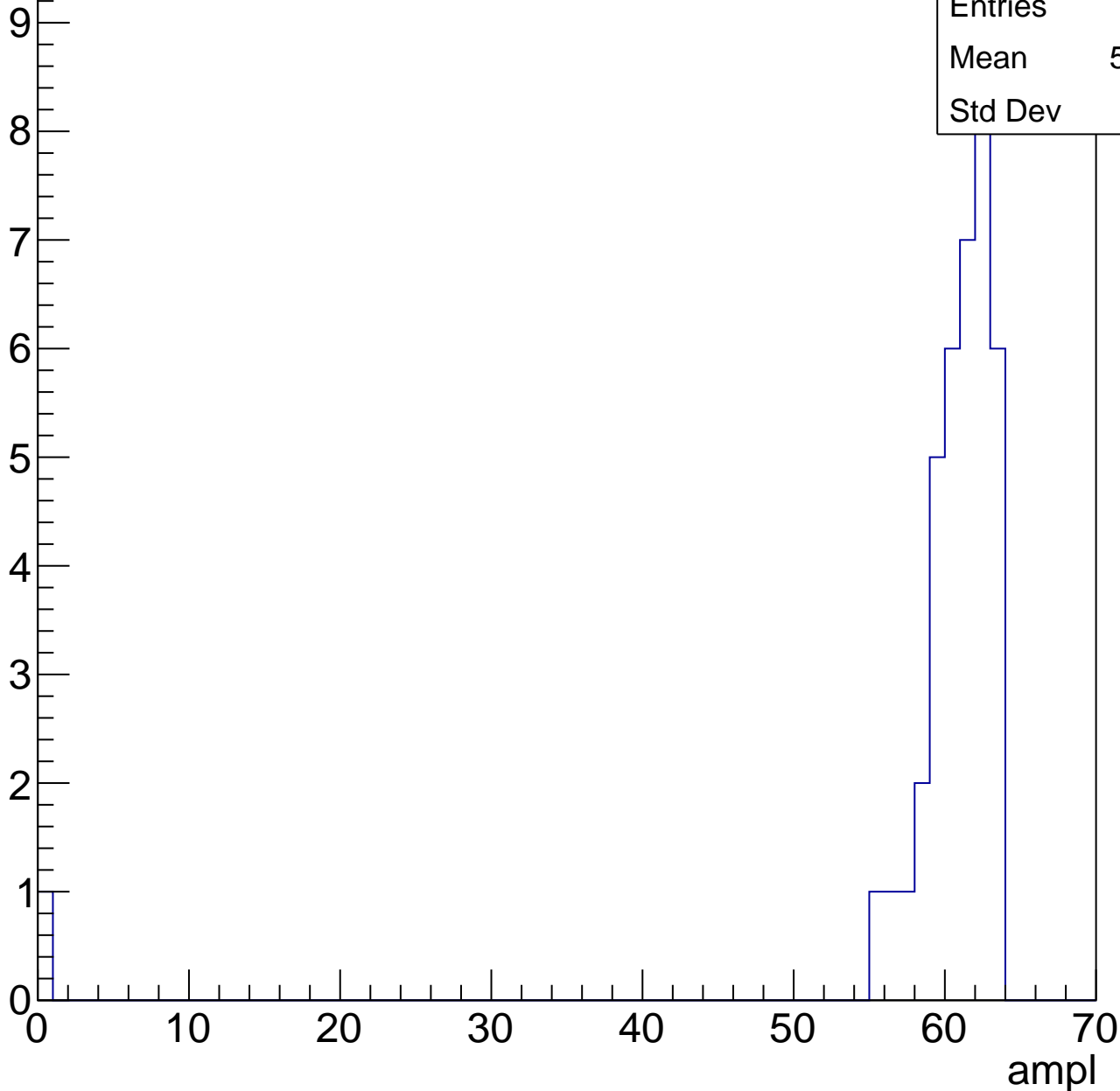


B0L001S, U14-ch32, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

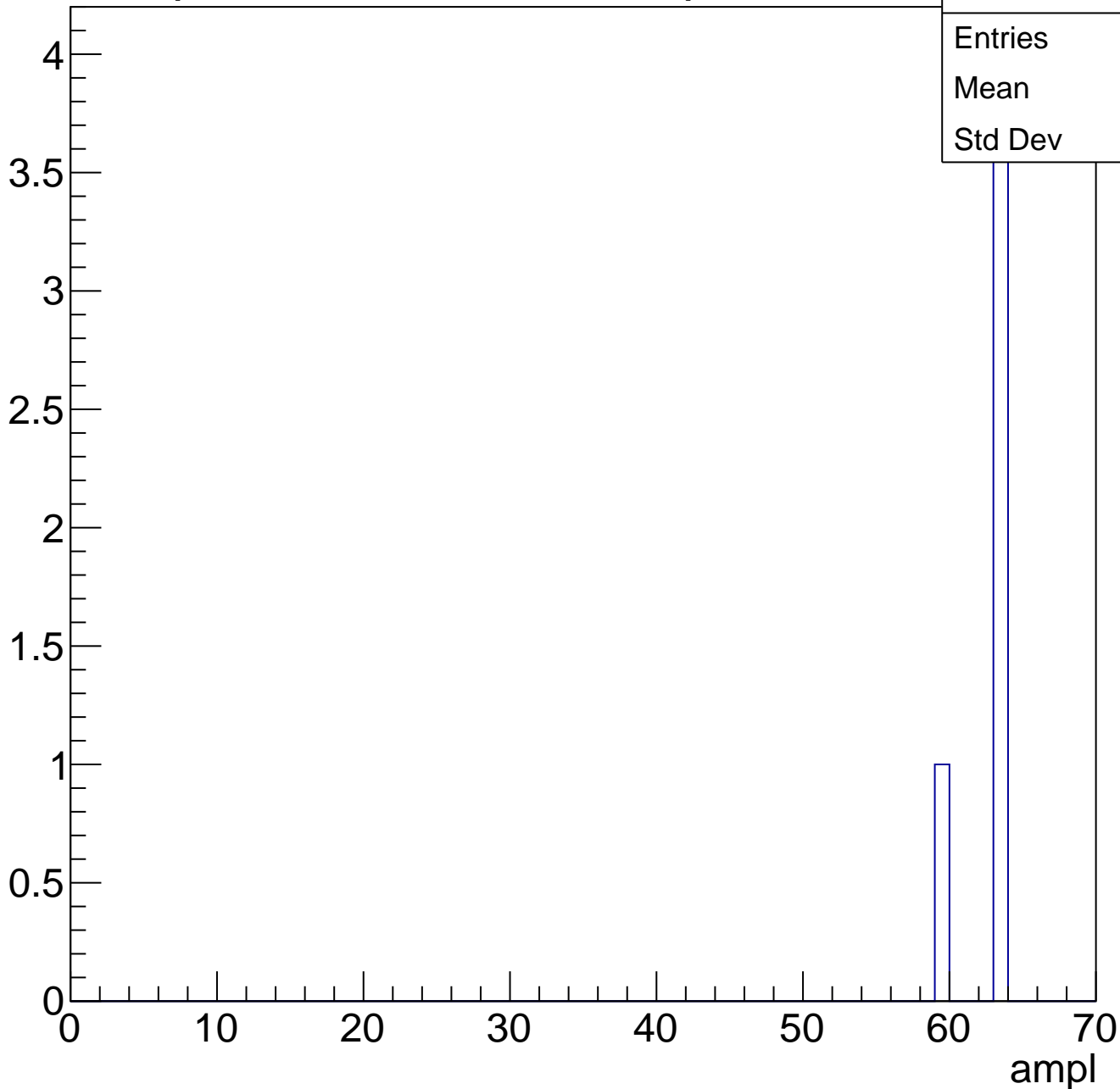
Entries	39
Mean	59.03
Std Dev	9.77



B0L001S, U14-ch32, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch32, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch33, adc0

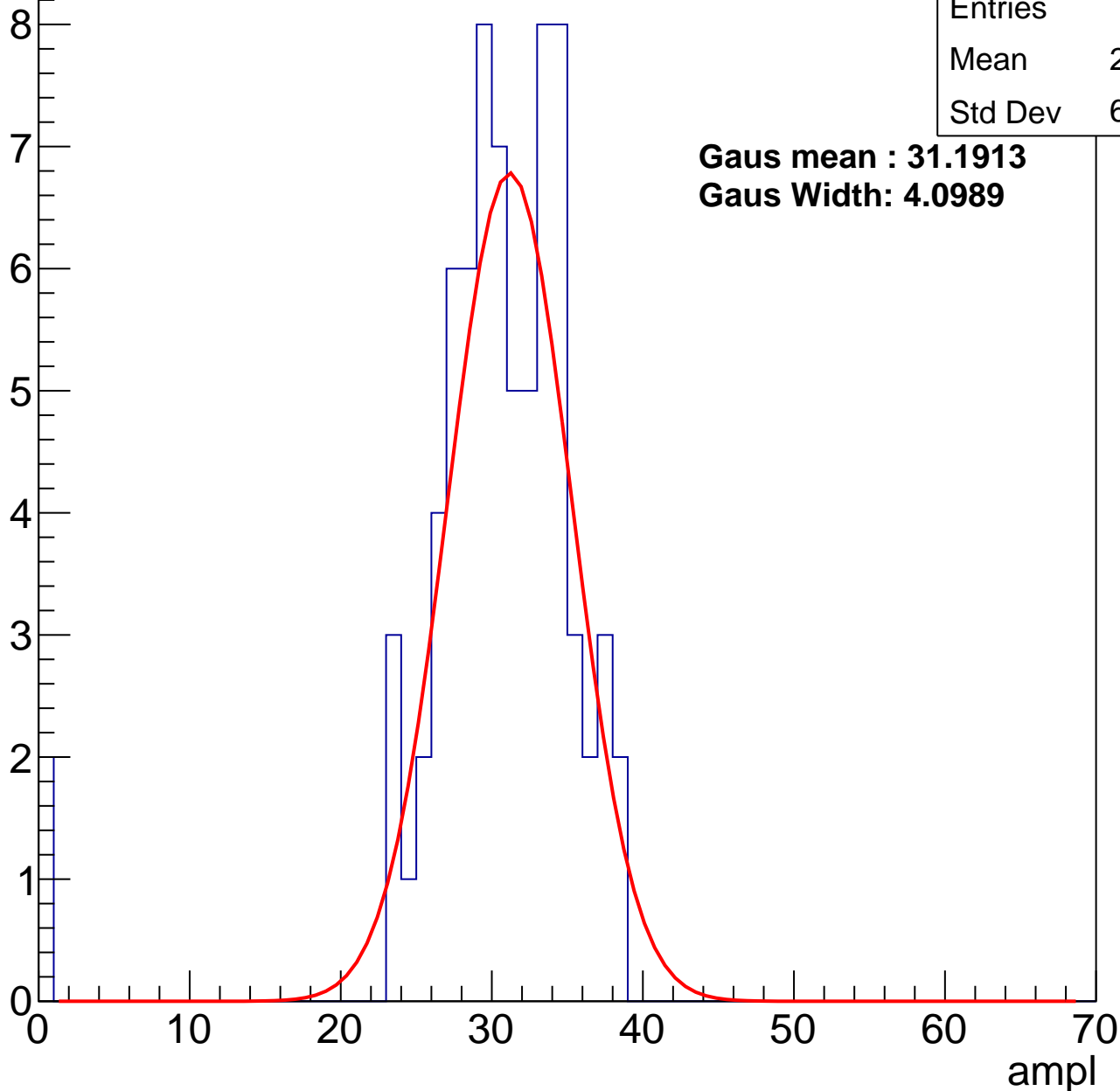
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.79
Std Dev	6.139

Gaus mean : 31.1913

Gaus Width: 4.0989



B0L001S, U14-ch33, adc1

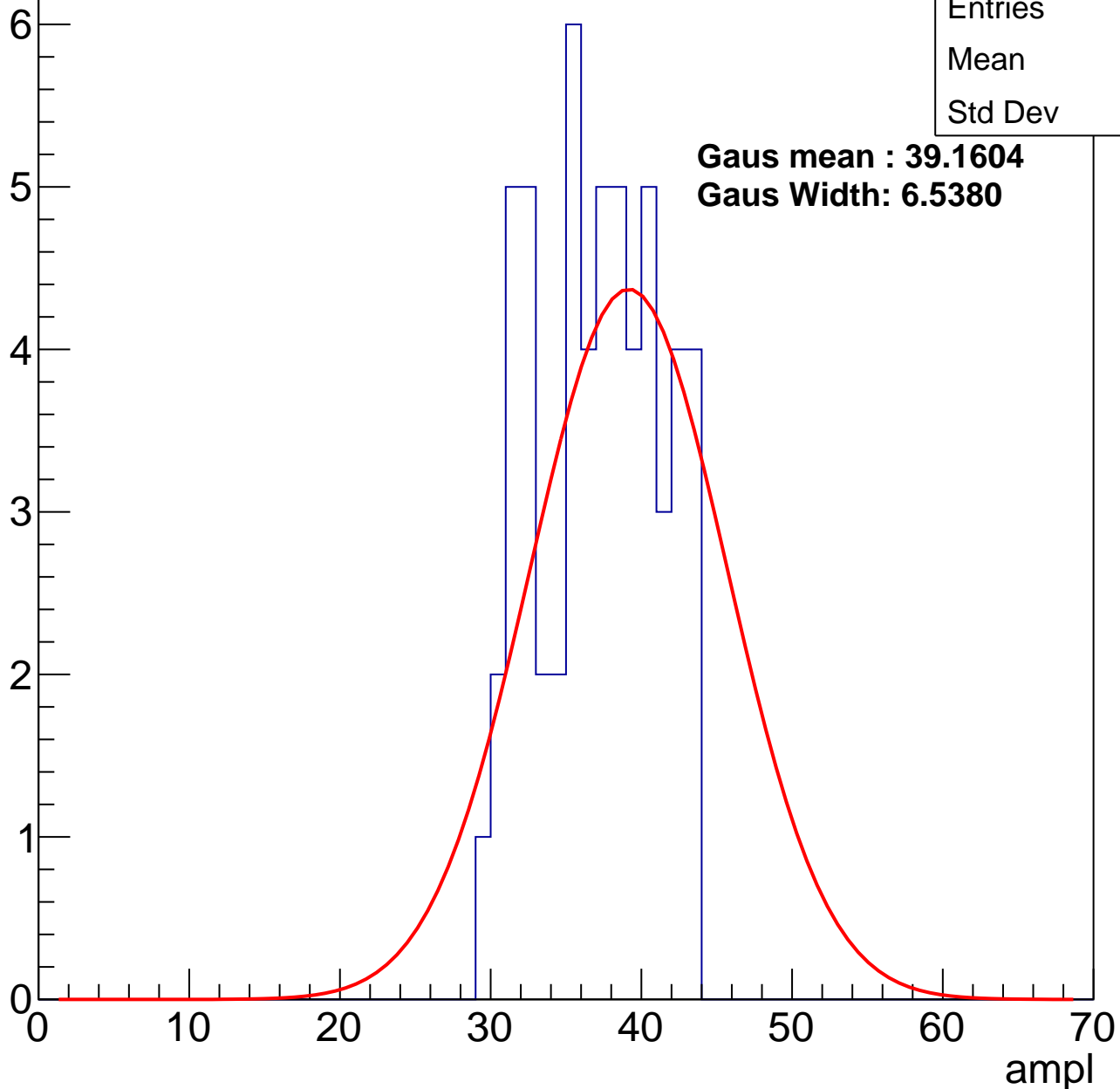
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	36.6
Std Dev	3.96

Gaus mean : 39.1604

Gaus Width: 6.5380



B0L001S, U14-ch33, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	43.27
Std Dev	3.985

Gaus mean : 43.7743

Gaus Width: 4.4004

Entry

10

8

6

4

2

0

0

10

20

30

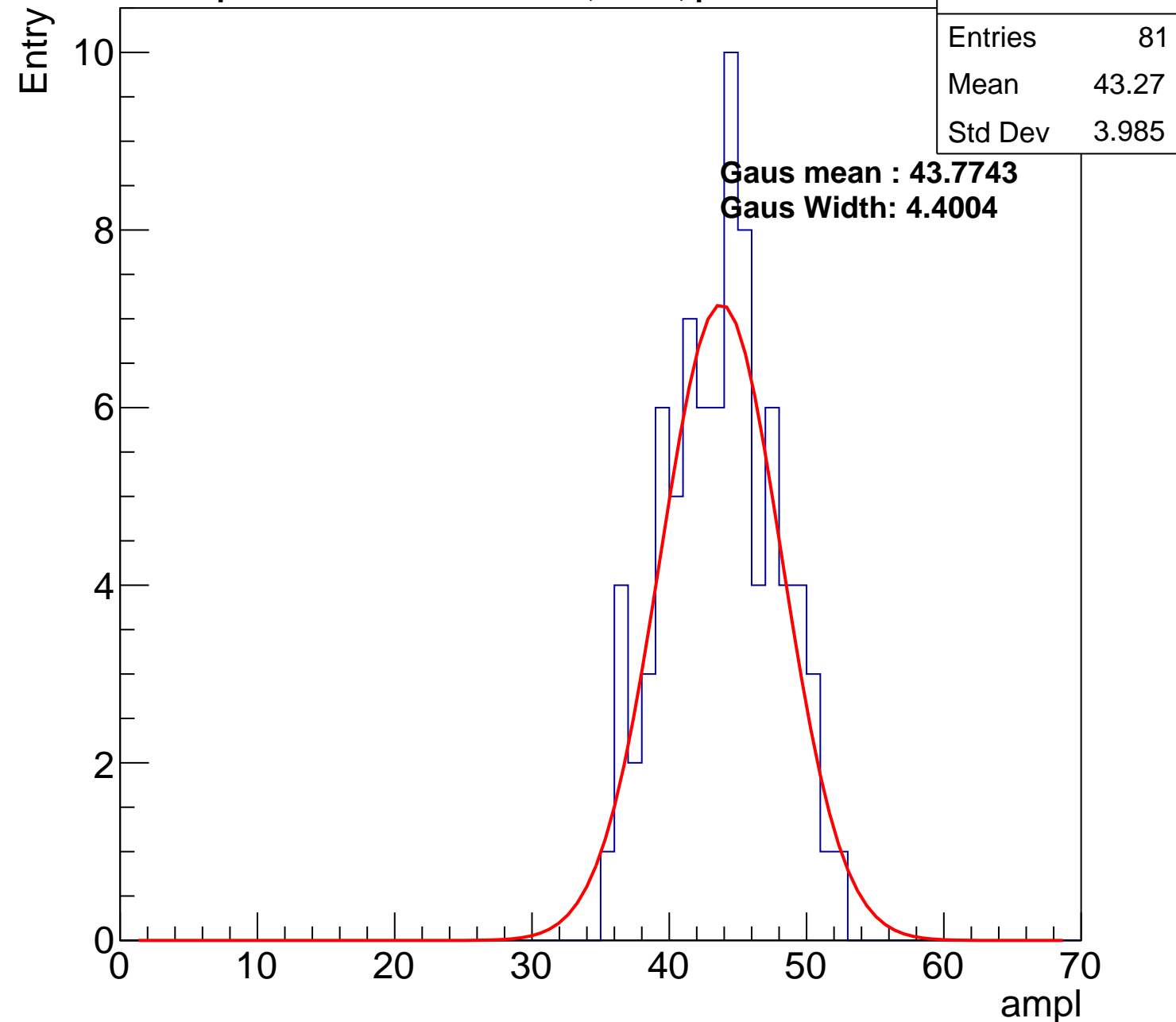
40

50

60

70

ampl

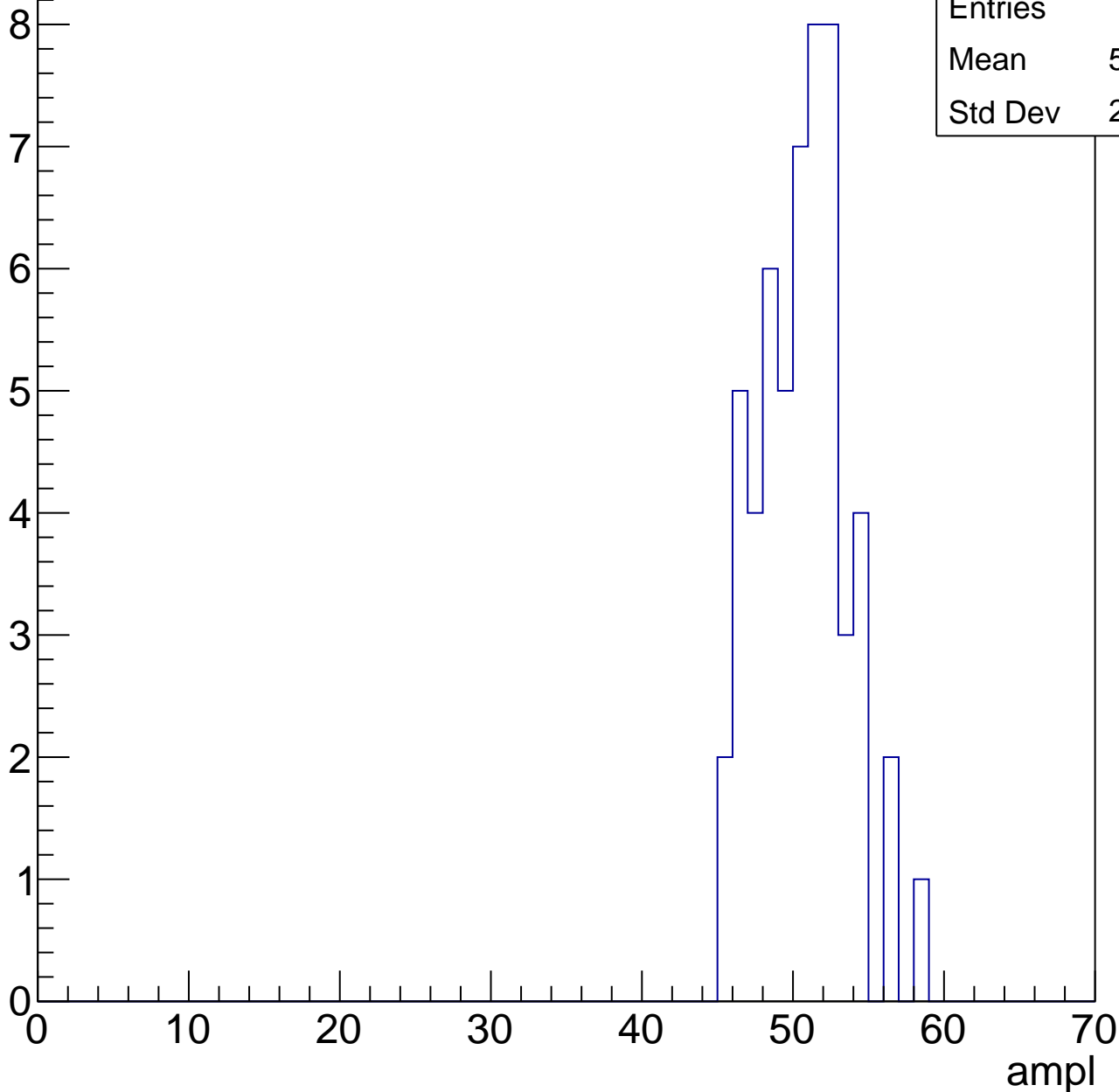


B0L001S, U14-ch33, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.18
Std Dev	2.893

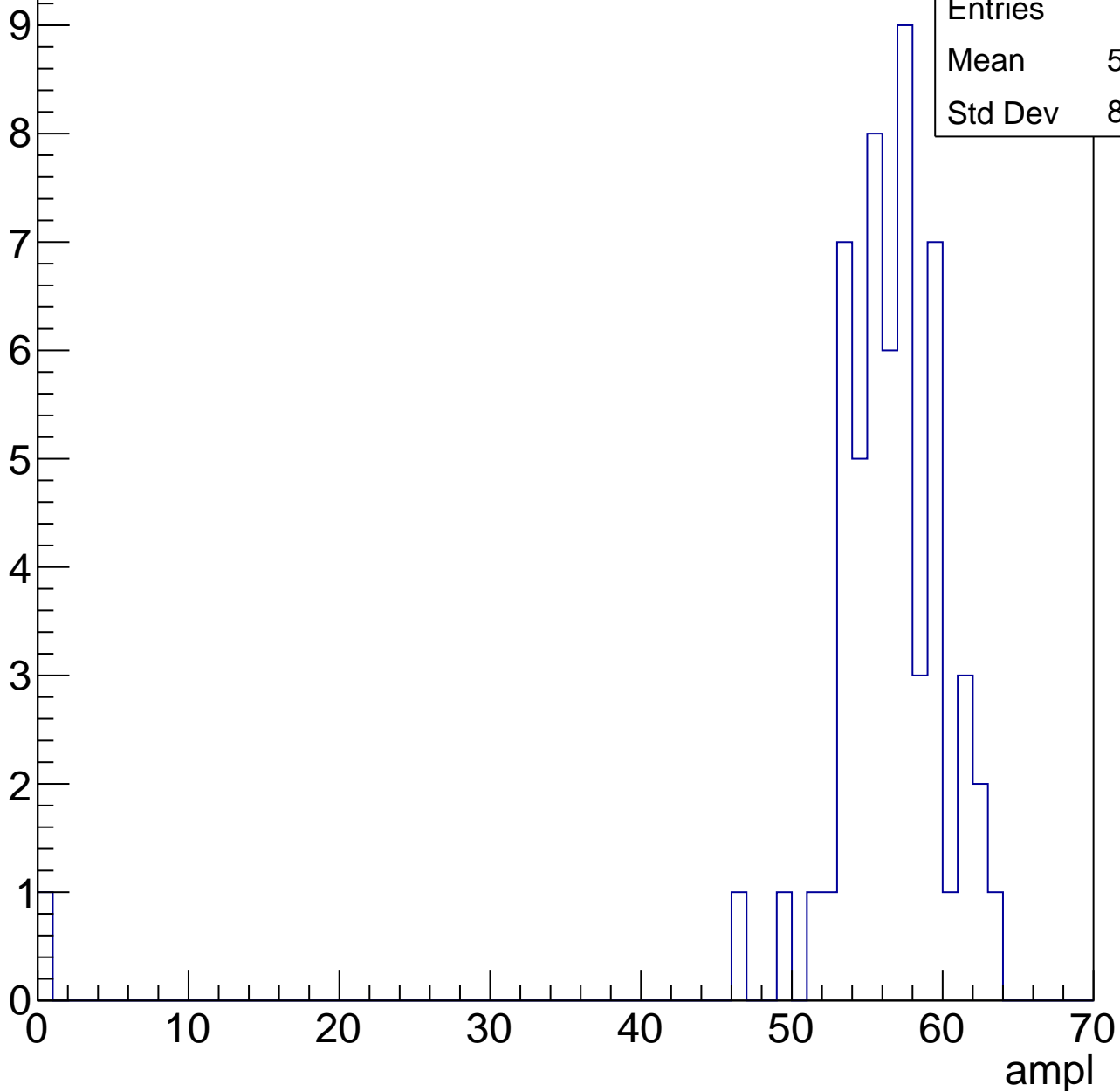


B0L001S, U14-ch33, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	55.18
Std Dev	8.029



B0L001S, U14-ch33, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

8
7
6
5
4
3
2
1
0

Entries	42
Mean	60.02
Std Dev	2.464

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

B0L001S, U14-ch33, adc6

calib_packv5_042523_0143.root, FC#9, port A1

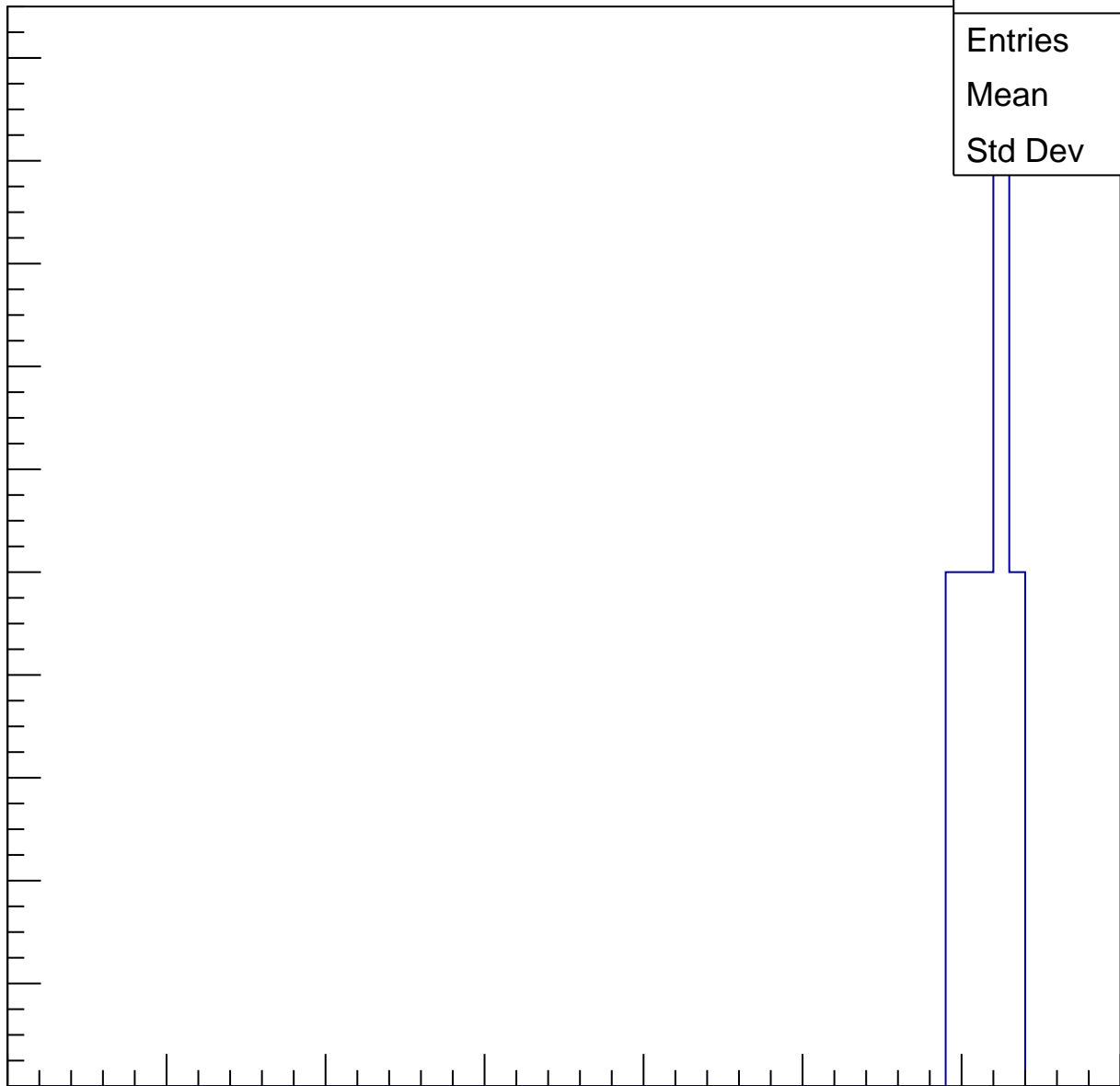
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.344

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch33, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch34, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	30.17
Std Dev	4.841

Gaus mean : 30.8672

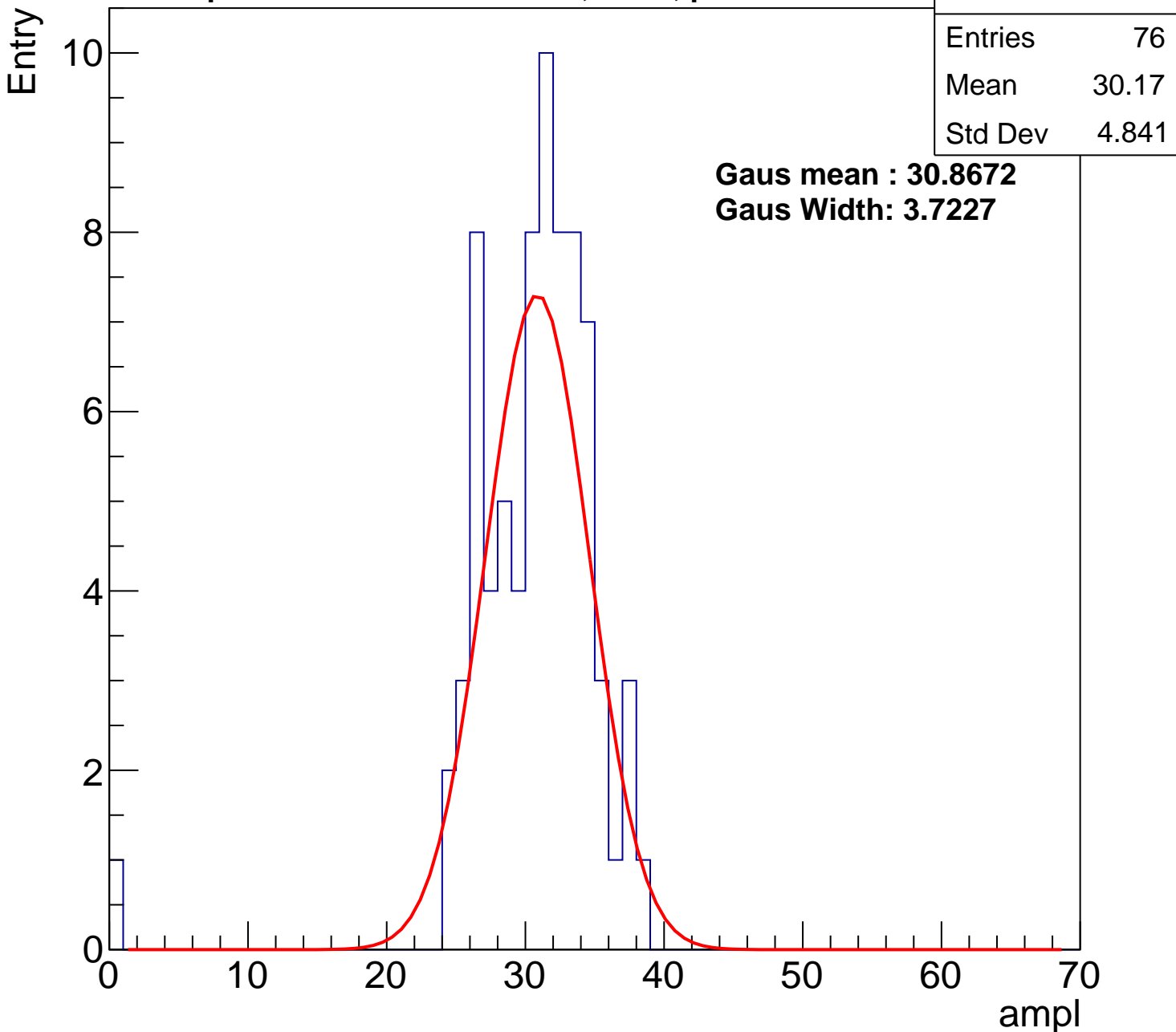
Gaus Width: 3.7227

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch34, adc1

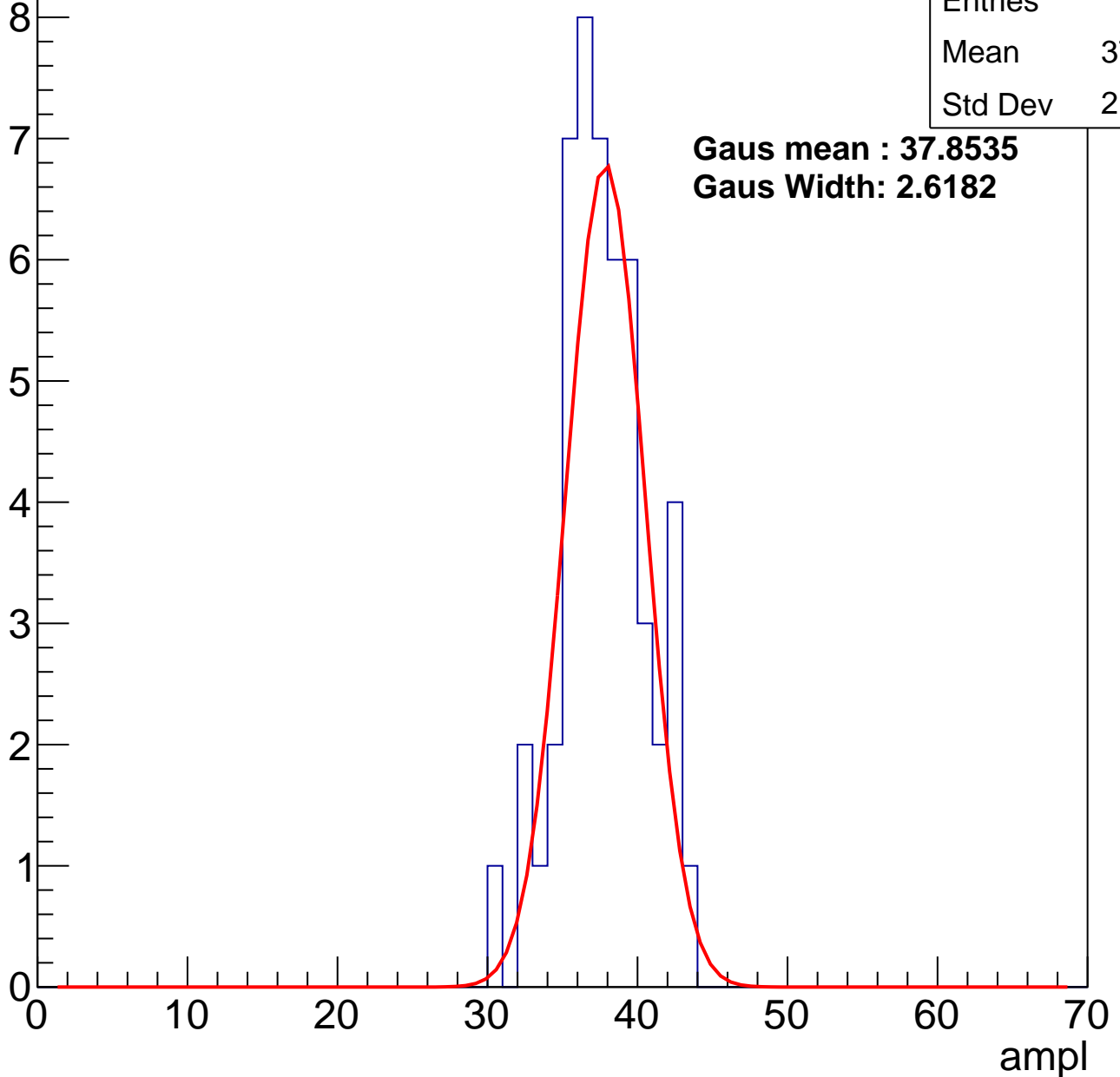
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	37.24
Std Dev	2.797

Gaus mean : 37.8535

Gaus Width: 2.6182



B0L001S, U14-ch34, adc2

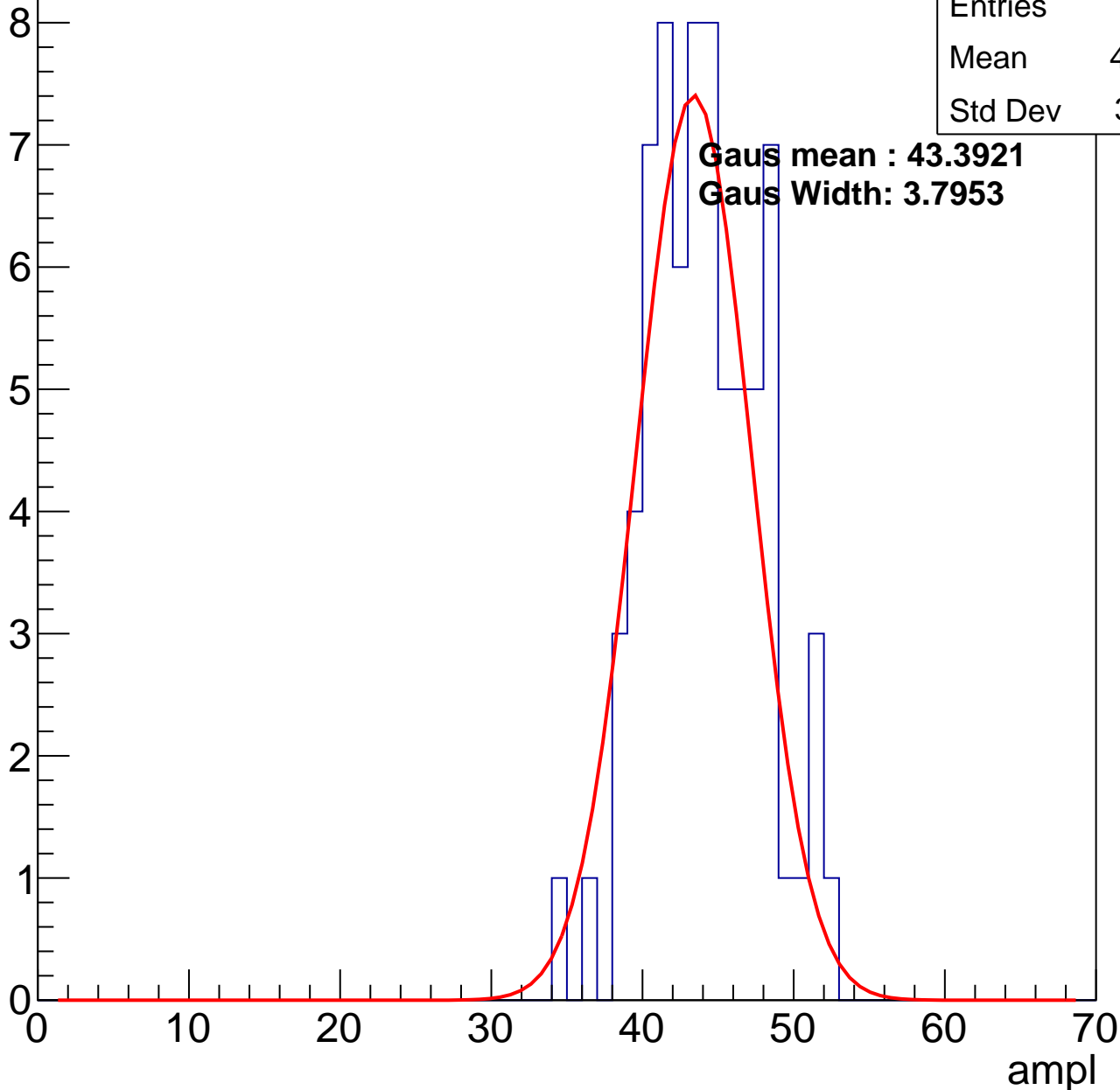
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	43.59
Std Dev	3.731

Gaus mean : 43.3921

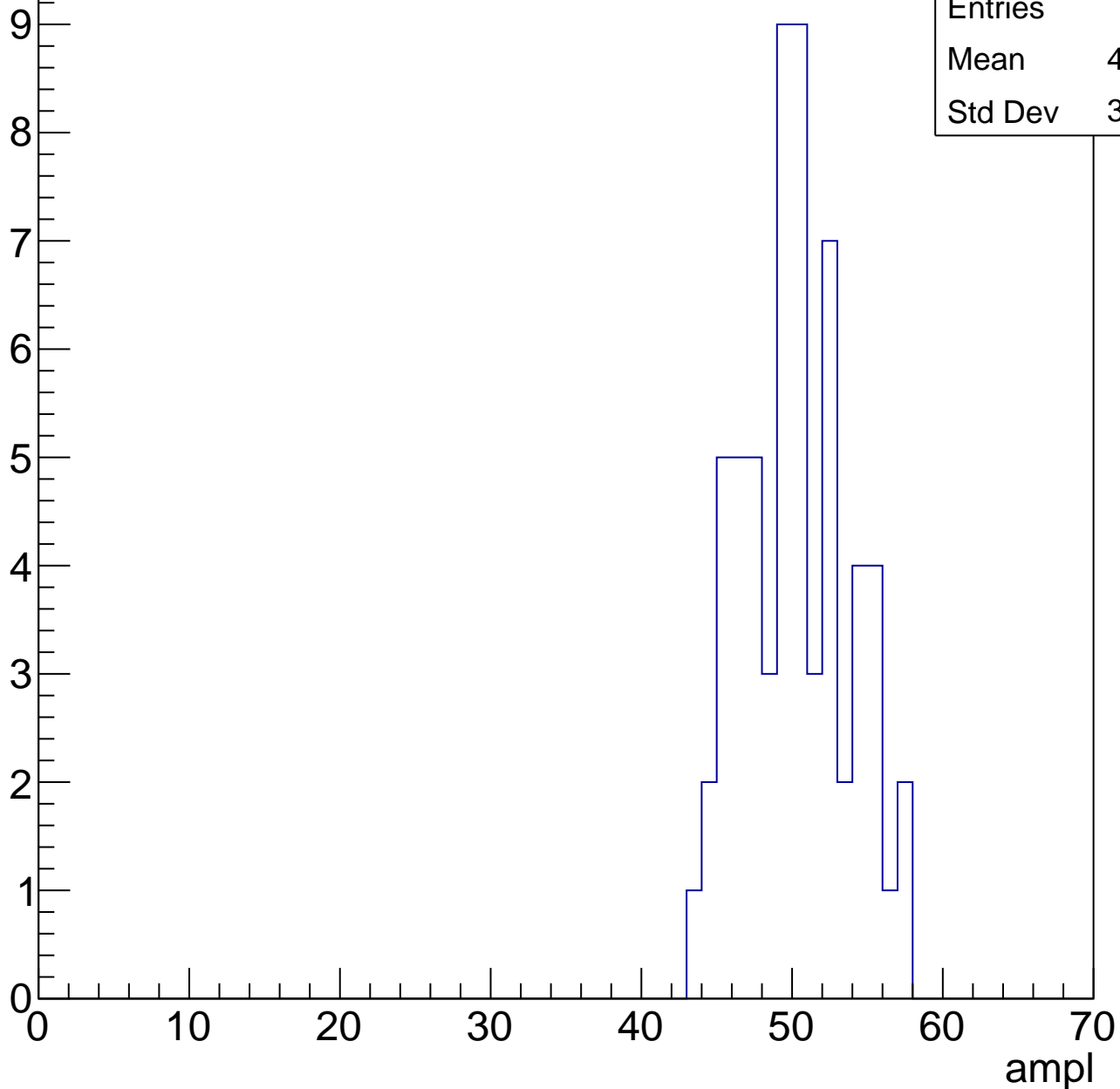
Gaus Width: 3.7953



B0L001S, U14-ch34, adc3

calib_packv5_042523_0143.root, FC#9, port A1

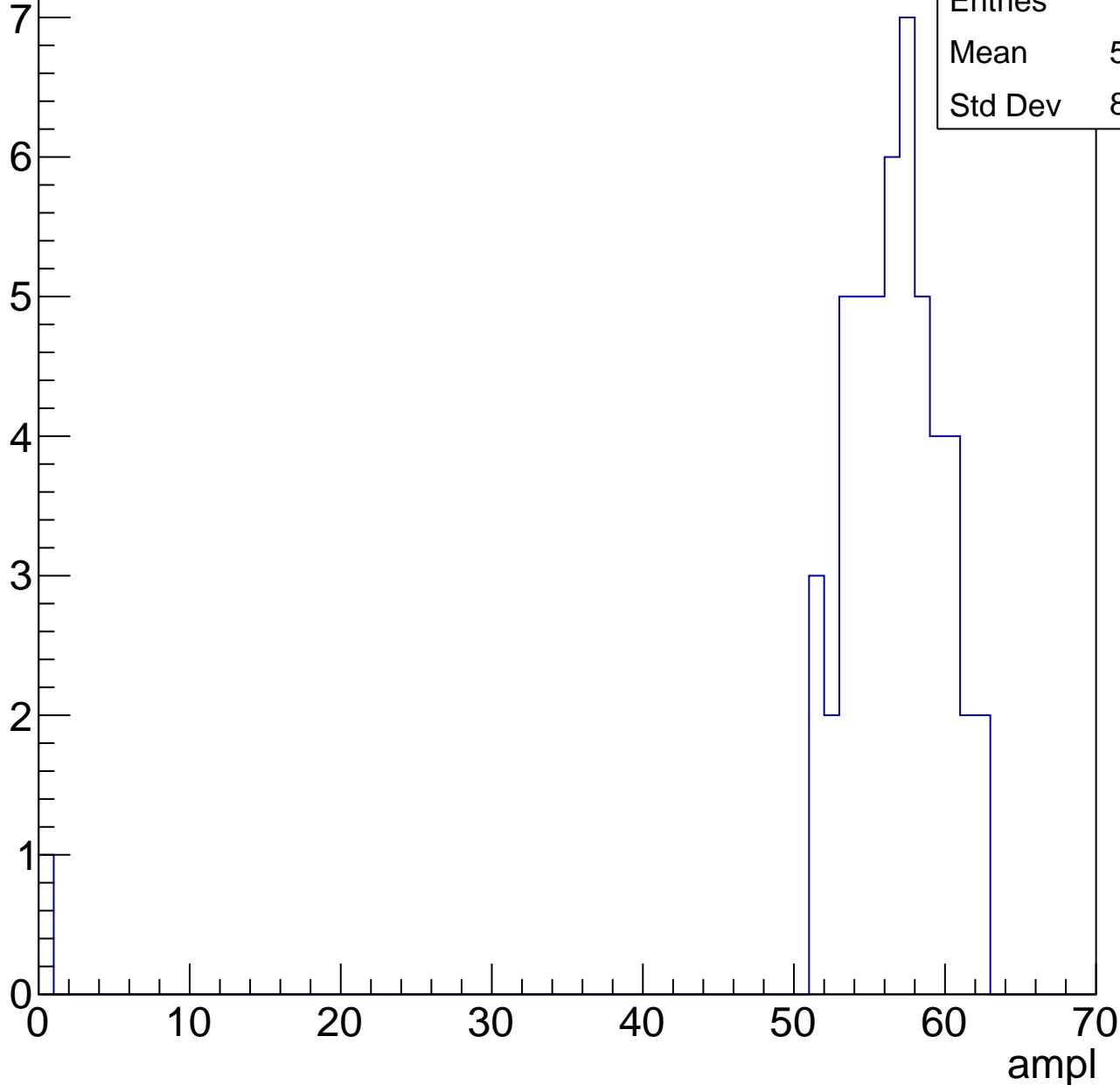
Entry



B0L001S, U14-ch34, adc4

calib_packv5_042523_0143.root, FC#9, port A1

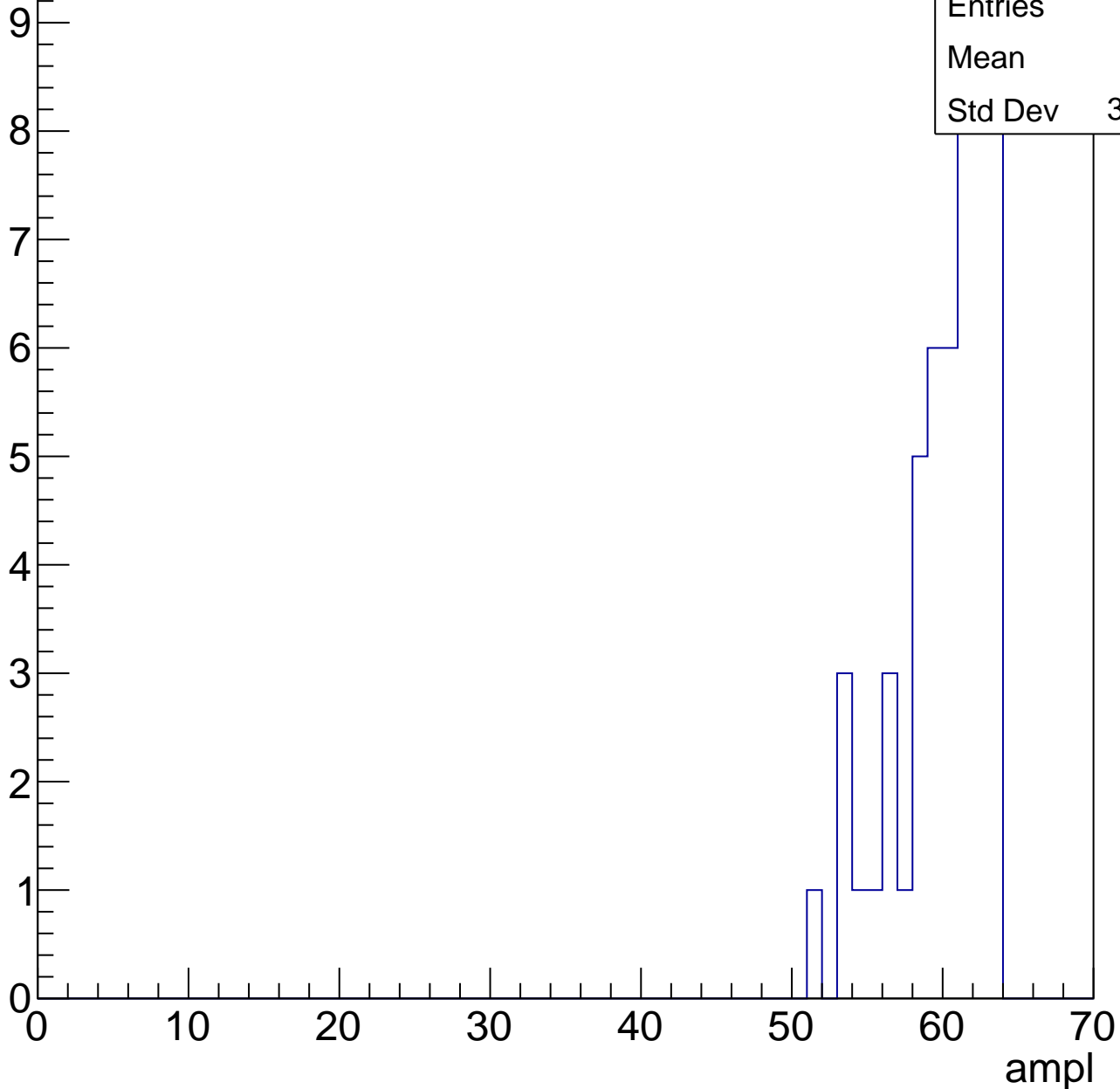
Entry



B0L001S, U14-ch34, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch34, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch34, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch35, adc0

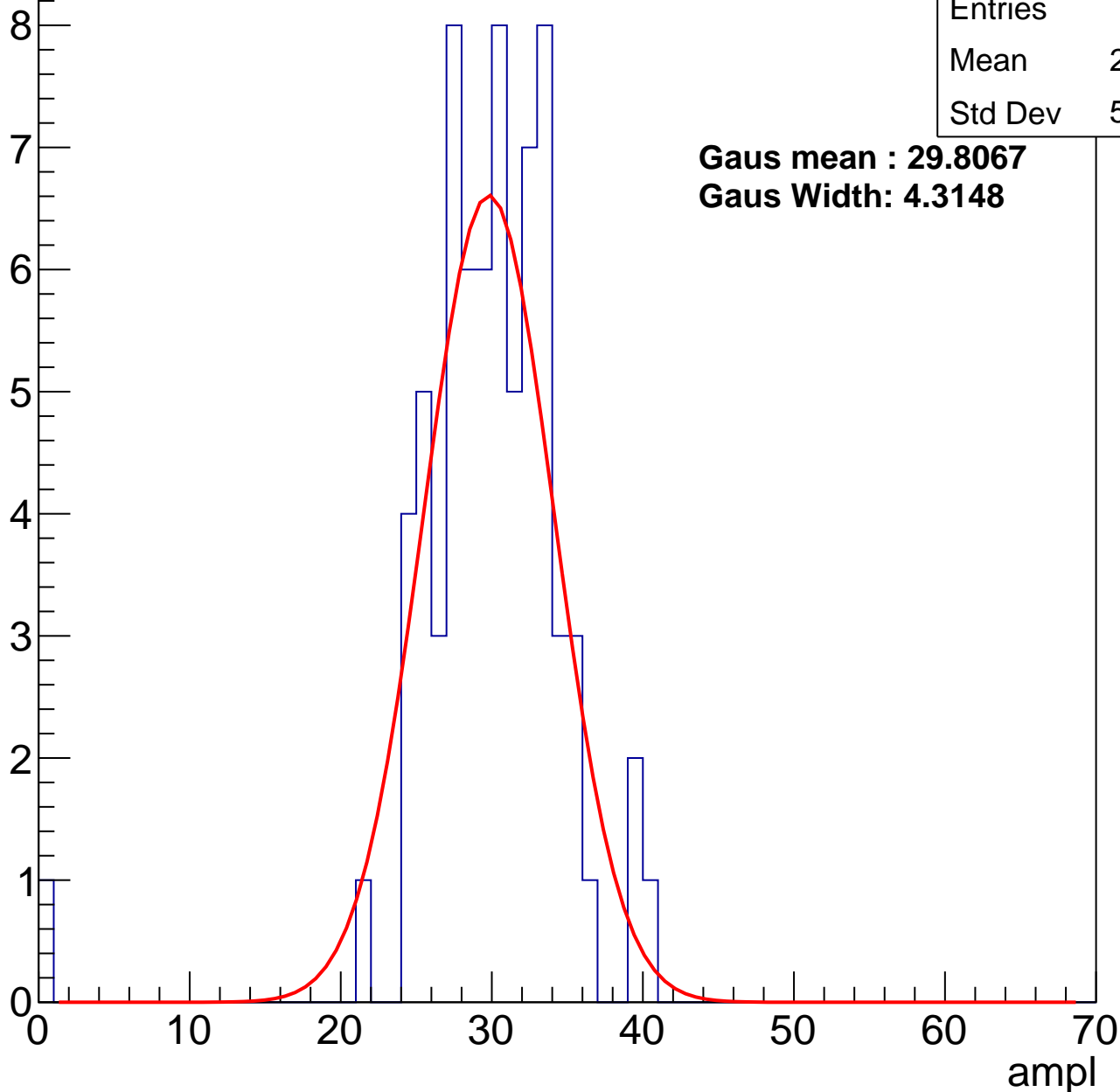
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	29.47
Std Dev	5.145

Gaus mean : 29.8067

Gaus Width: 4.3148



B0L001S, U14-ch35, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	79
Mean	37.66
Std Dev	3.841

Gaus mean : 37.8966

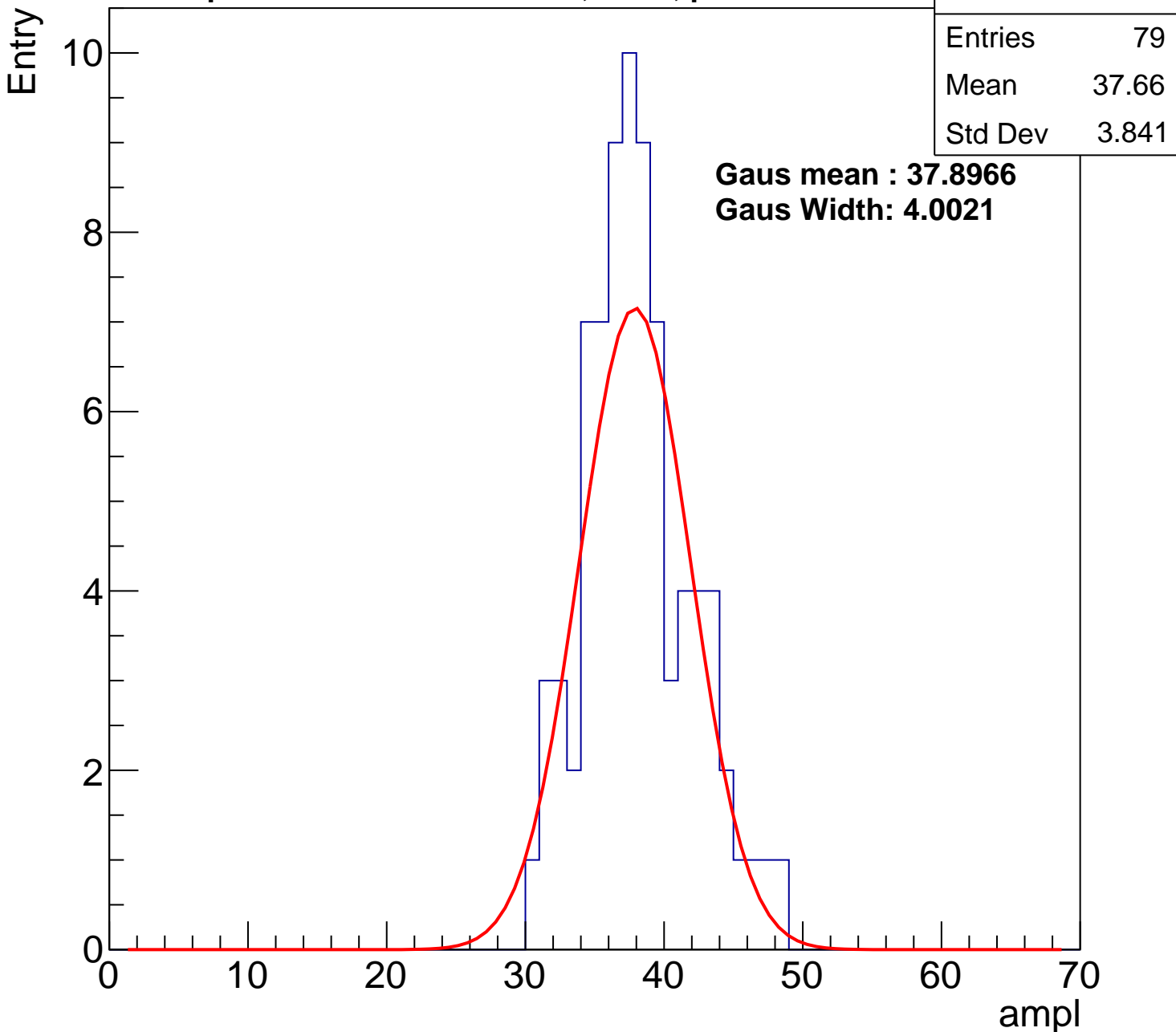
Gaus Width: 4.0021

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch35, adc2

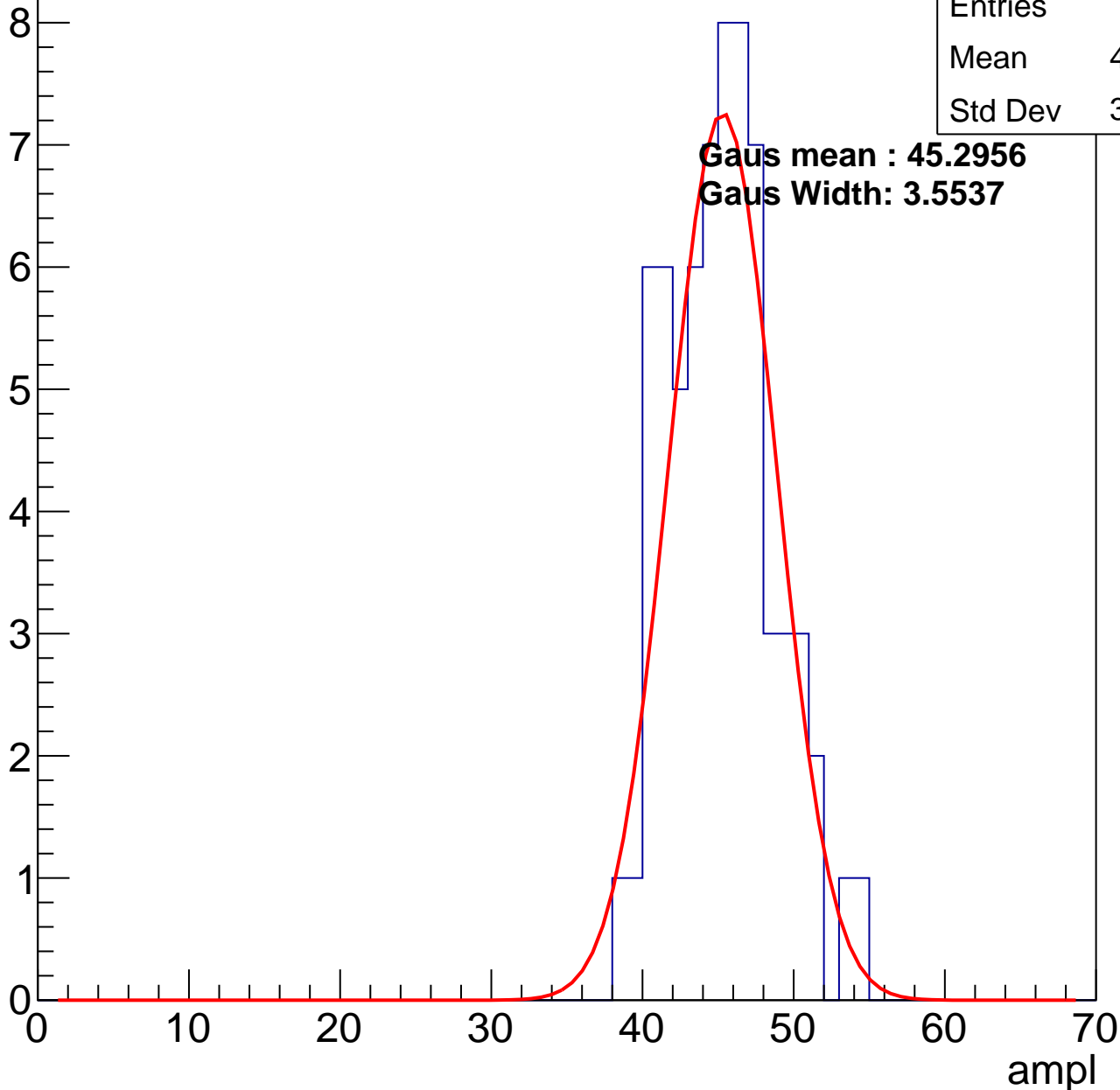
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.79
Std Dev	3.445

Gaus mean : 45.2956

Gaus Width: 3.5537

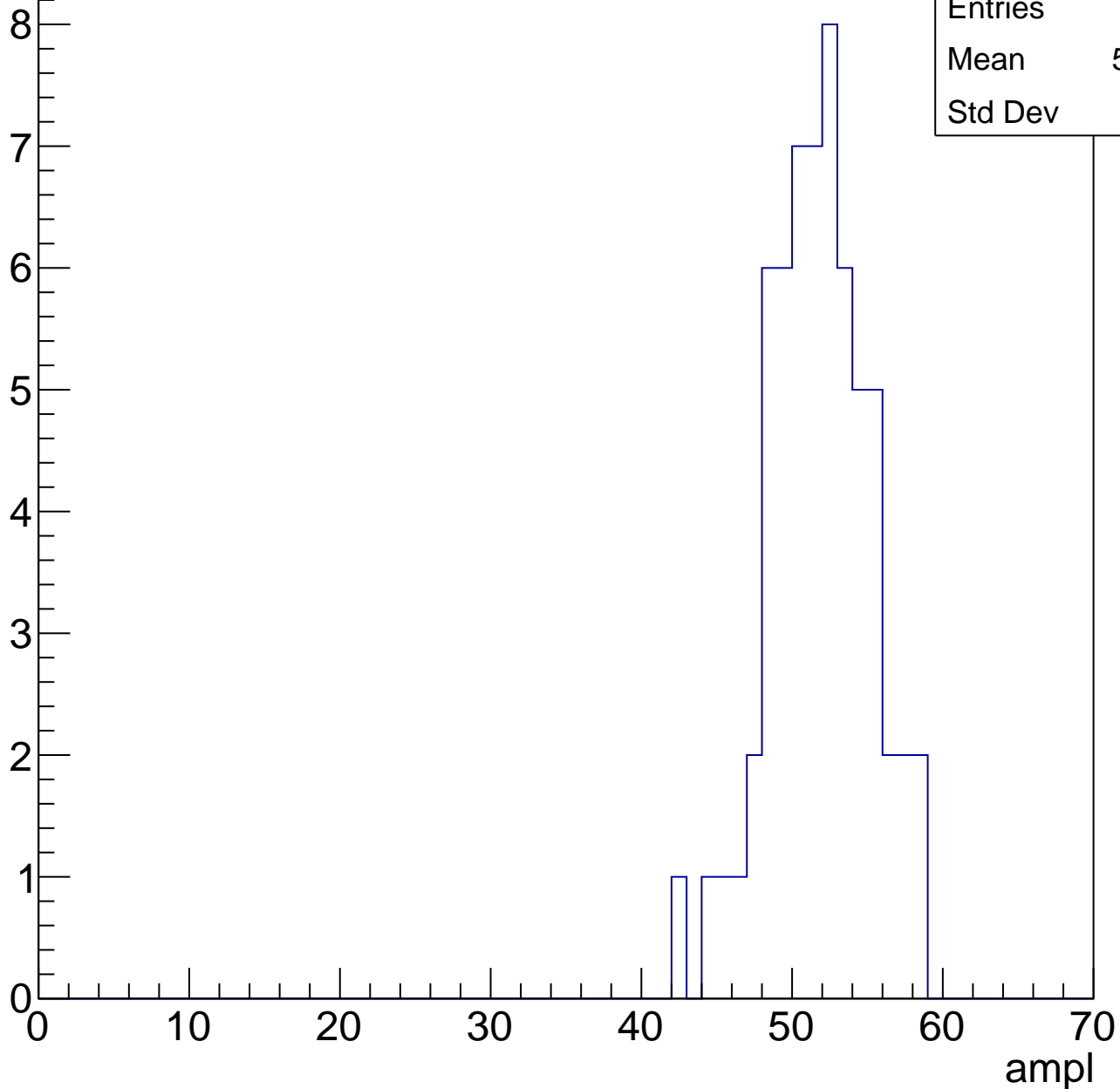


B0L001S, U14-ch35, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.31
Std Dev	3.31

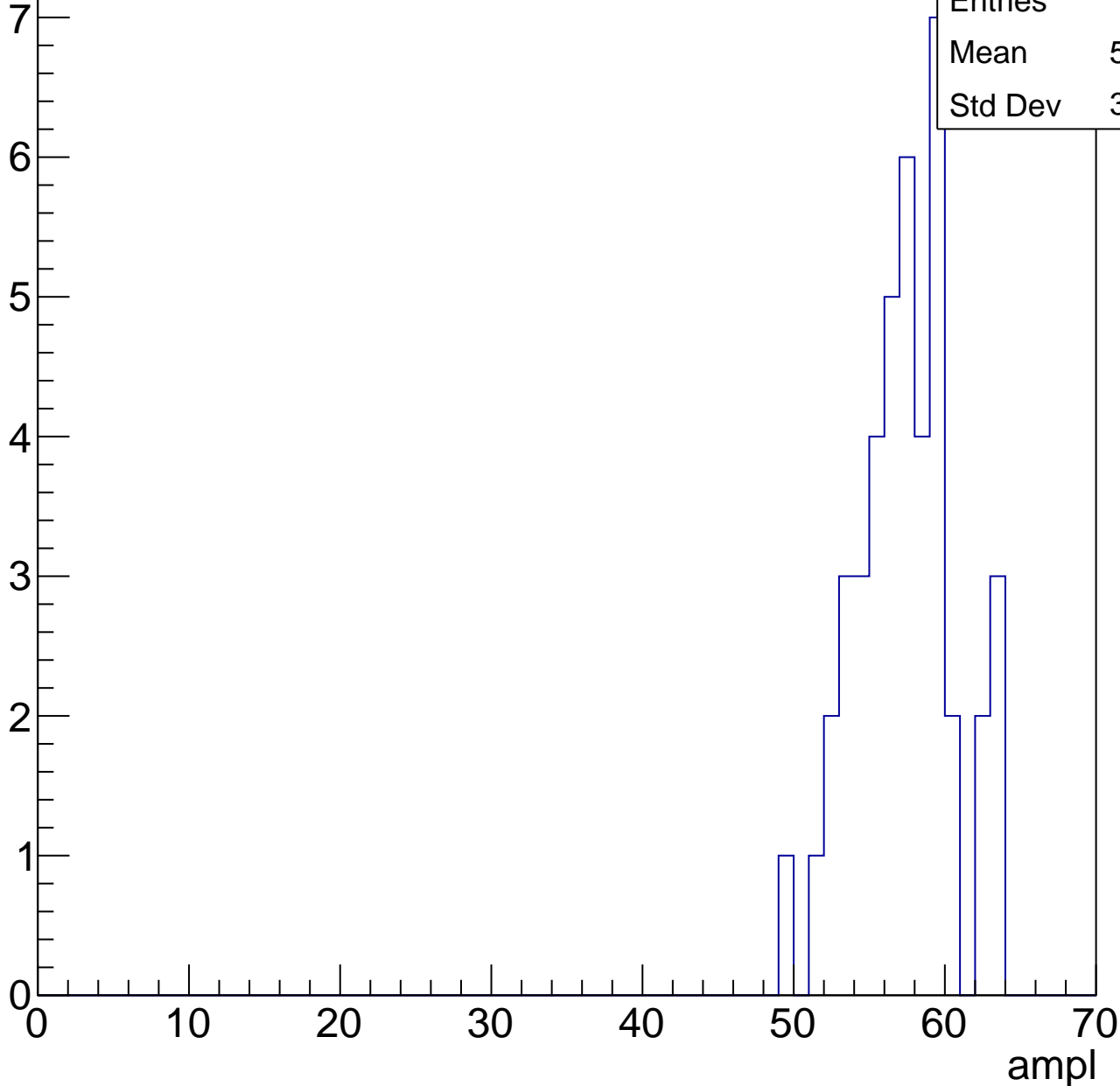


B0L001S, U14-ch35, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

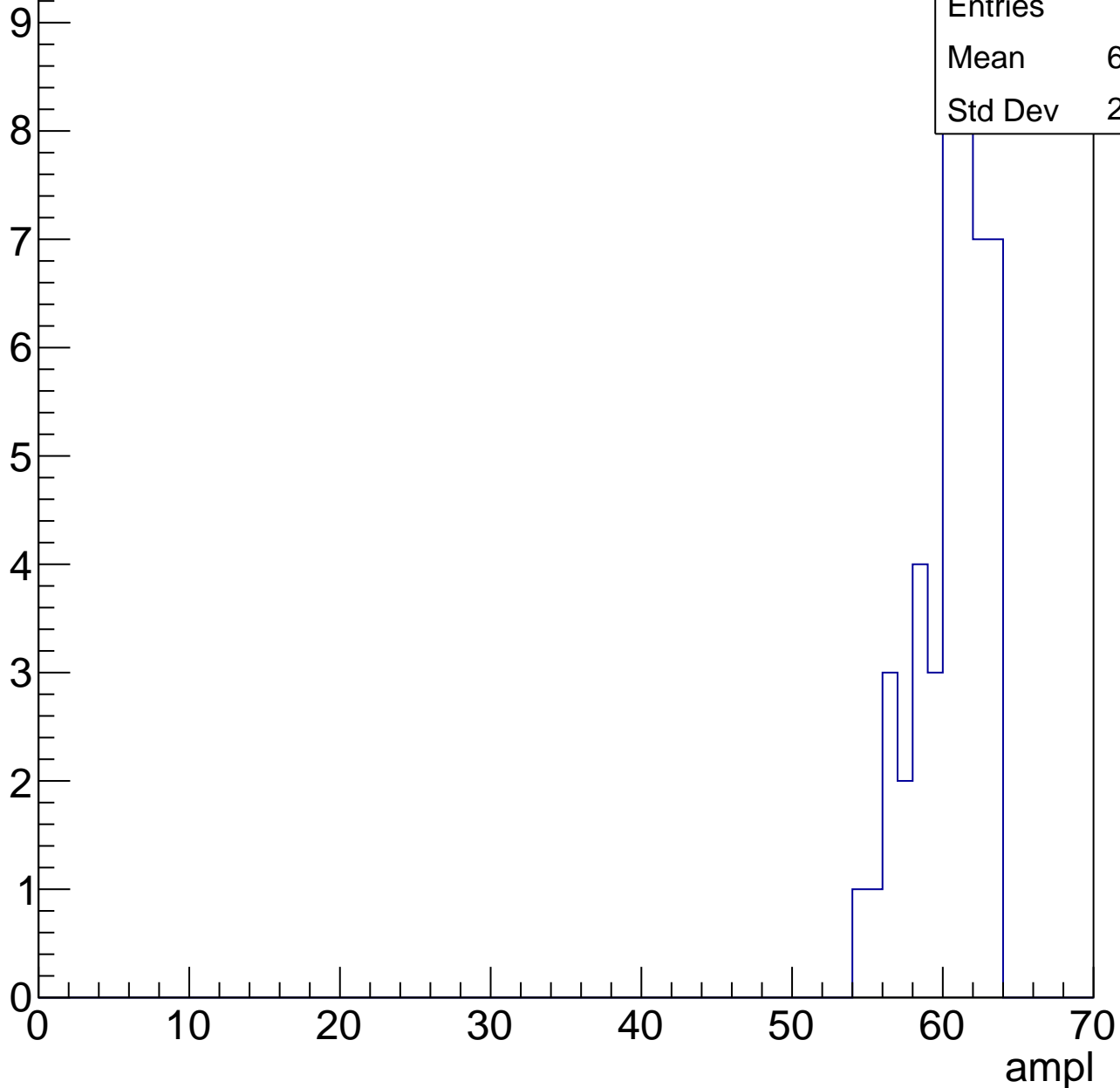
Entries	43
Mean	56.86
Std Dev	3.246



B0L001S, U14-ch35, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch35, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch35, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch36, adc0

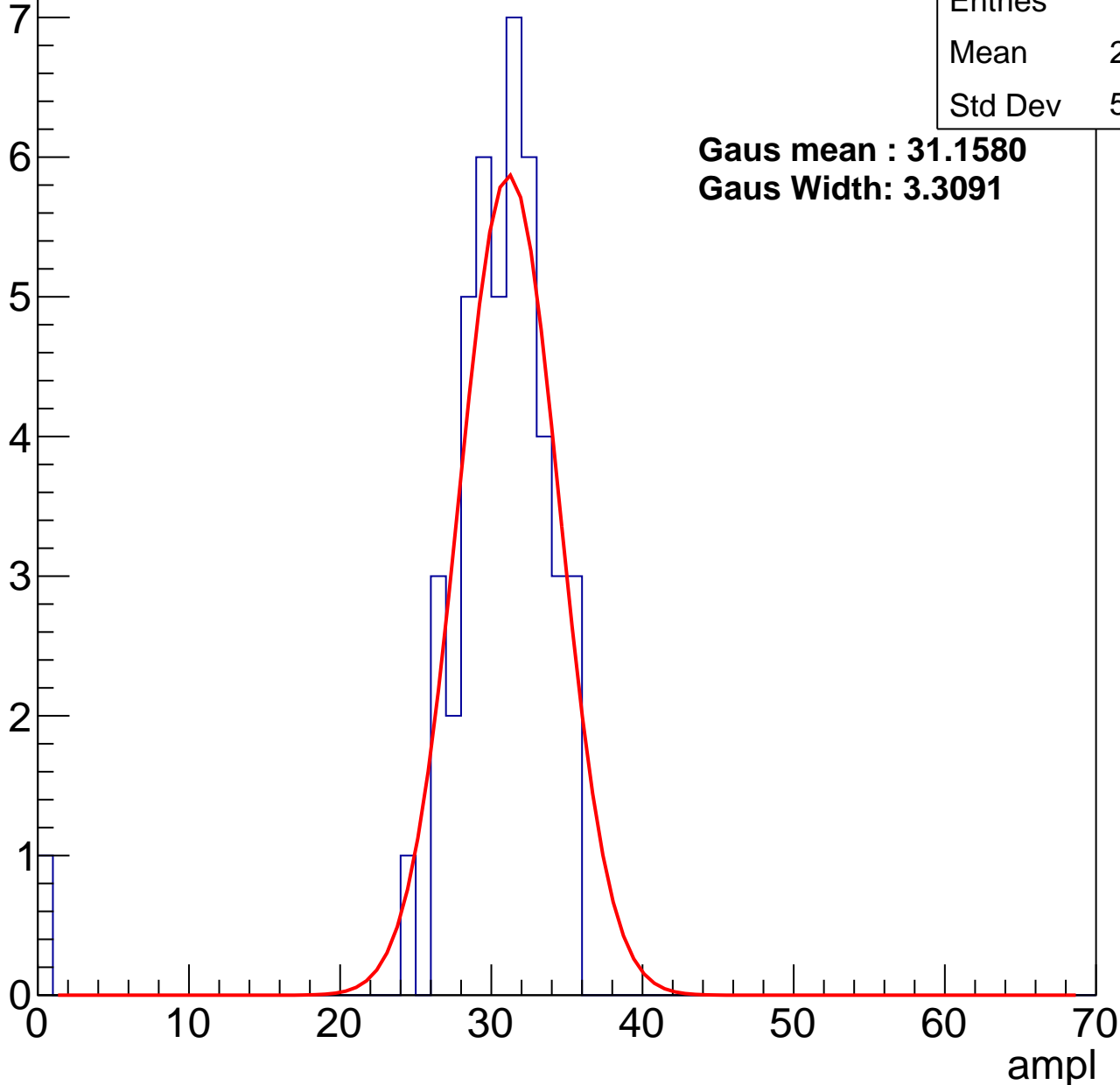
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	29.74
Std Dev	5.139

Gaus mean : 31.1580

Gaus Width: 3.3091



B0L001S, U14-ch36, adc1

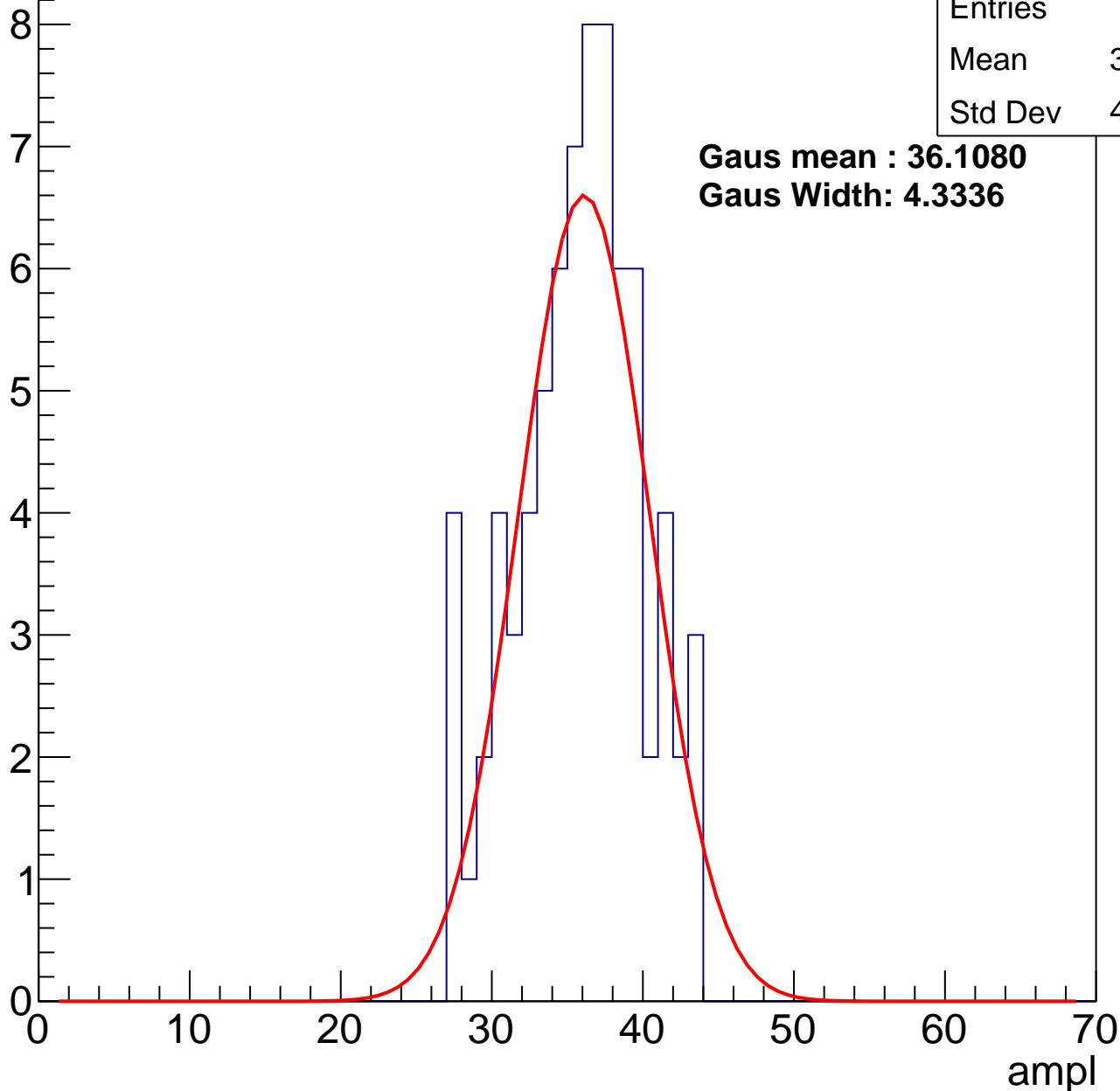
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	35.36
Std Dev	4.085

Gaus mean : 36.1080

Gaus Width: 4.3336



B0L001S, U14-ch36, adc2

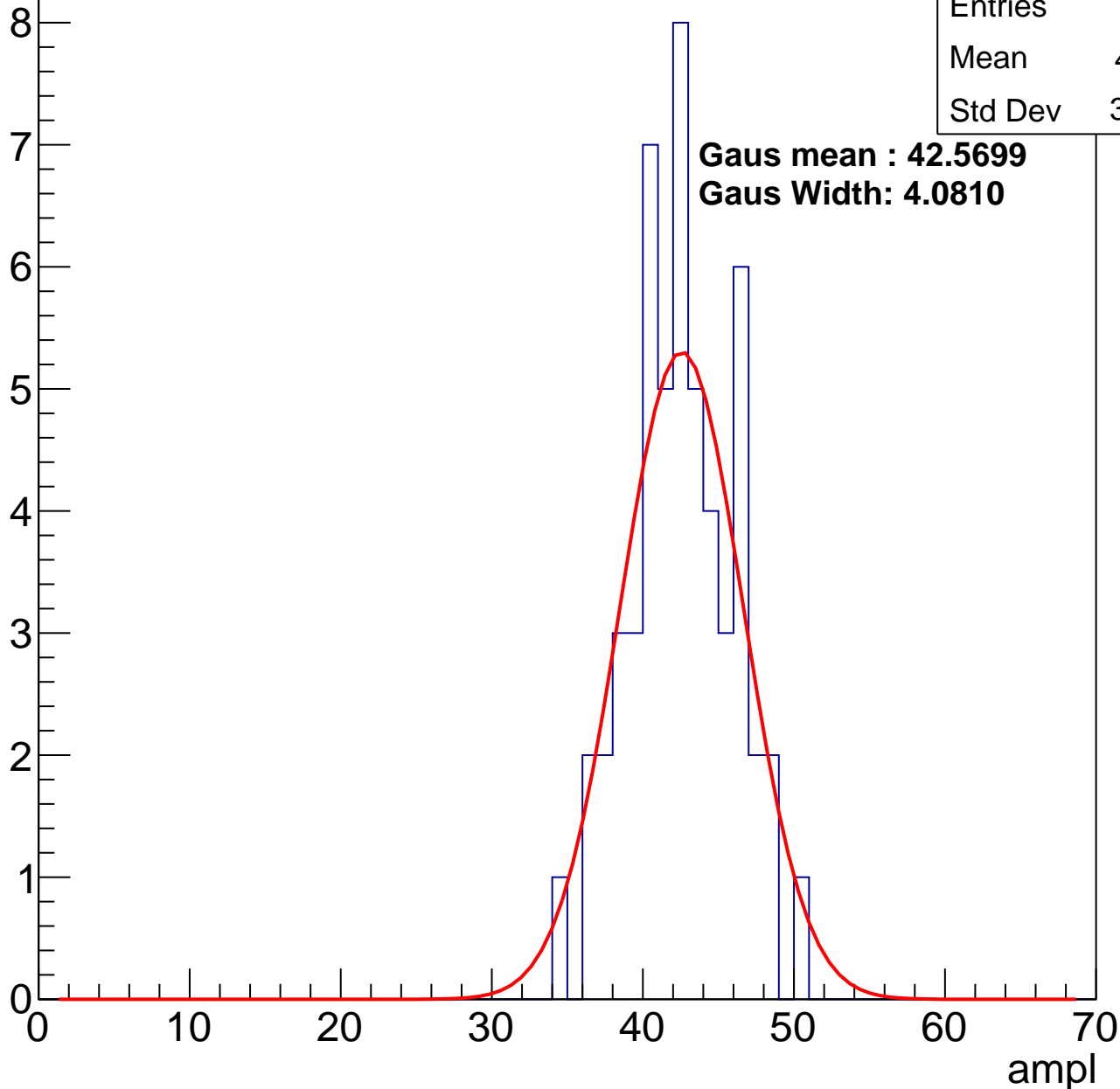
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	42.11
Std Dev	3.398

Gaus mean : 42.5699

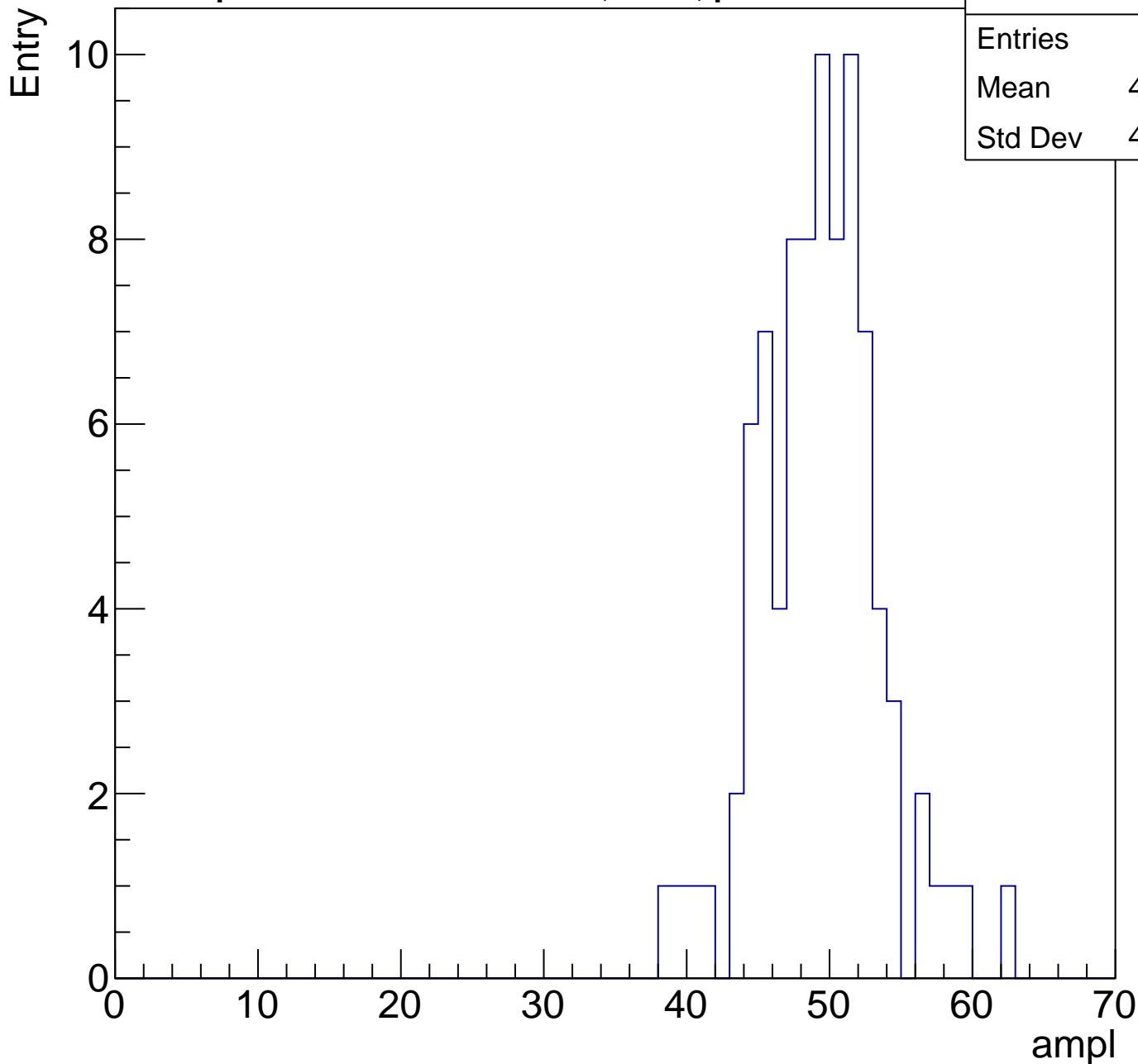
Gaus Width: 4.0810



B0L001S, U14-ch36, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	87
Mean	48.89
Std Dev	4.203

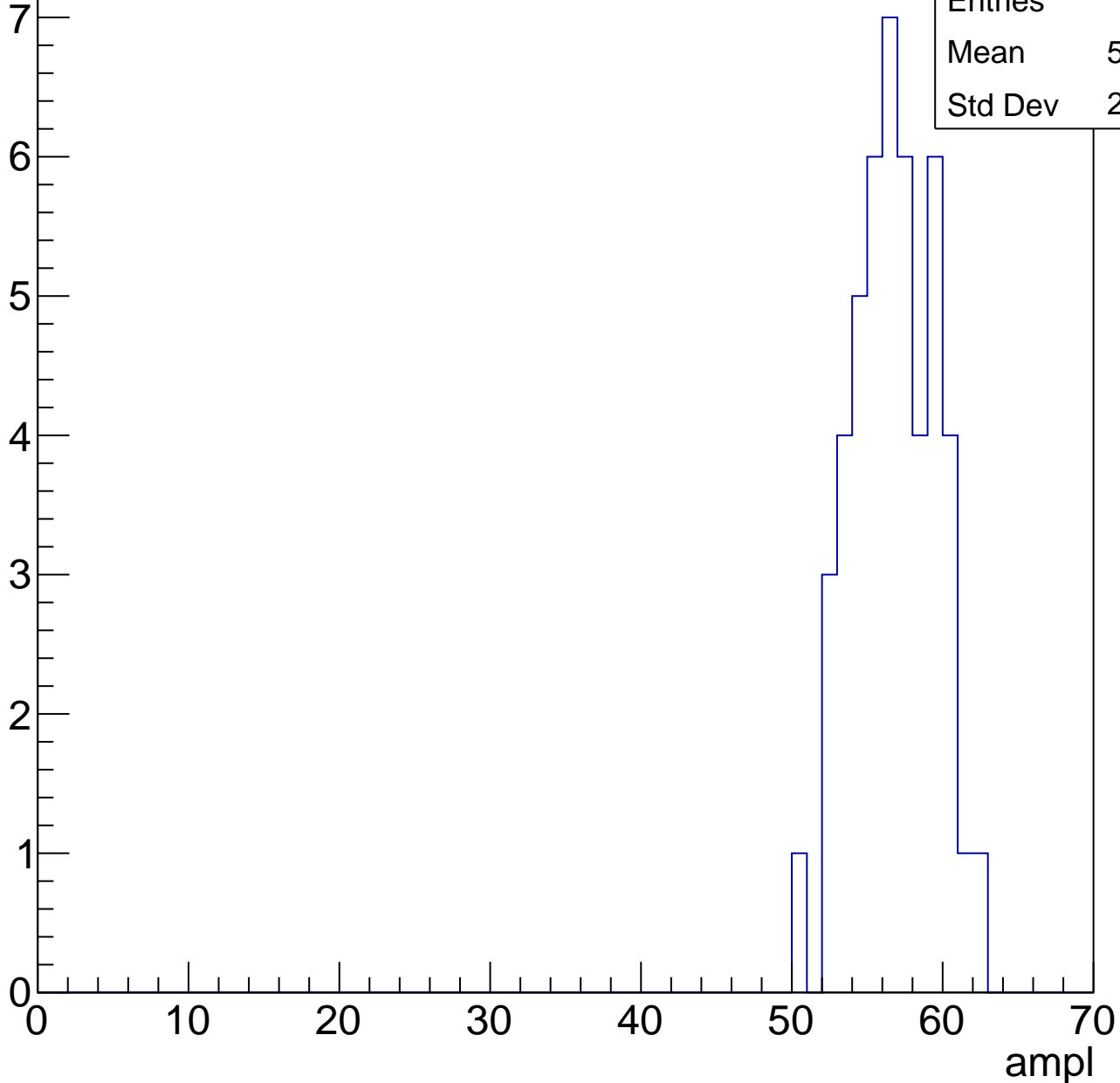


B0L001S, U14-ch36, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	56.27
Std Dev	2.675

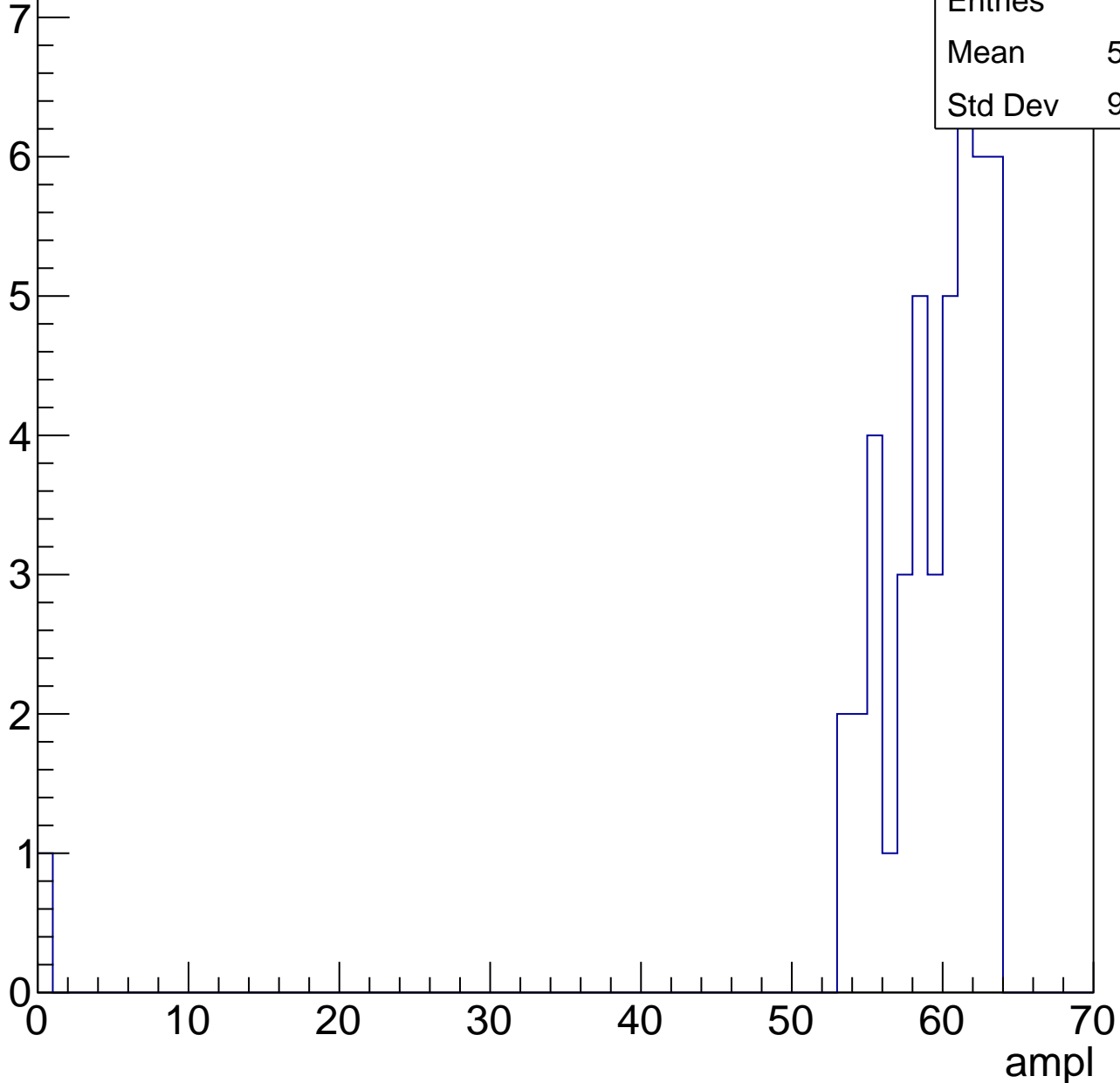


B0L001S, U14-ch36, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

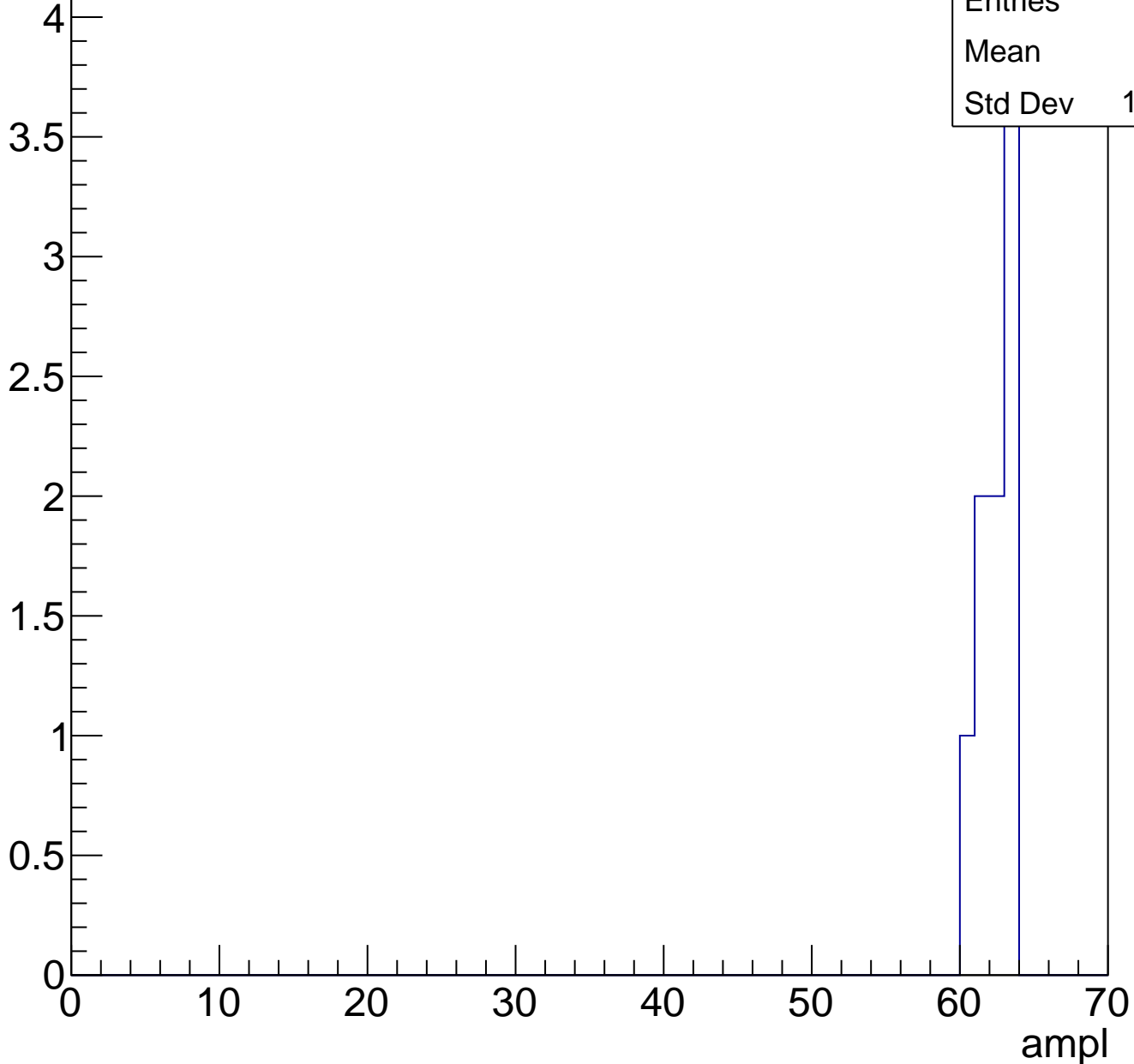
Entries	45
Mean	57.89
Std Dev	9.214



B0L001S, U14-ch36, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch36, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch37, adc0

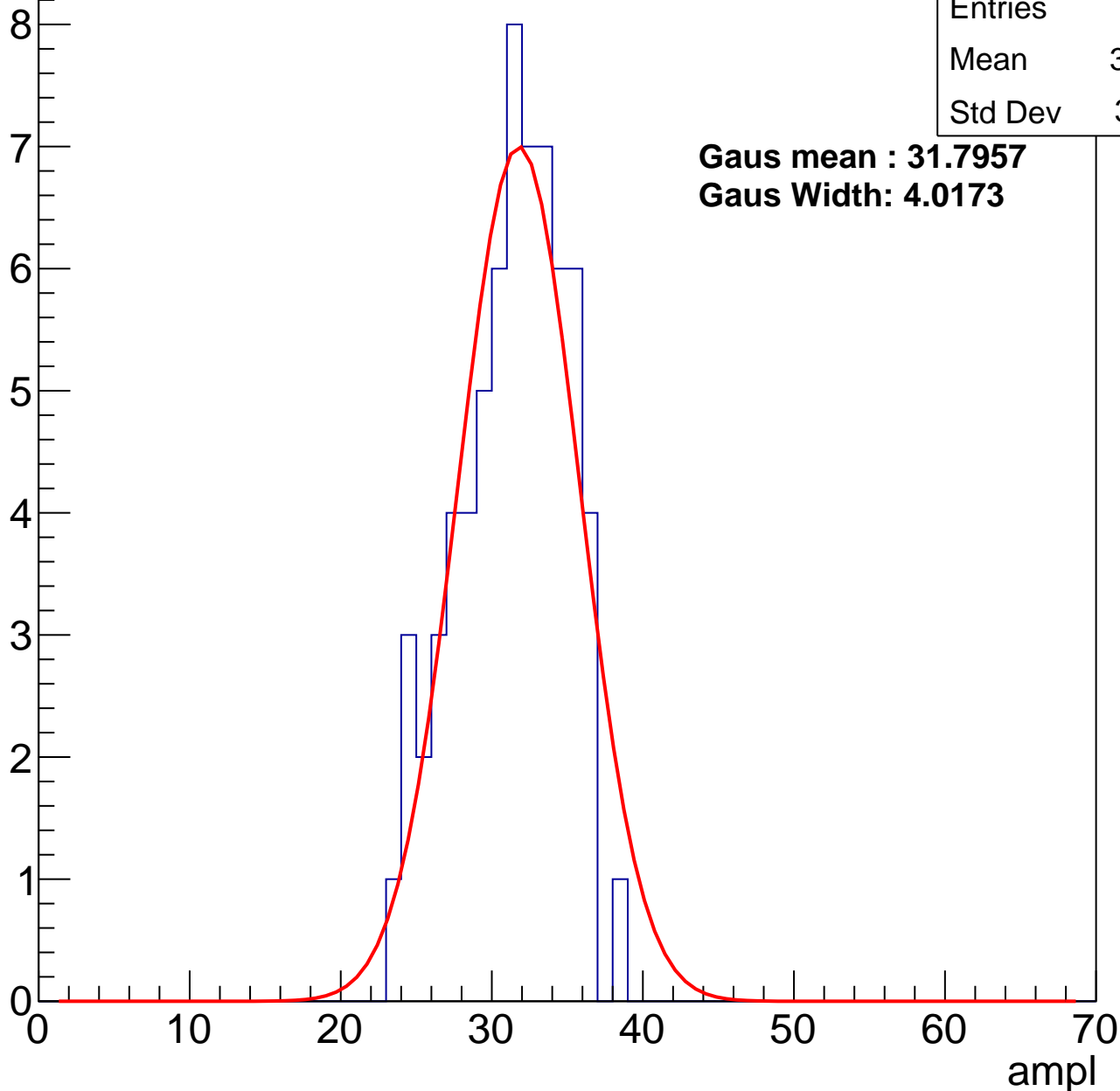
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	30.85
Std Dev	3.491

Gaus mean : 31.7957

Gaus Width: 4.0173



B0L001S, U14-ch37, adc1

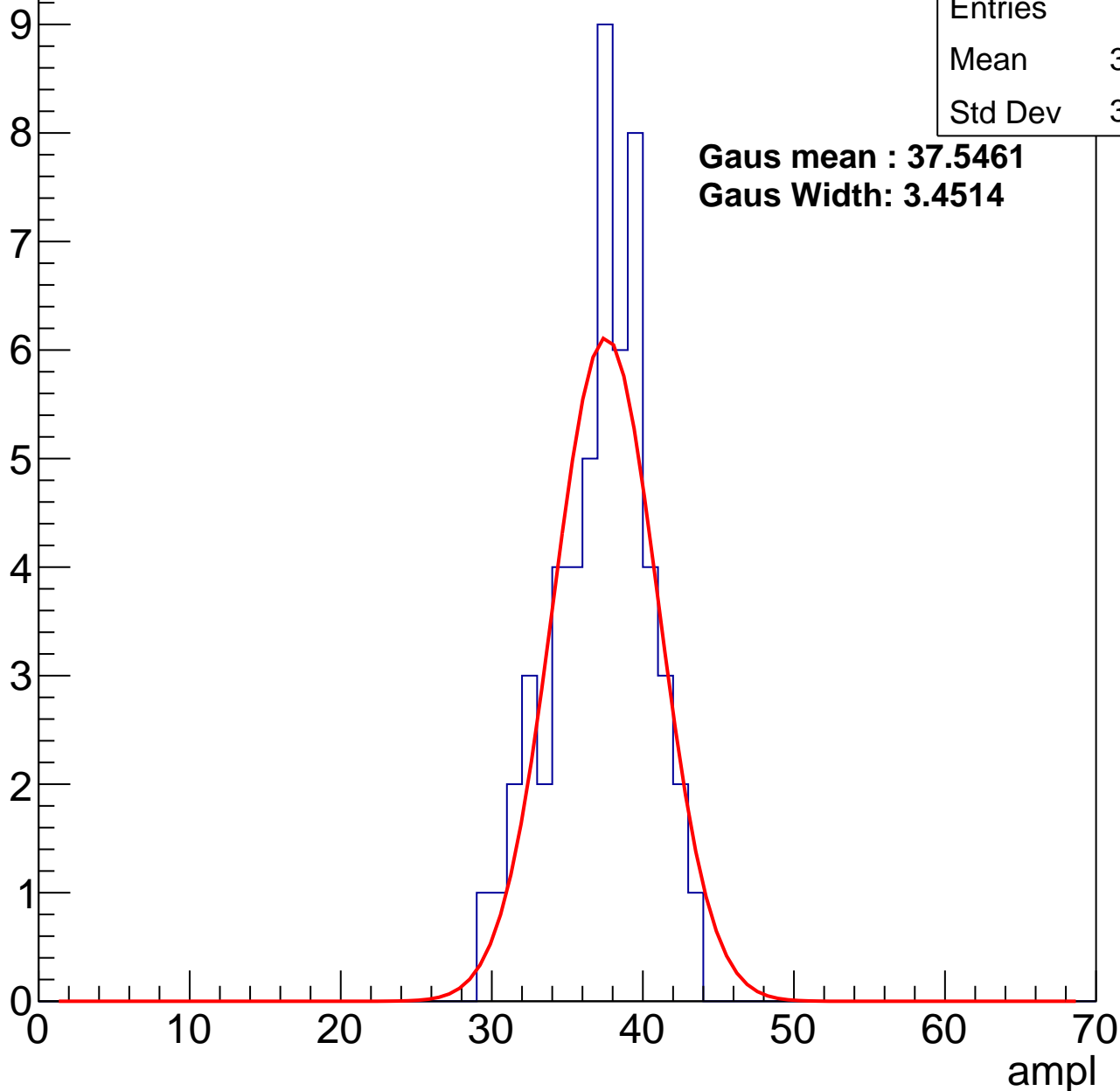
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	36.76
Std Dev	3.174

Gaus mean : 37.5461

Gaus Width: 3.4514



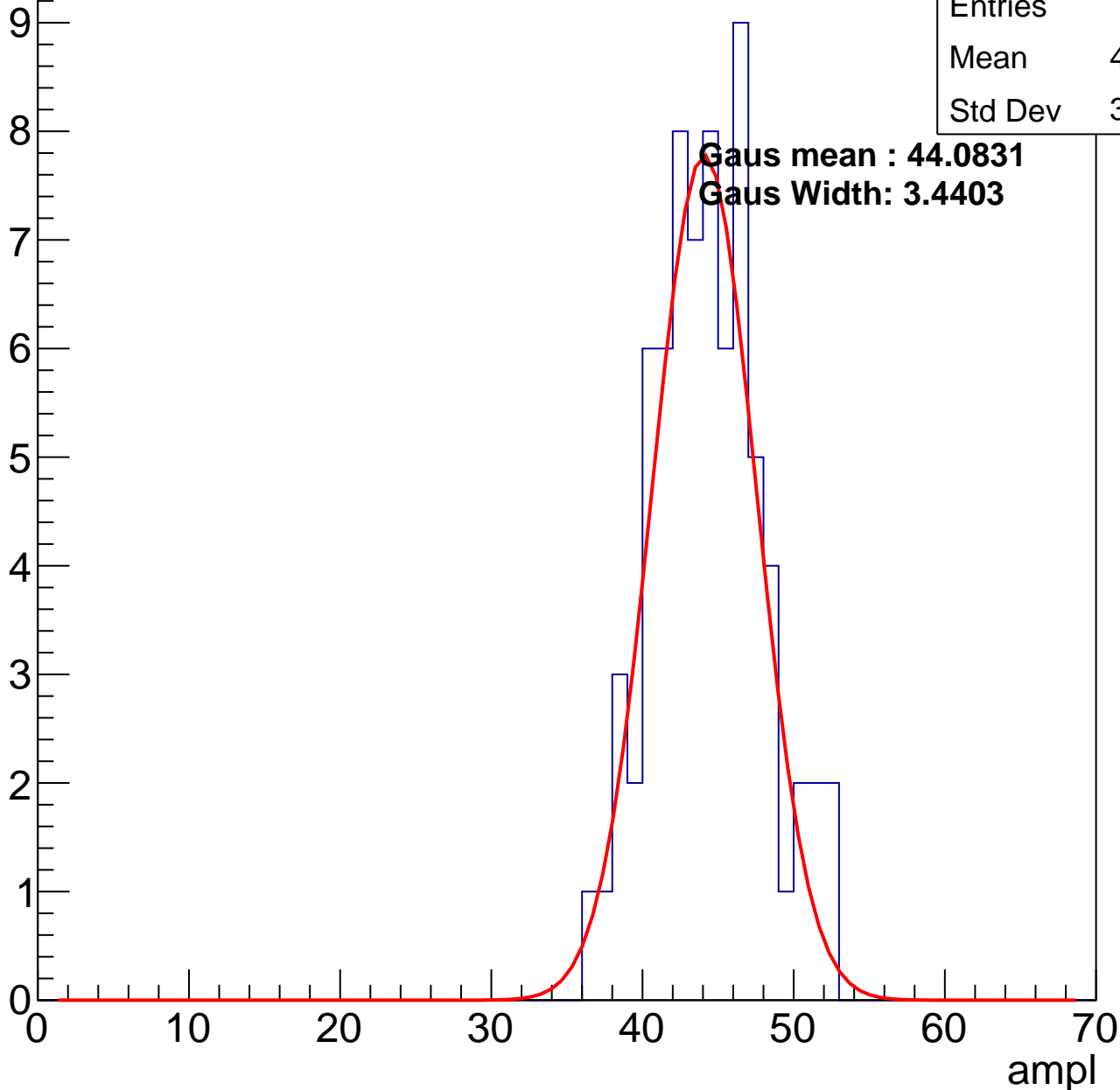
B0L001S, U14-ch37, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.92
Std Dev	3.576

Gaus mean : 44.0831
Gaus Width: 3.4403

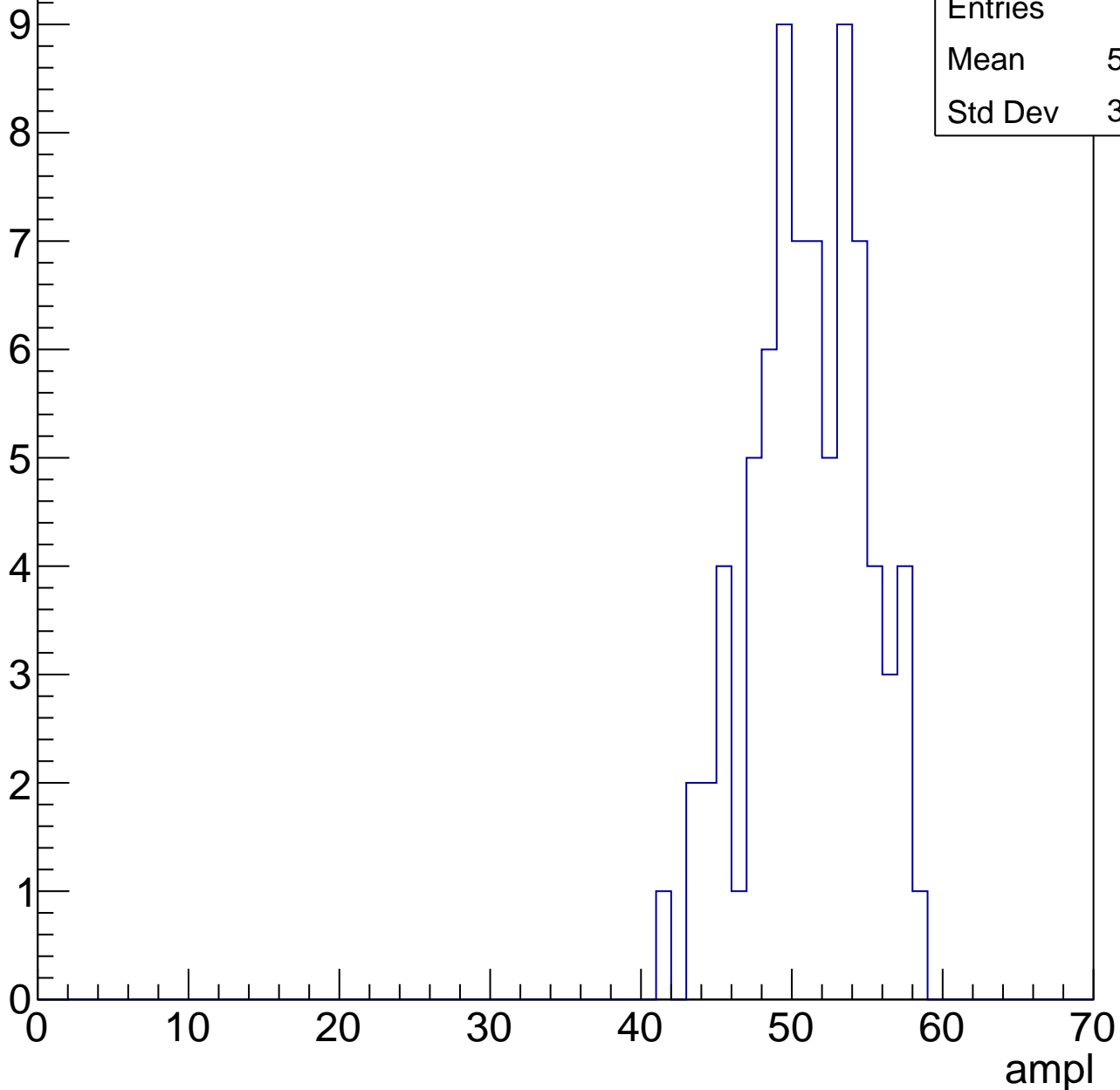


B0L001S, U14-ch37, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	50.66
Std Dev	3.778

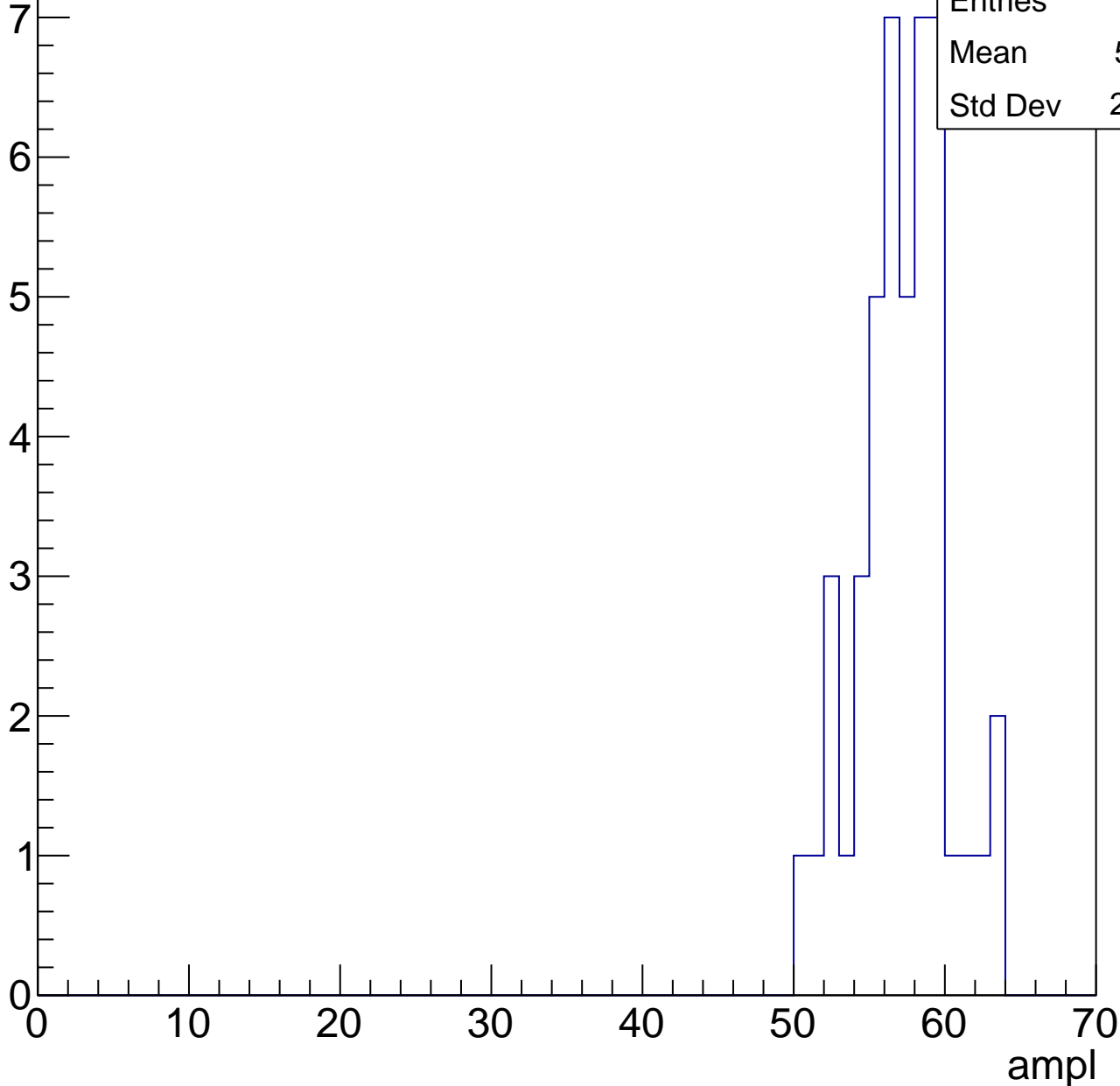


B0L001S, U14-ch37, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	56.71
Std Dev	2.918

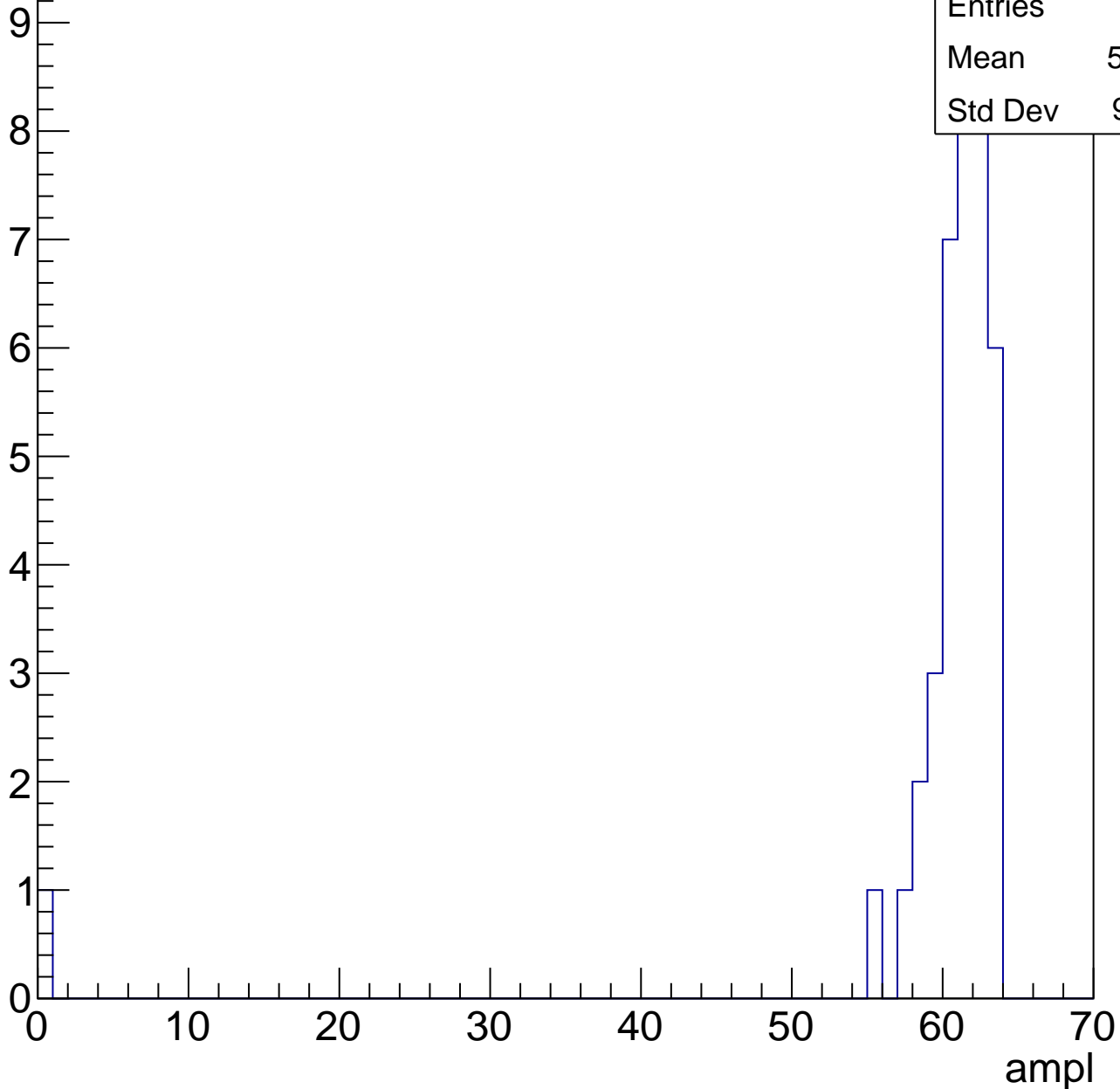


B0L001S, U14-ch37, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	59.18
Std Dev	9.891



B0L001S, U14-ch37, adc6

calib_packv5_042523_0143.root, FC#9, port A1

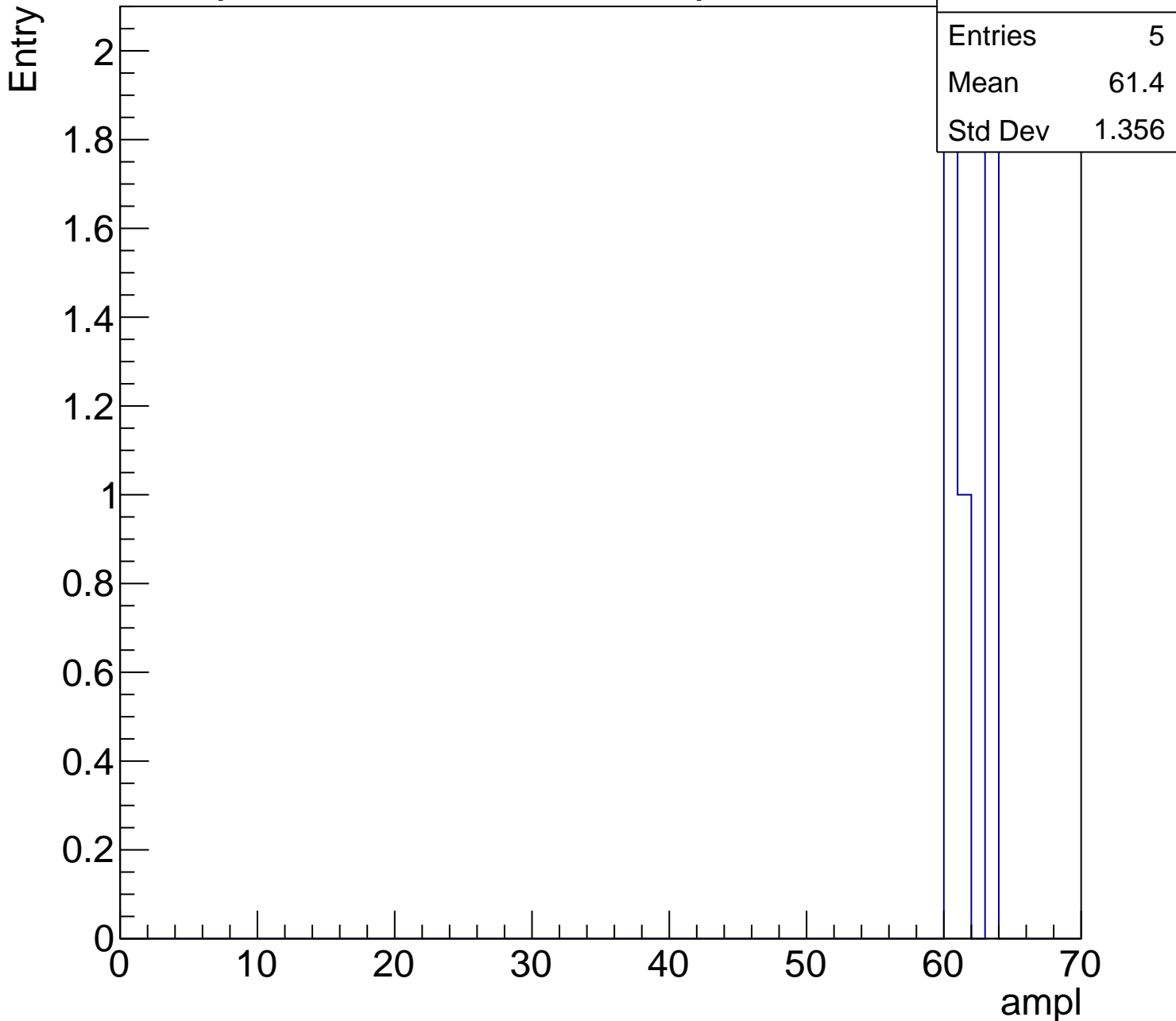
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch37, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch38, adc0

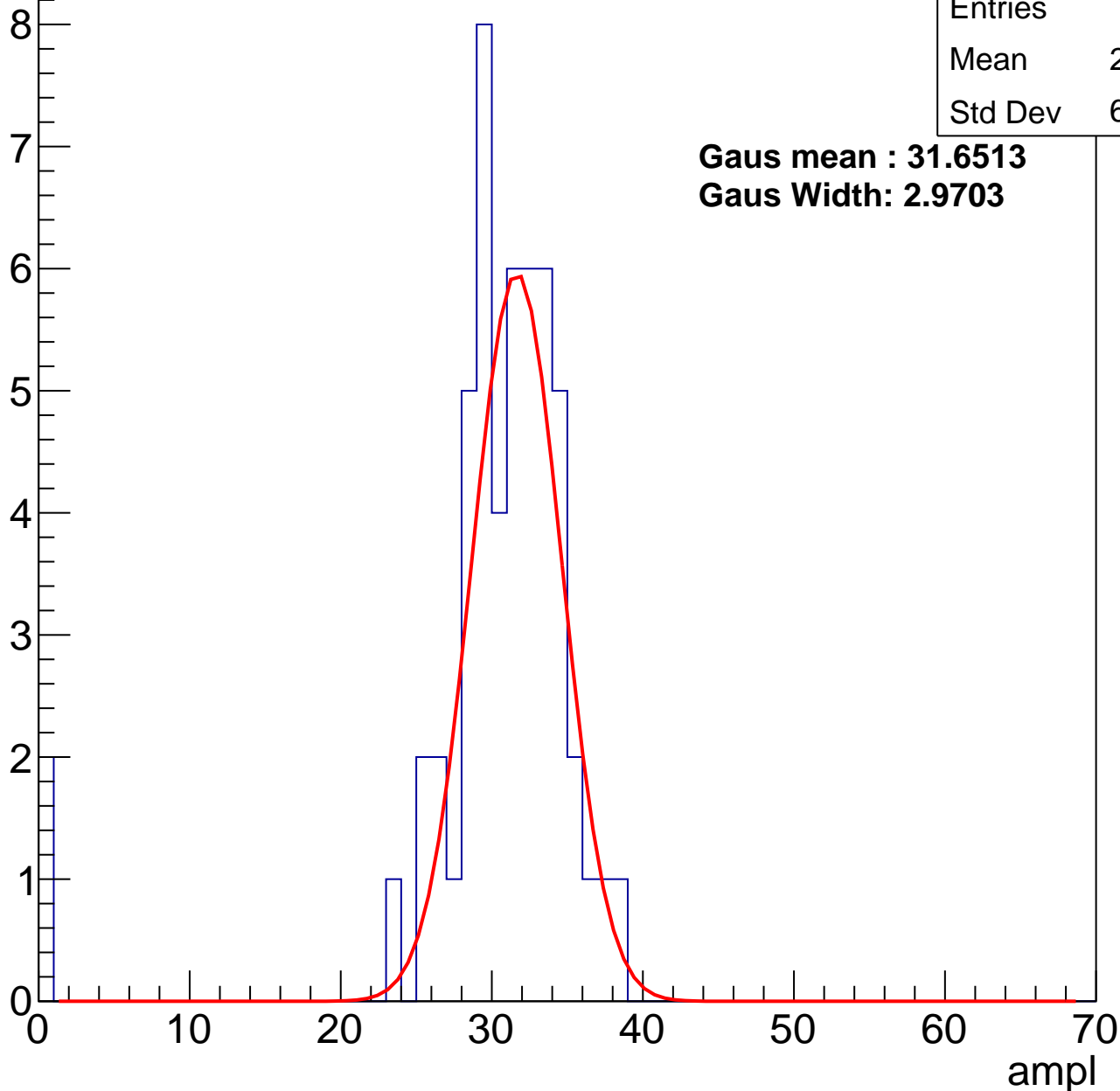
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	29.64
Std Dev	6.622

Gaus mean : 31.6513

Gaus Width: 2.9703



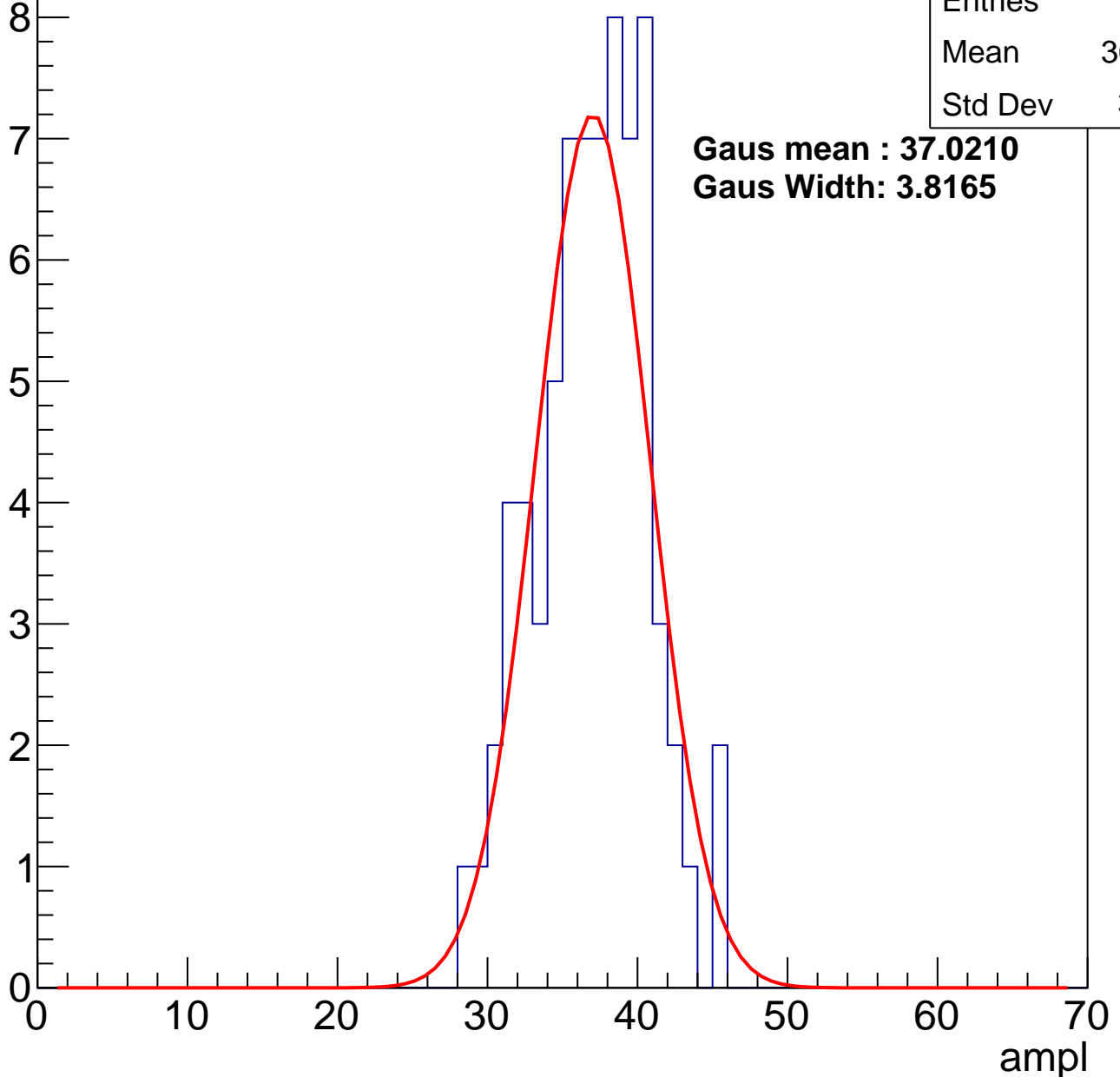
B0L001S, U14-ch38, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.54
Std Dev	3.67

Gaus mean : 37.0210
Gaus Width: 3.8165



B0L001S, U14-ch38, adc2

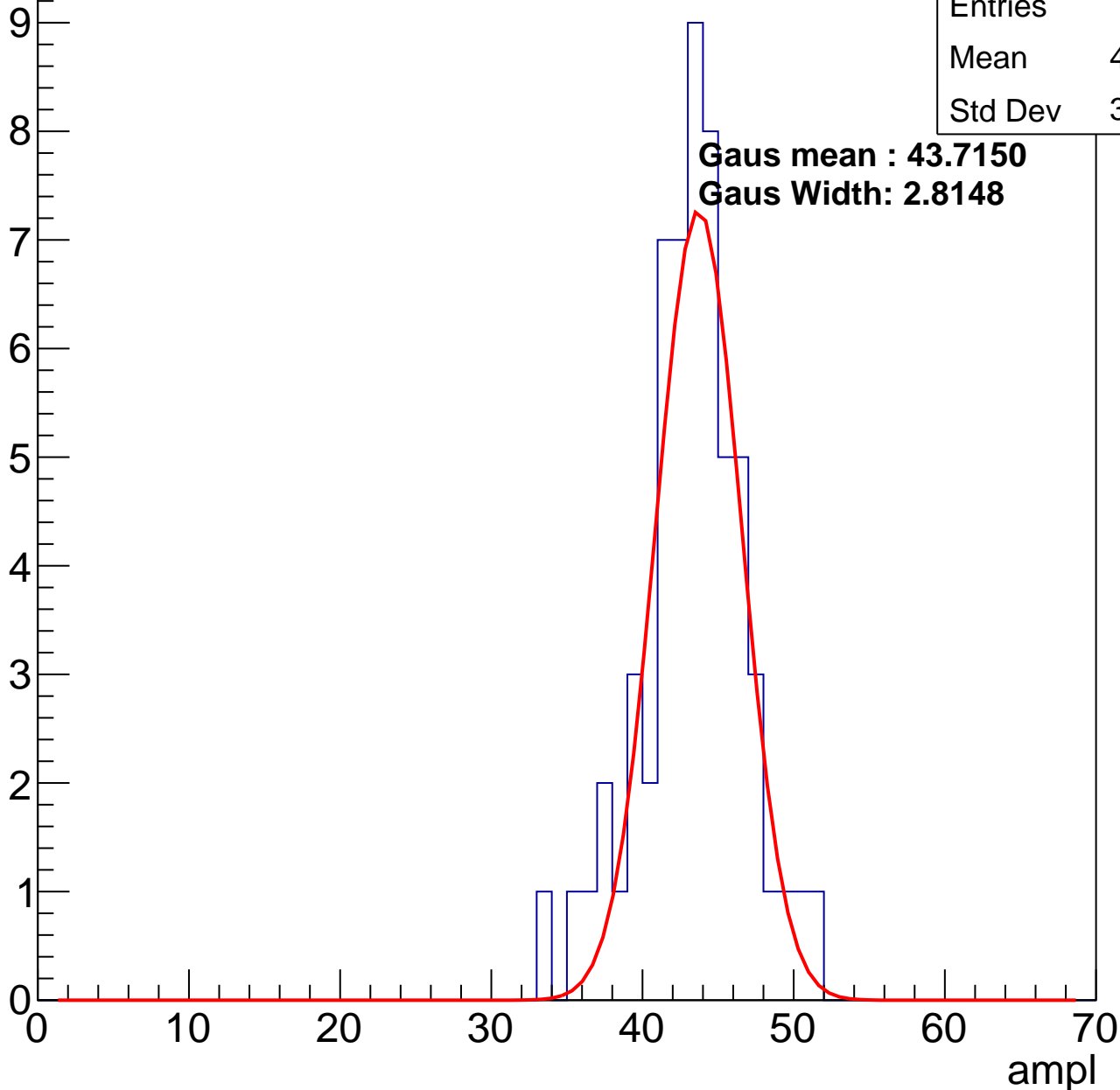
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	42.83
Std Dev	3.455

Gaus mean : 43.7150

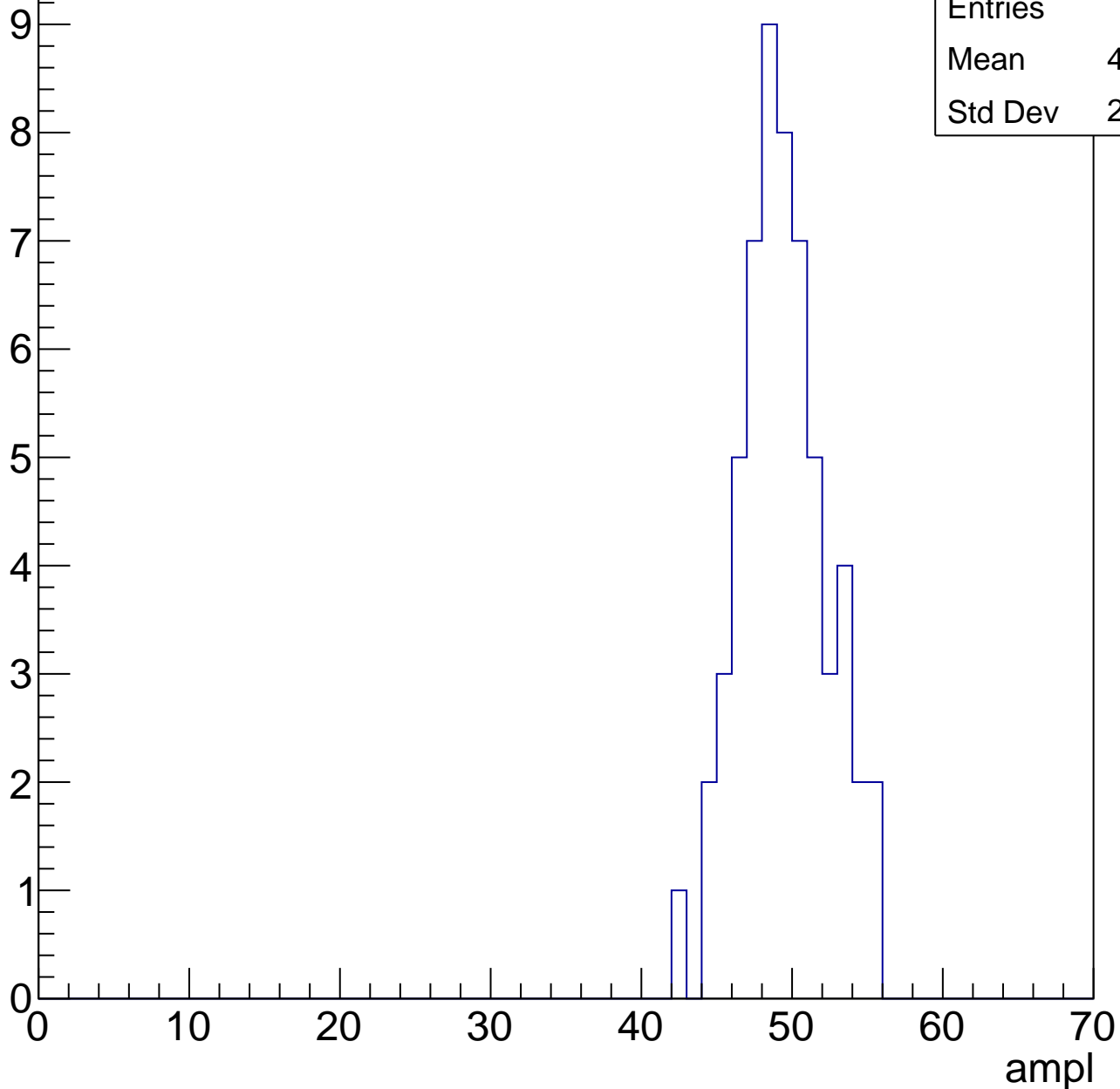
Gaus Width: 2.8148



B0L001S, U14-ch38, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

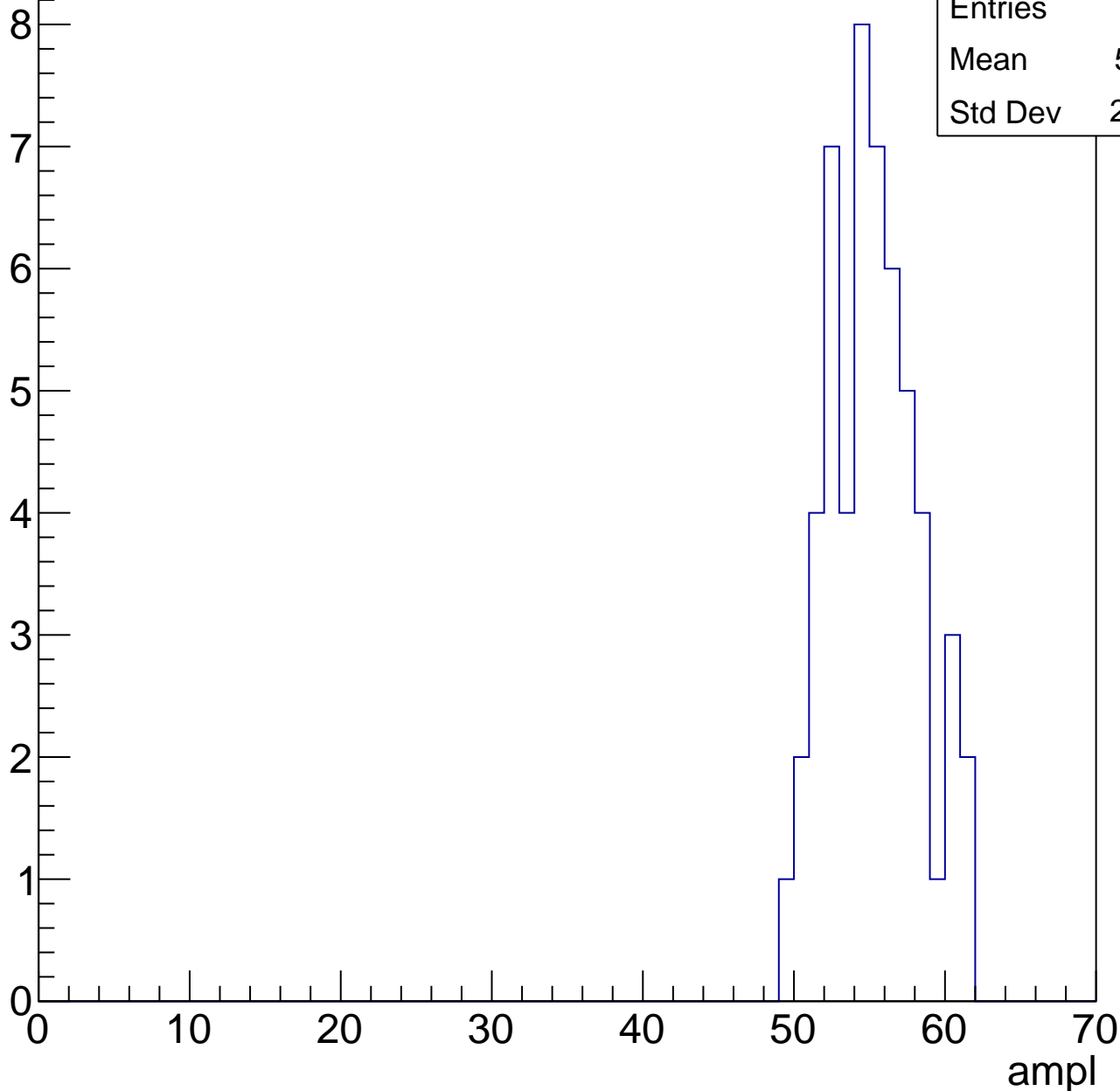


B0L001S, U14-ch38, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	54.81
Std Dev	2.919



B0L001S, U14-ch38, adc5

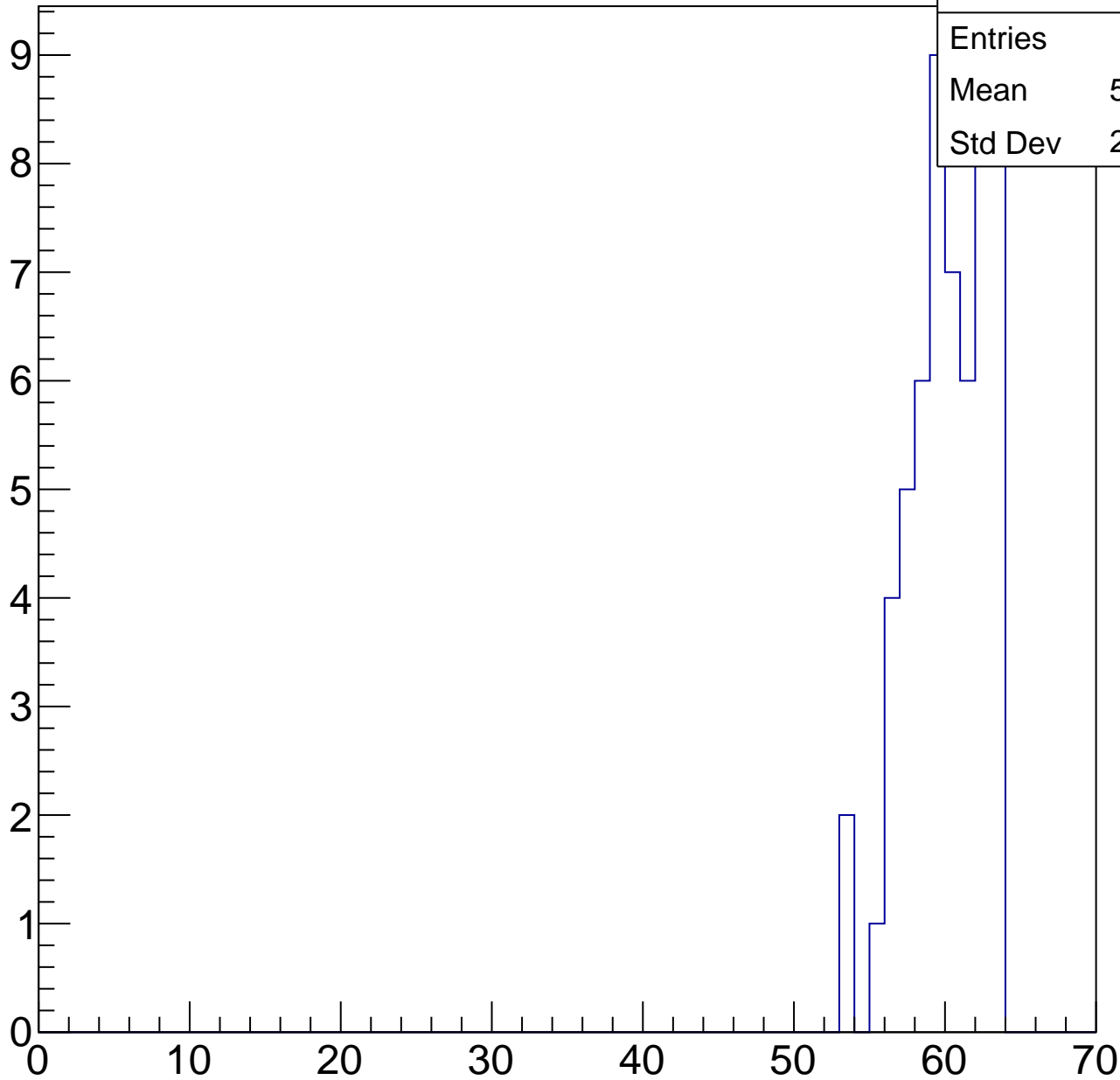
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	56
Mean	59.55
Std Dev	2.549

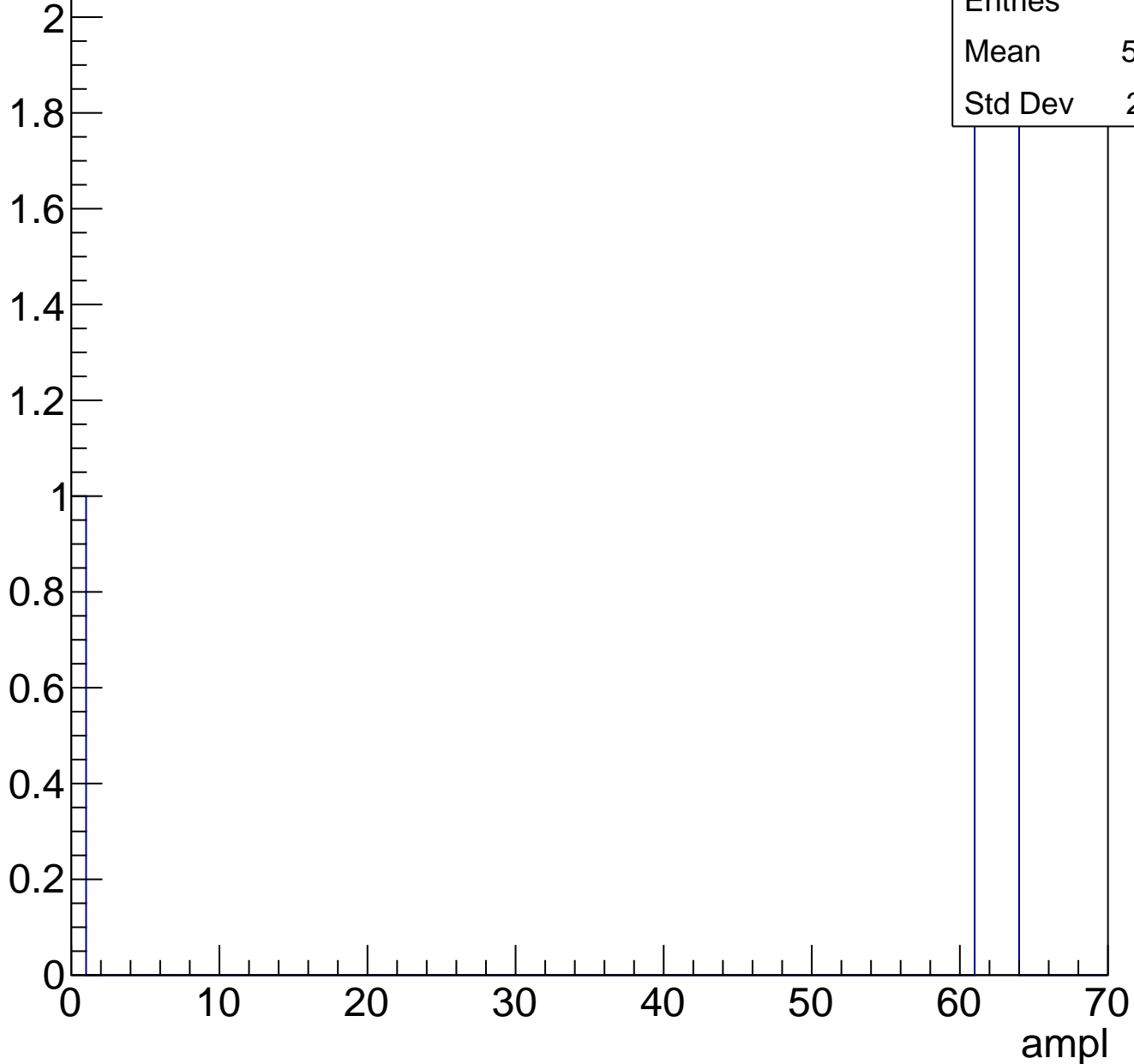
ampl



B0L001S, U14-ch38, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch38, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch39, adc0

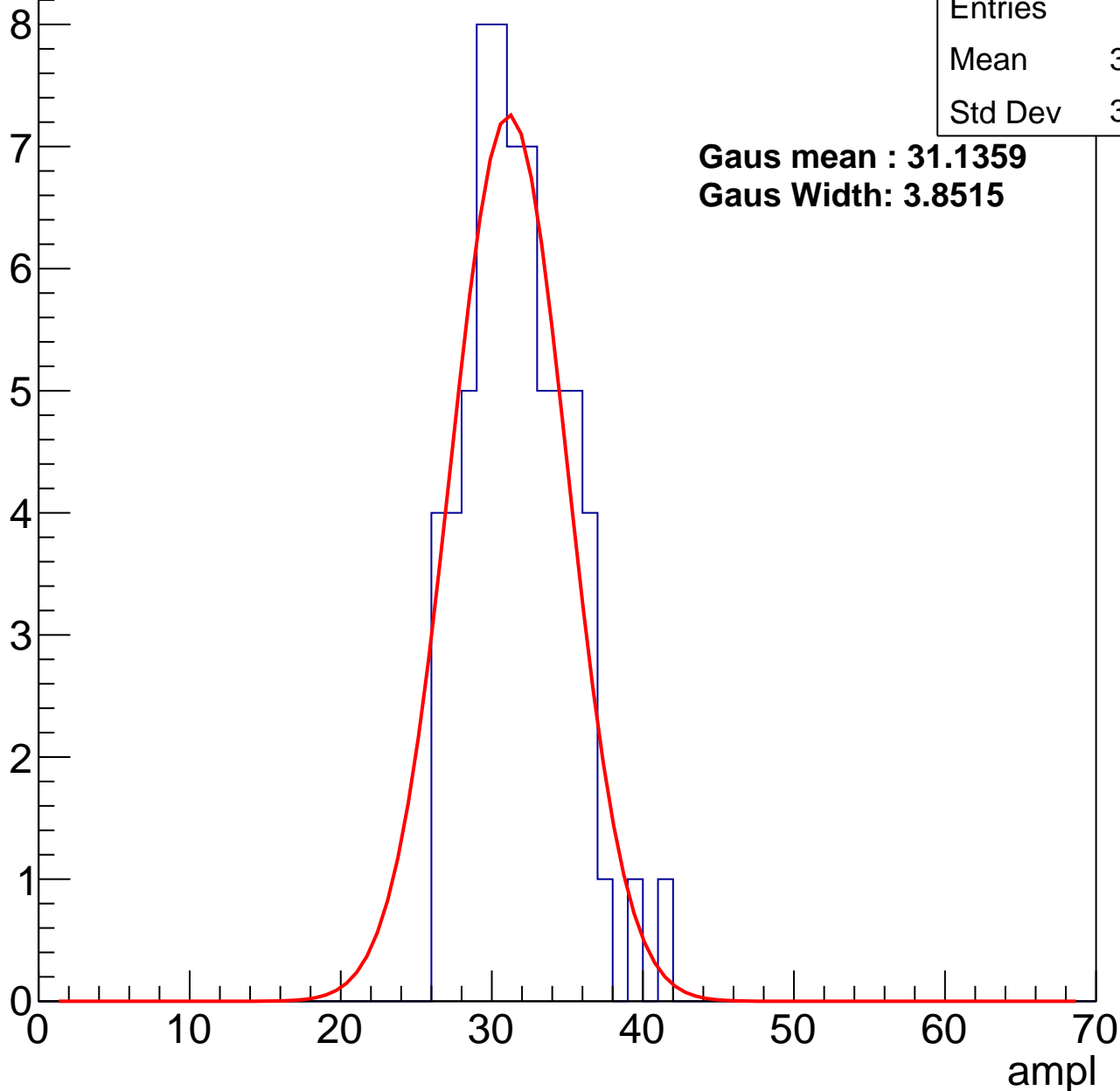
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	31.32
Std Dev	3.268

Gaus mean : 31.1359

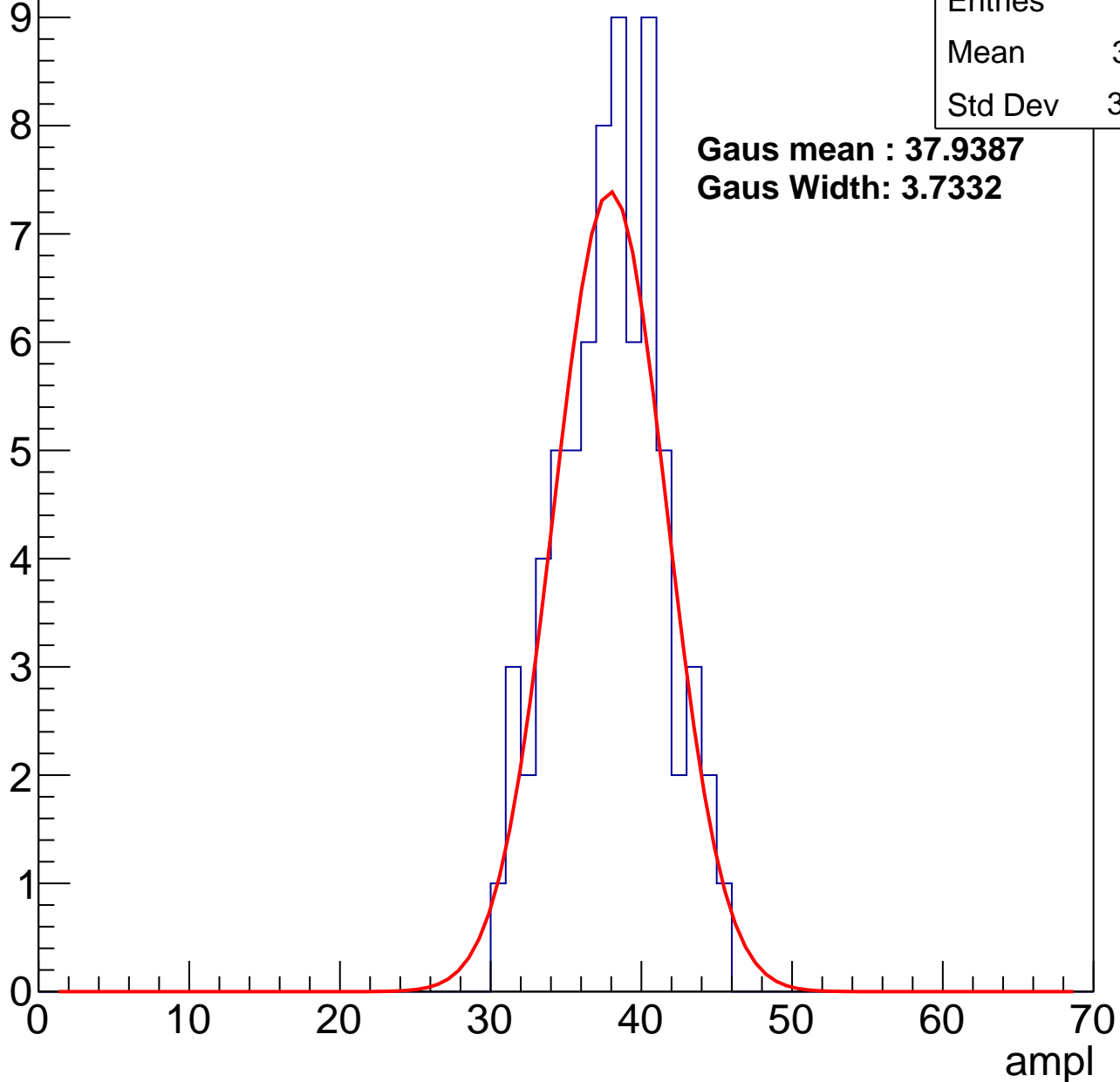
Gaus Width: 3.8515



B0L001S, U14-ch39, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch39, adc2

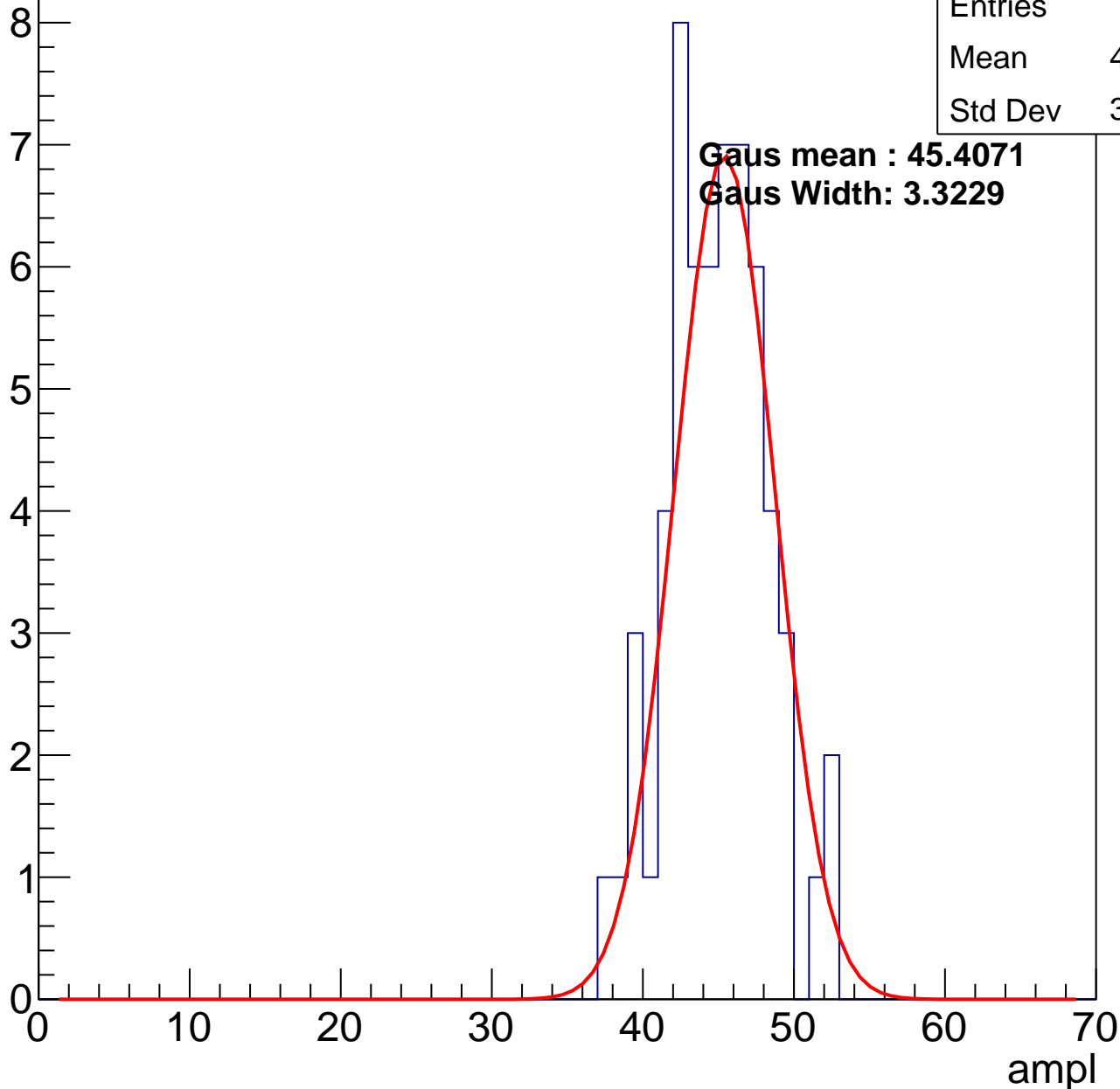
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	44.45
Std Dev	3.273

Gaus mean : 45.4071

Gaus Width: 3.3229

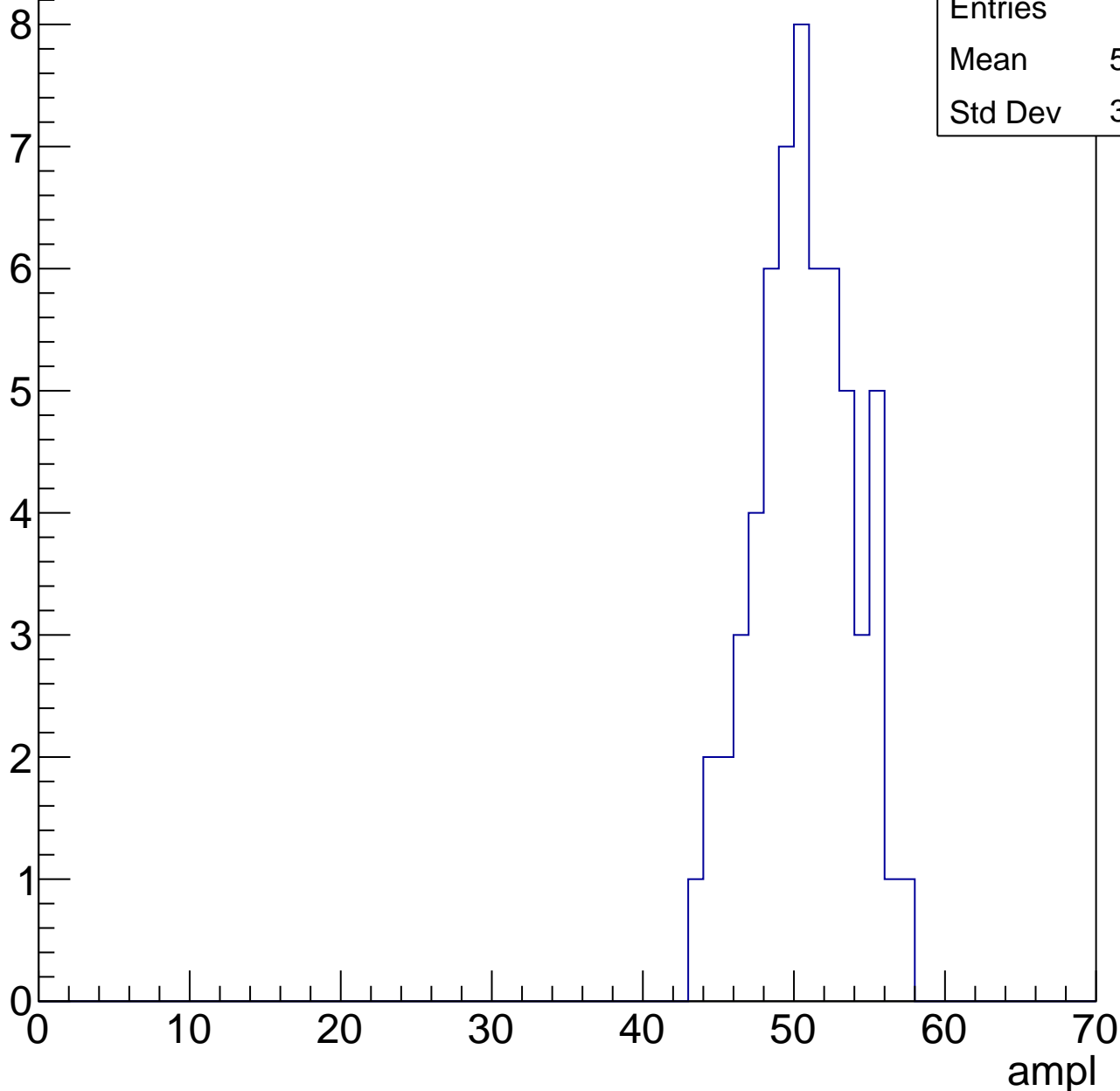


B0L001S, U14-ch39, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	50.18
Std Dev	3.207

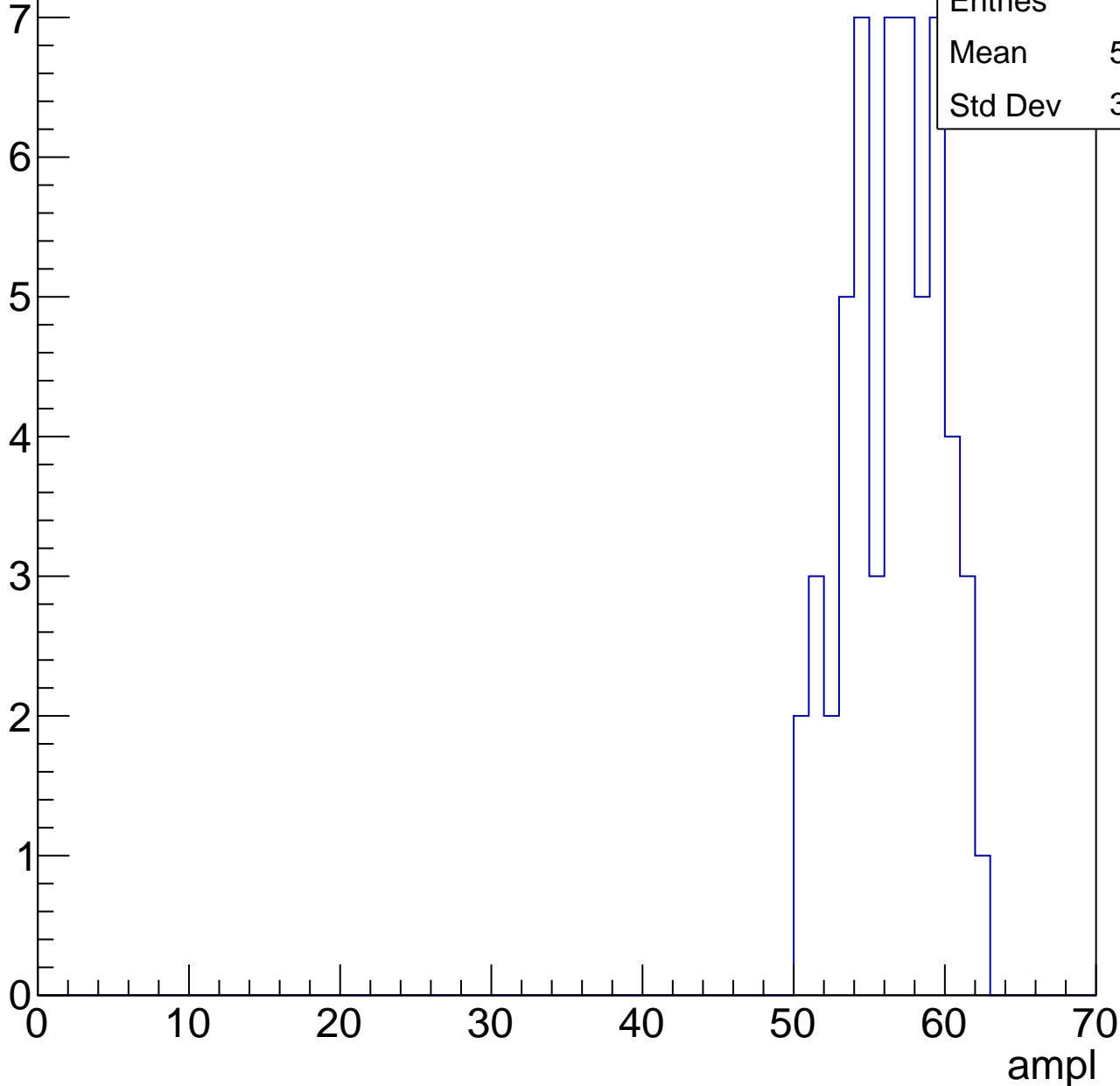


B0L001S, U14-ch39, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.14
Std Dev	3.044

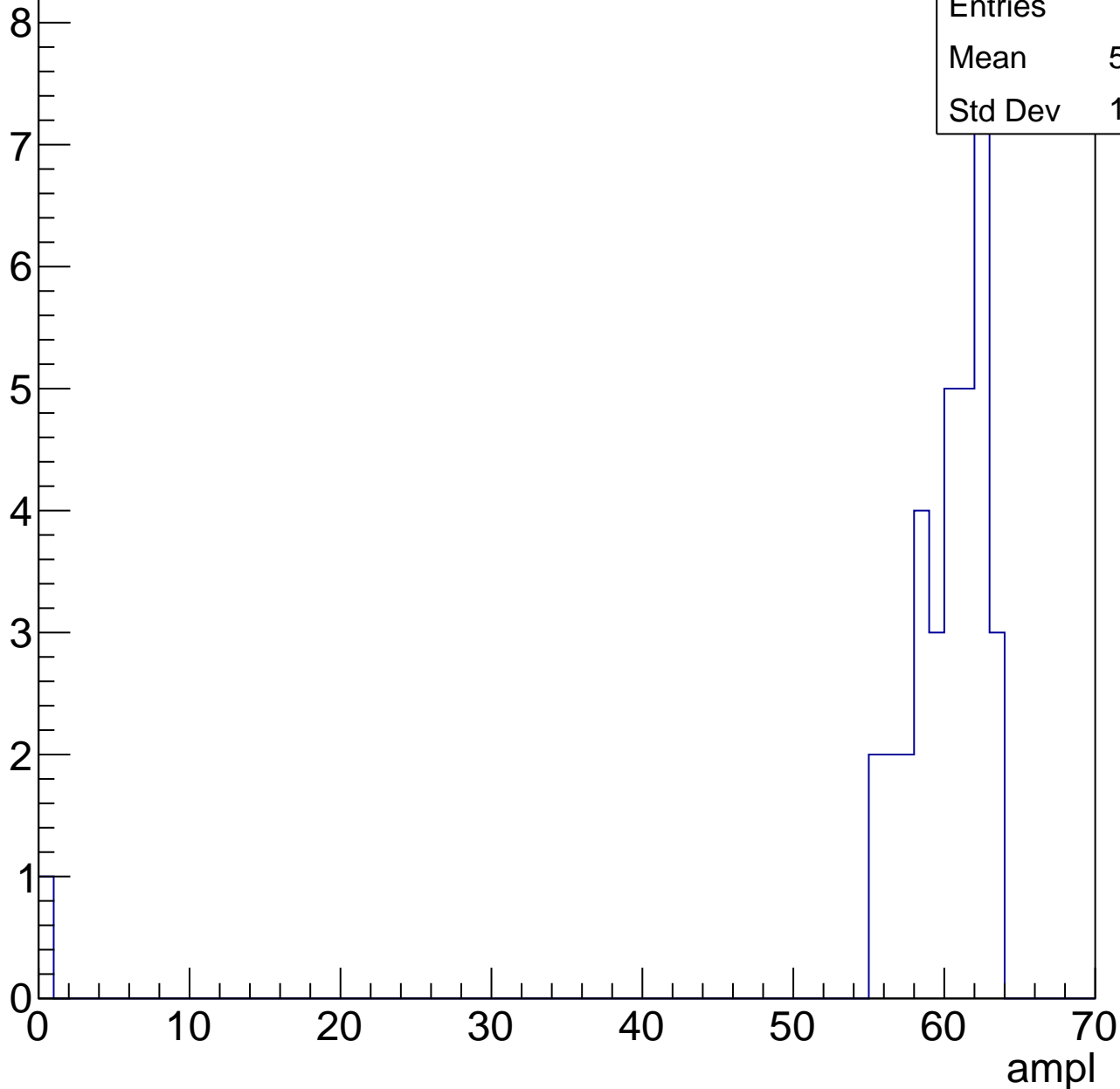


B0L001S, U14-ch39, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.14
Std Dev	10.23

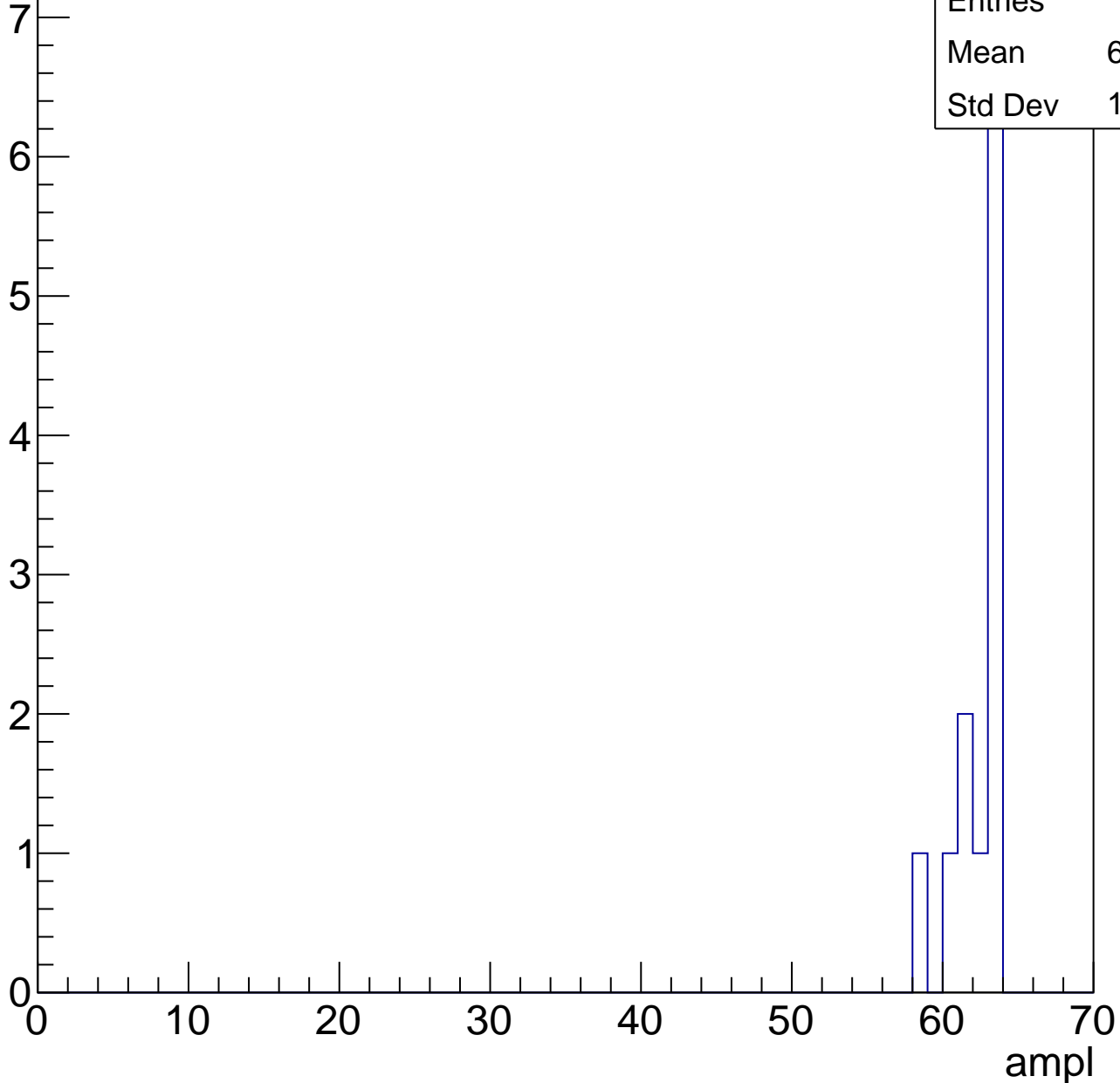


B0L001S, U14-ch39, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	12
Mean	61.92
Std Dev	1.552



B0L001S, U14-ch39, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch40, adc0

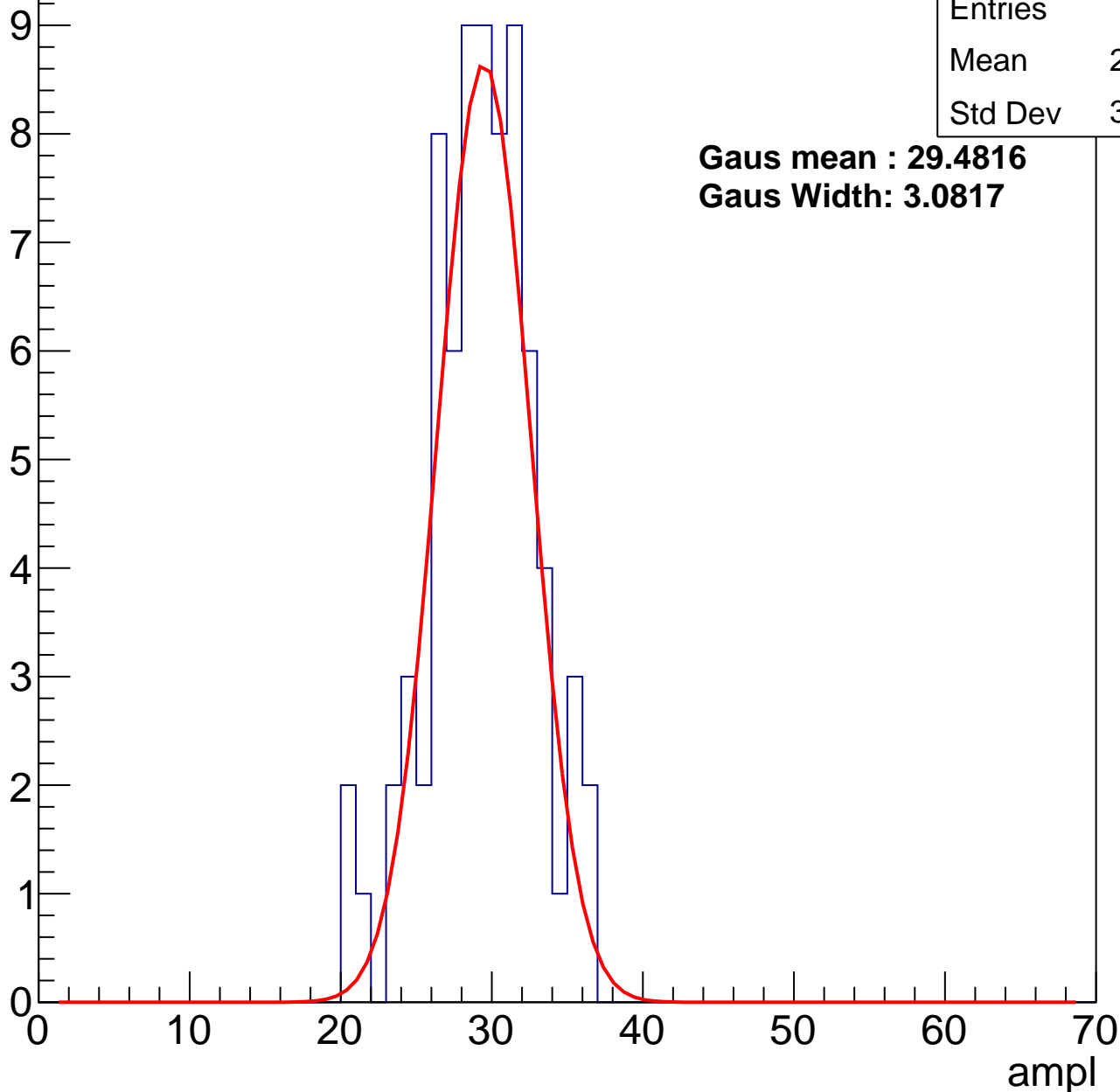
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	28.88
Std Dev	3.468

Gaus mean : 29.4816

Gaus Width: 3.0817



B0L001S, U14-ch40, adc1

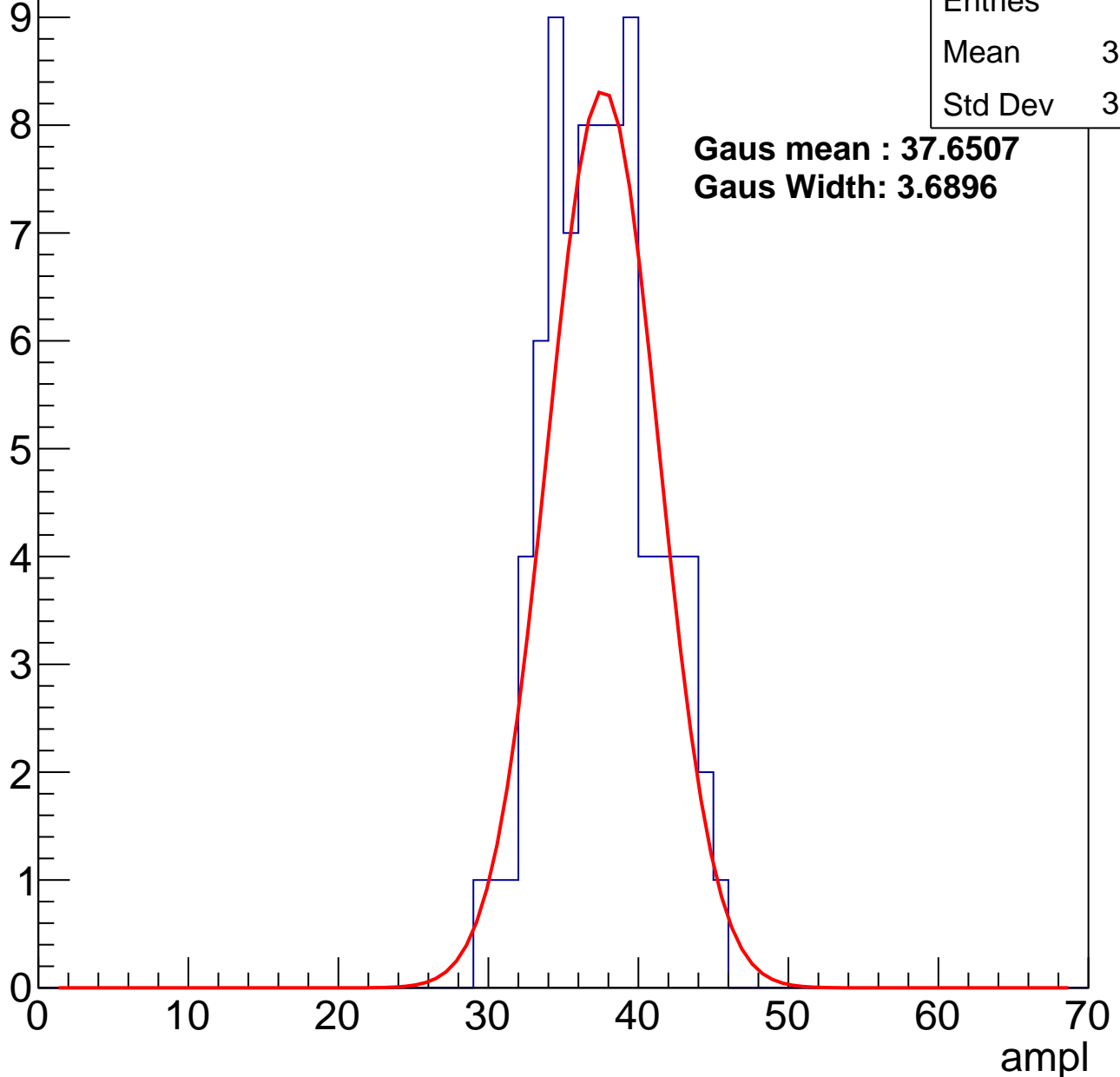
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.07
Std Dev	3.534

Gaus mean : 37.6507

Gaus Width: 3.6896



B0L001S, U14-ch40, adc2

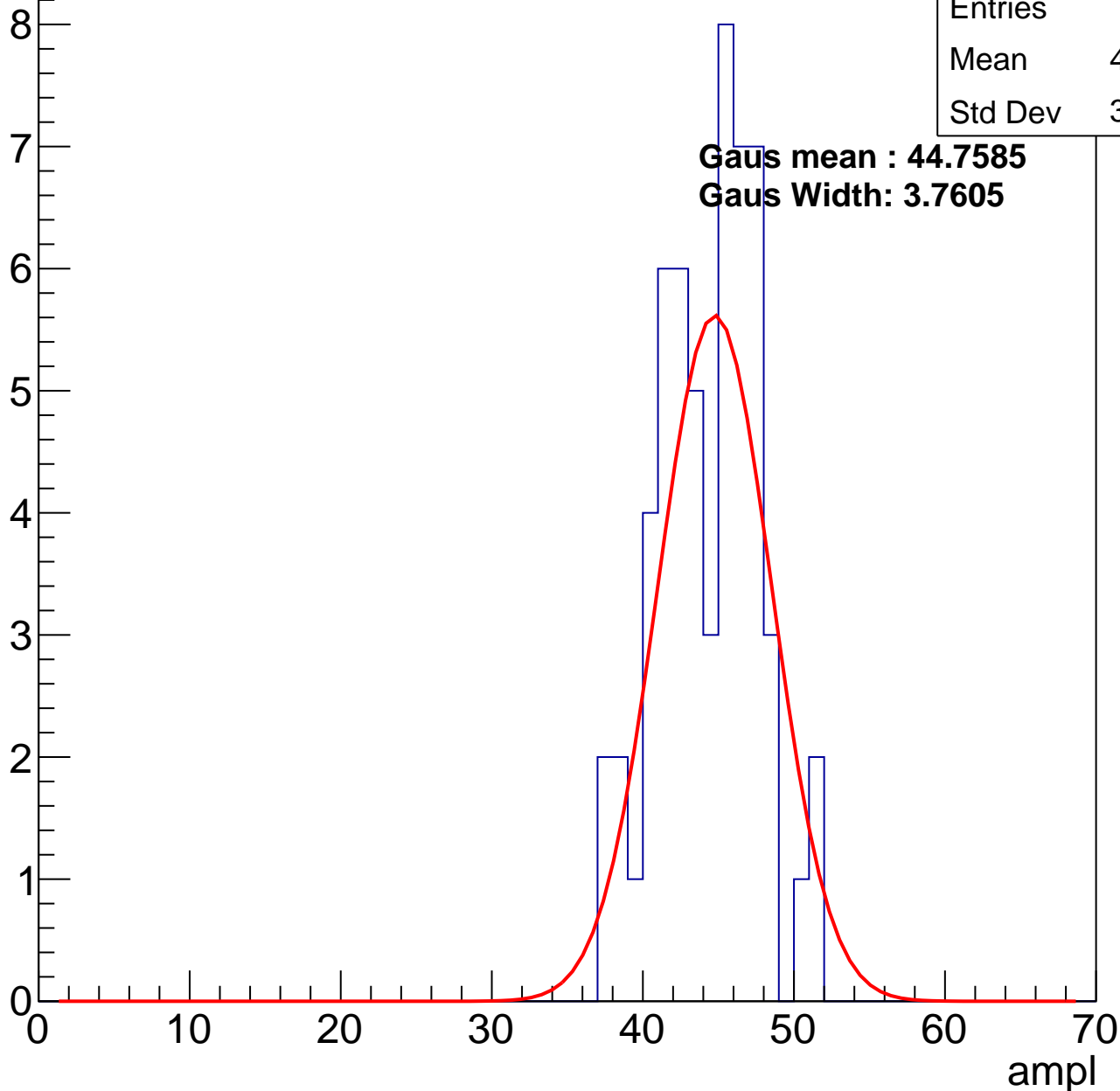
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	43.88
Std Dev	3.304

Gaus mean : 44.7585

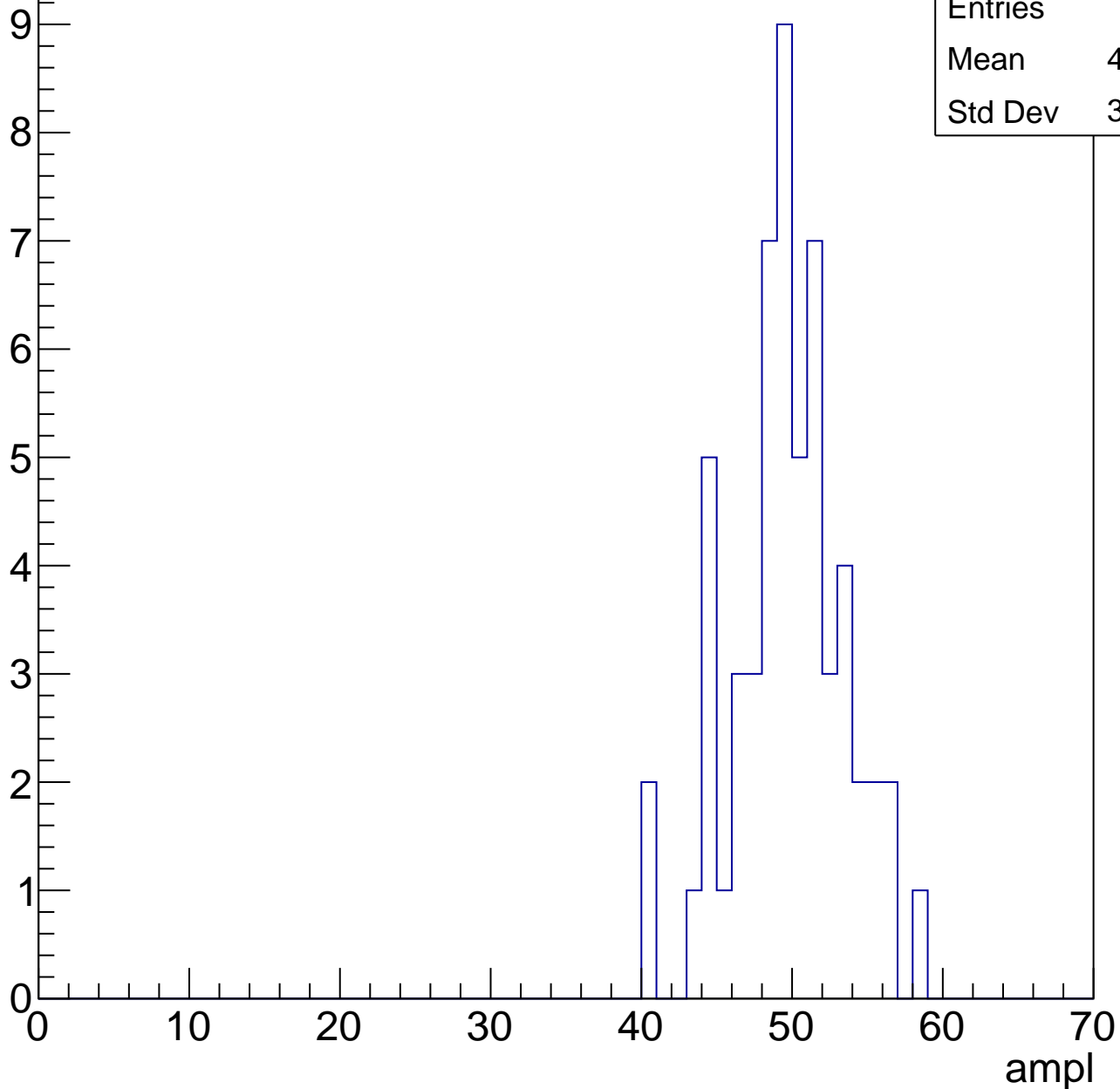
Gaus Width: 3.7605



B0L001S, U14-ch40, adc3

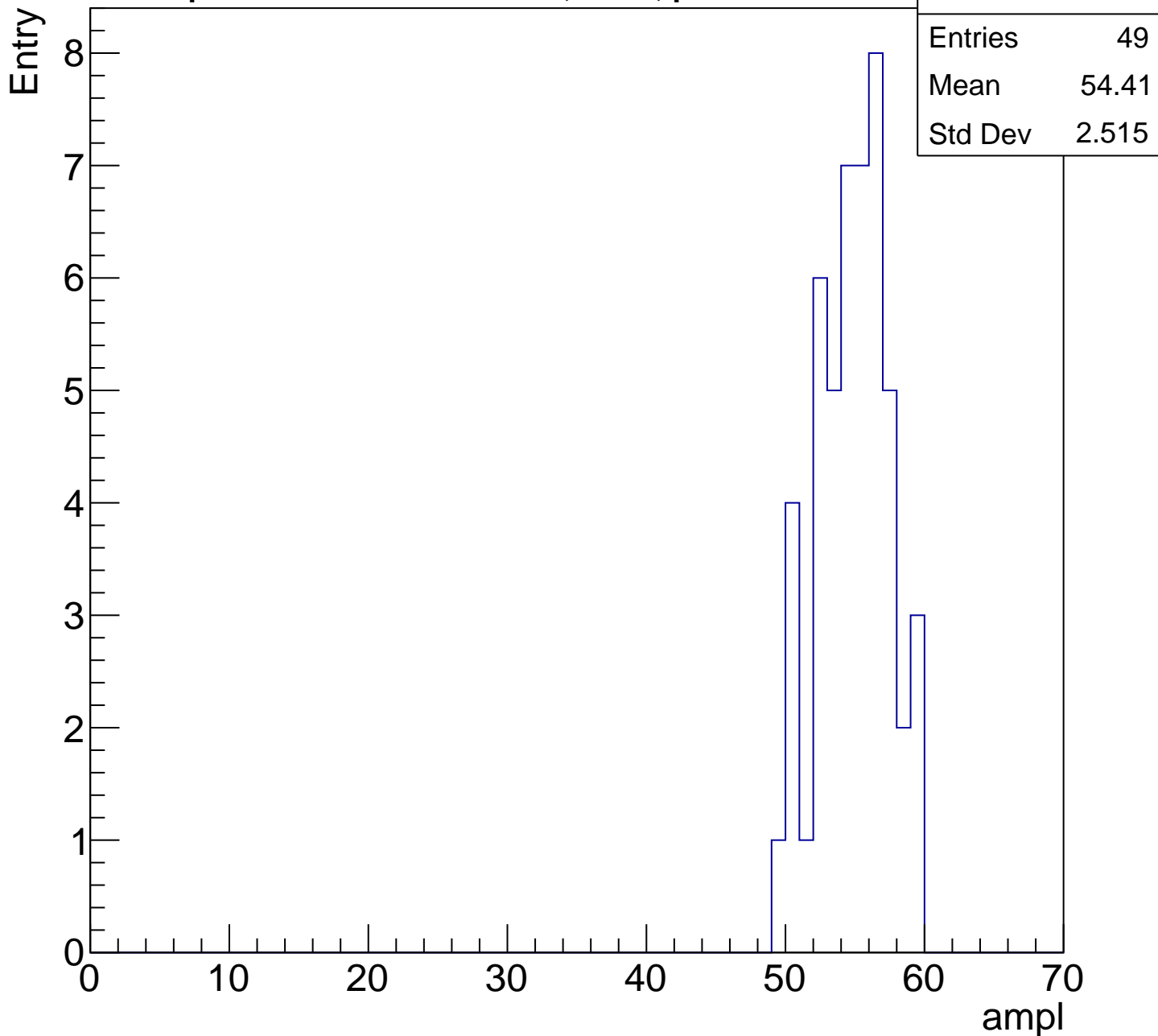
calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch40, adc4

calib_packv5_042523_0143.root, FC#9, port A1

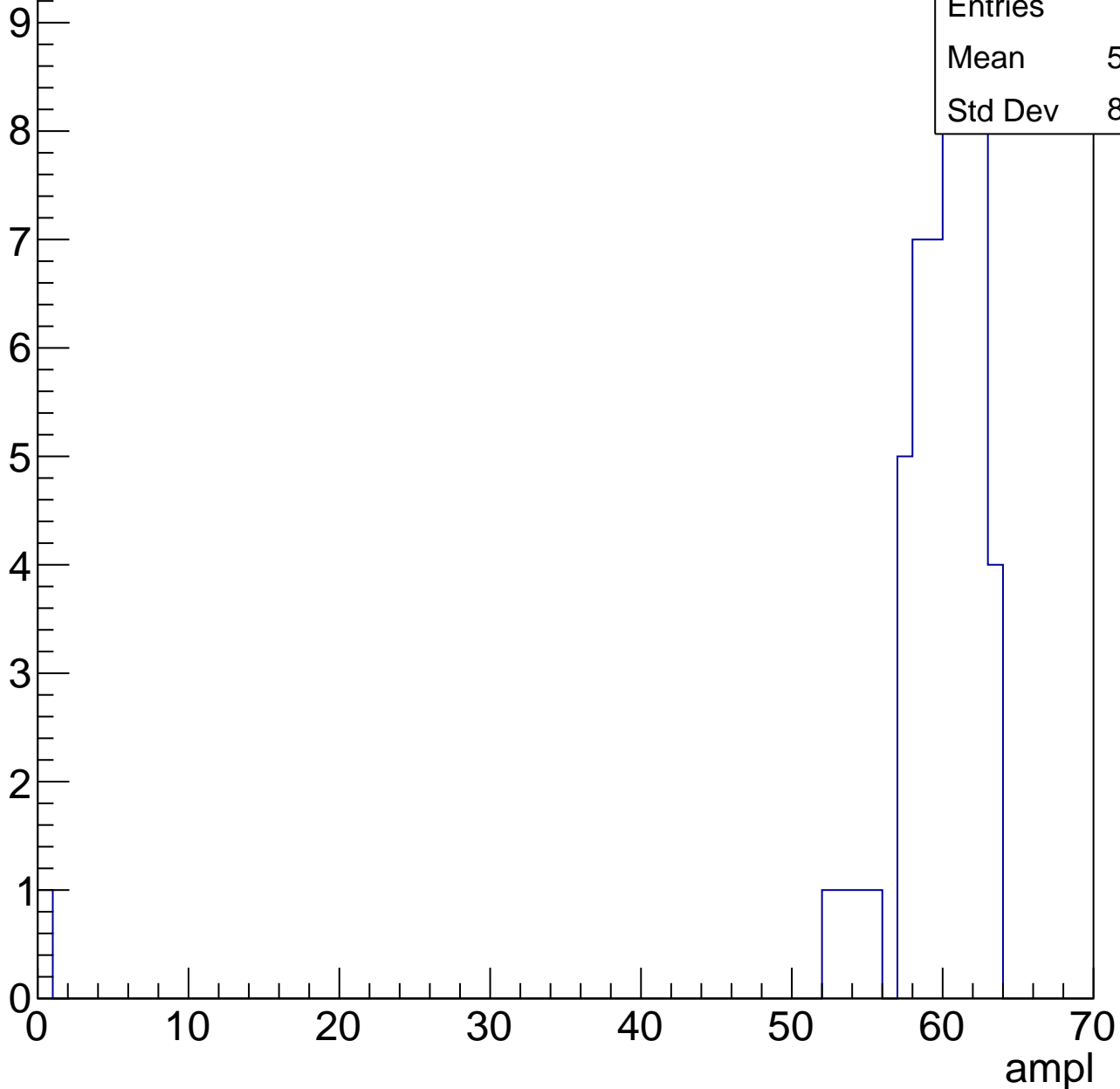


B0L001S, U14-ch40, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.42
Std Dev	8.468

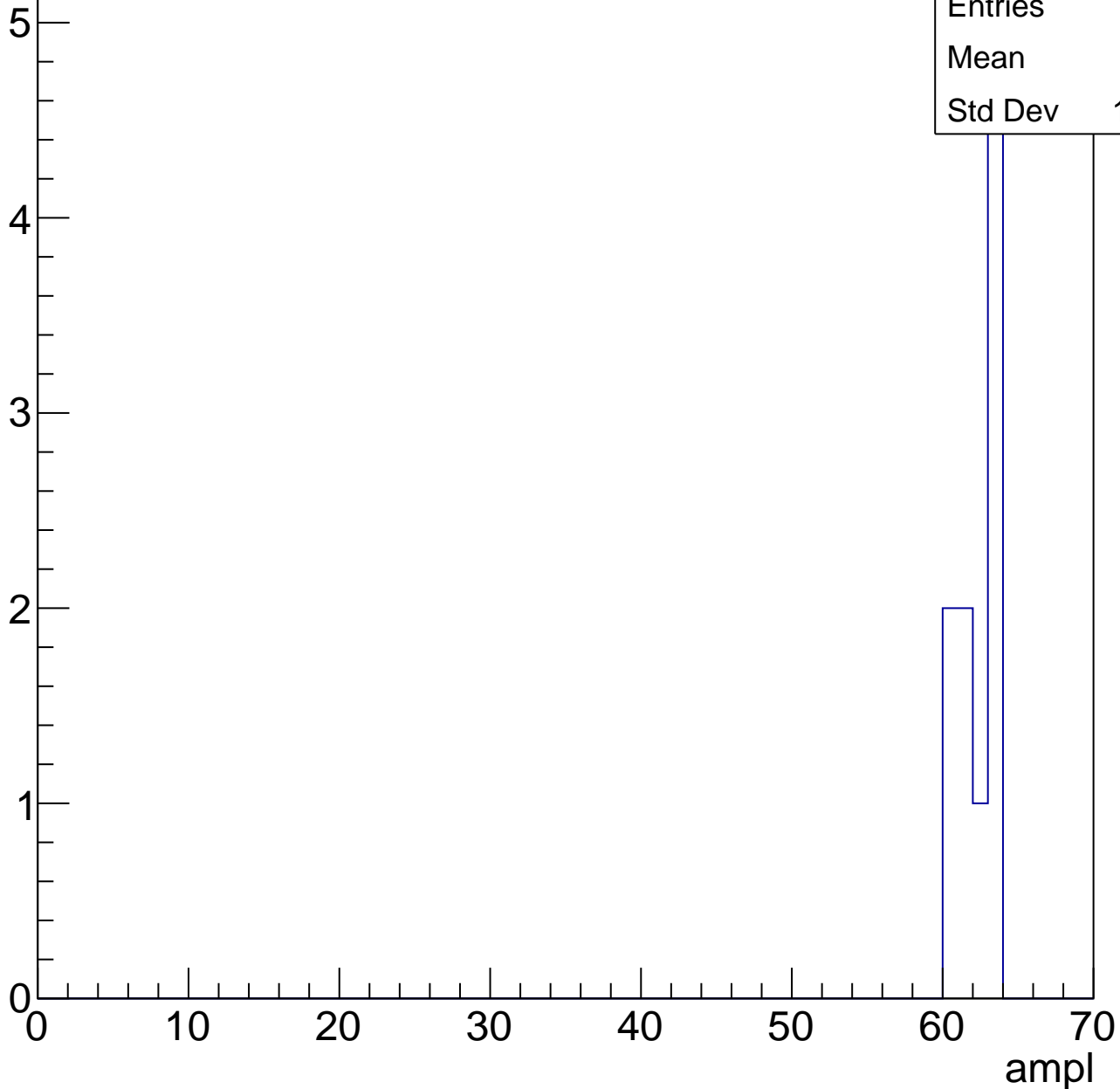


B0L001S, U14-ch40, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	10
Mean	61.9
Std Dev	1.221



B0L001S, U14-ch40, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch41, adc0

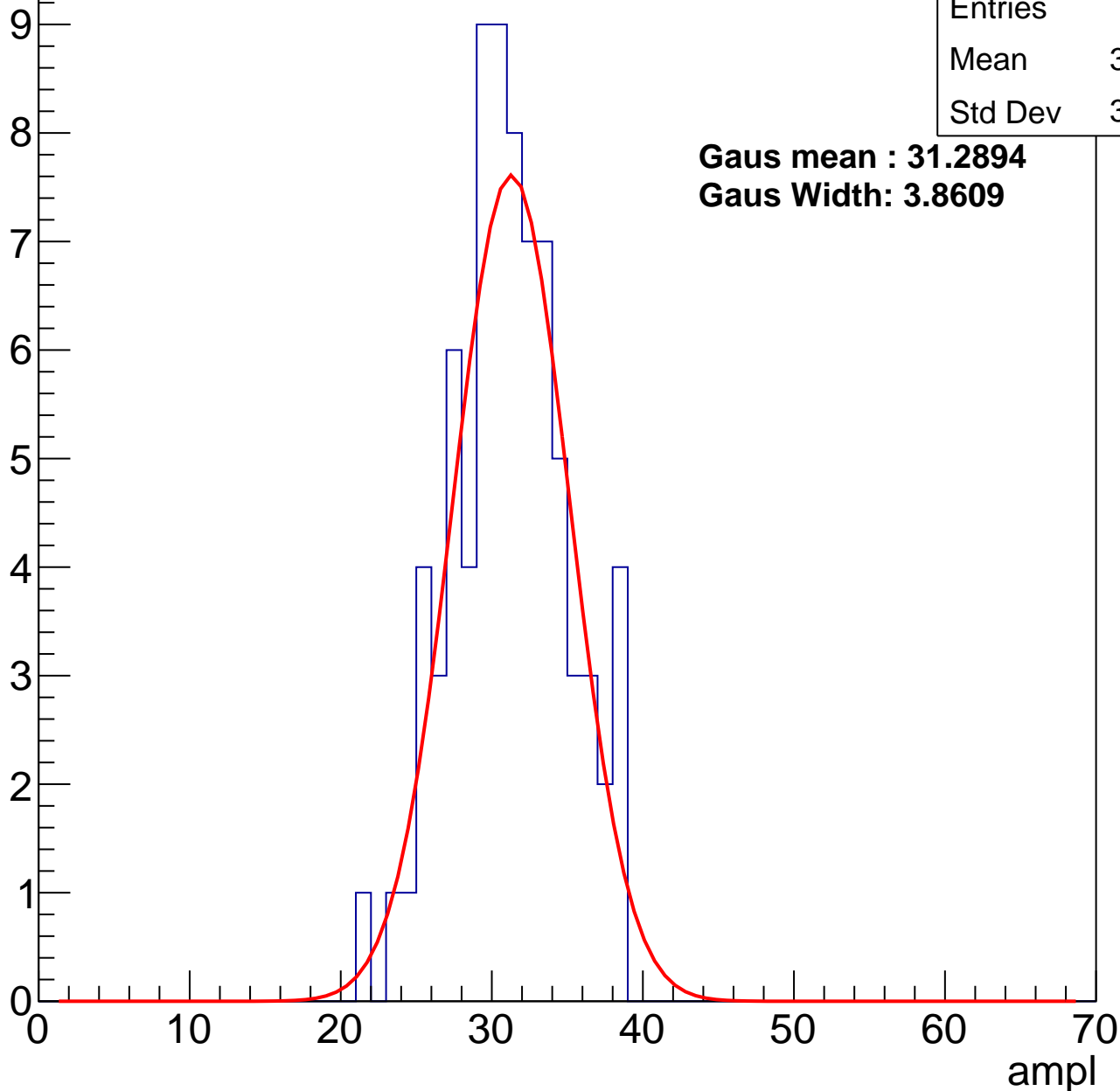
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	30.69
Std Dev	3.732

Gaus mean : 31.2894

Gaus Width: 3.8609



B0L001S, U14-ch41, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	38.28
Std Dev	4.08

7

6

5

4

3

2

1

0

Gaus mean : 38.0770

Gaus Width: 5.0110

0

10

20

30

40

50

60

70

ampl

B0L001S, U14-ch41, adc2

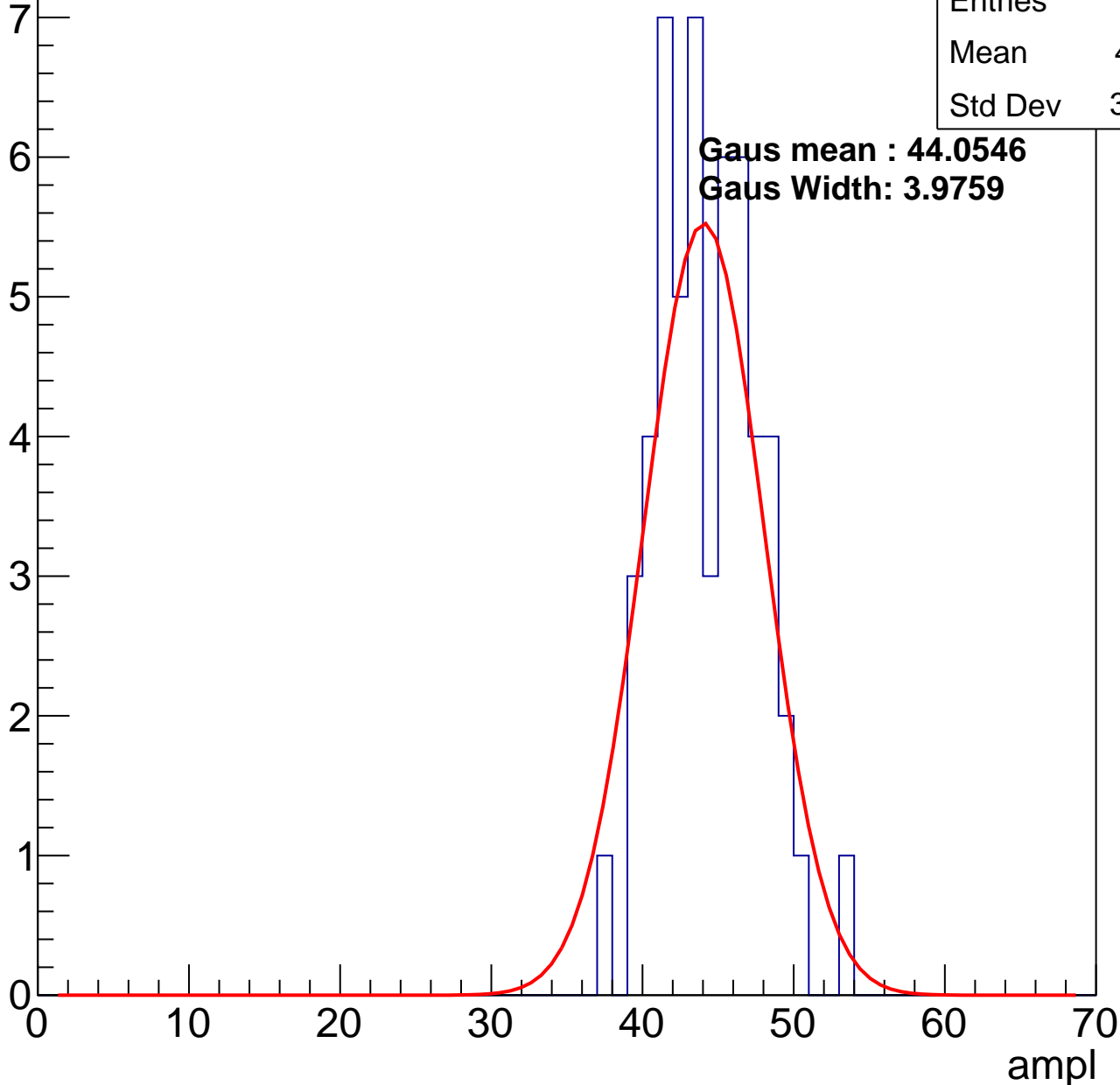
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	43.91
Std Dev	3.262

Gaus mean : 44.0546

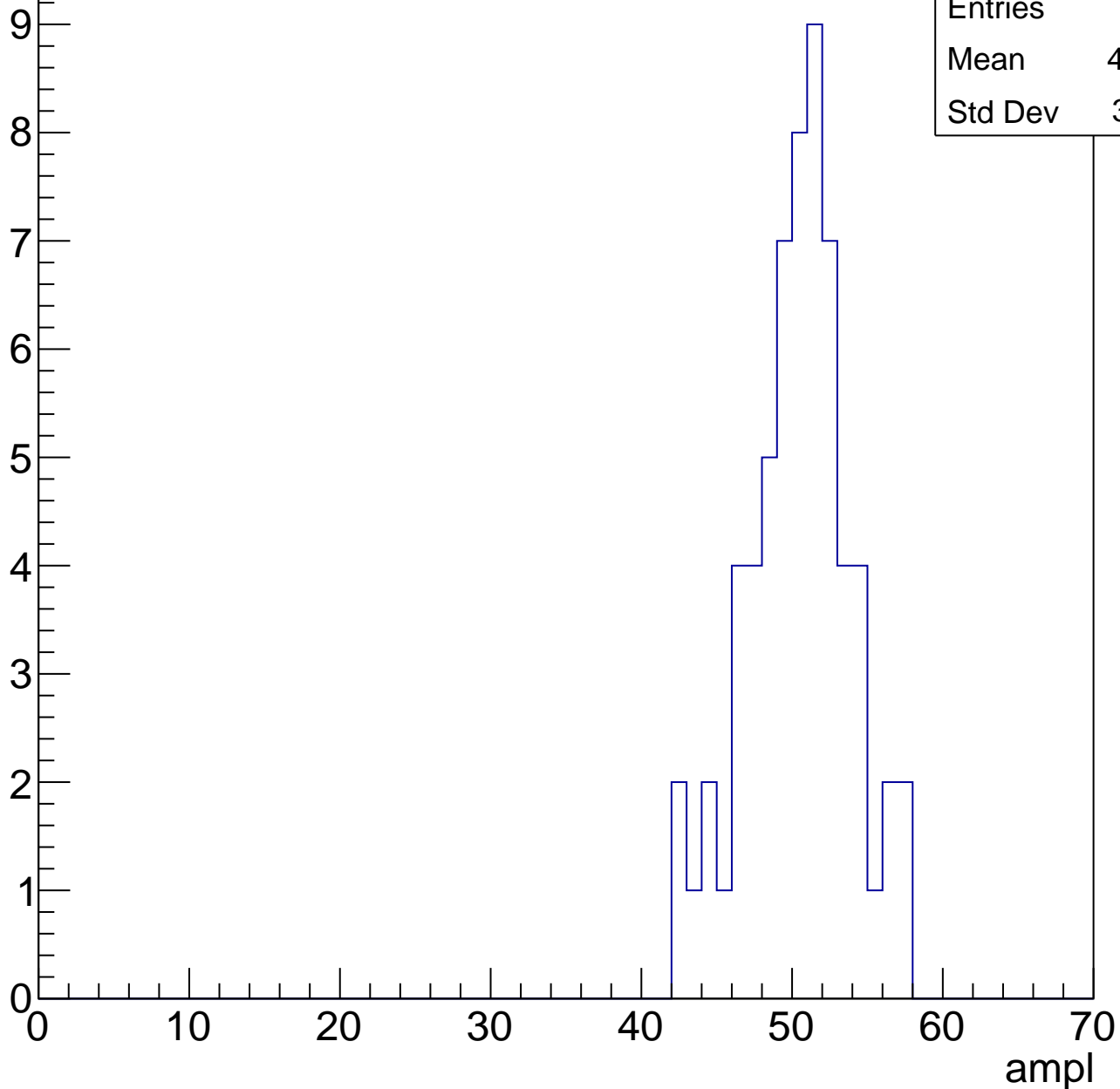
Gaus Width: 3.9759



B0L001S, U14-ch41, adc3

calib_packv5_042523_0143.root, FC#9, port A1

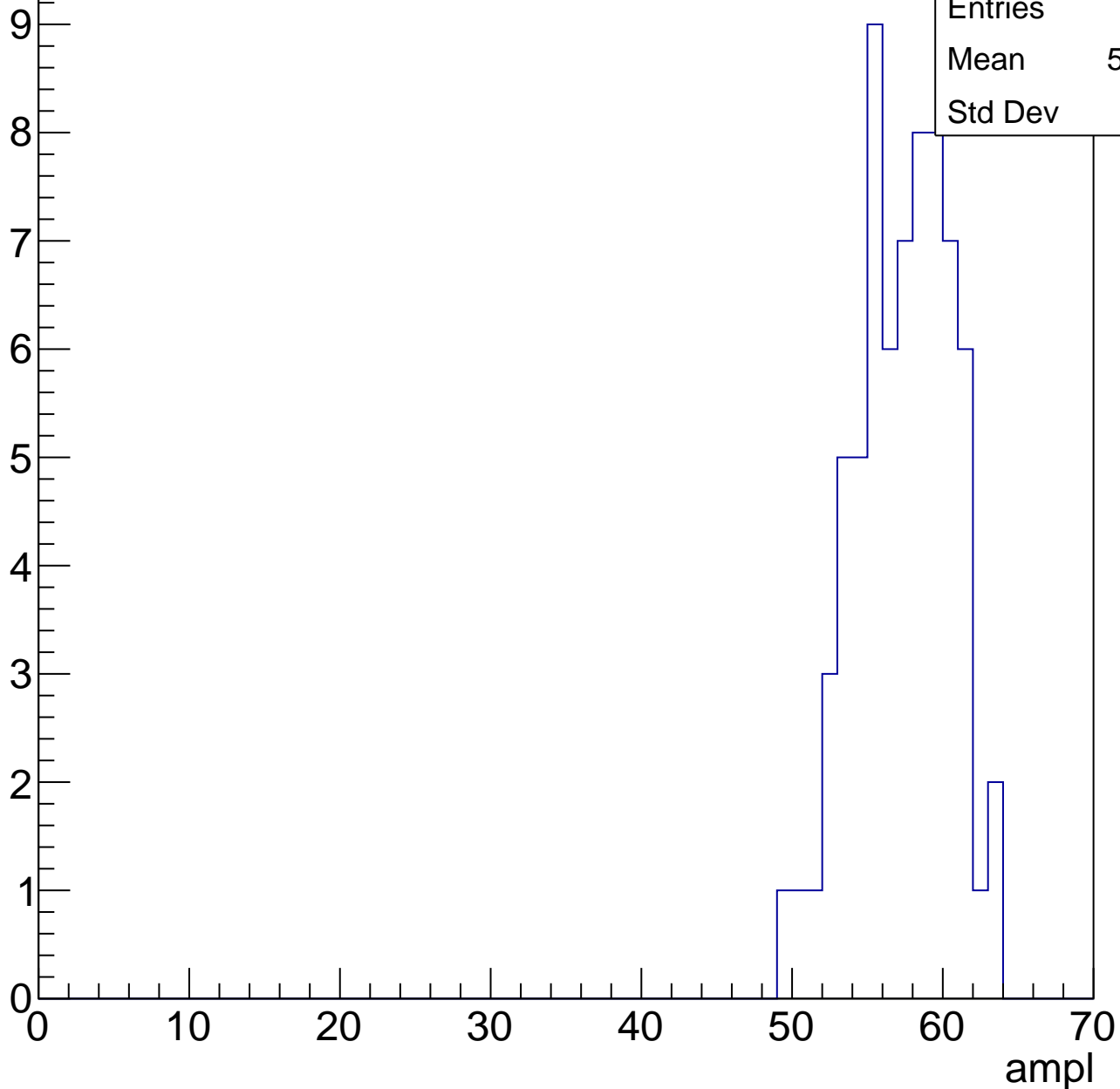
Entry



B0L001S, U14-ch41, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



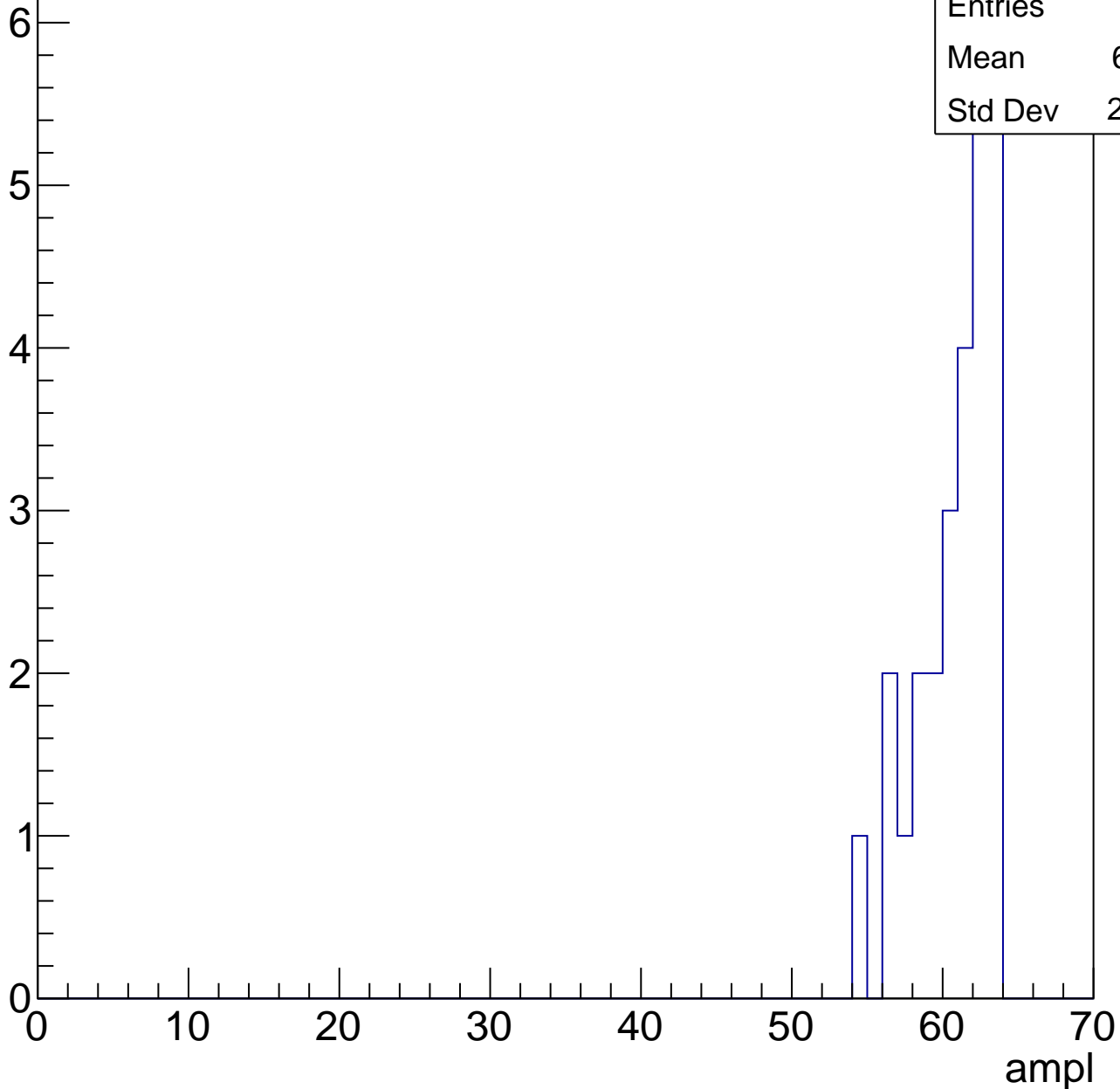
Entries	70
Mean	56.87
Std Dev	3.13

B0L001S, U14-ch41, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

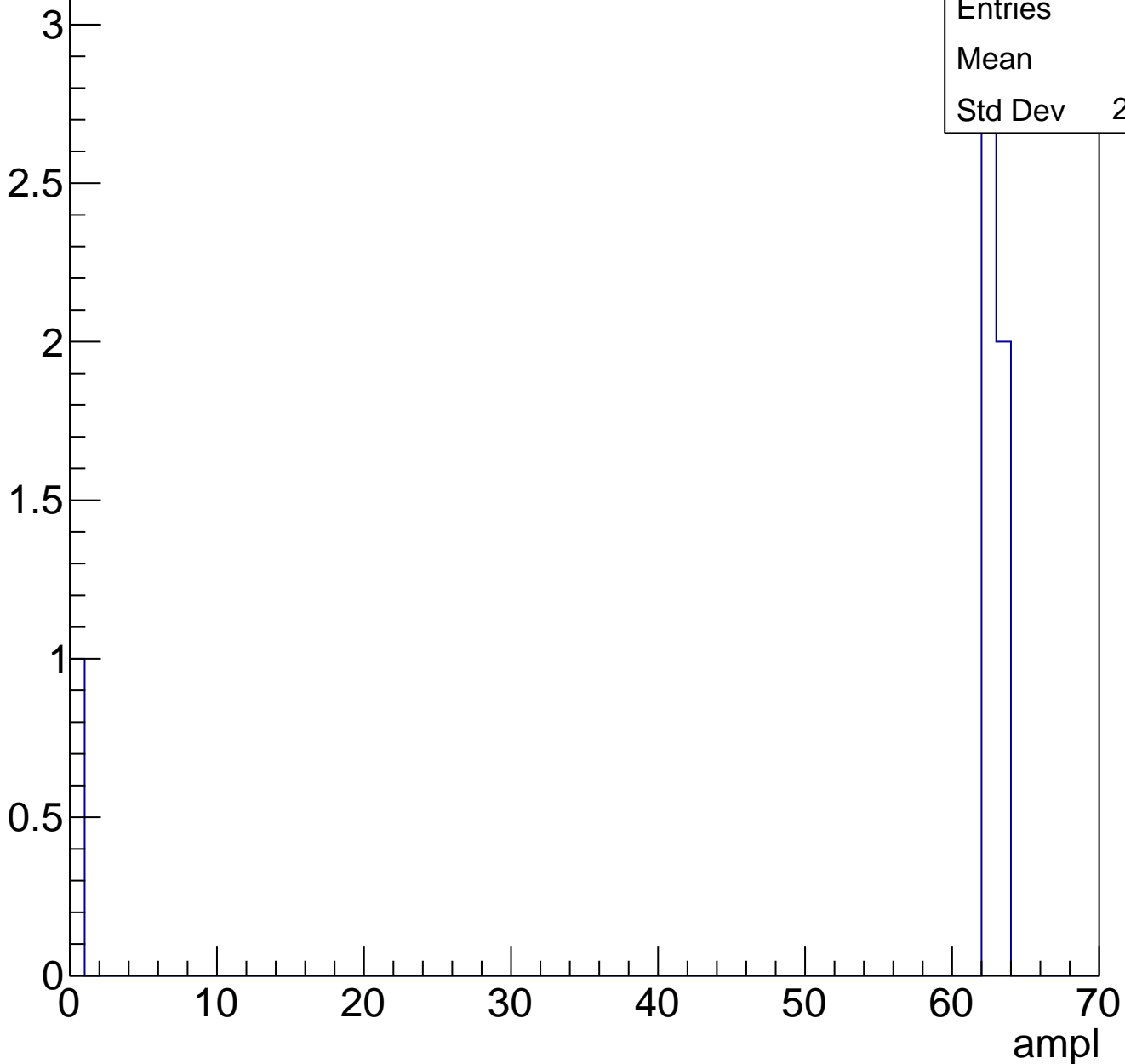
Entries	27
Mean	60.41
Std Dev	2.468



B0L001S, U14-ch41, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch41, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch42, adc0

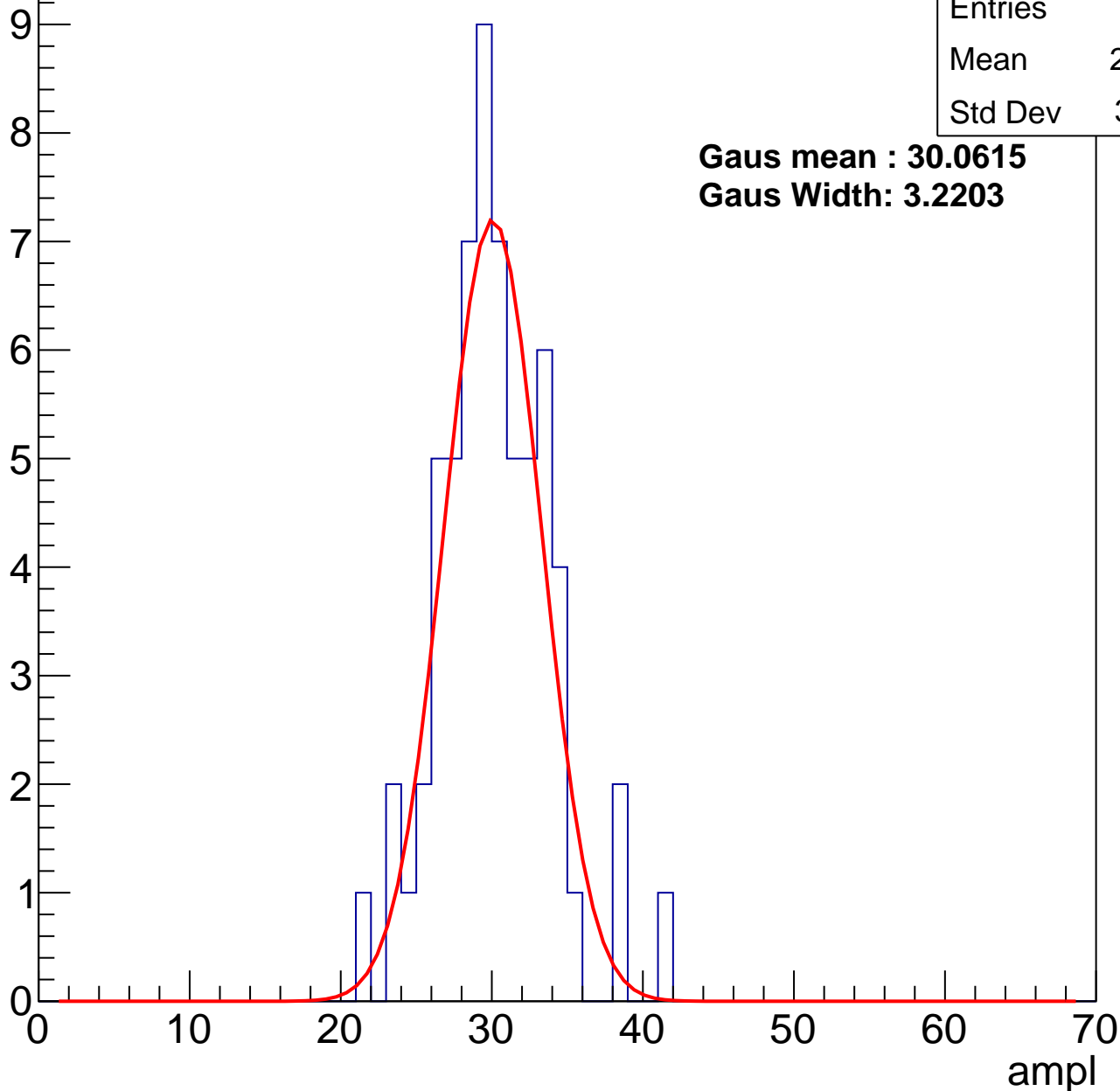
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.75
Std Dev	3.651

Gaus mean : 30.0615

Gaus Width: 3.2203



B0L001S, U14-ch42, adc1

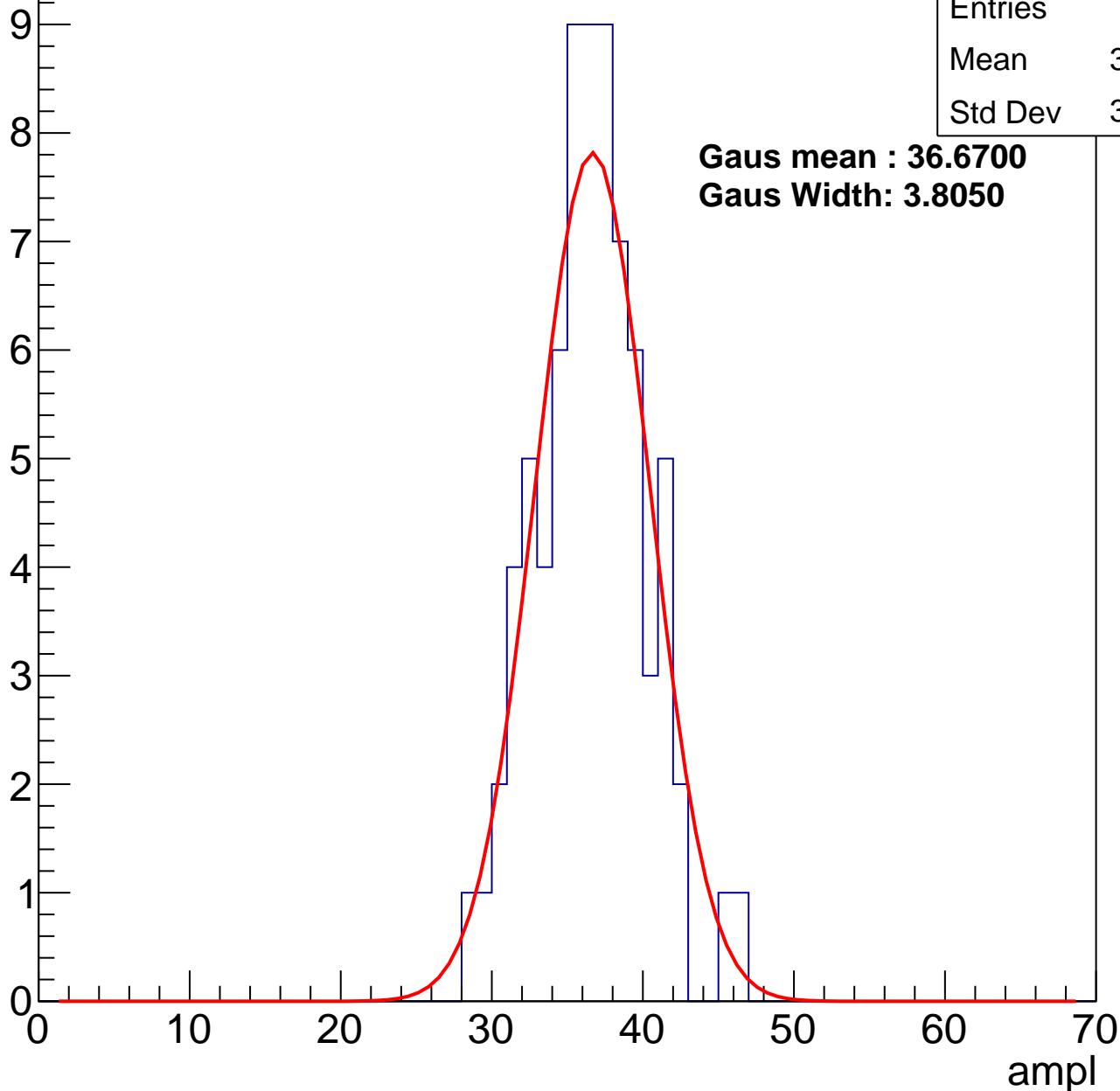
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.12
Std Dev	3.559

Gaus mean : 36.6700

Gaus Width: 3.8050



B0L001S, U14-ch42, adc2

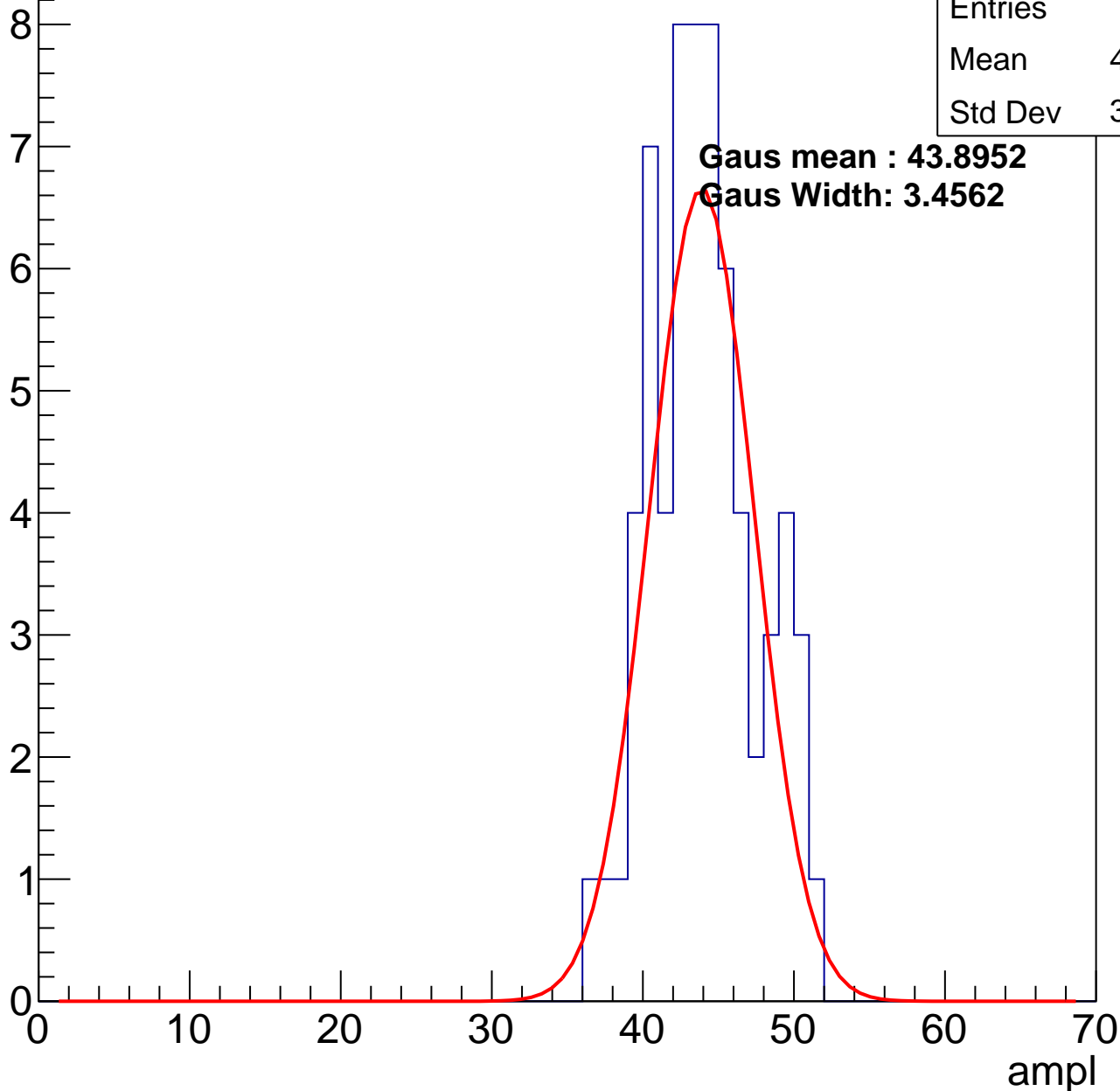
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.57
Std Dev	3.442

Gaus mean : 43.8952

Gaus Width: 3.4562

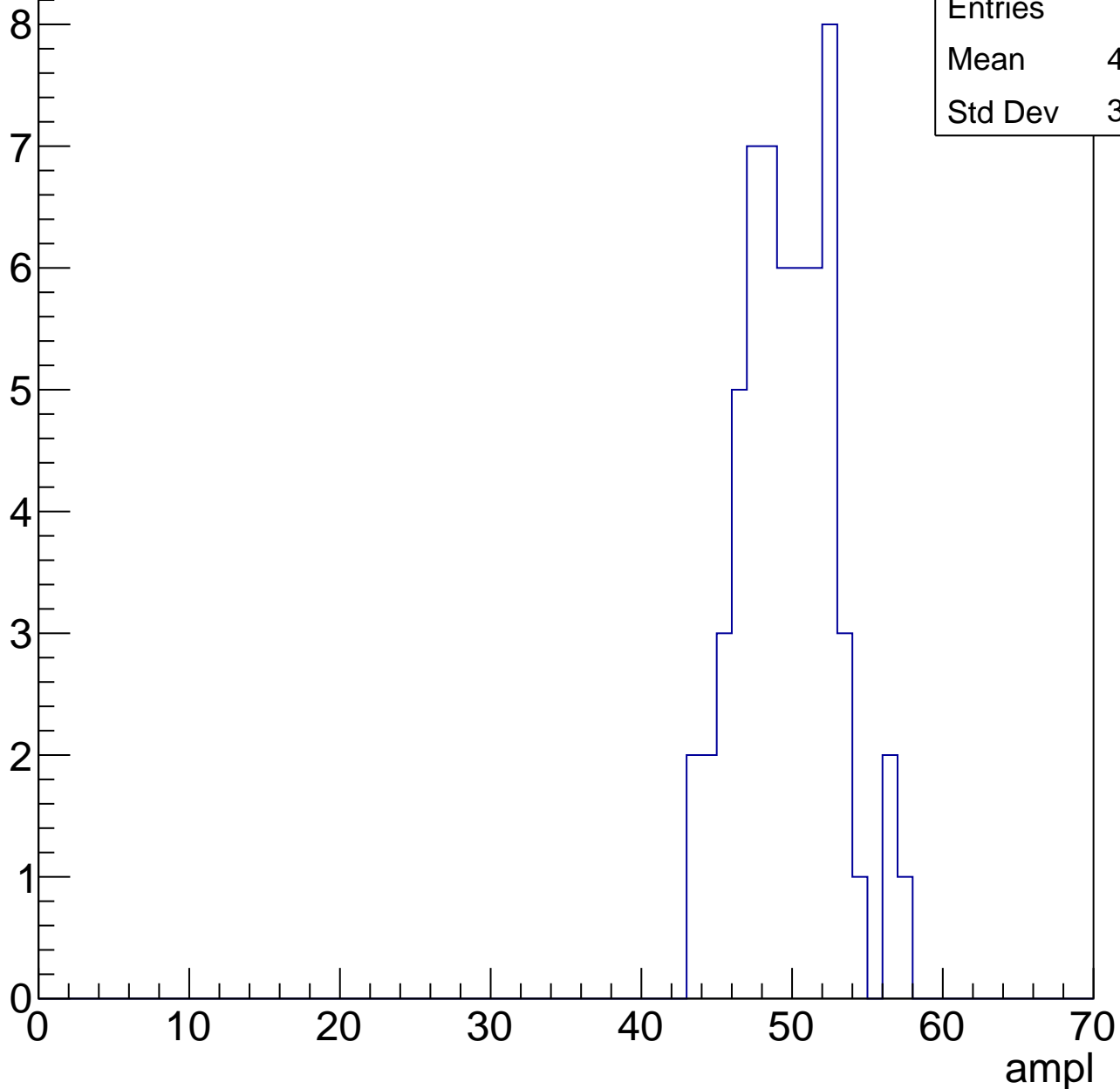


B0L001S, U14-ch42, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

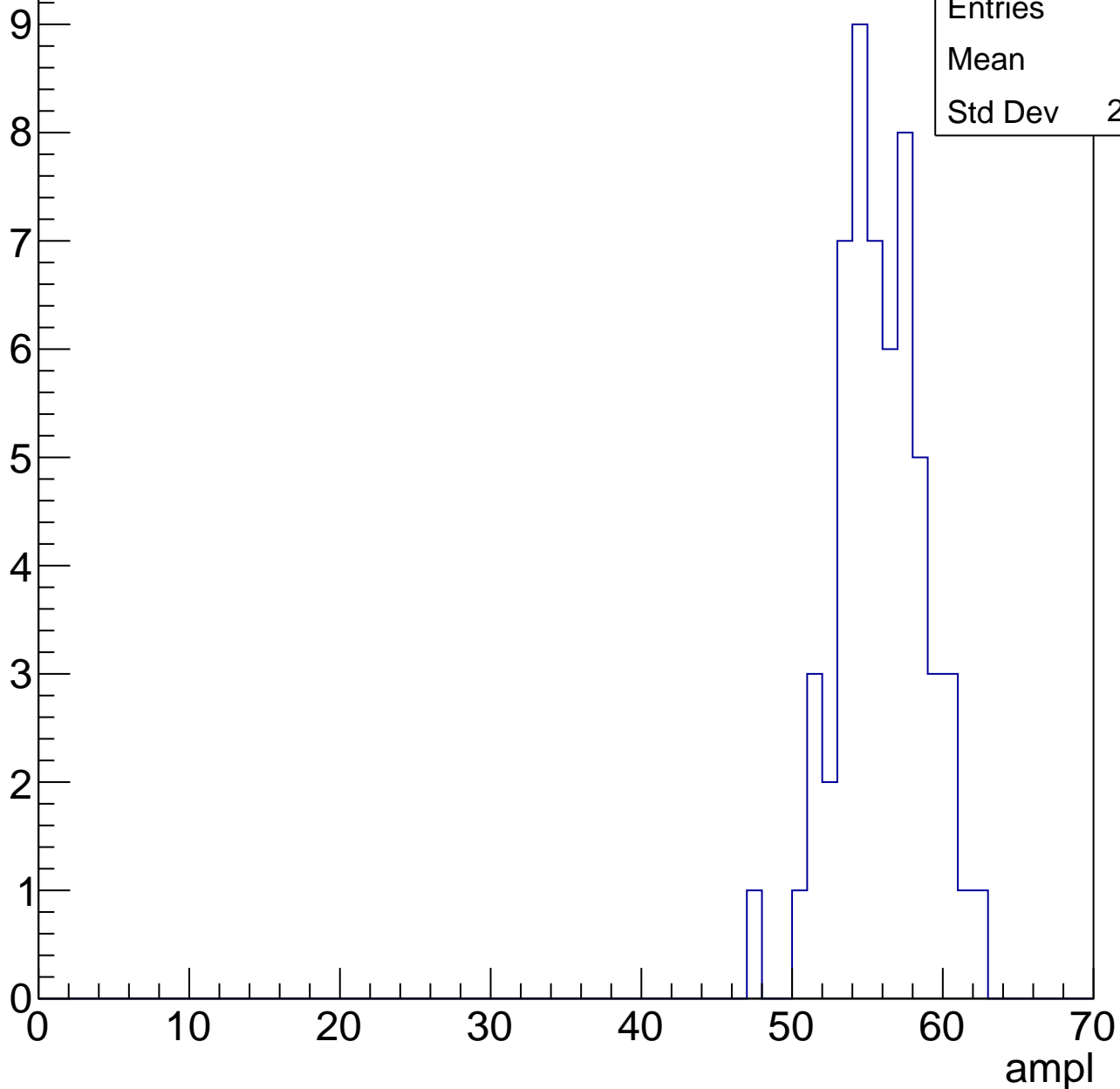
Entries	59
Mean	49.19
Std Dev	3.149



B0L001S, U14-ch42, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



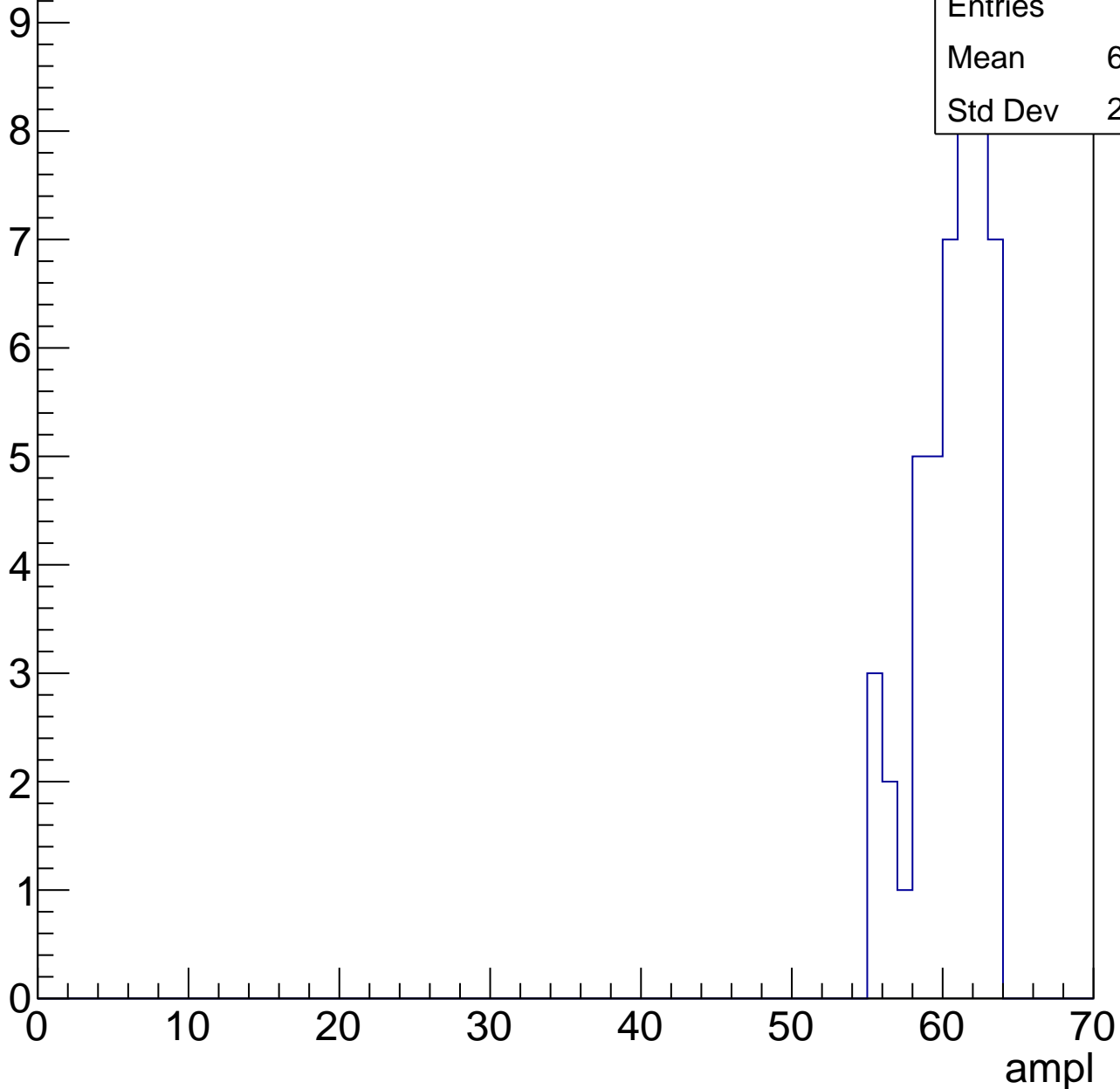
Entries	57
Mean	55.4
Std Dev	2.883

B0L001S, U14-ch42, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

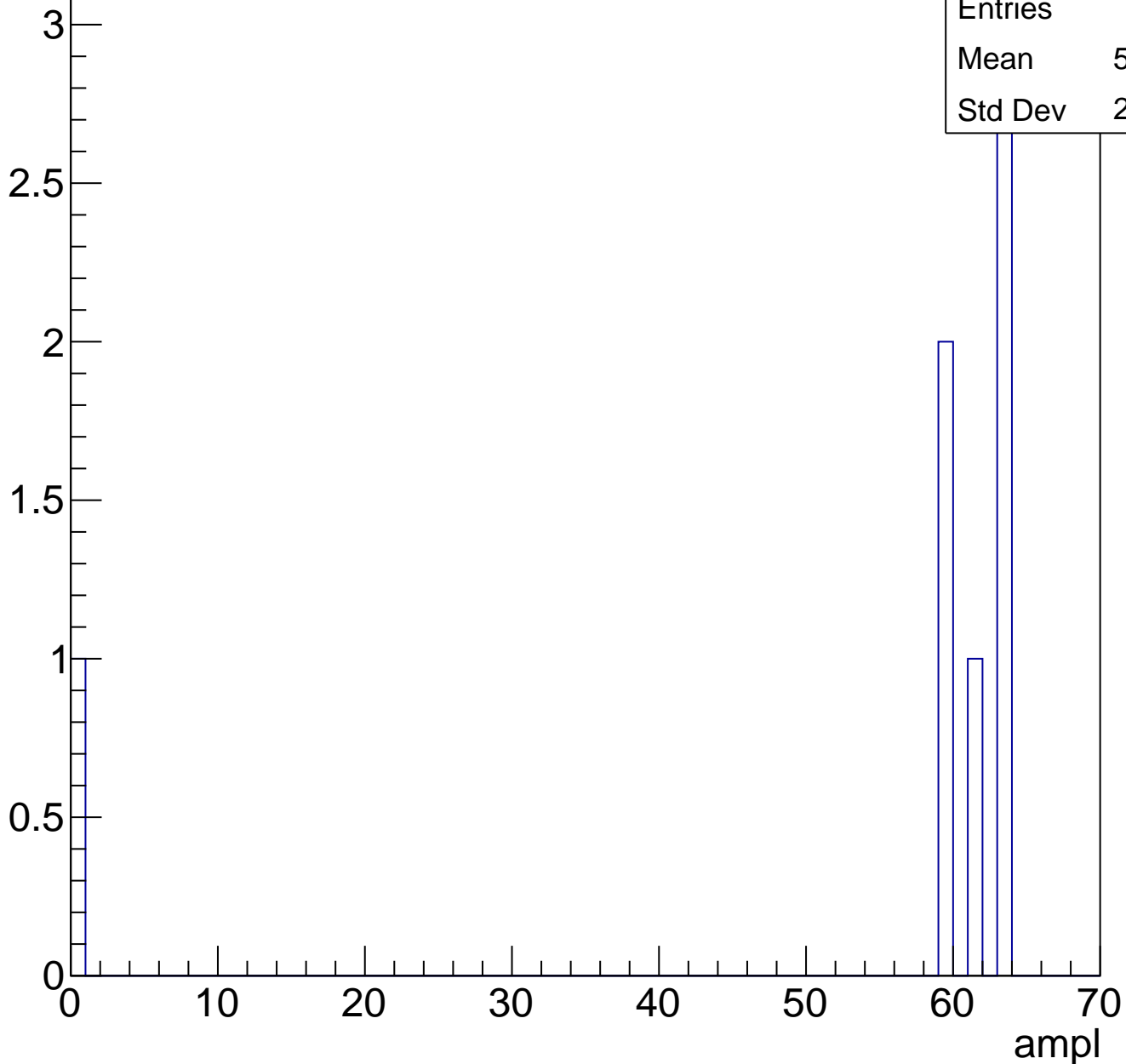
Entries	47
Mean	60.13
Std Dev	2.294



B0L001S, U14-ch42, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch42, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch43, adc0

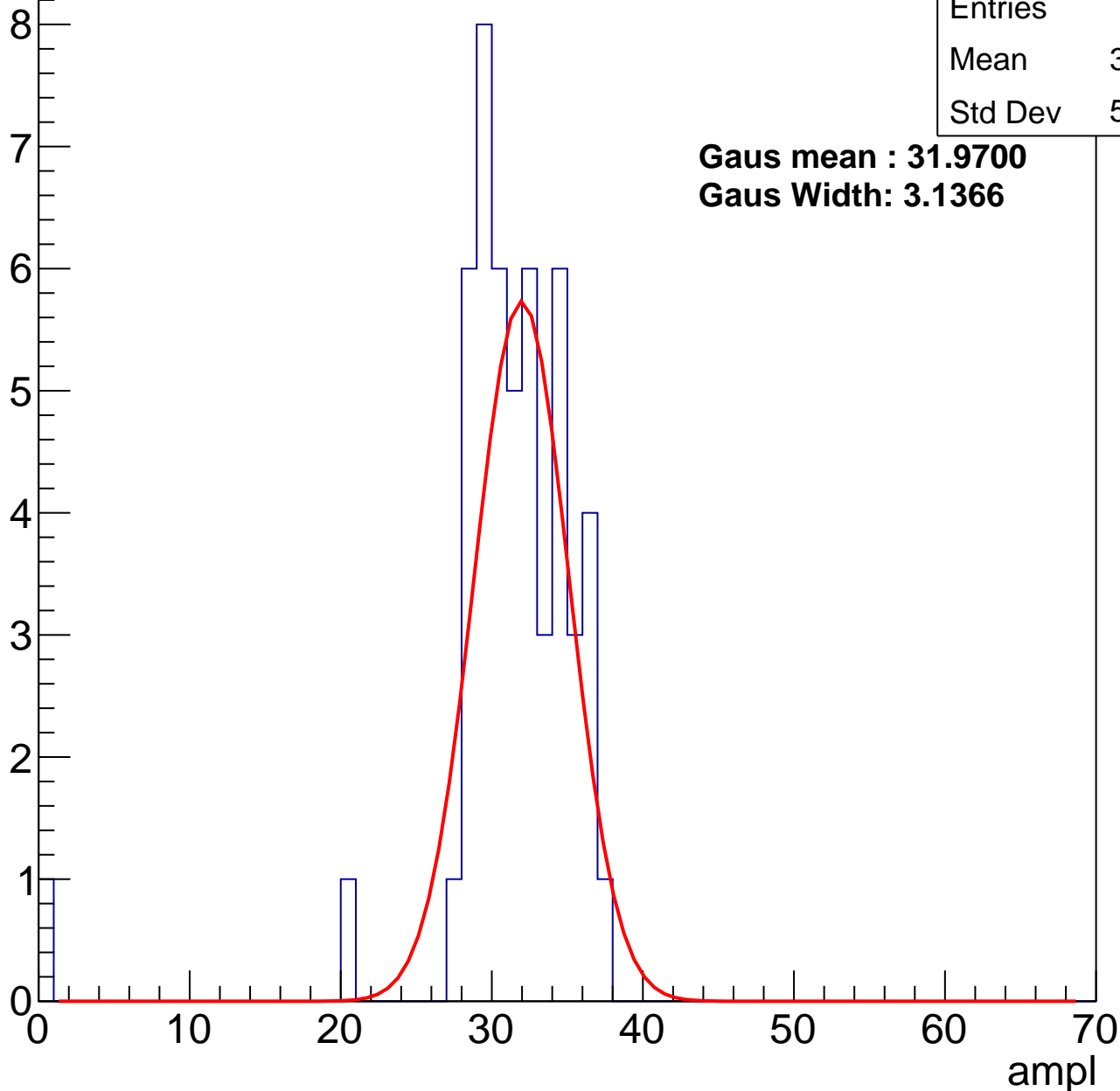
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	30.65
Std Dev	5.313

Gaus mean : 31.9700

Gaus Width: 3.1366



B0L001S, U14-ch43, adc1

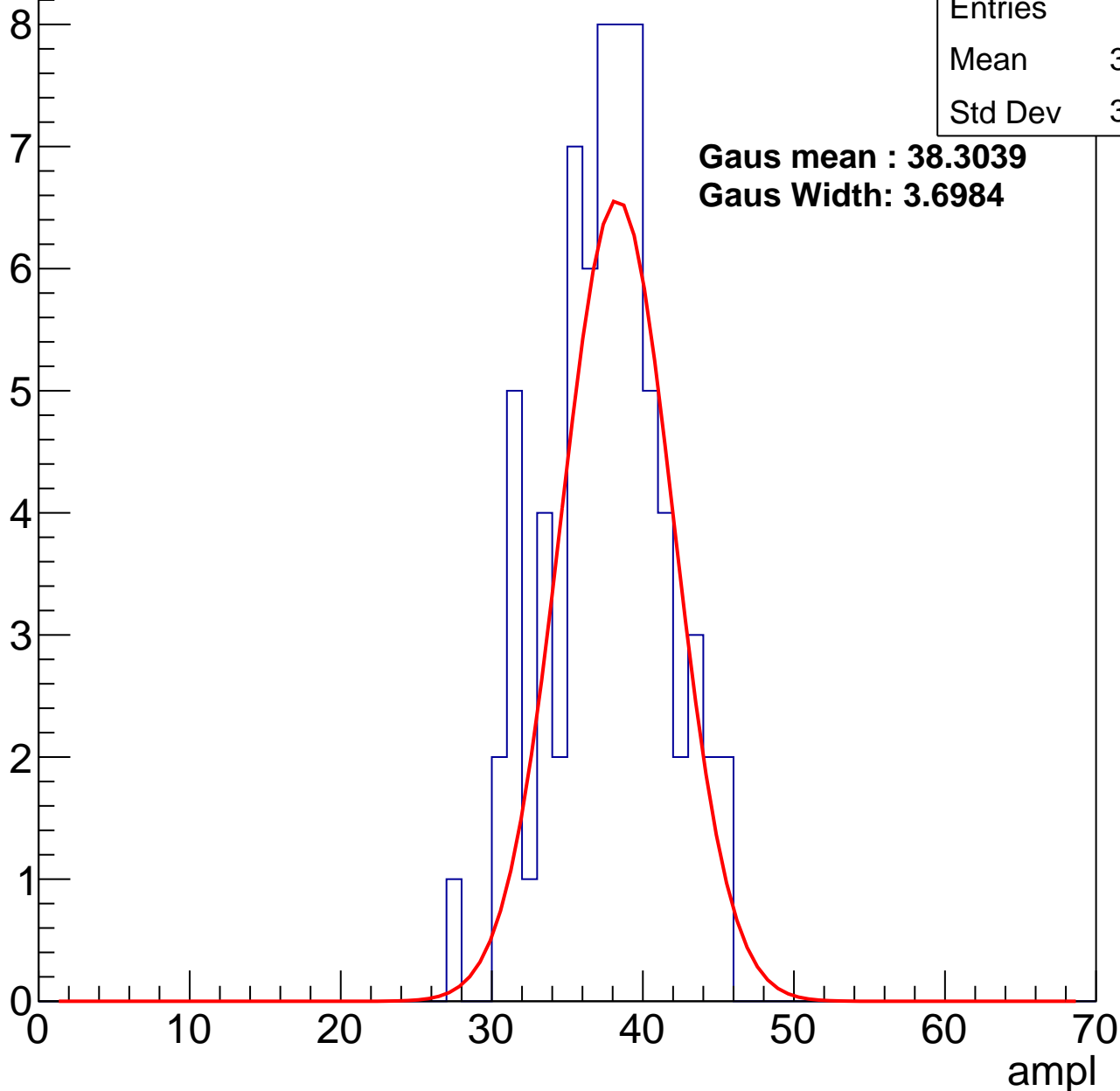
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	37.17
Std Dev	3.873

Gaus mean : 38.3039

Gaus Width: 3.6984



B0L001S, U14-ch43, adc2

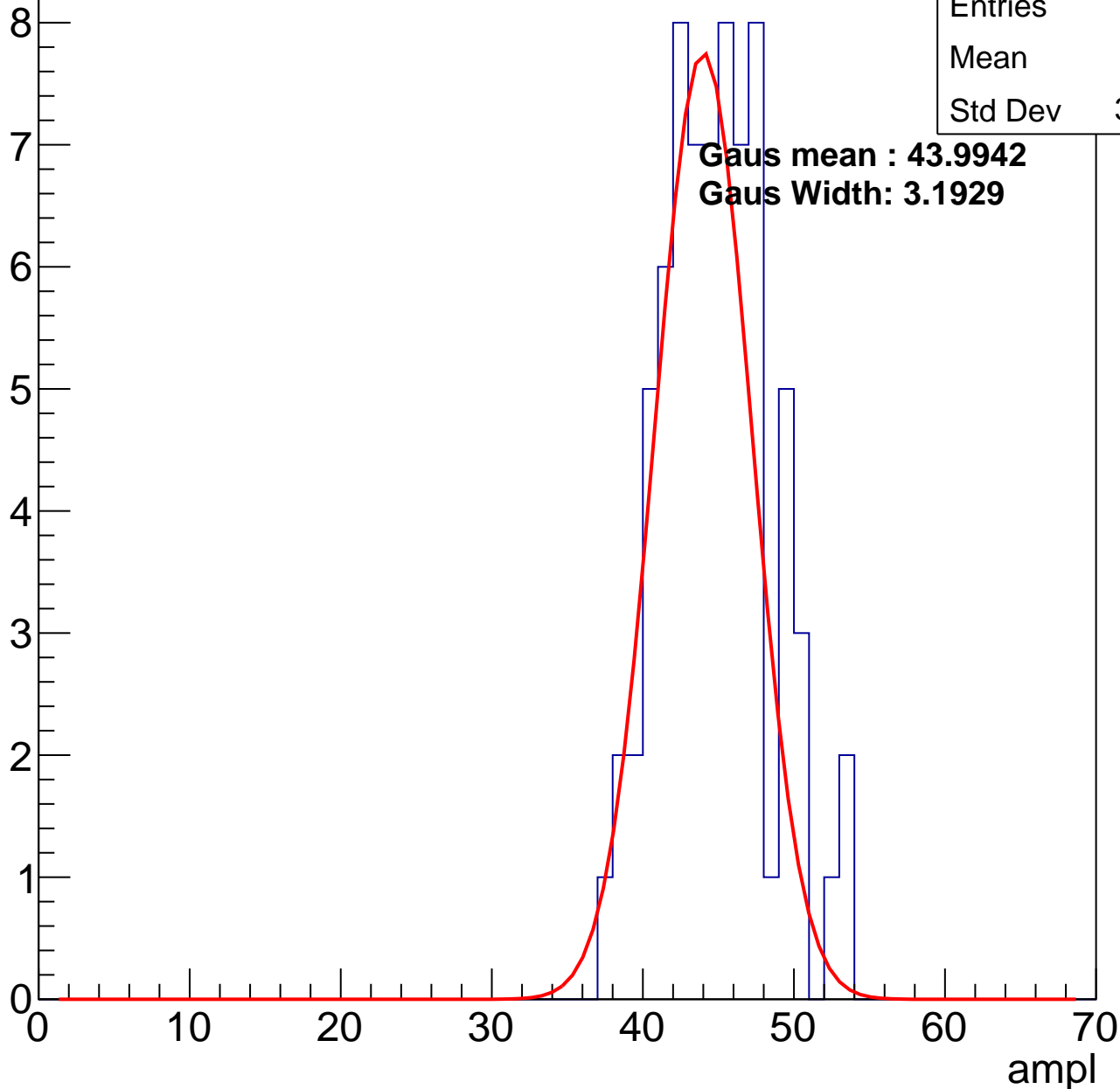
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	44.4
Std Dev	3.541

Gaus mean : 43.9942

Gaus Width: 3.1929

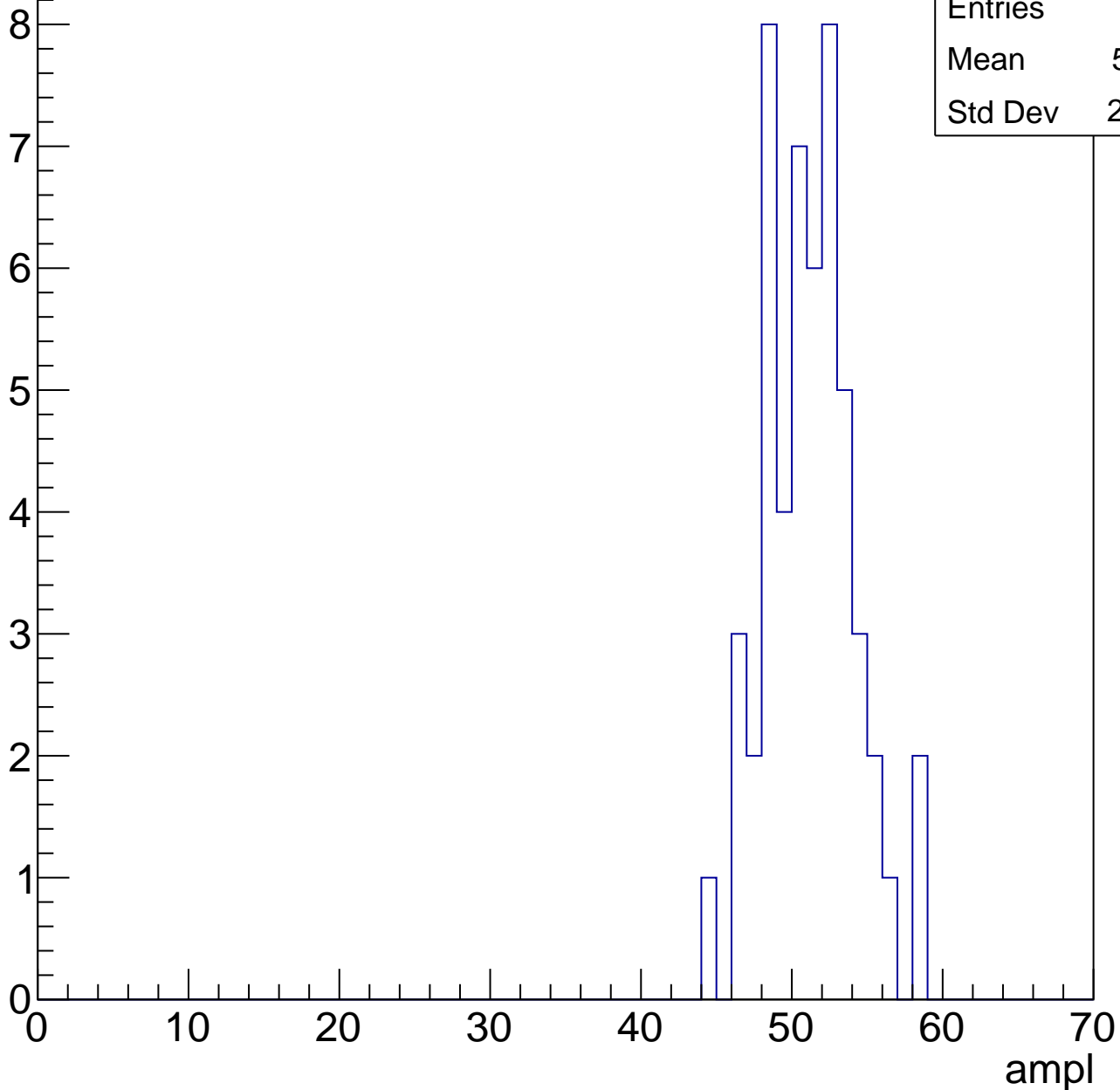


B0L001S, U14-ch43, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	50.71
Std Dev	2.963



B0L001S, U14-ch43, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	56.71
Std Dev	3.264

Entry

10

8

6

4

2

0

0

10

20

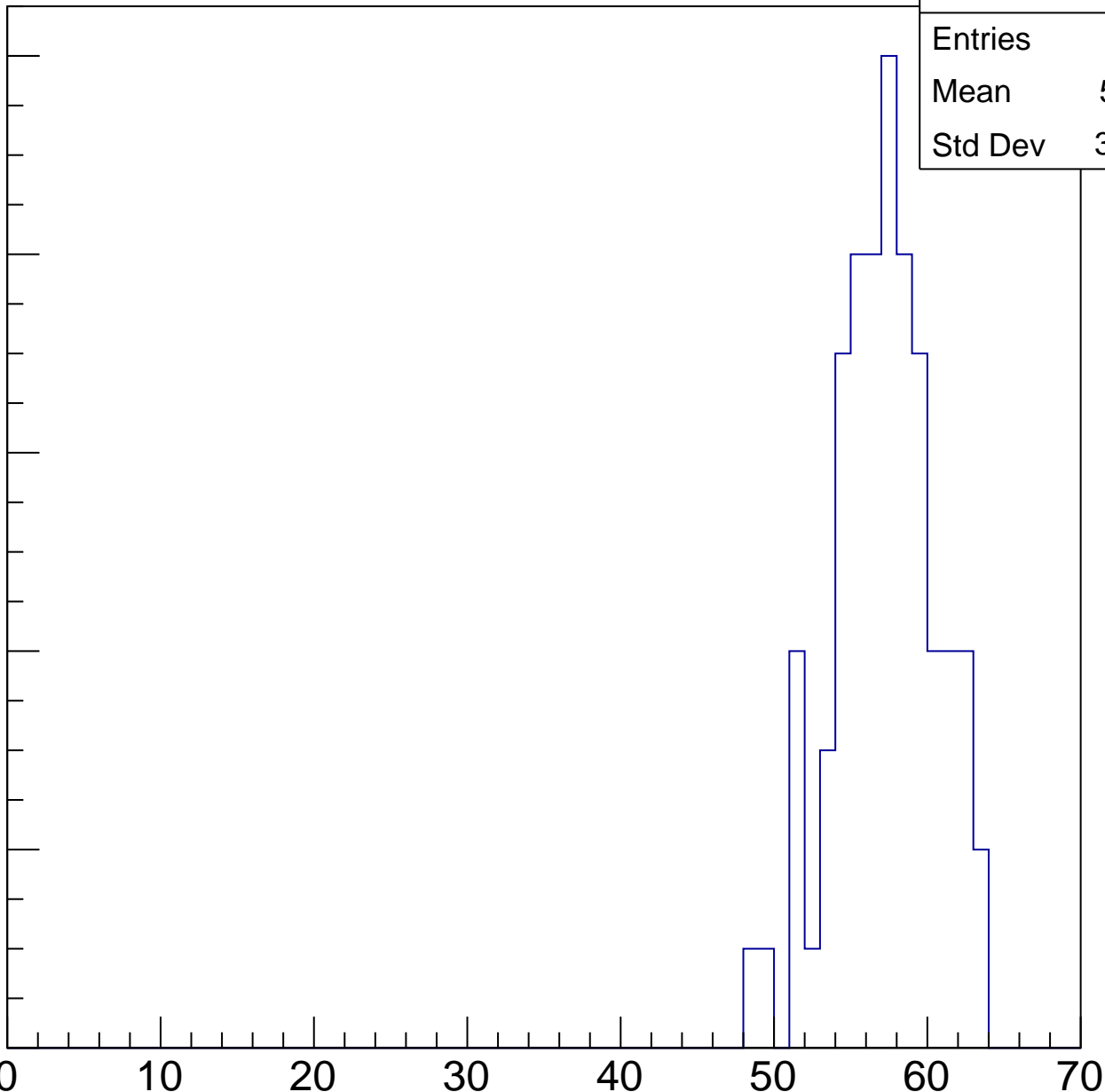
30

40

50

60

ampl

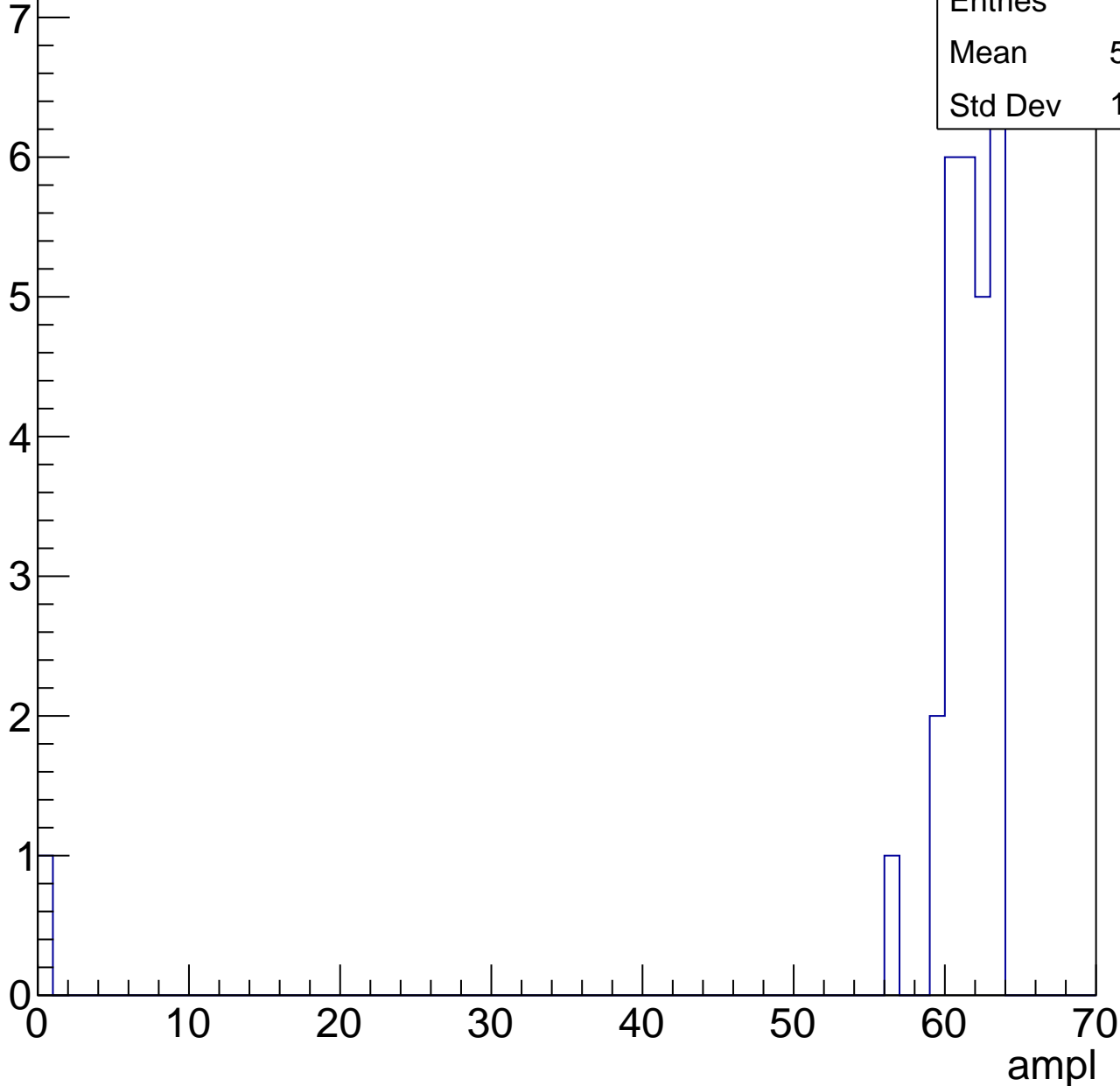


B0L001S, U14-ch43, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

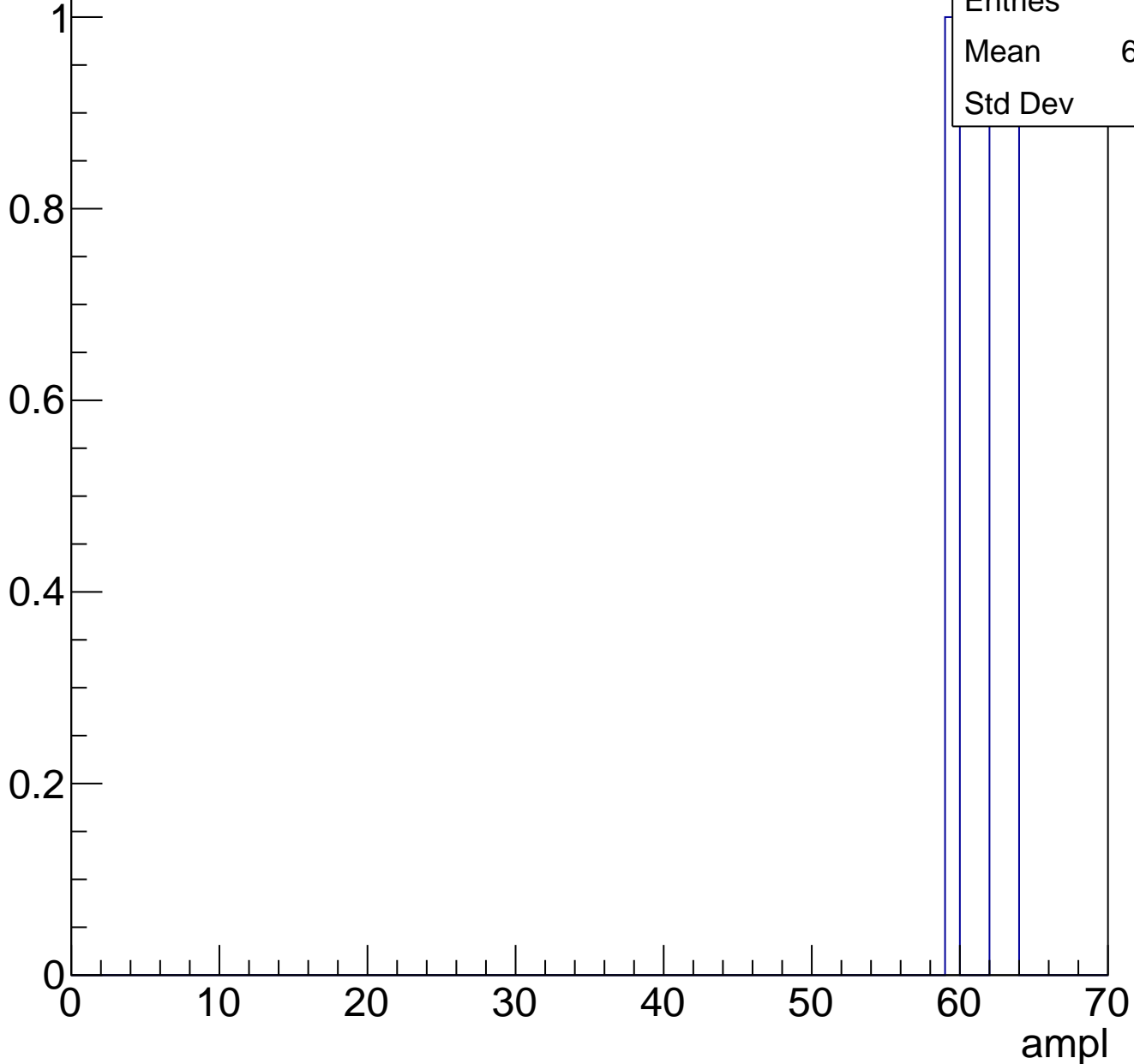
Entries	28
Mean	58.96
Std Dev	11.46



B0L001S, U14-ch43, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch43, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch44, adc0

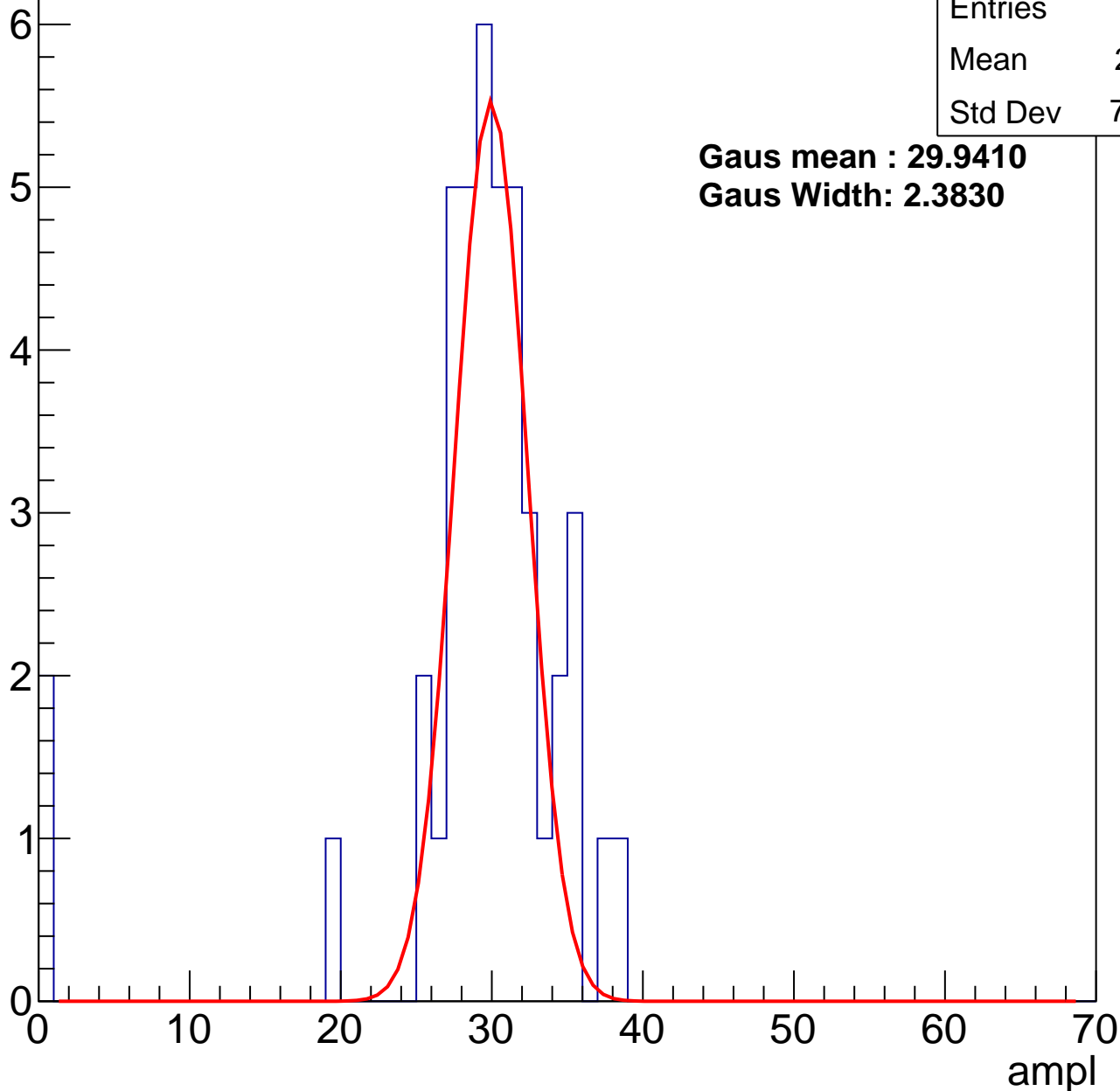
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	28.51
Std Dev	7.167

Gaus mean : 29.9410

Gaus Width: 2.3830



B0L001S, U14-ch44, adc1

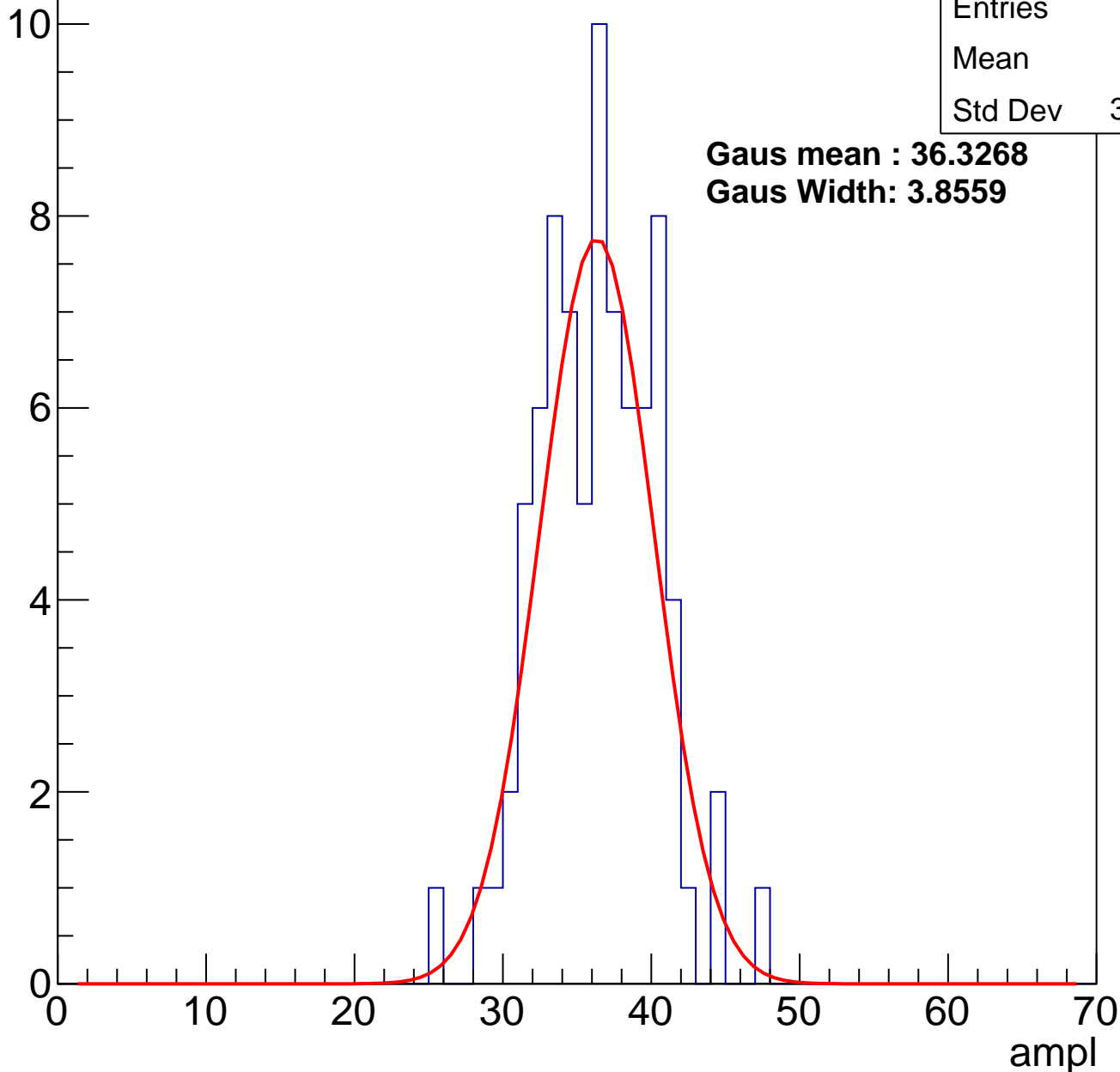
calib_packv5_042523_0143.root, FC#9, port A1

Entries	81
Mean	35.9
Std Dev	3.896

Gaus mean : 36.3268

Gaus Width: 3.8559

Entry



B0L001S, U14-ch44, adc2

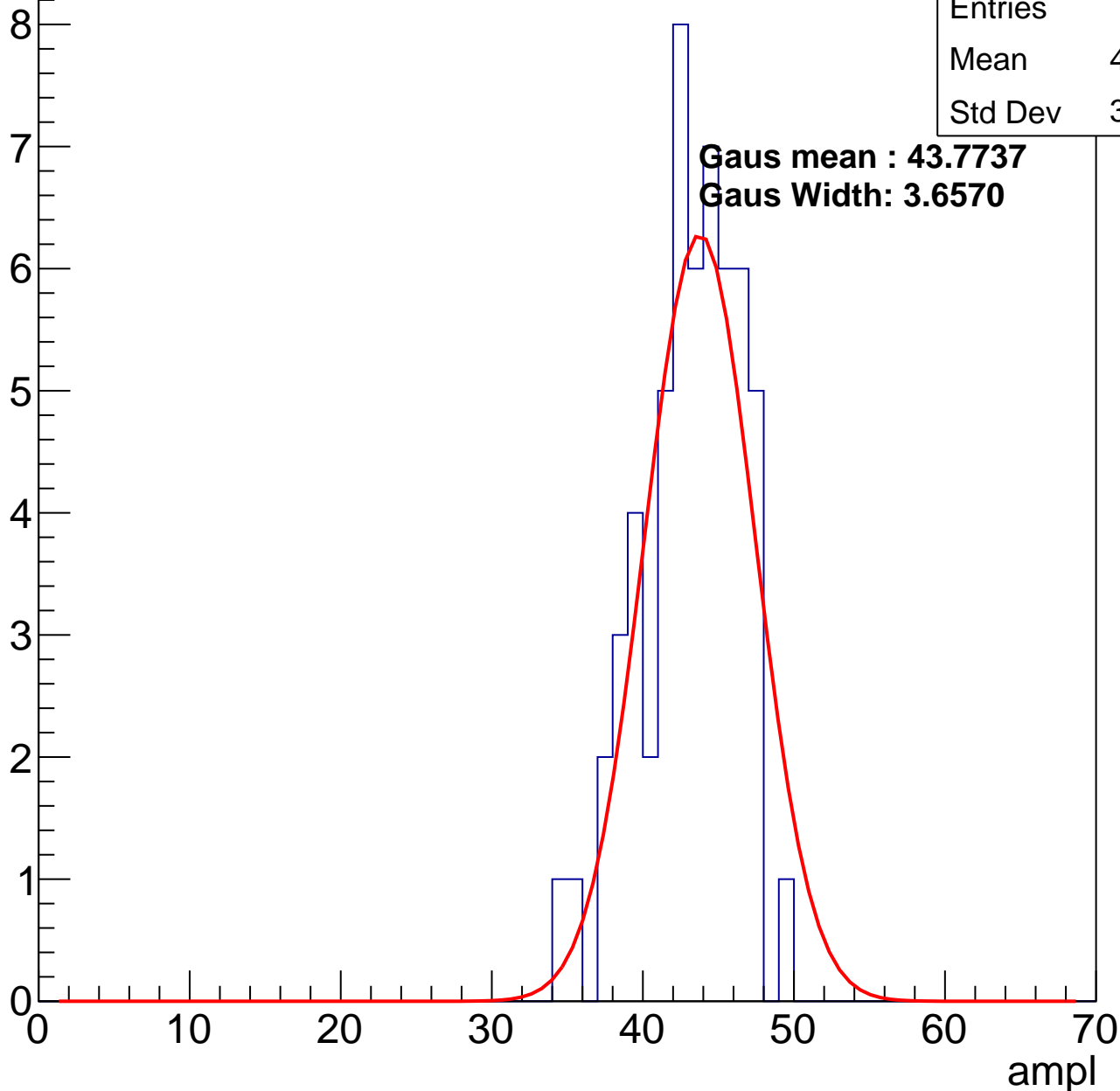
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	42.63
Std Dev	3.232

Gaus mean : 43.7737

Gaus Width: 3.6570

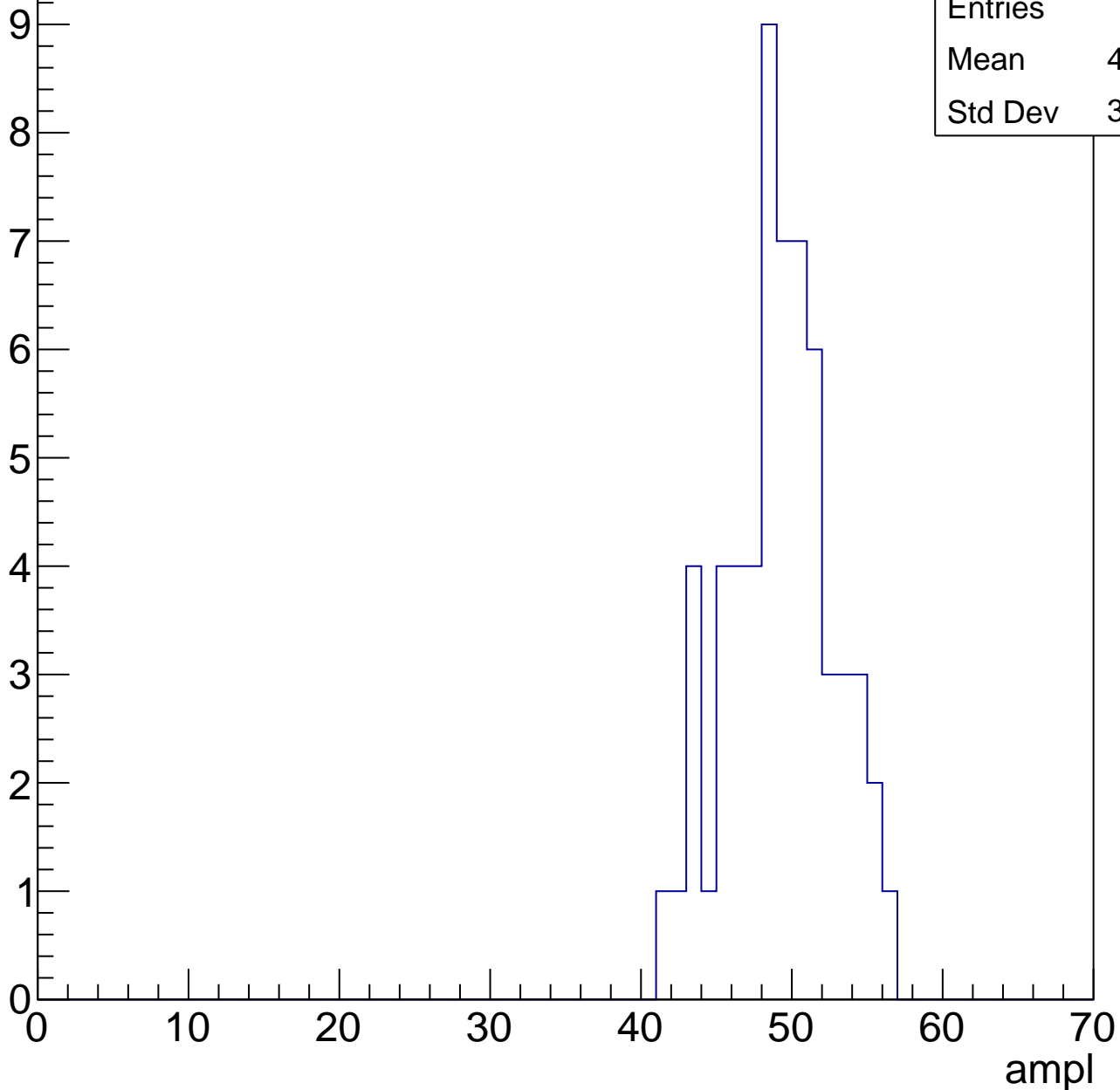


B0L001S, U14-ch44, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

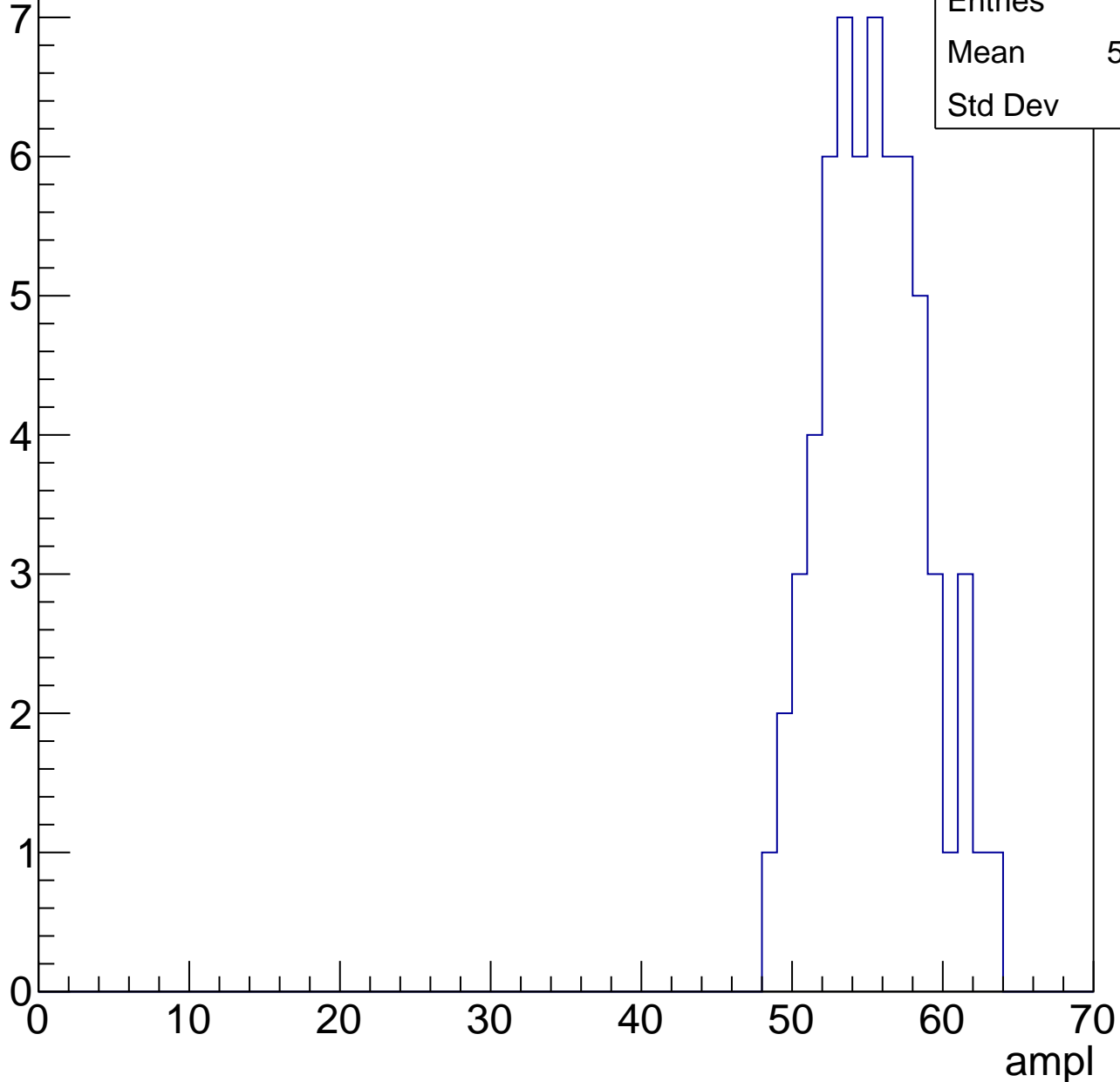
Entries	60
Mean	48.75
Std Dev	3.428



B0L001S, U14-ch44, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

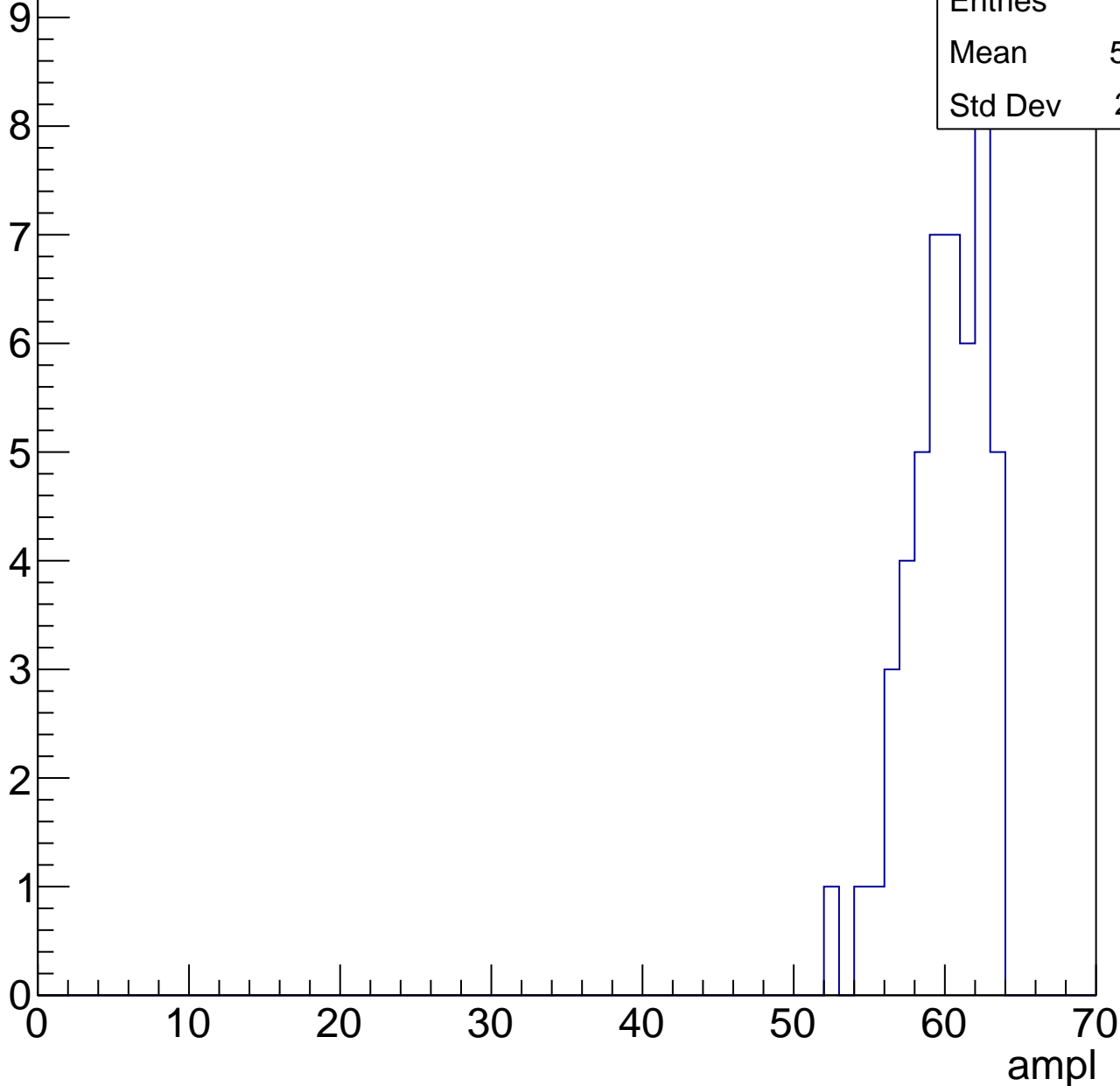


B0L001S, U14-ch44, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	59.57
Std Dev	2.531



B0L001S, U14-ch44, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.4
Std Dev	1.356

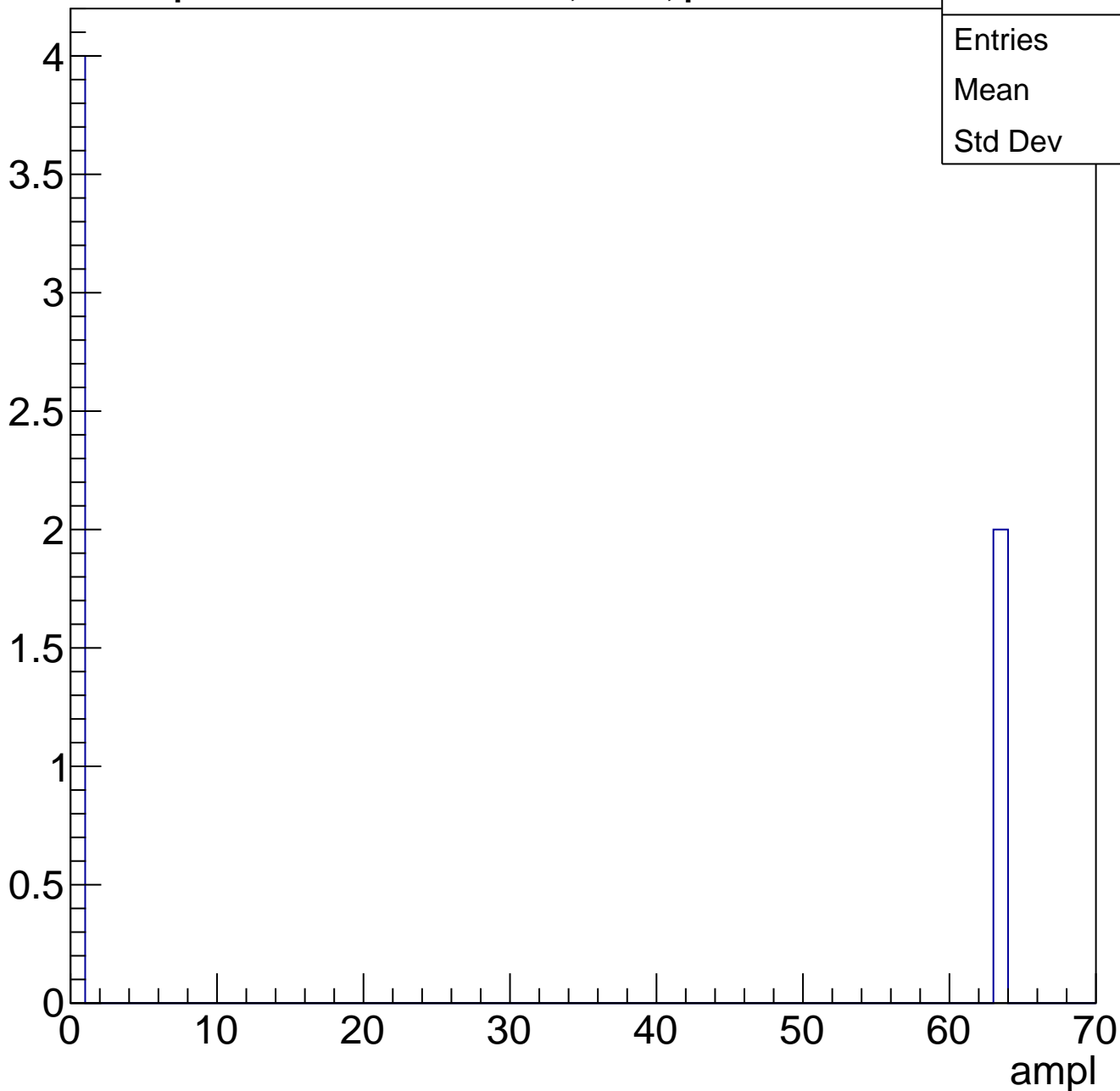
ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch44, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch45, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	32.33
Std Dev	5.11

Gaus mean : 34.0609

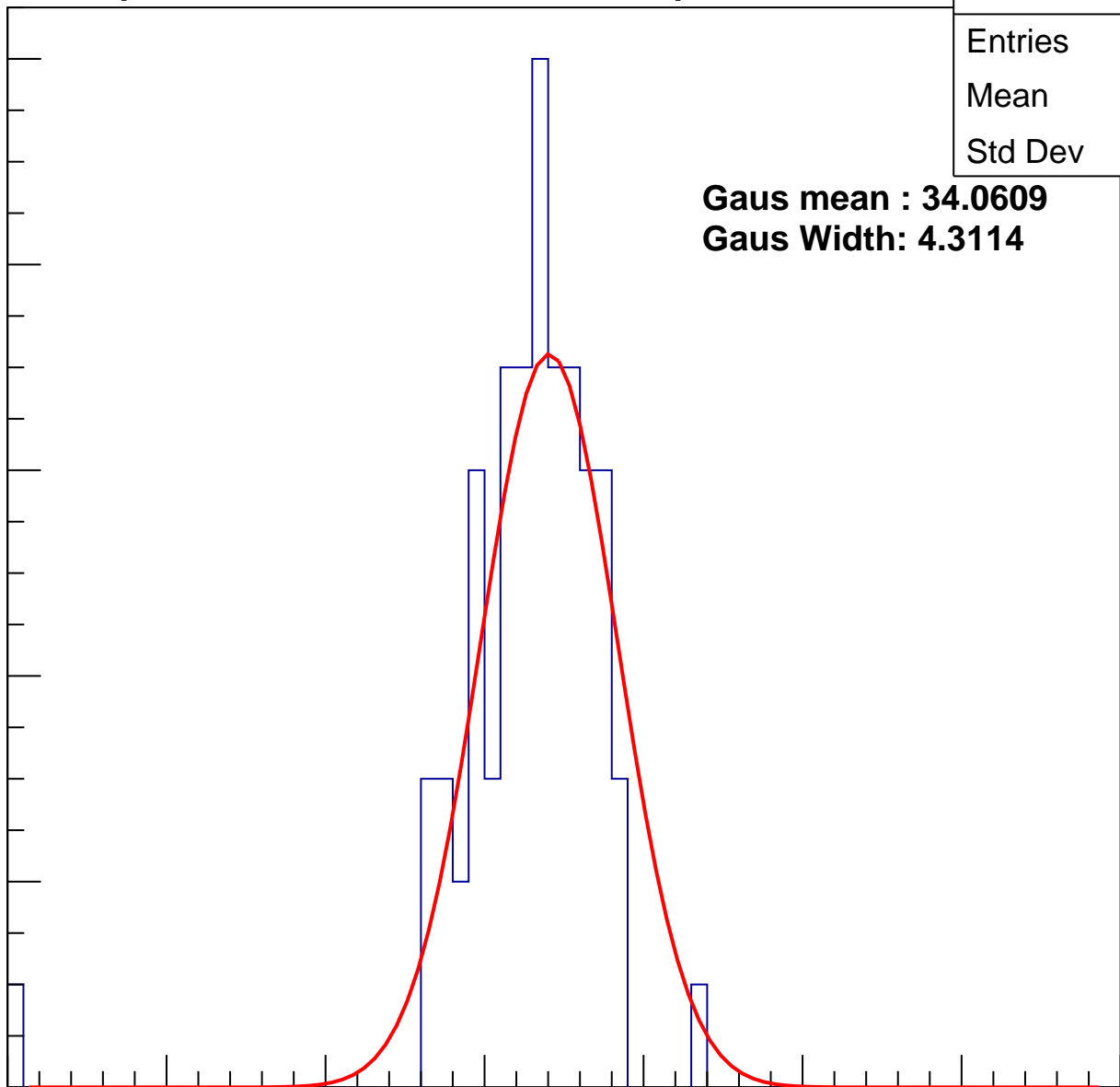
Gaus Width: 4.3114

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch45, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	50
Mean	39.46
Std Dev	3.806

Gaus mean : 39.2032

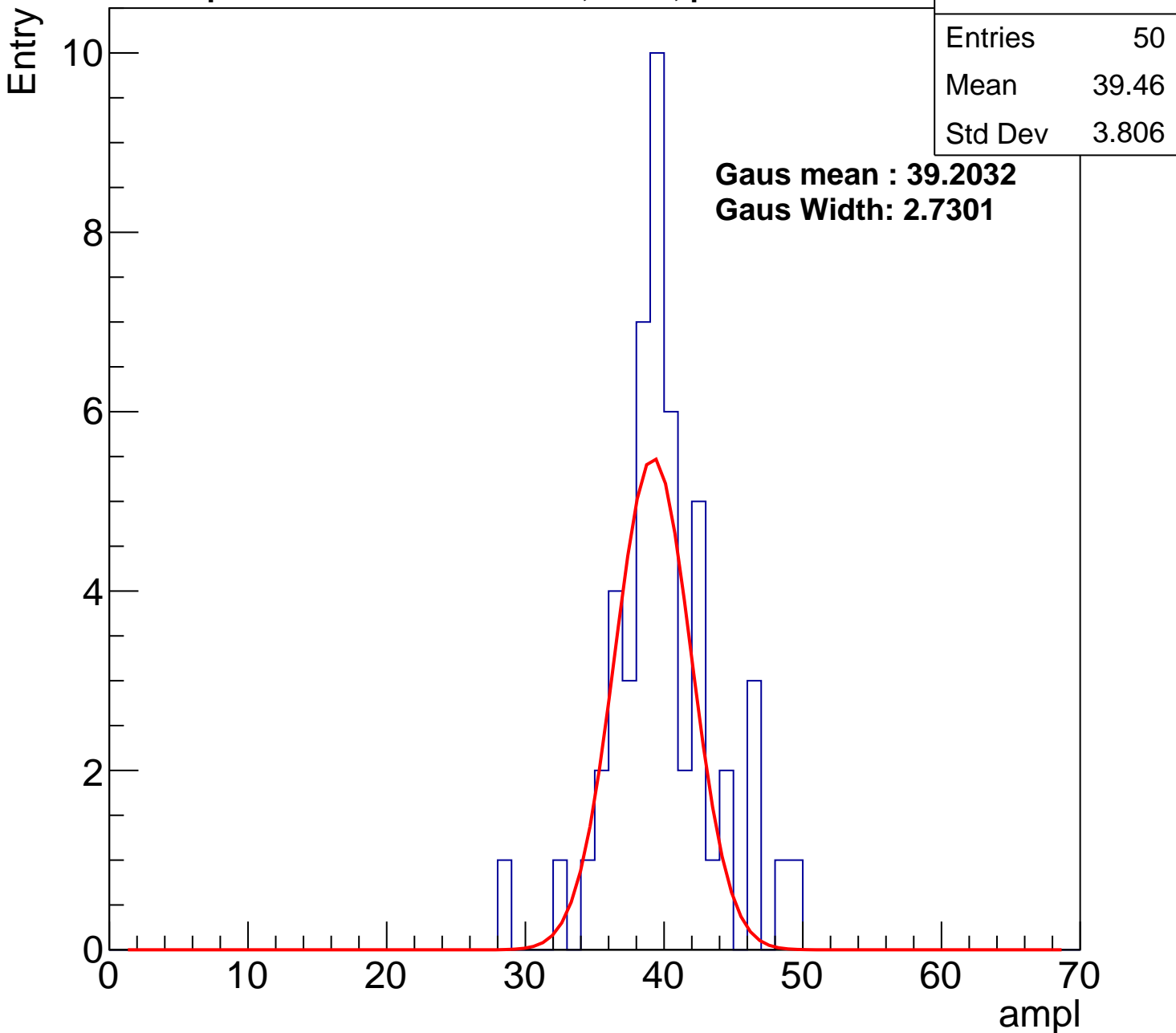
Gaus Width: 2.7301

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch45, adc2

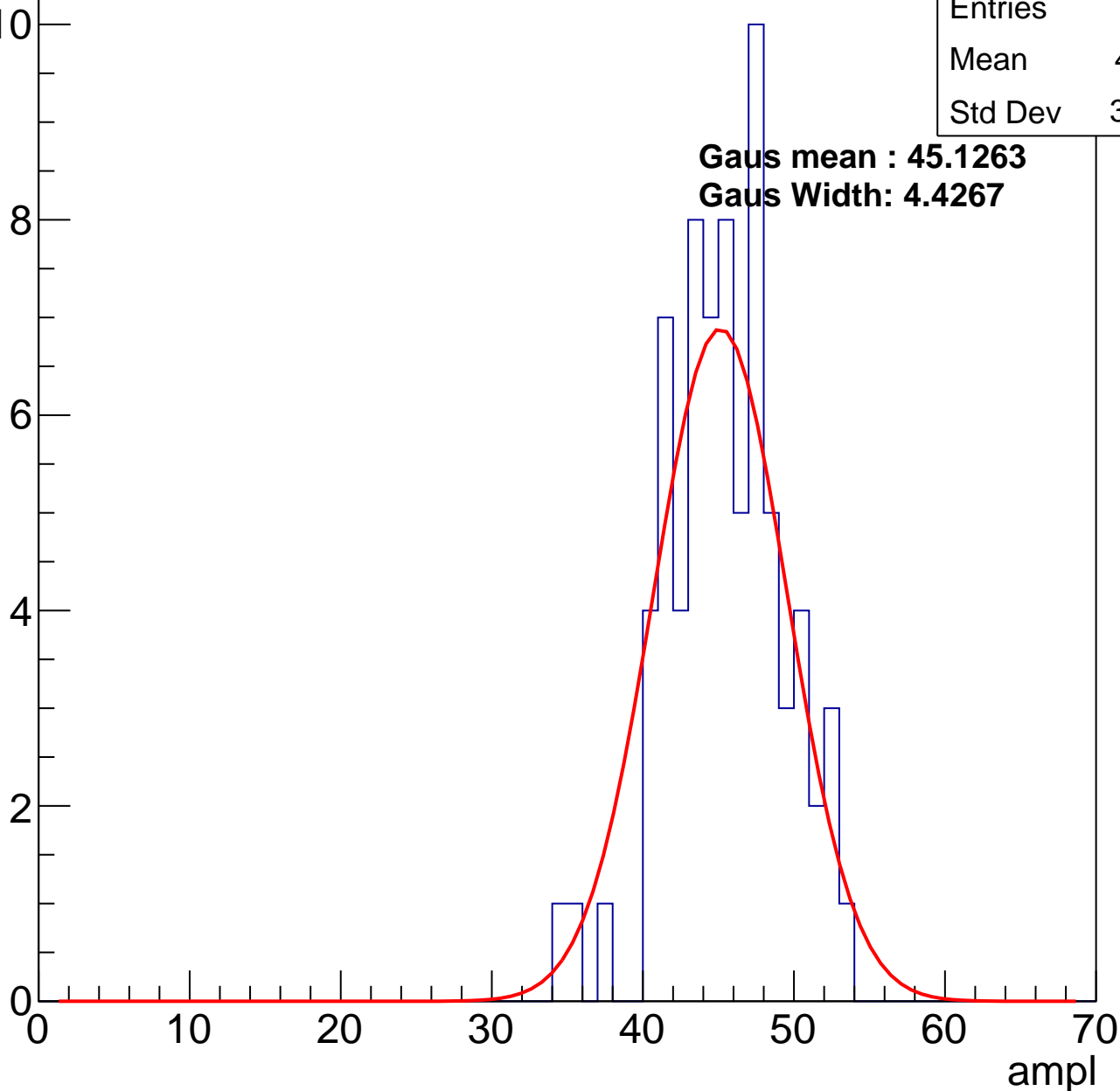
calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	45.01
Std Dev	3.843

Gaus mean : 45.1263

Gaus Width: 4.4267

Entry

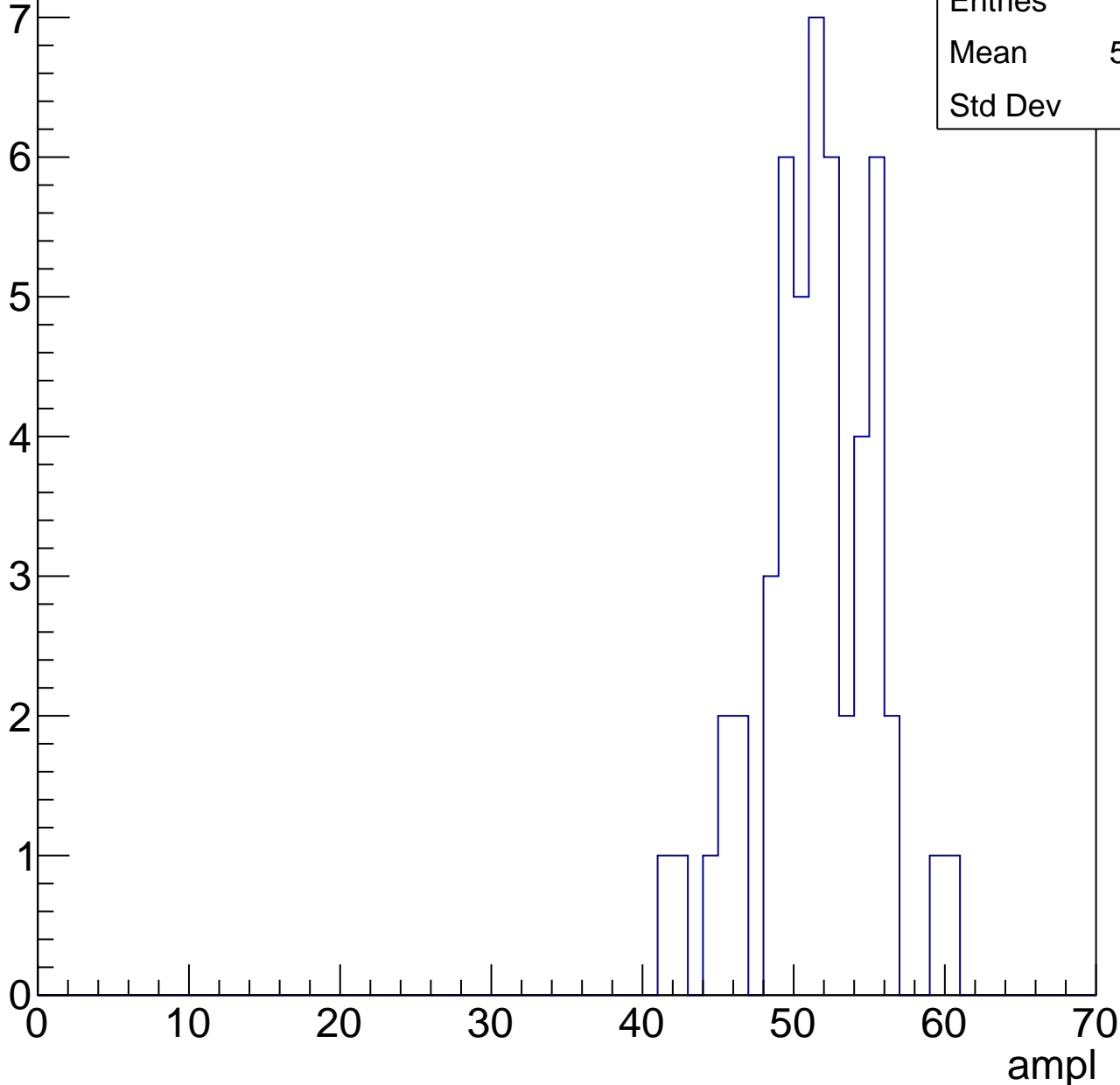


B0L001S, U14-ch45, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	50.98
Std Dev	3.87

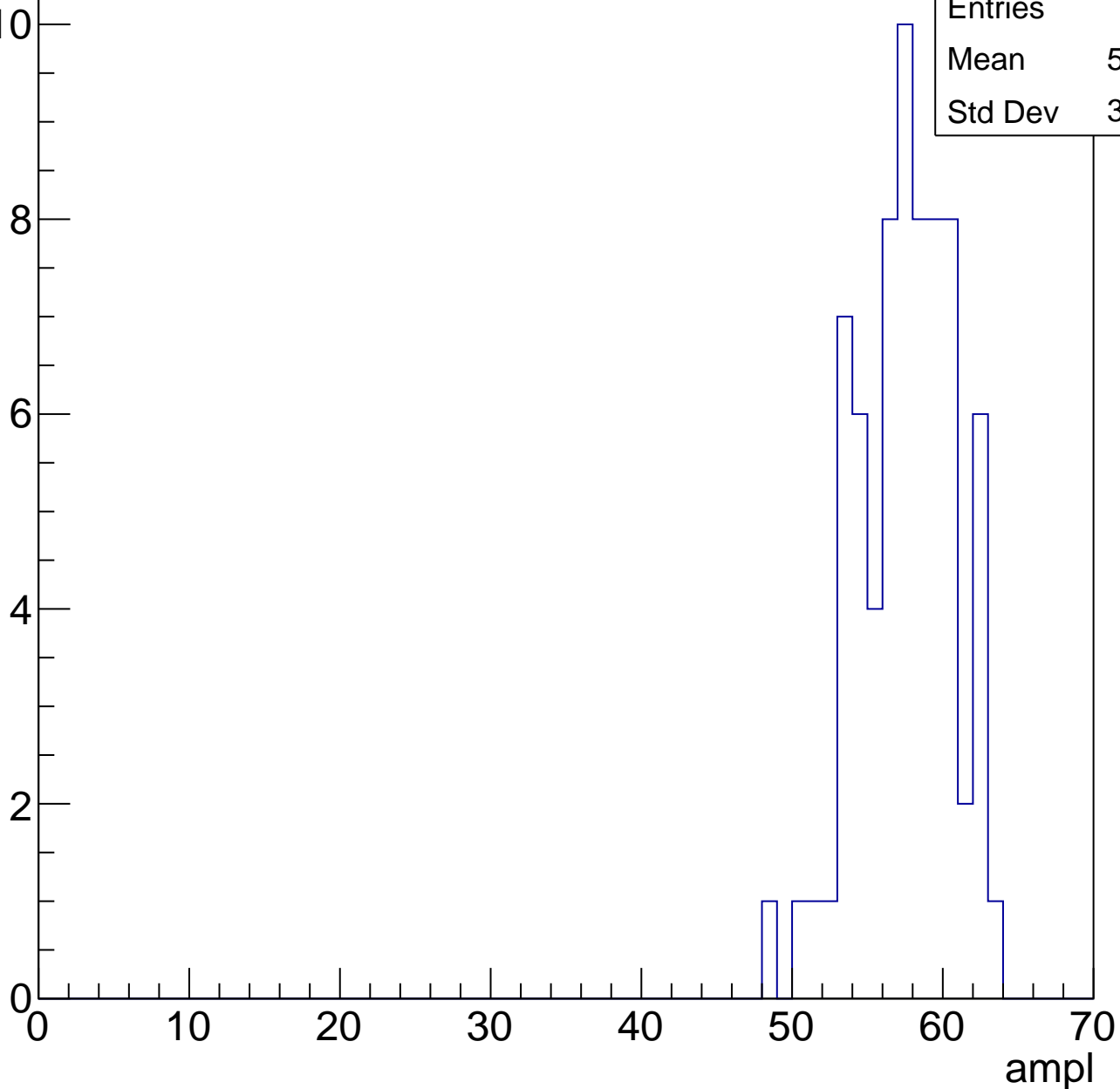


B0L001S, U14-ch45, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	57.04
Std Dev	3.142

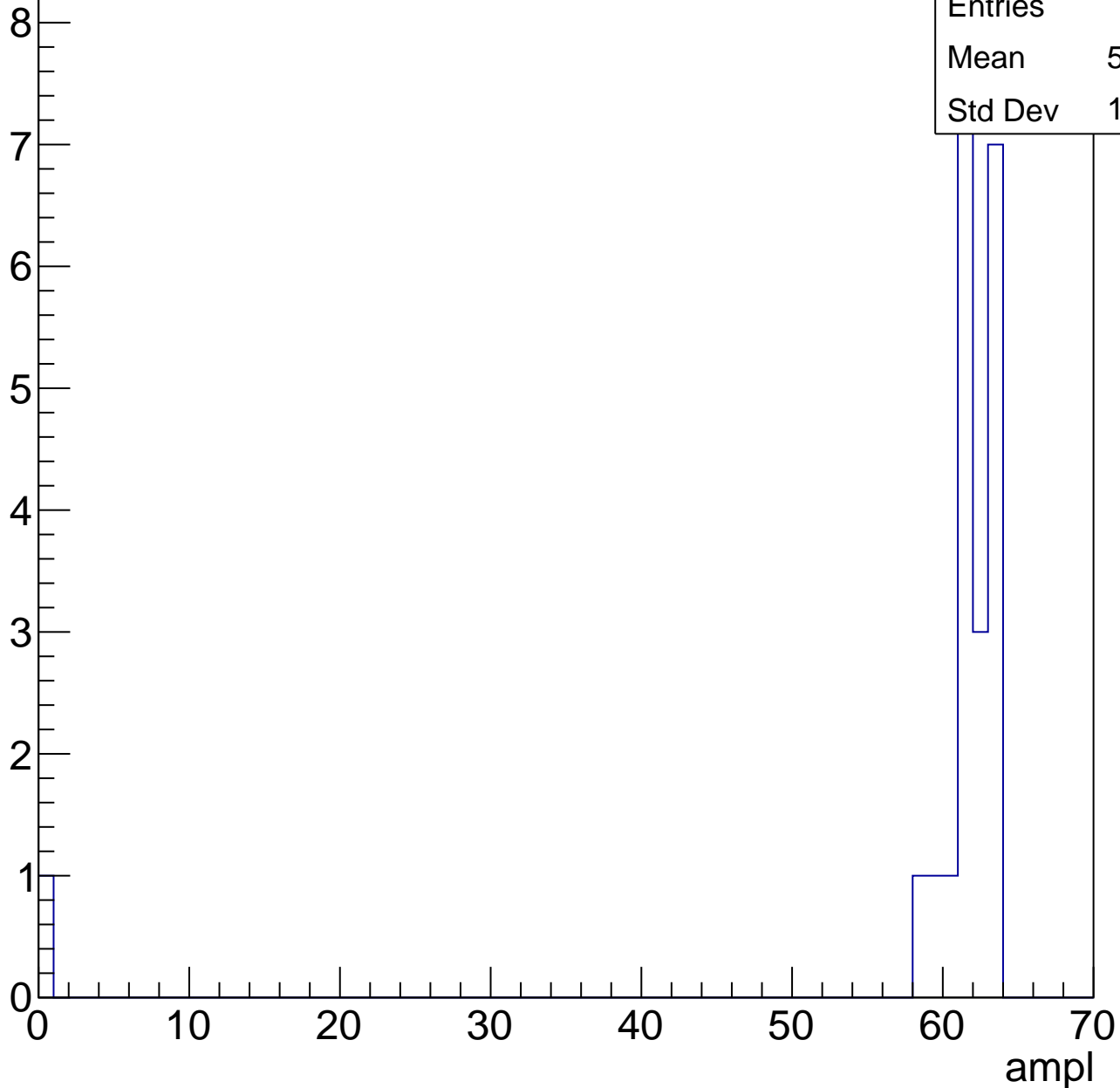


B0L001S, U14-ch45, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	58.73
Std Dev	12.88



B0L001S, U14-ch45, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch45, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	8.333
Std Dev	11.79

B0L001S, U14-ch46, adc0

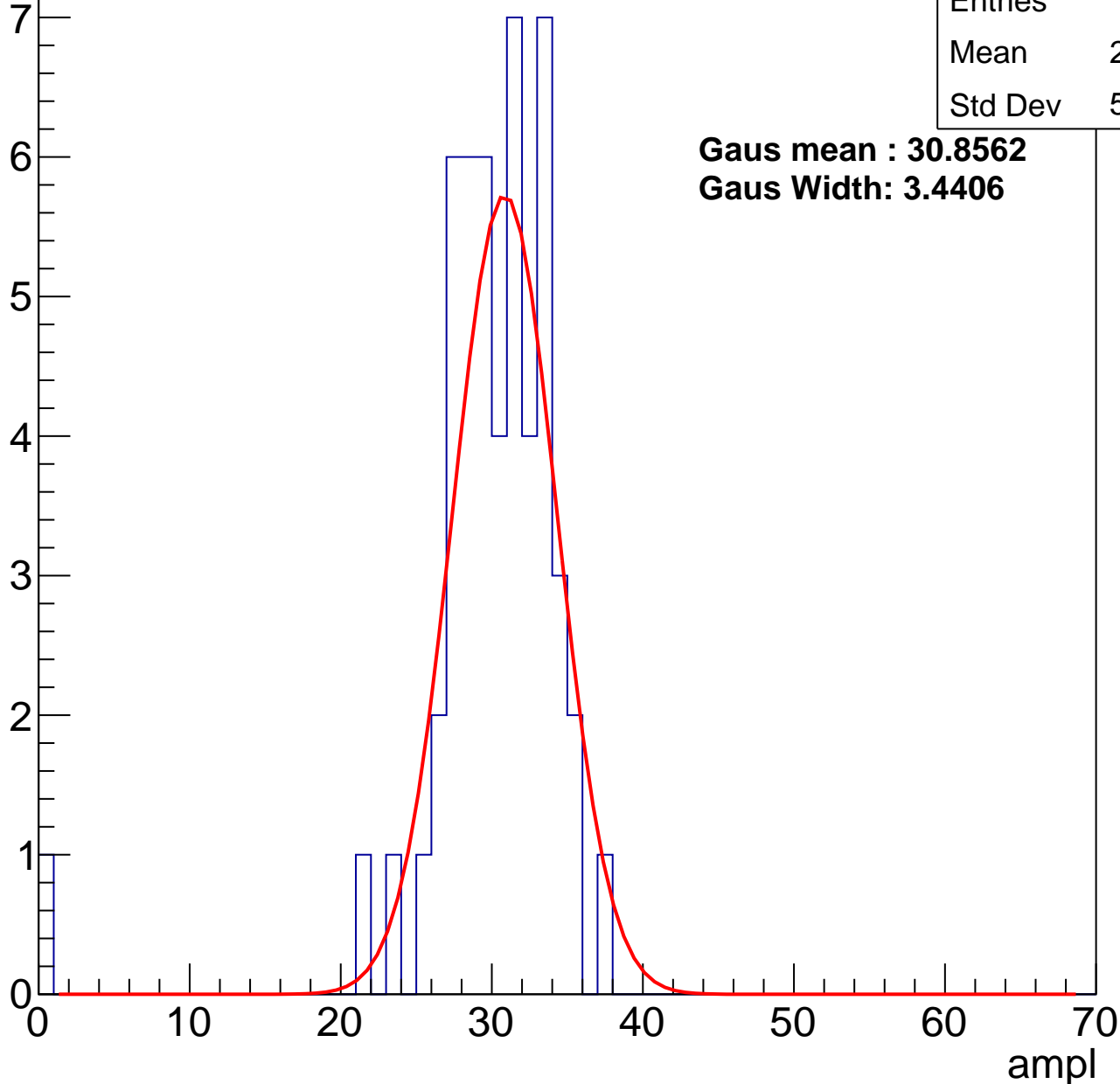
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	29.42
Std Dev	5.164

Gaus mean : 30.8562

Gaus Width: 3.4406



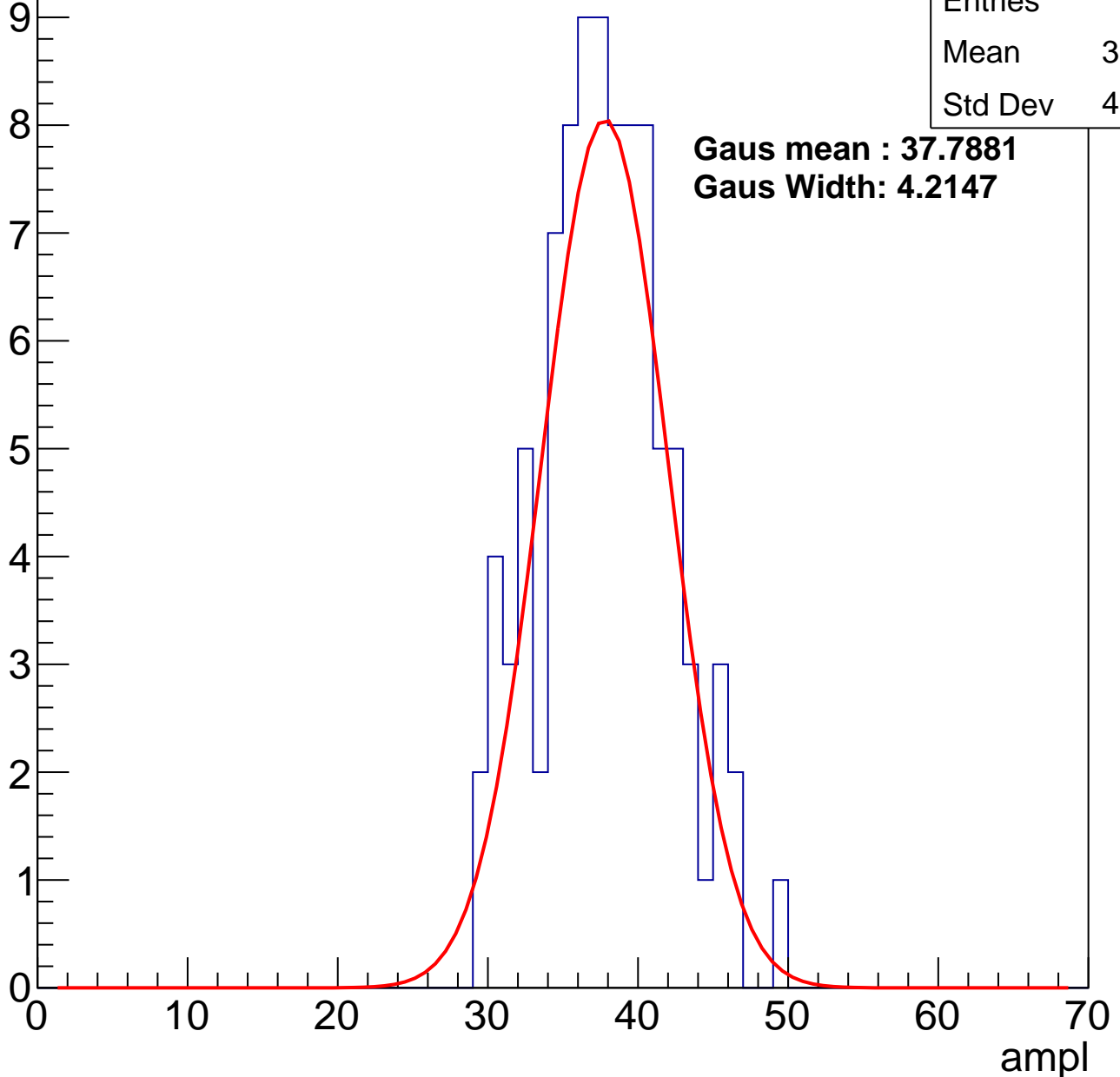
B0L001S, U14-ch46, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	93
Mean	37.33
Std Dev	4.218

Gaus mean : 37.7881
Gaus Width: 4.2147



B0L001S, U14-ch46, adc2

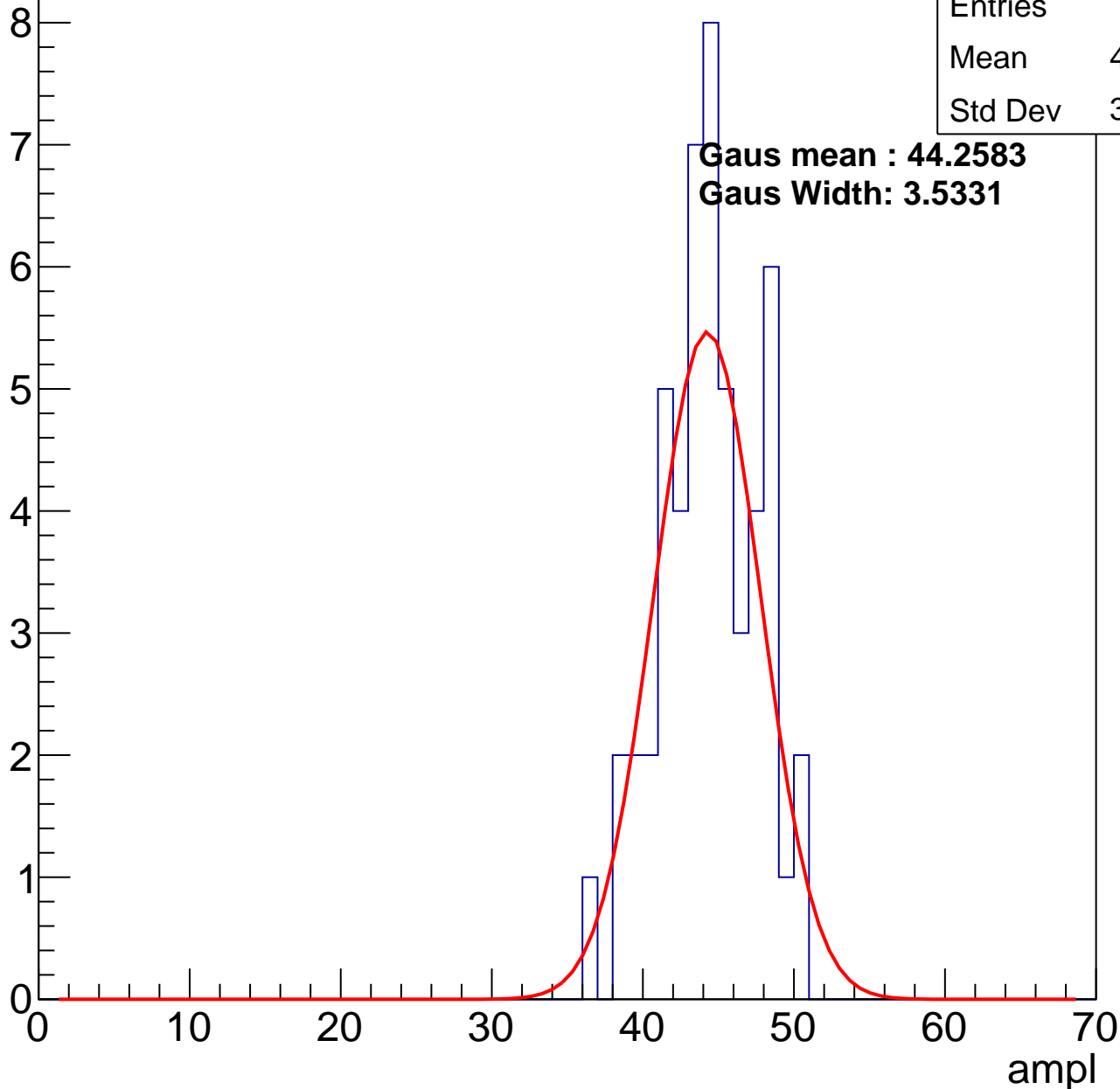
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	43.92
Std Dev	3.198

Gaus mean : 44.2583

Gaus Width: 3.5331

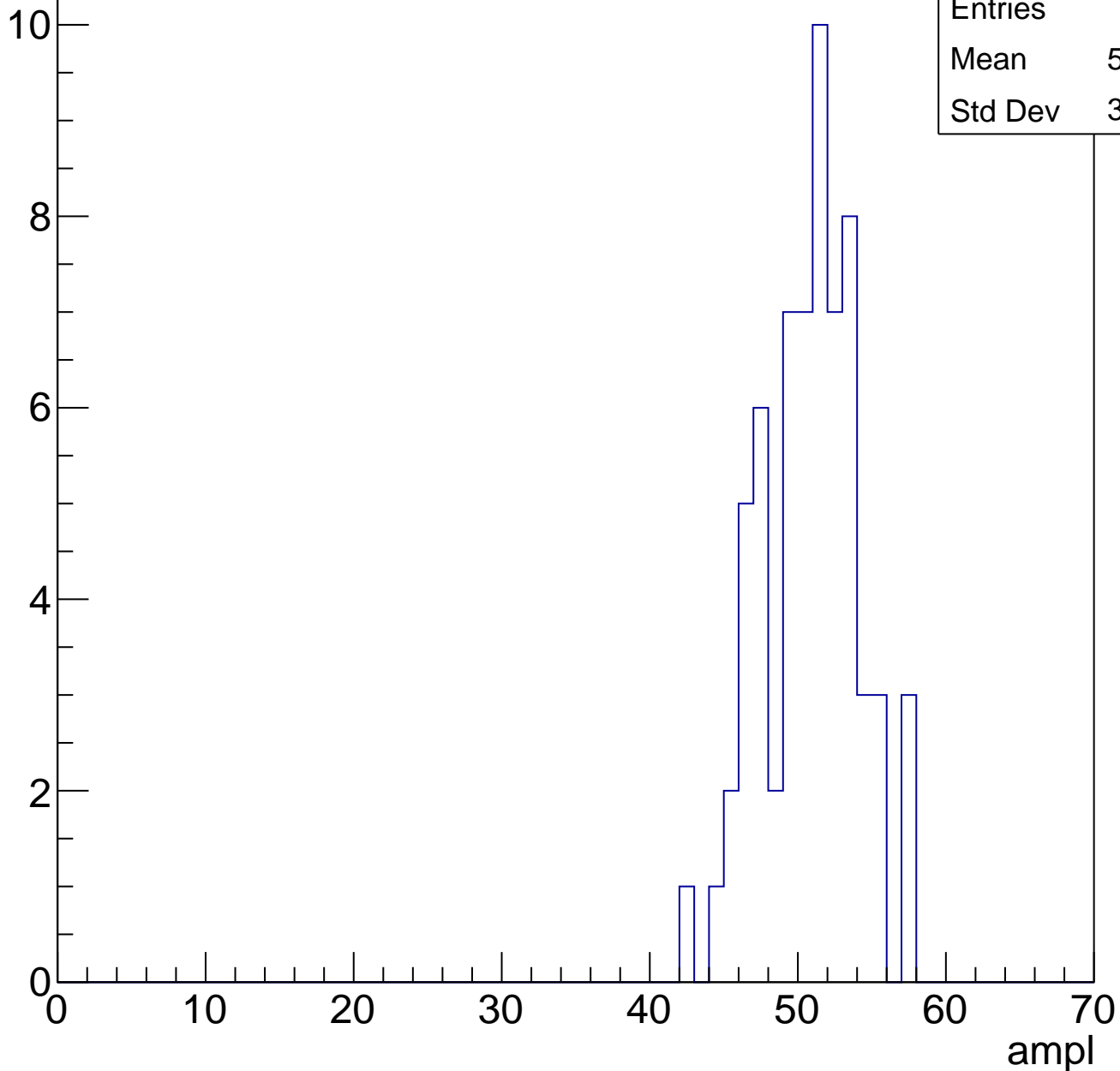


B0L001S, U14-ch46, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	50.35
Std Dev	3.213

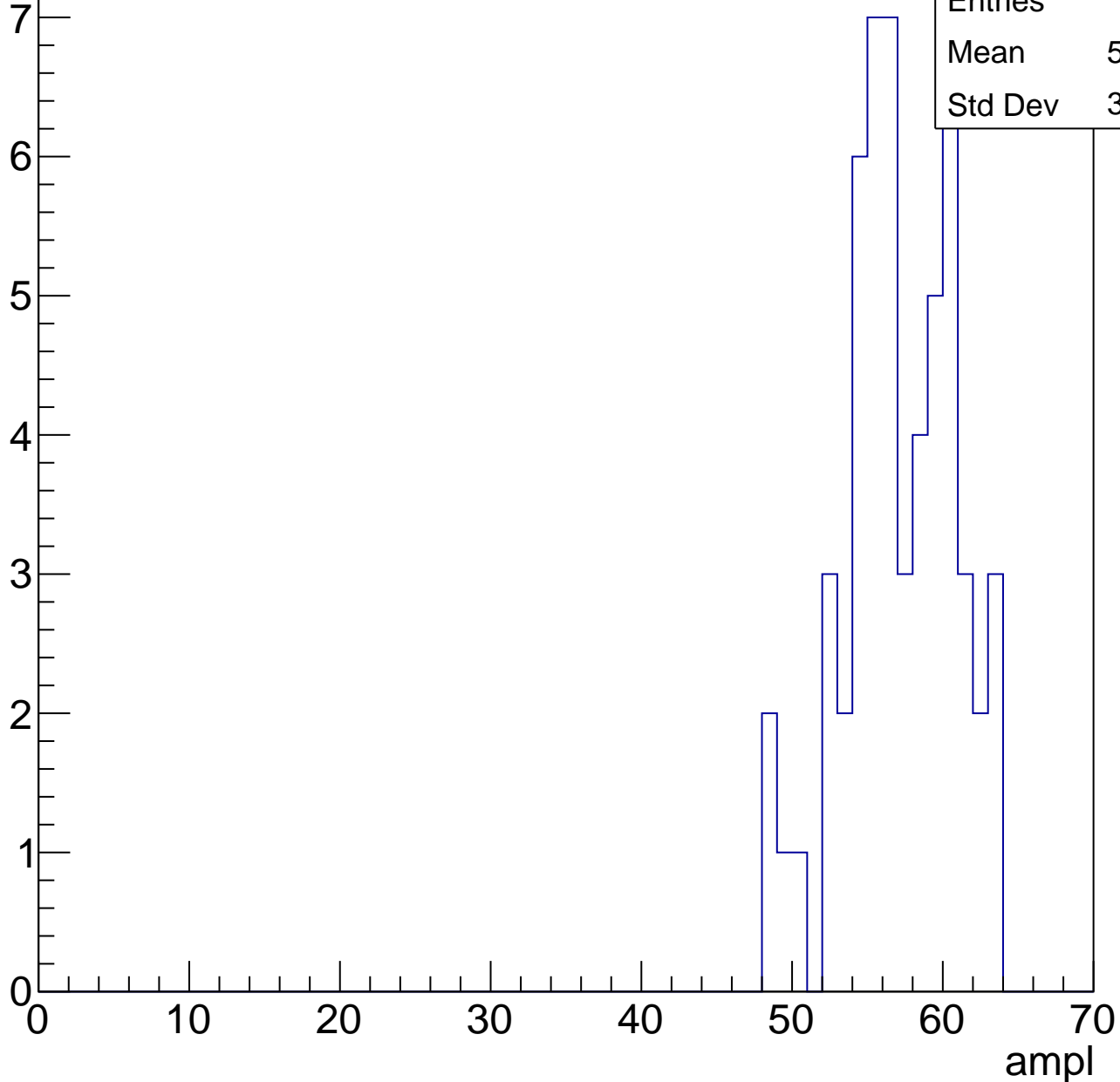
Entry



B0L001S, U14-ch46, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



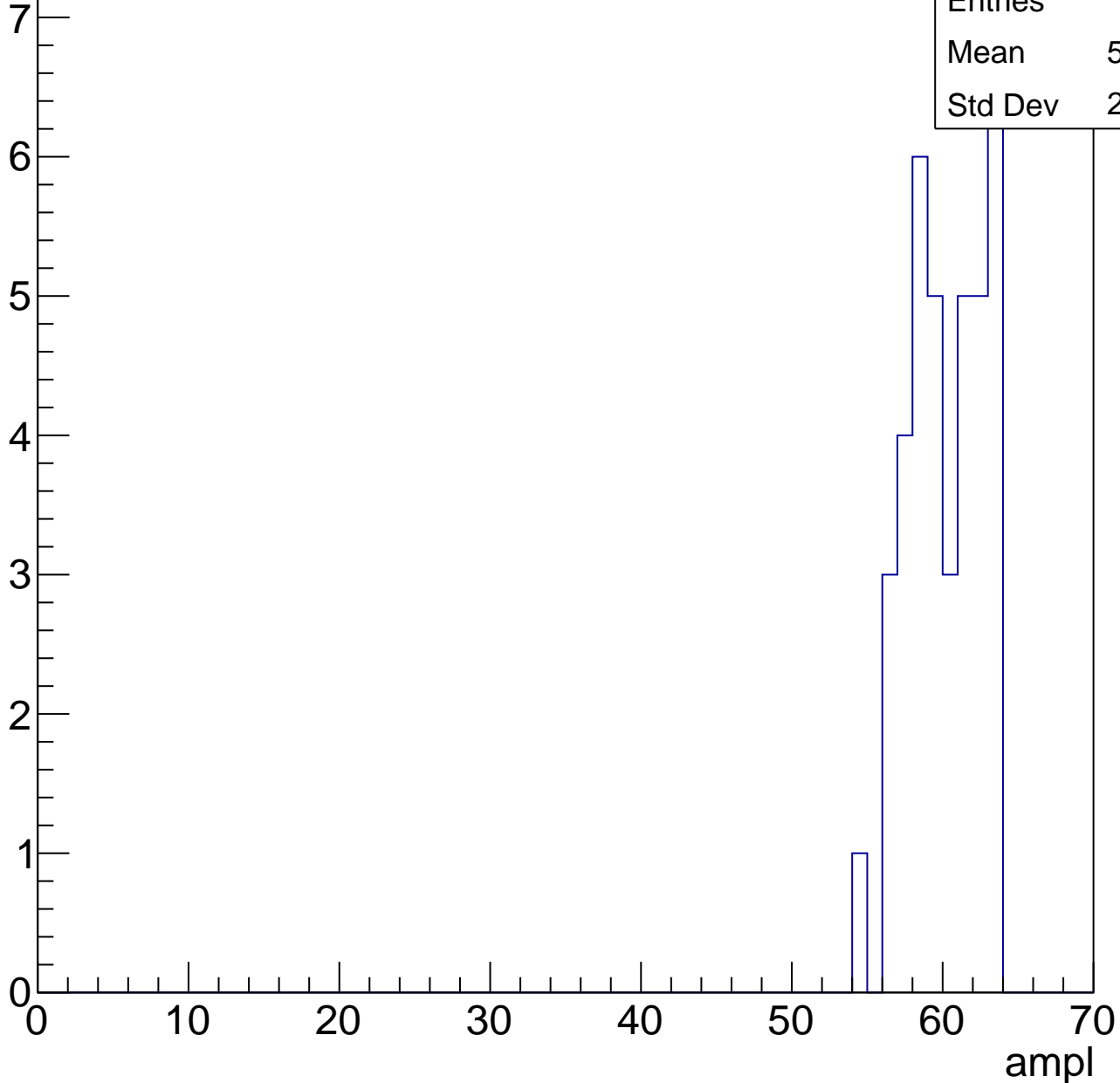
Entries	56
Mean	56.64
Std Dev	3.676

B0L001S, U14-ch46, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

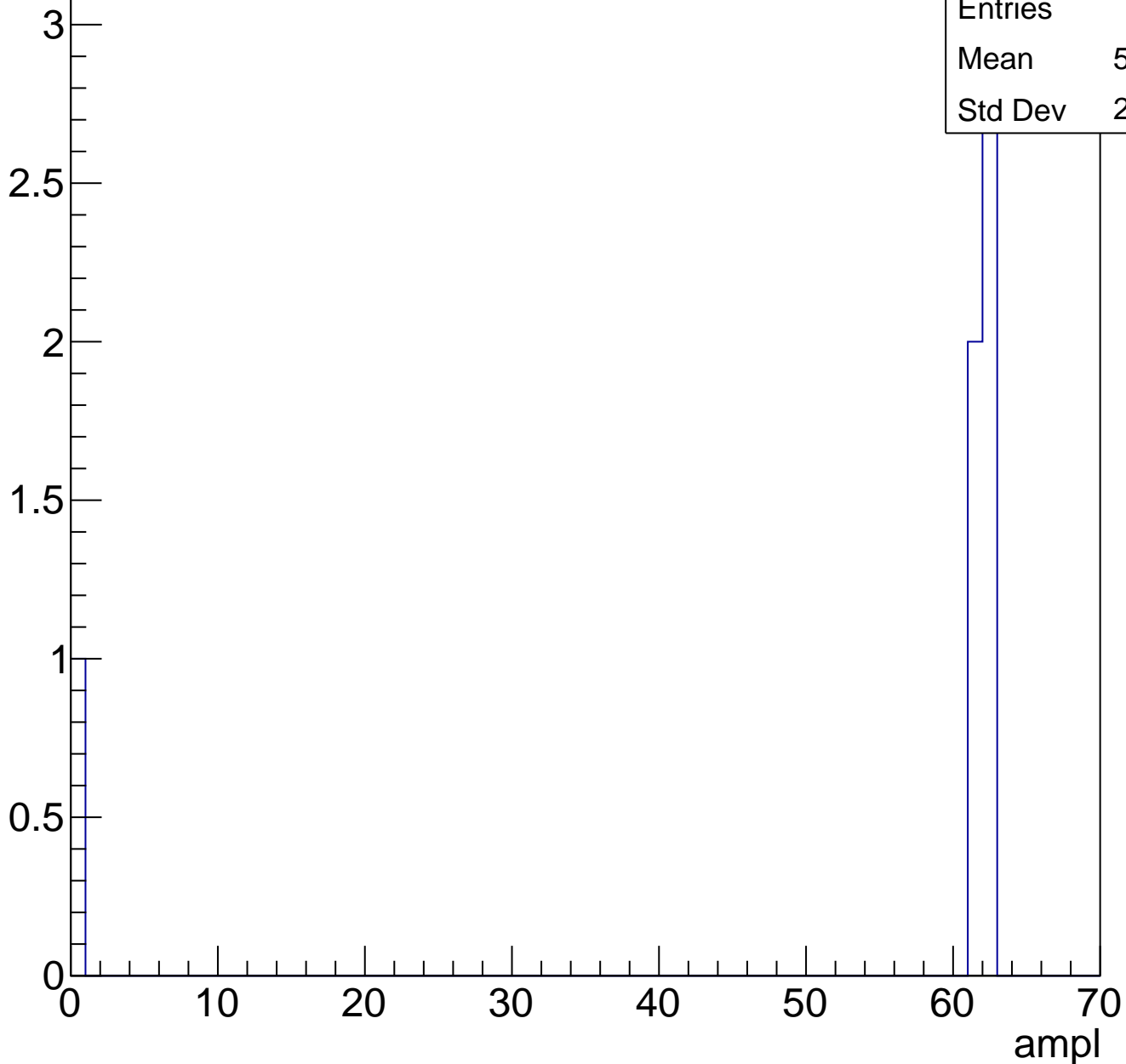
Entries	39
Mean	59.72
Std Dev	2.449



B0L001S, U14-ch46, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch46, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch47, adc0

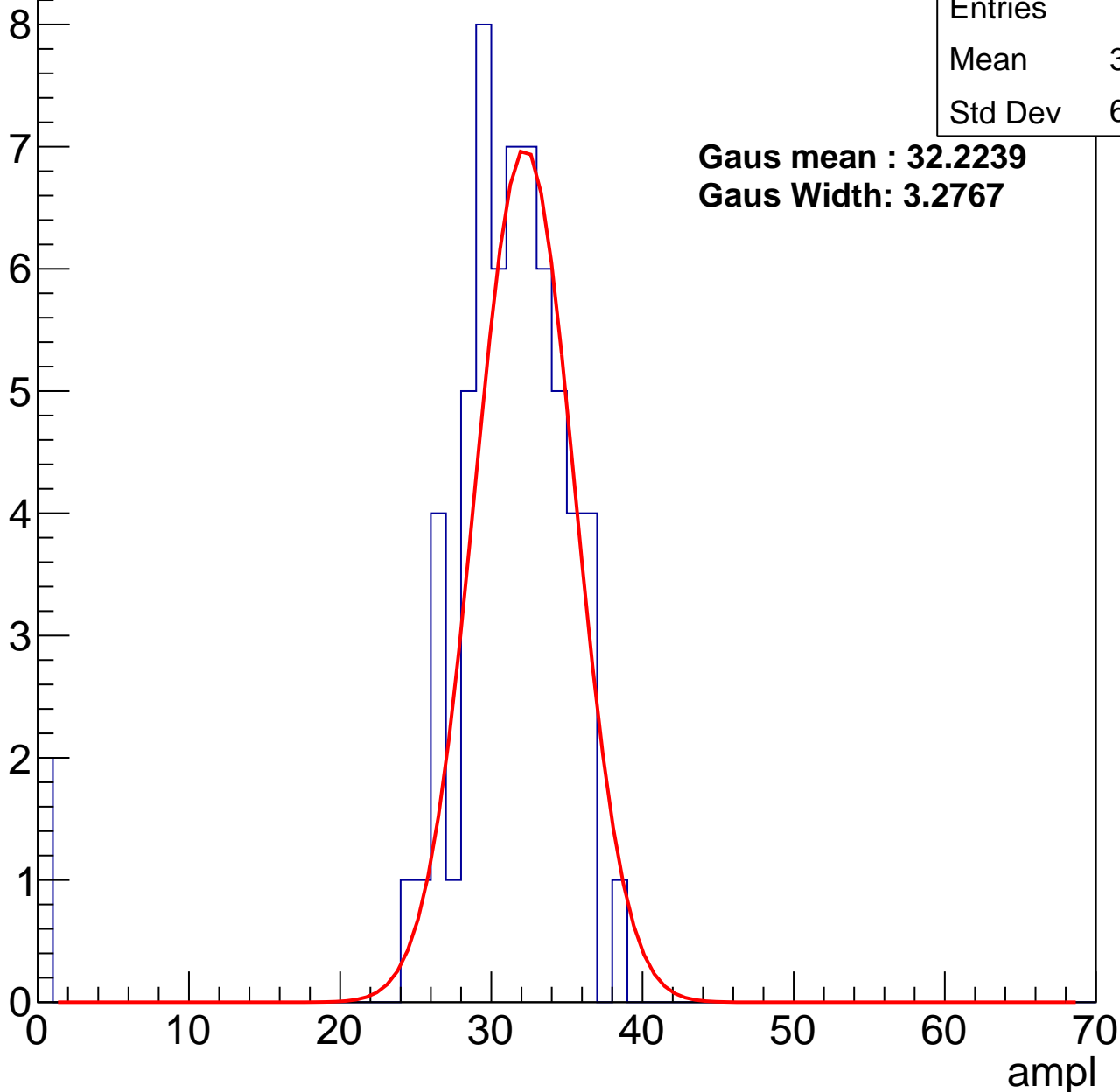
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.05
Std Dev	6.272

Gaus mean : 32.2239

Gaus Width: 3.2767



B0L001S, U14-ch47, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	83
Mean	38.41
Std Dev	3.664

Gaus mean : 38.3784

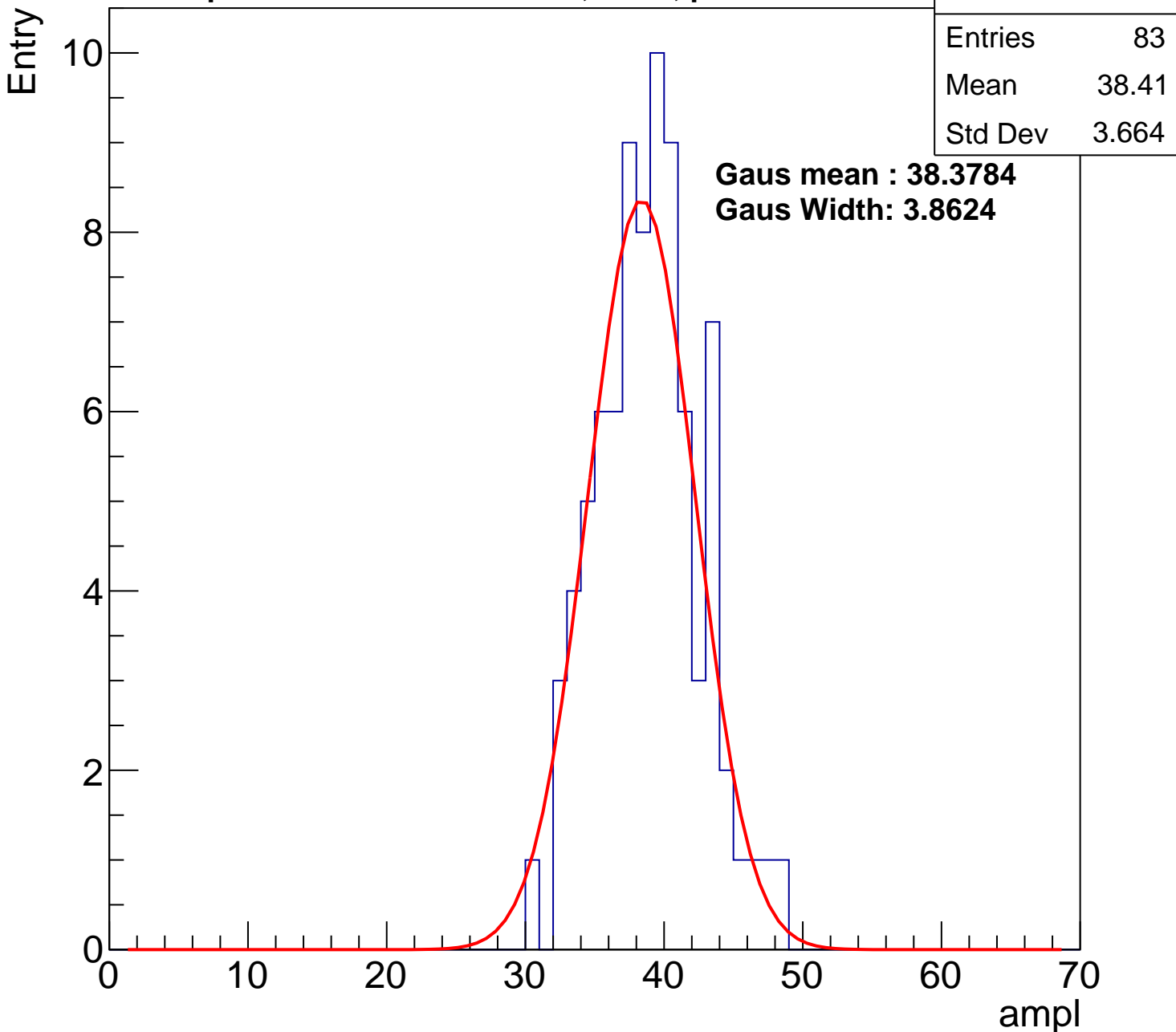
Gaus Width: 3.8624

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch47, adc2

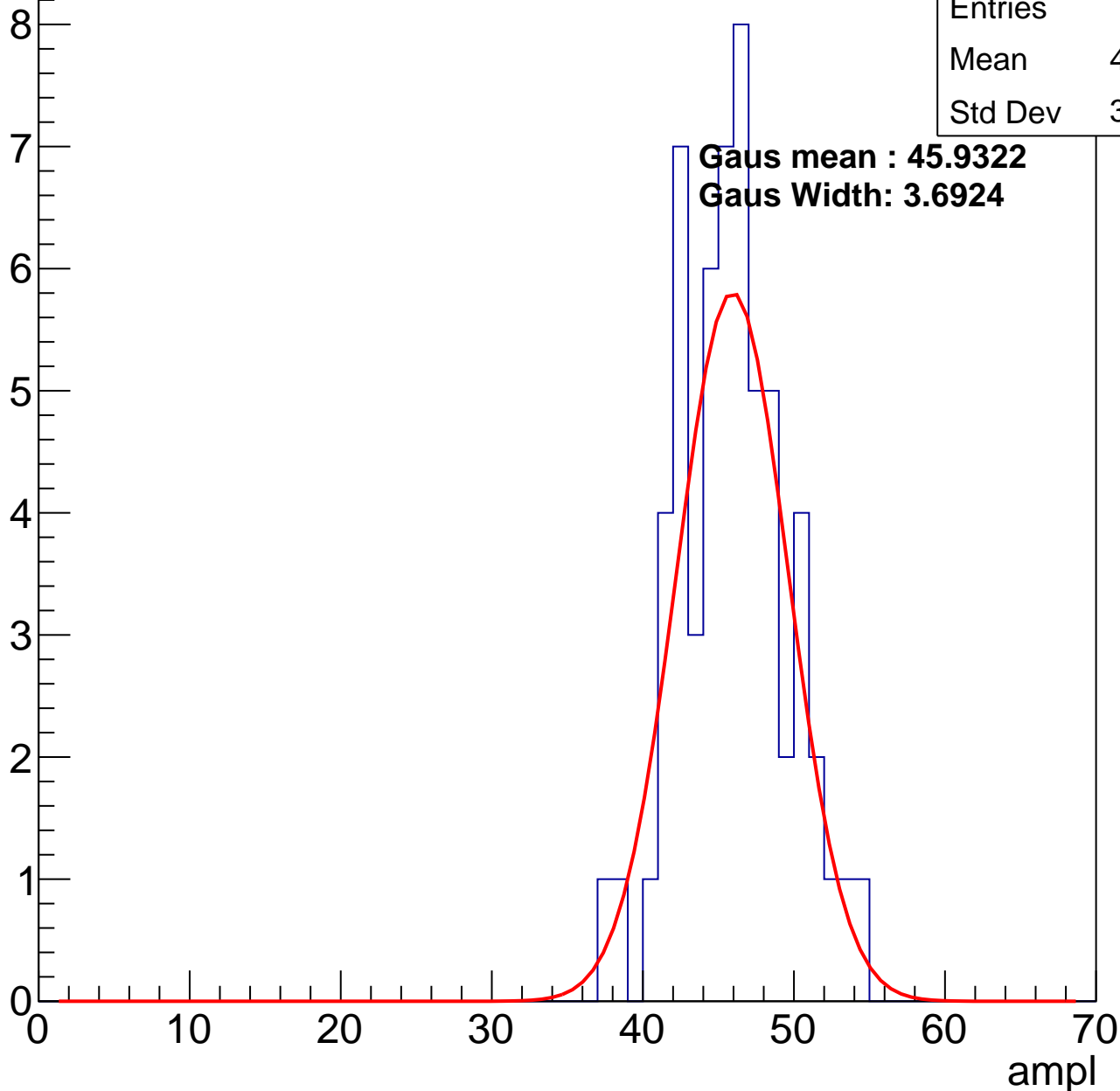
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	45.47
Std Dev	3.543

Gaus mean : 45.9322

Gaus Width: 3.6924

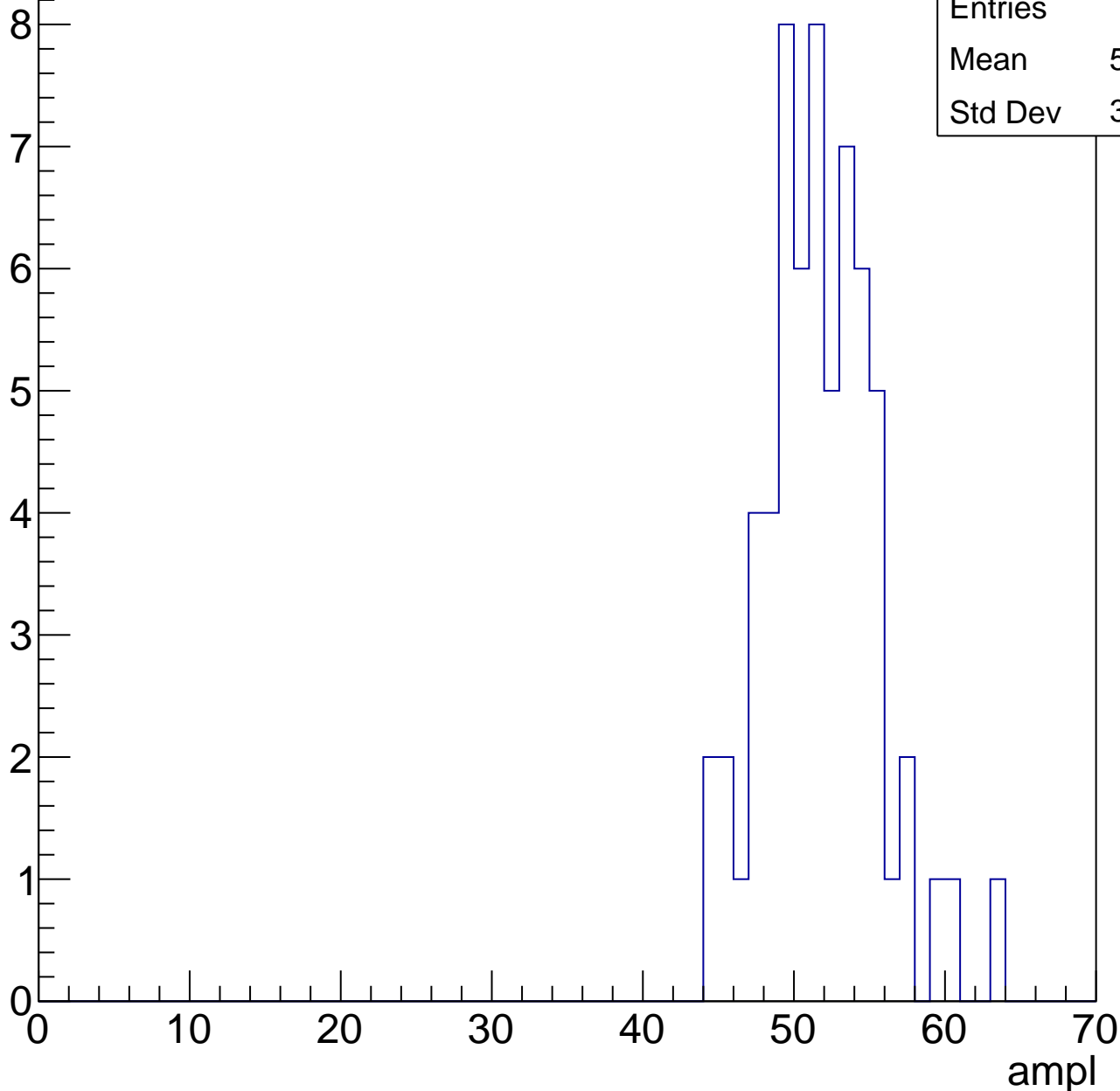


B0L001S, U14-ch47, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.34
Std Dev	3.705

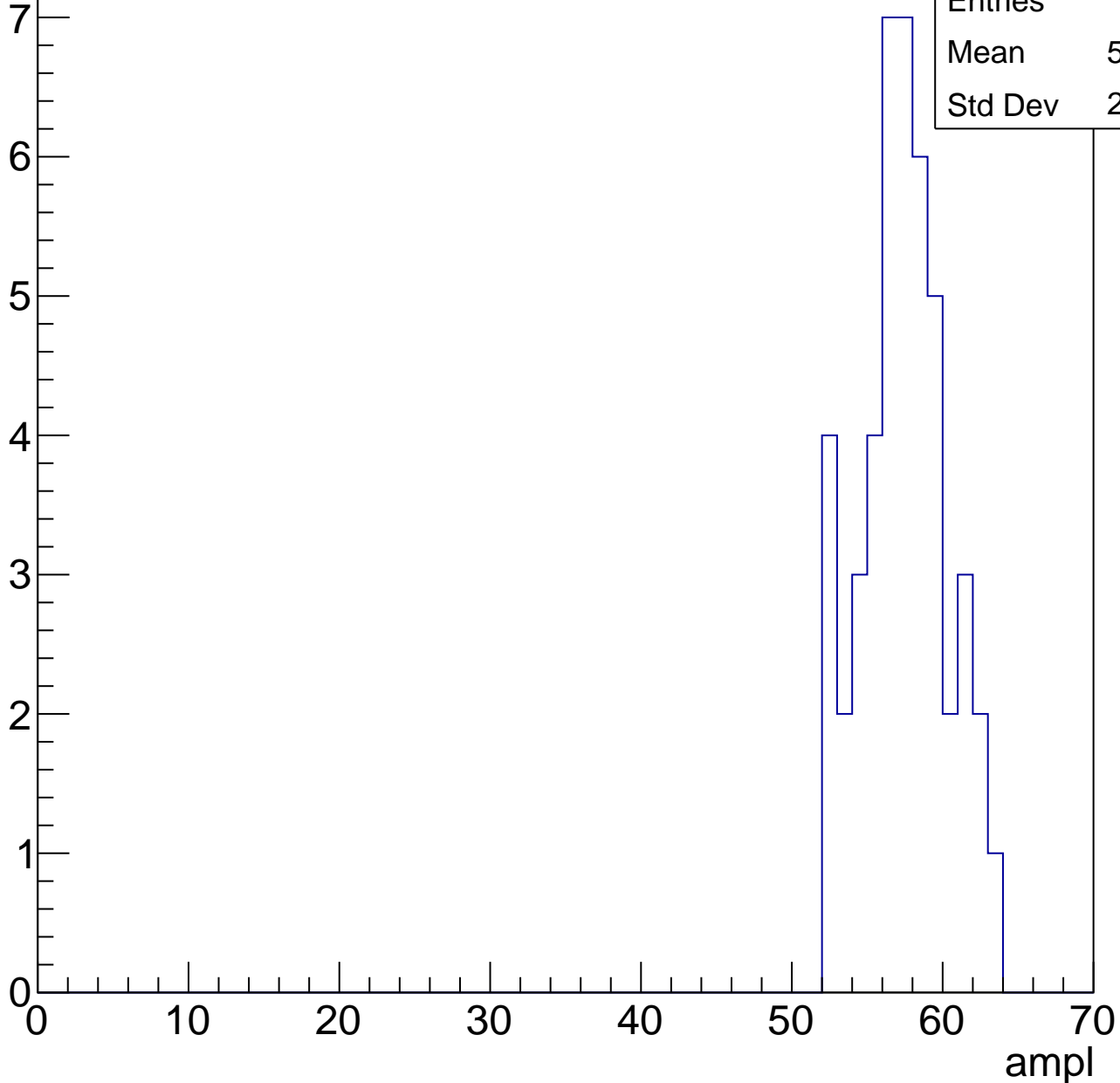


B0L001S, U14-ch47, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	56.96
Std Dev	2.797

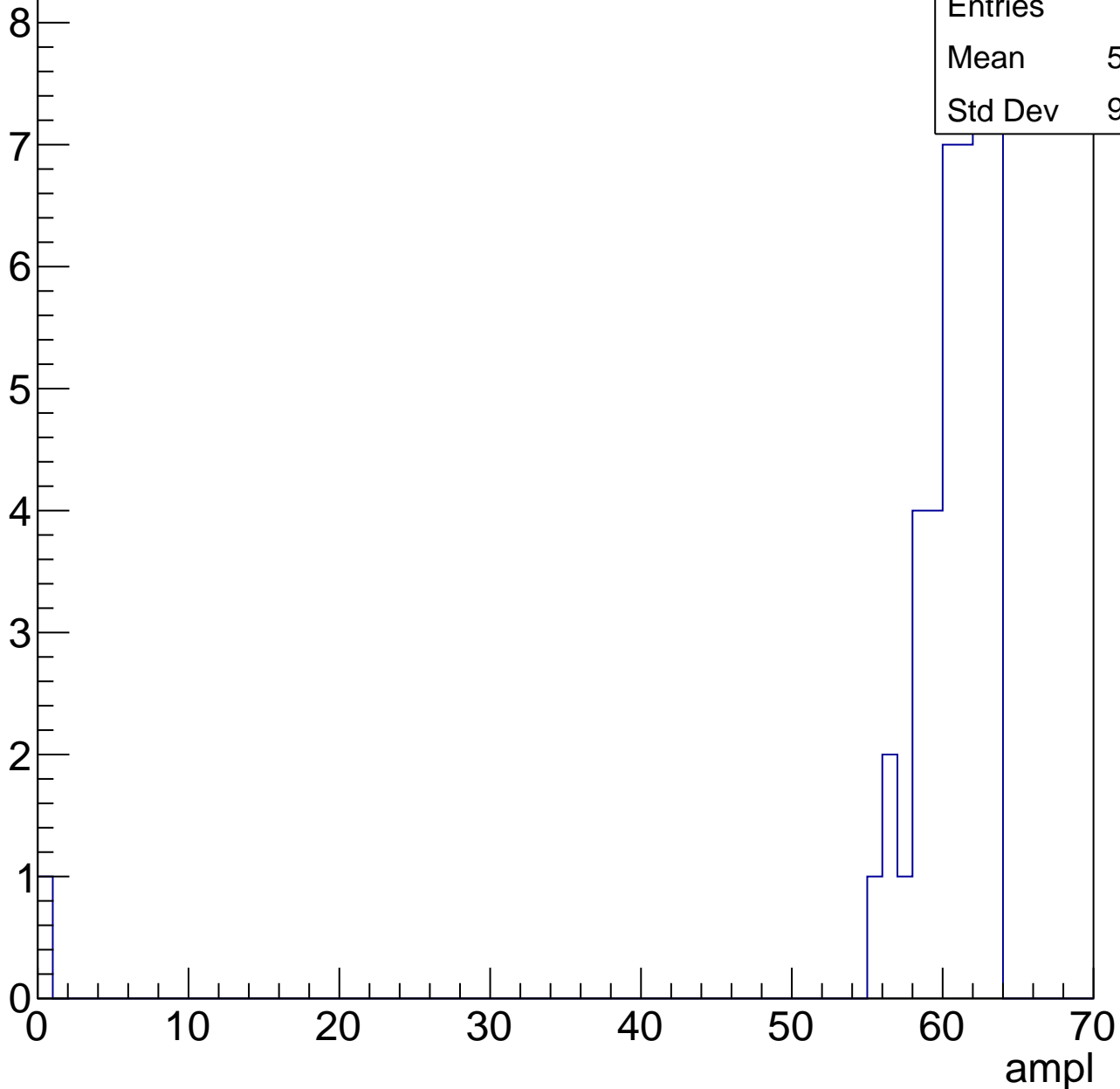


B0L001S, U14-ch47, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	59.05
Std Dev	9.348



B0L001S, U14-ch47, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch47, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch48, adc0

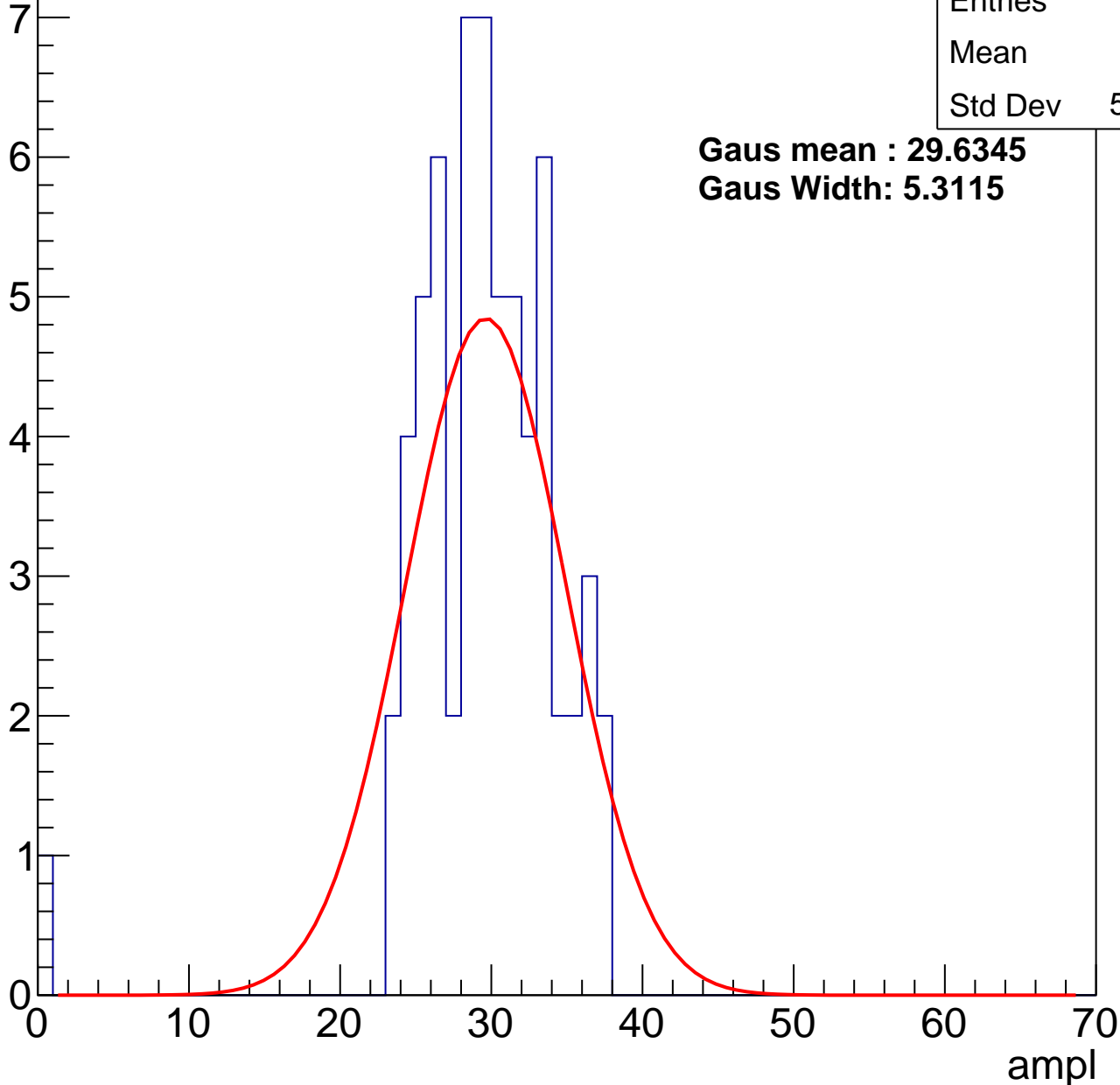
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29
Std Dev	5.219

Gaus mean : 29.6345

Gaus Width: 5.3115



B0L001S, U14-ch48, adc1

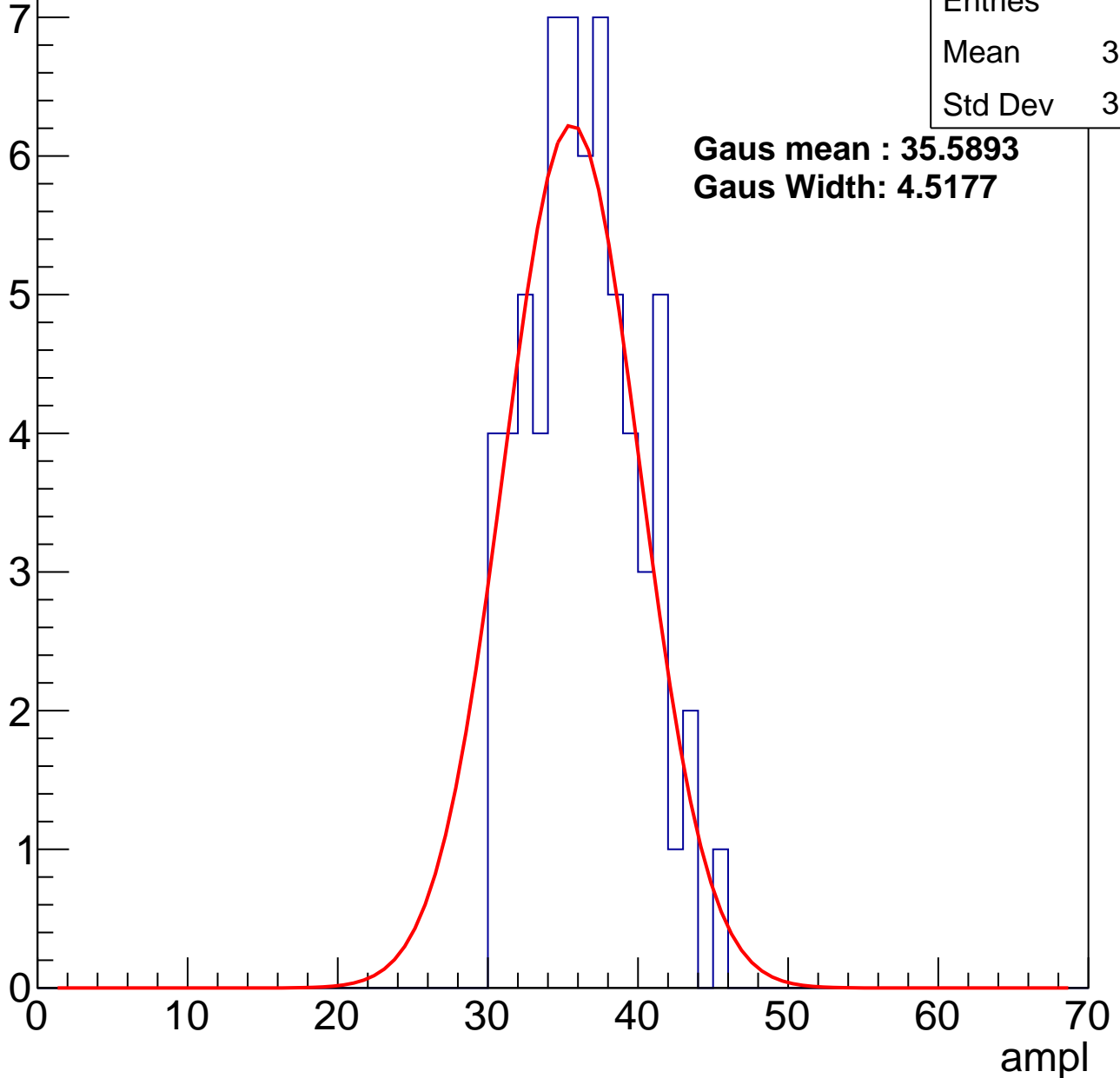
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	35.97
Std Dev	3.608

Gaus mean : 35.5893

Gaus Width: 4.5177



B0L001S, U14-ch48, adc2

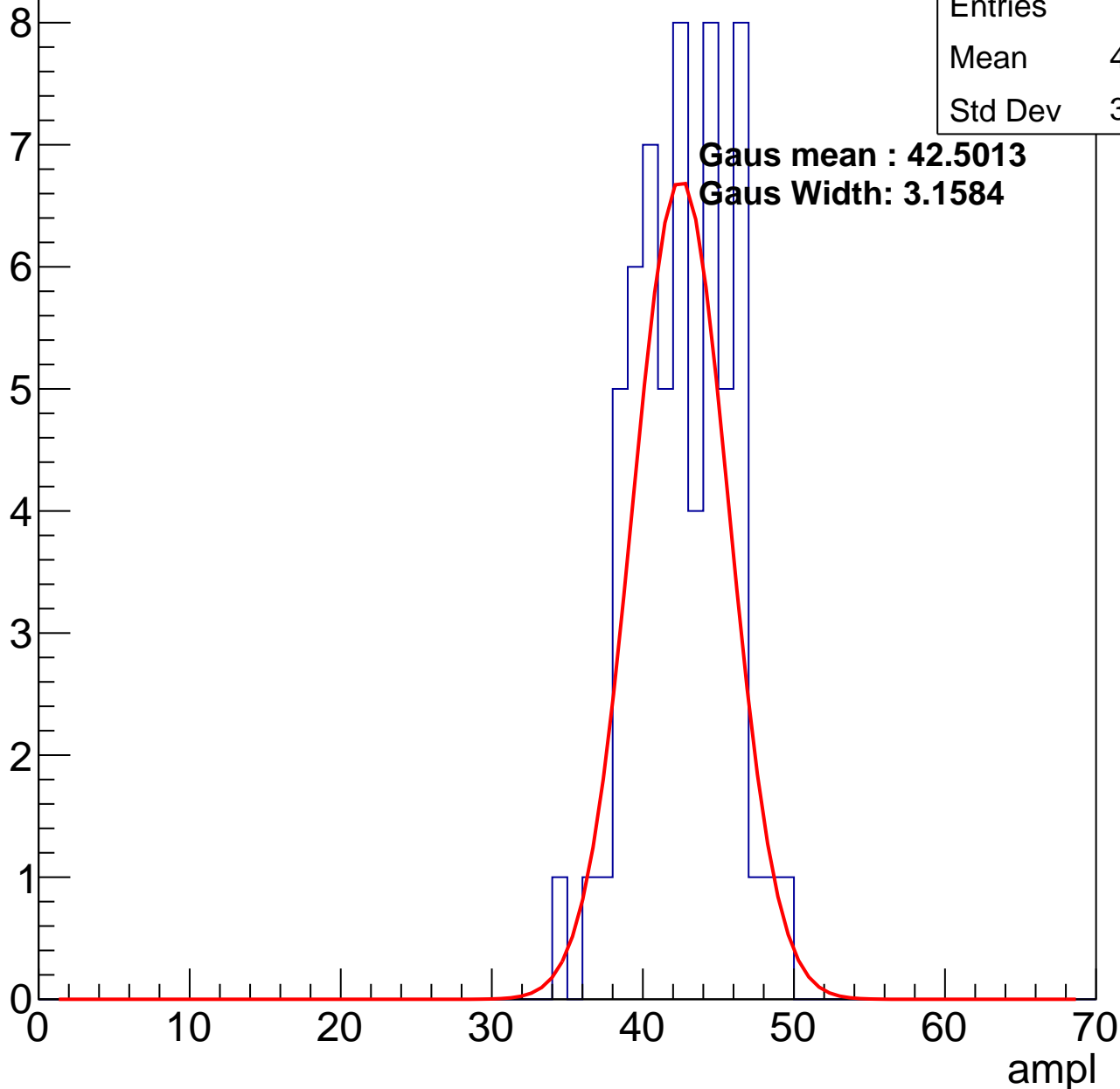
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	42.15
Std Dev	3.136

Gaus mean : 42.5013

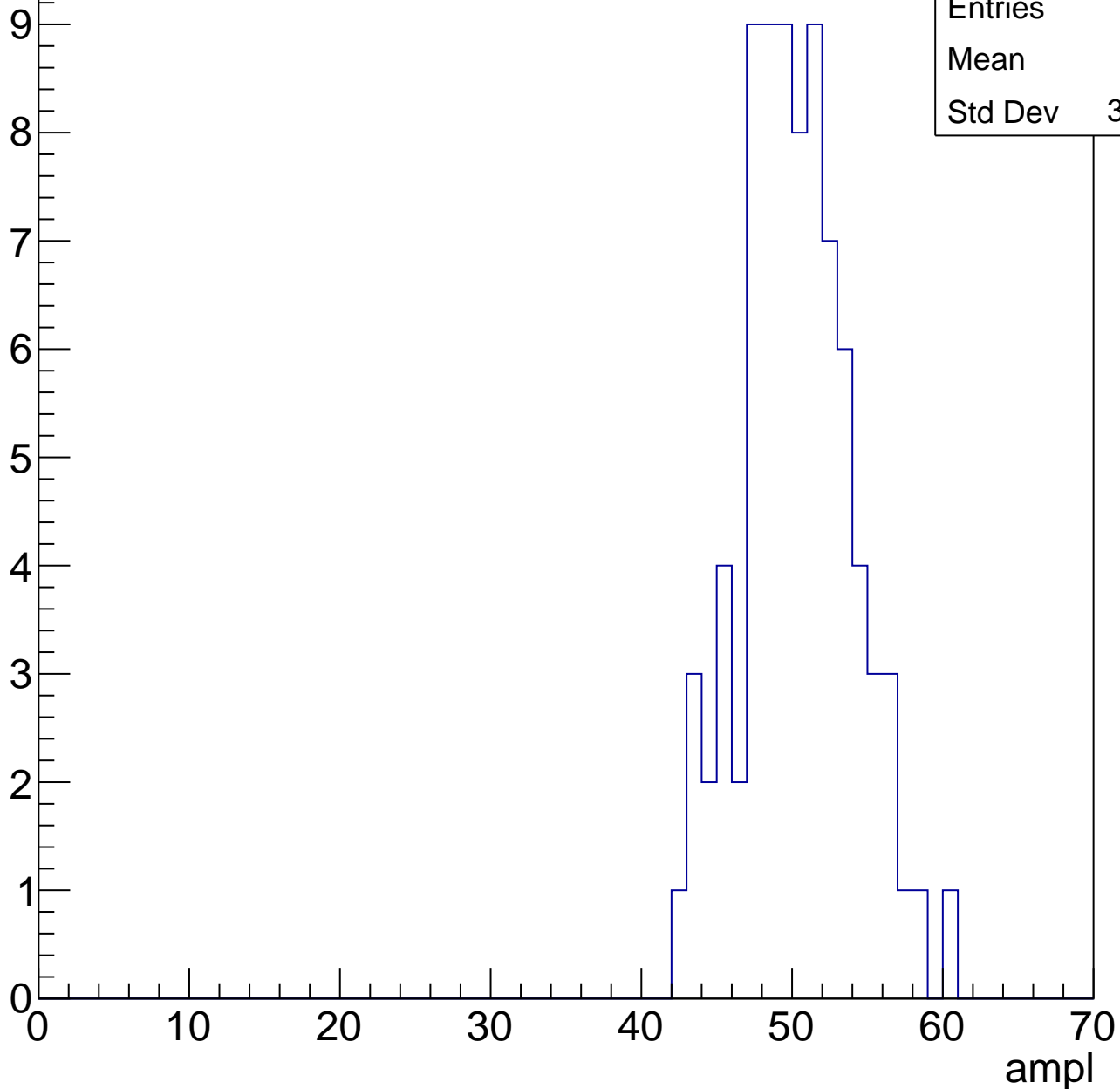
Gaus Width: 3.1584



B0L001S, U14-ch48, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	82
Mean	49.9
Std Dev	3.648

B0L001S, U14-ch48, adc4

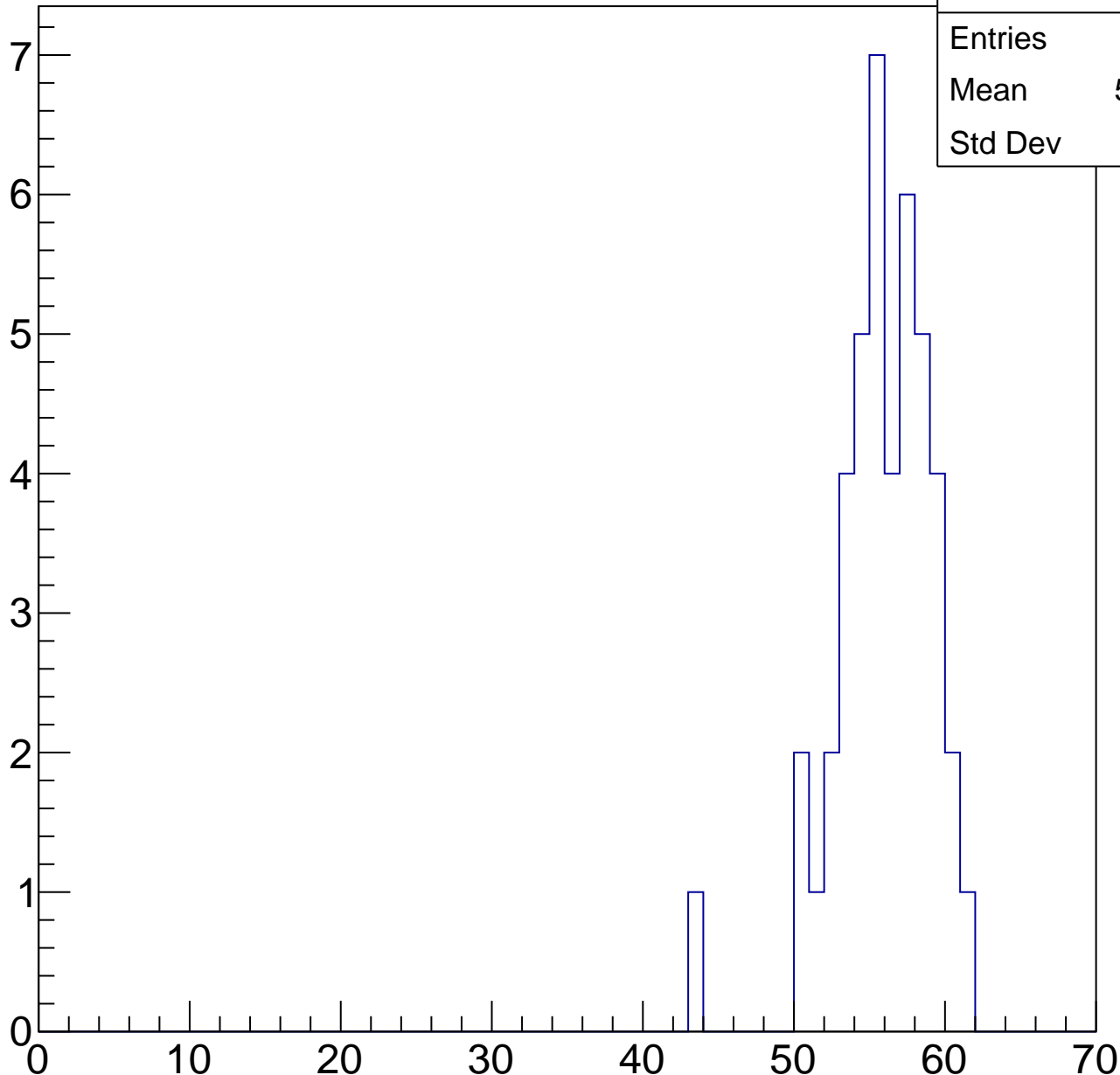
calib_packv5_042523_0143.root, FC#9, port A1

Entry

7
6
5
4
3
2
1
0

Entries	44
Mean	55.41
Std Dev	3.25

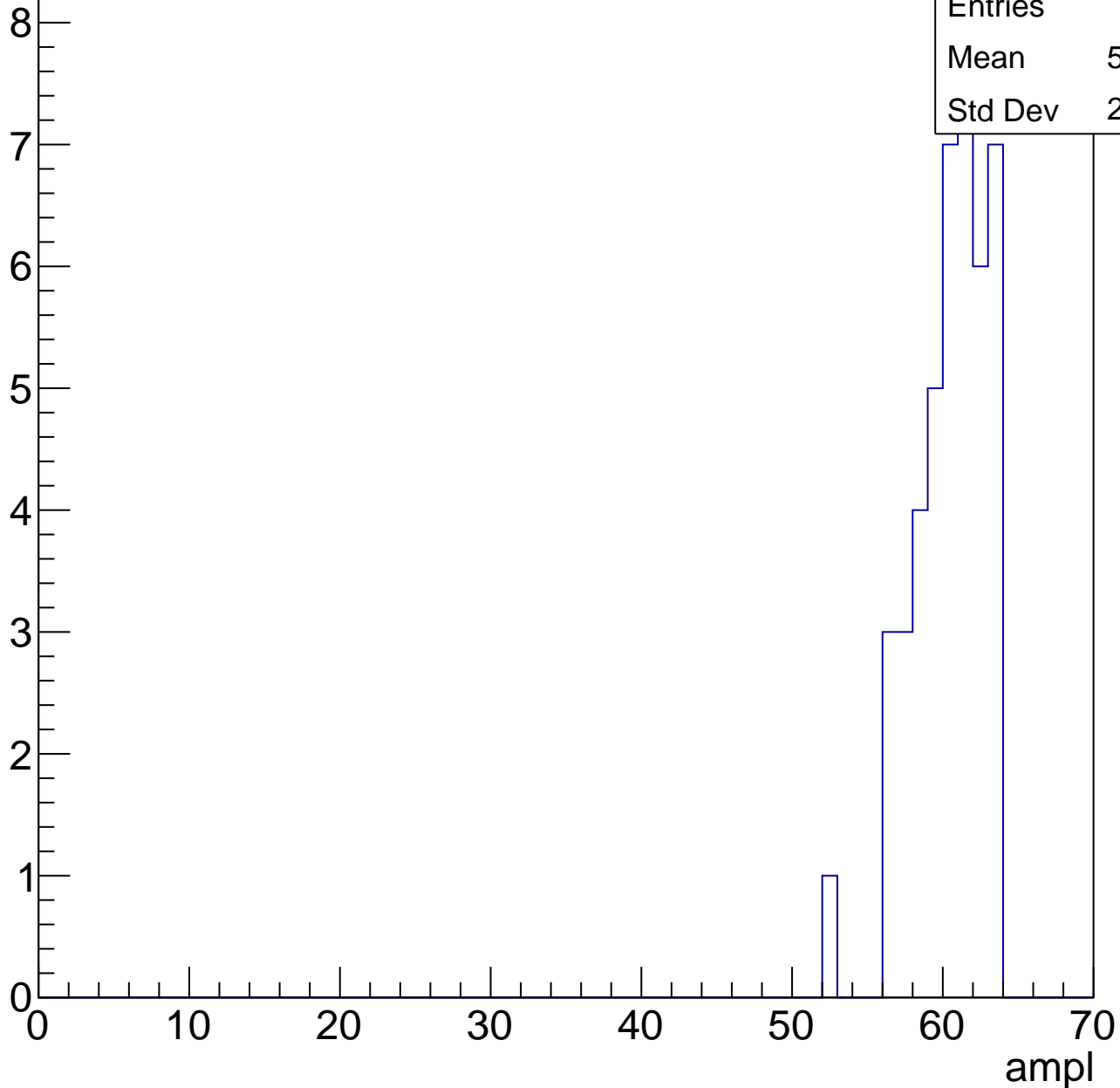
ampl



B0L001S, U14-ch48, adc5

calib_packv5_042523_0143.root, FC#9, port A1

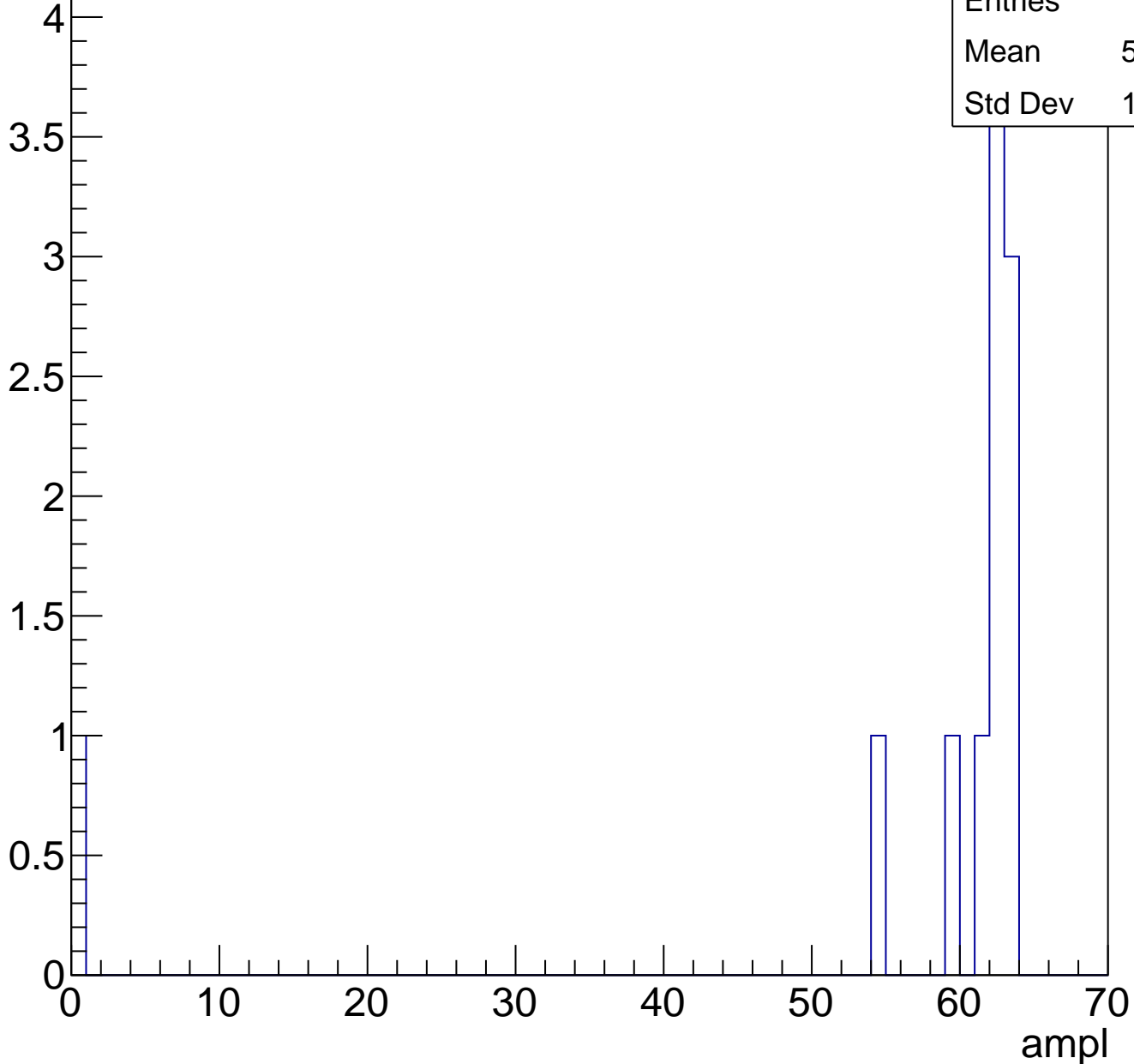
Entry



B0L001S, U14-ch48, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch48, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U14-ch49, adc0

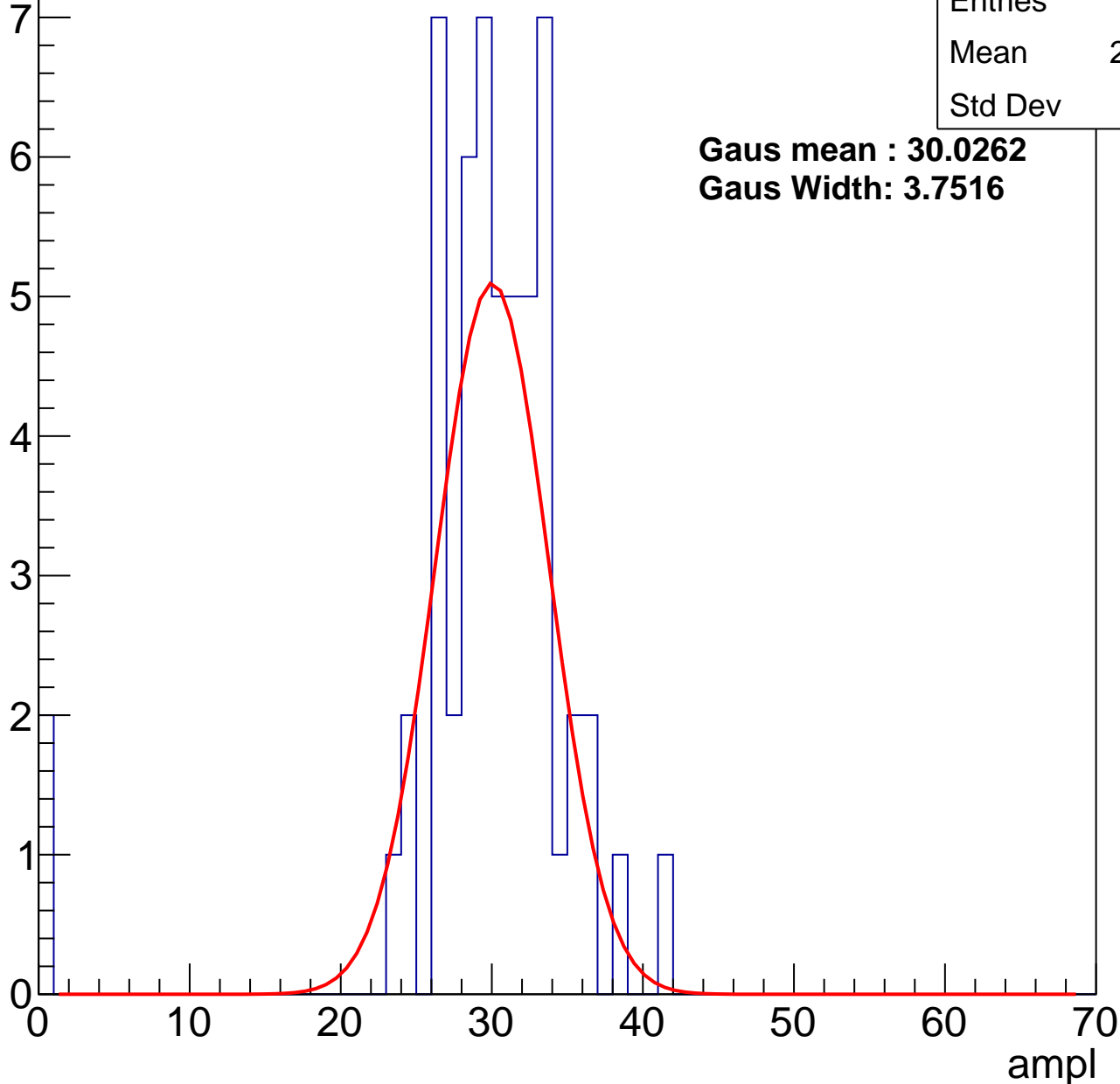
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	29.09
Std Dev	6.61

Gaus mean : 30.0262

Gaus Width: 3.7516



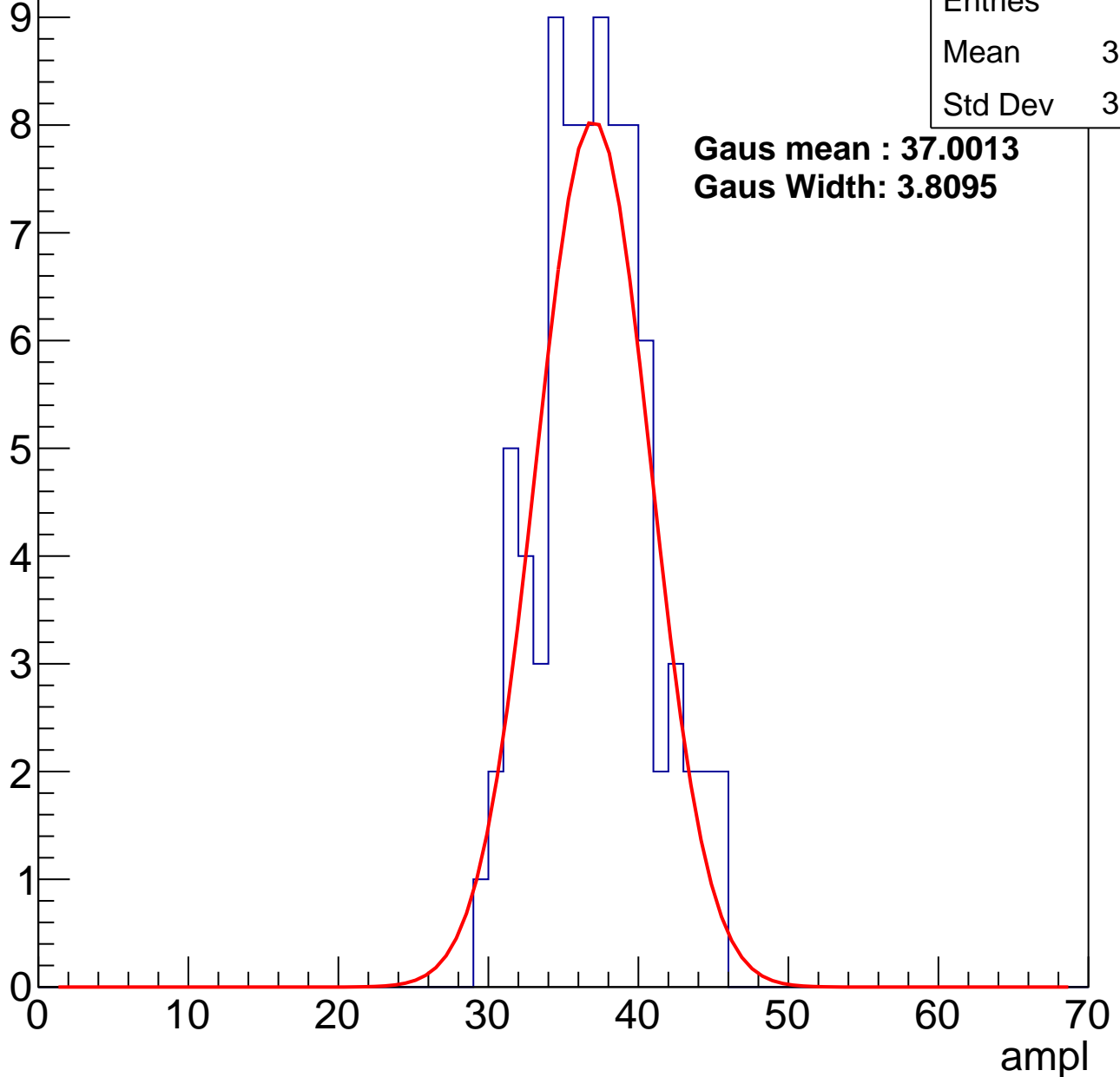
B0L001S, U14-ch49, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	36.66
Std Dev	3.663

Gaus mean : 37.0013
Gaus Width: 3.8095

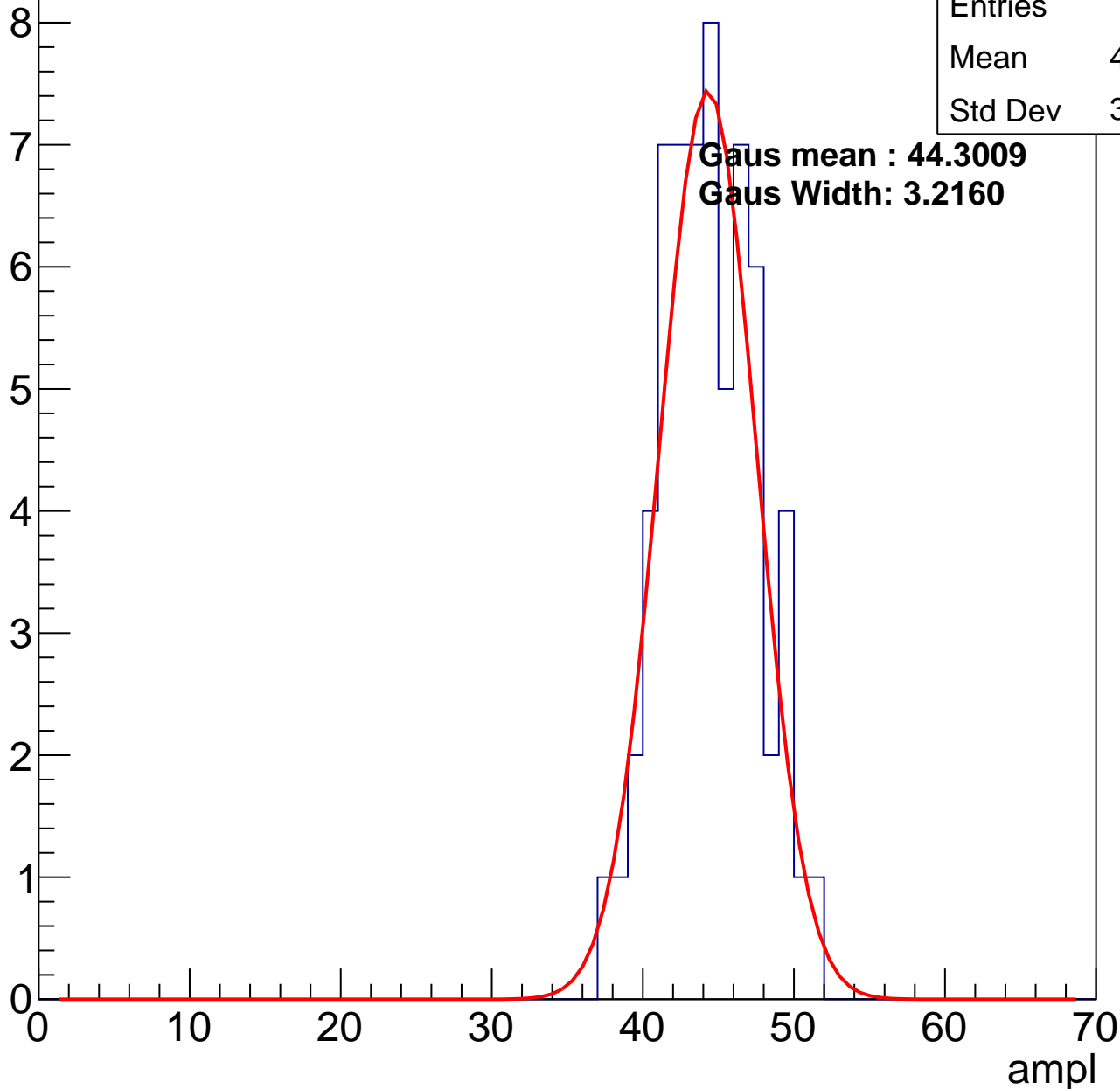


B0L001S, U14-ch49, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	43.95
Std Dev	3.088

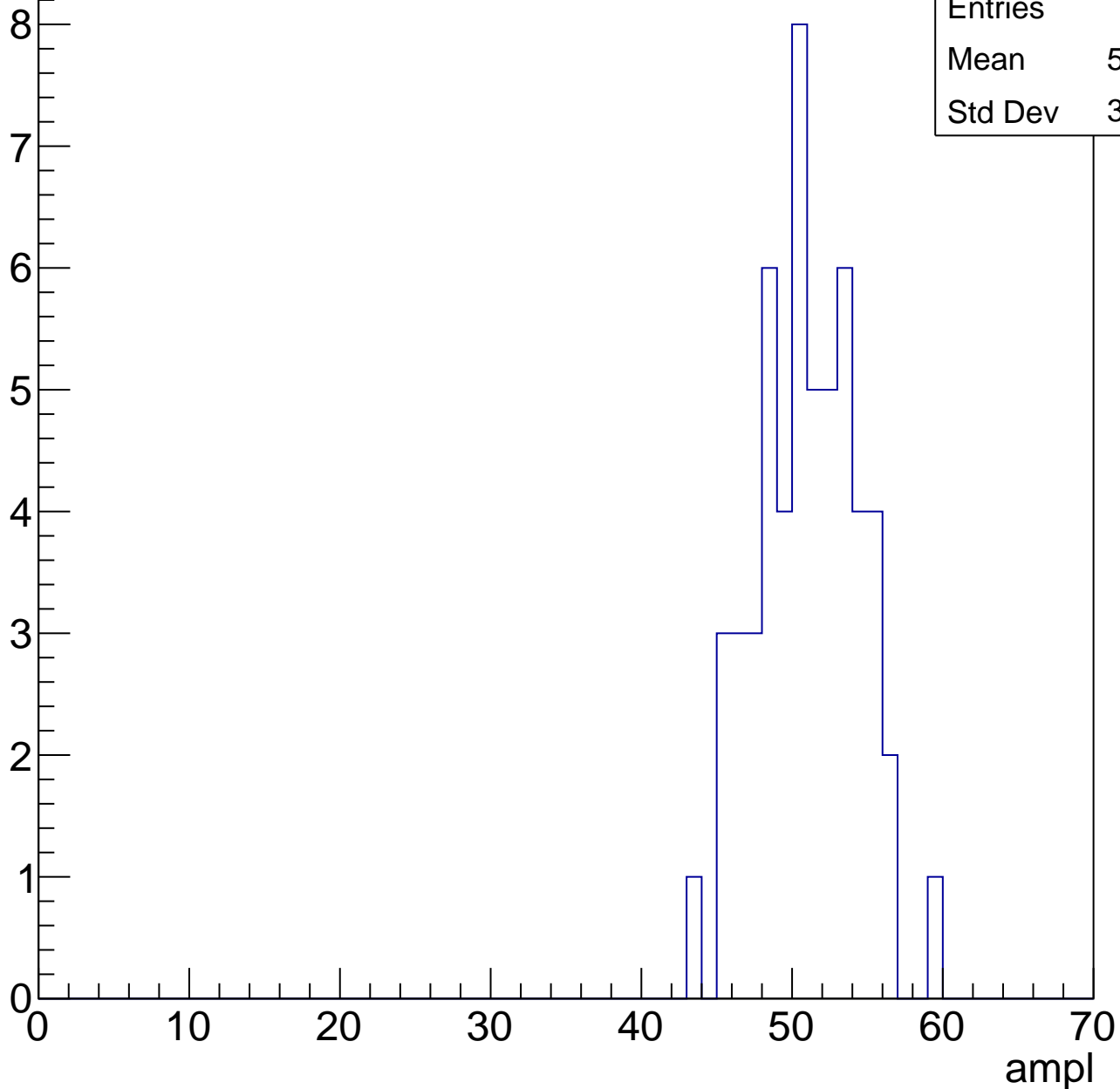


B0L001S, U14-ch49, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.56
Std Dev	3.318

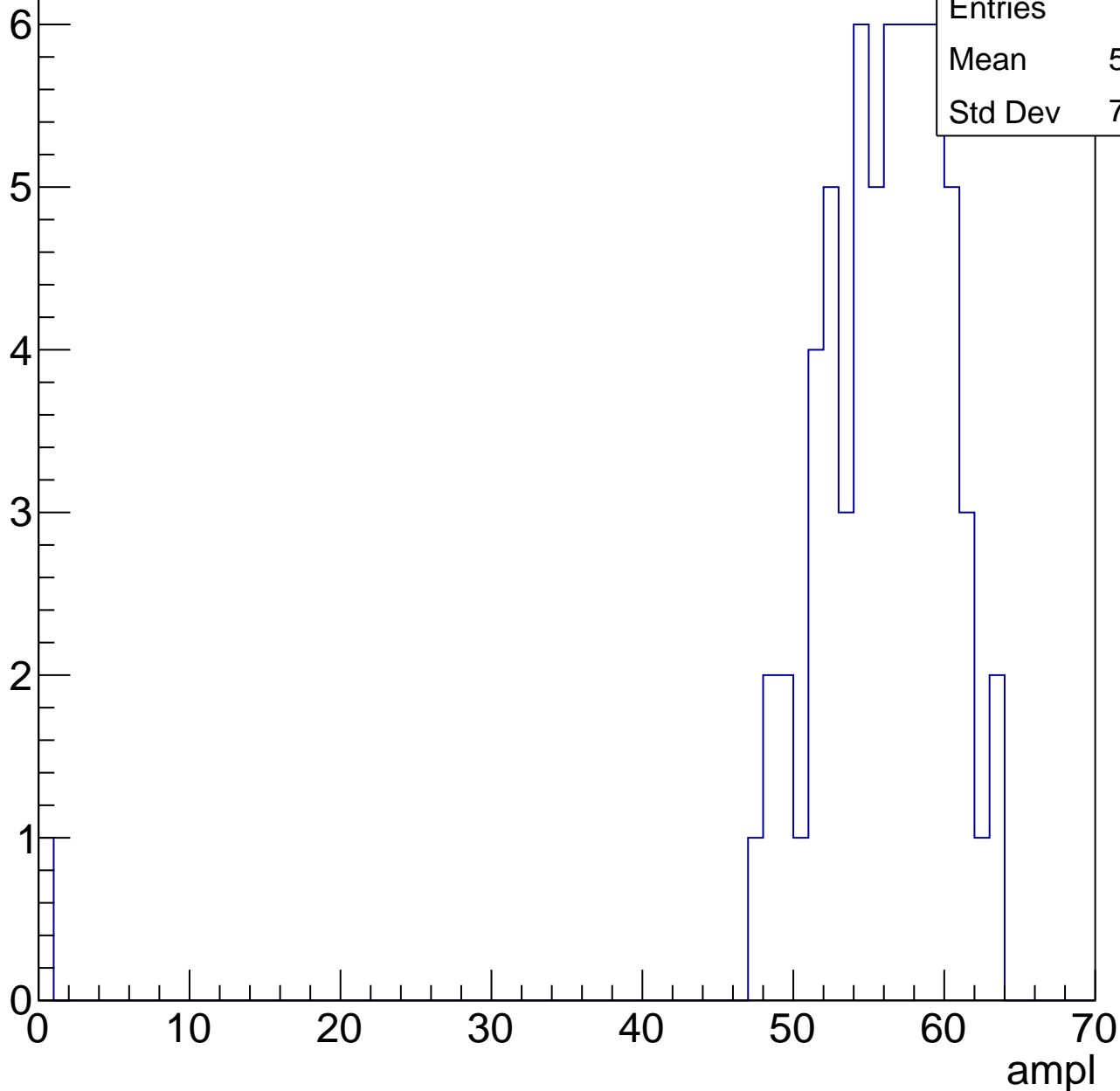


B0L001S, U14-ch49, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	54.83
Std Dev	7.844

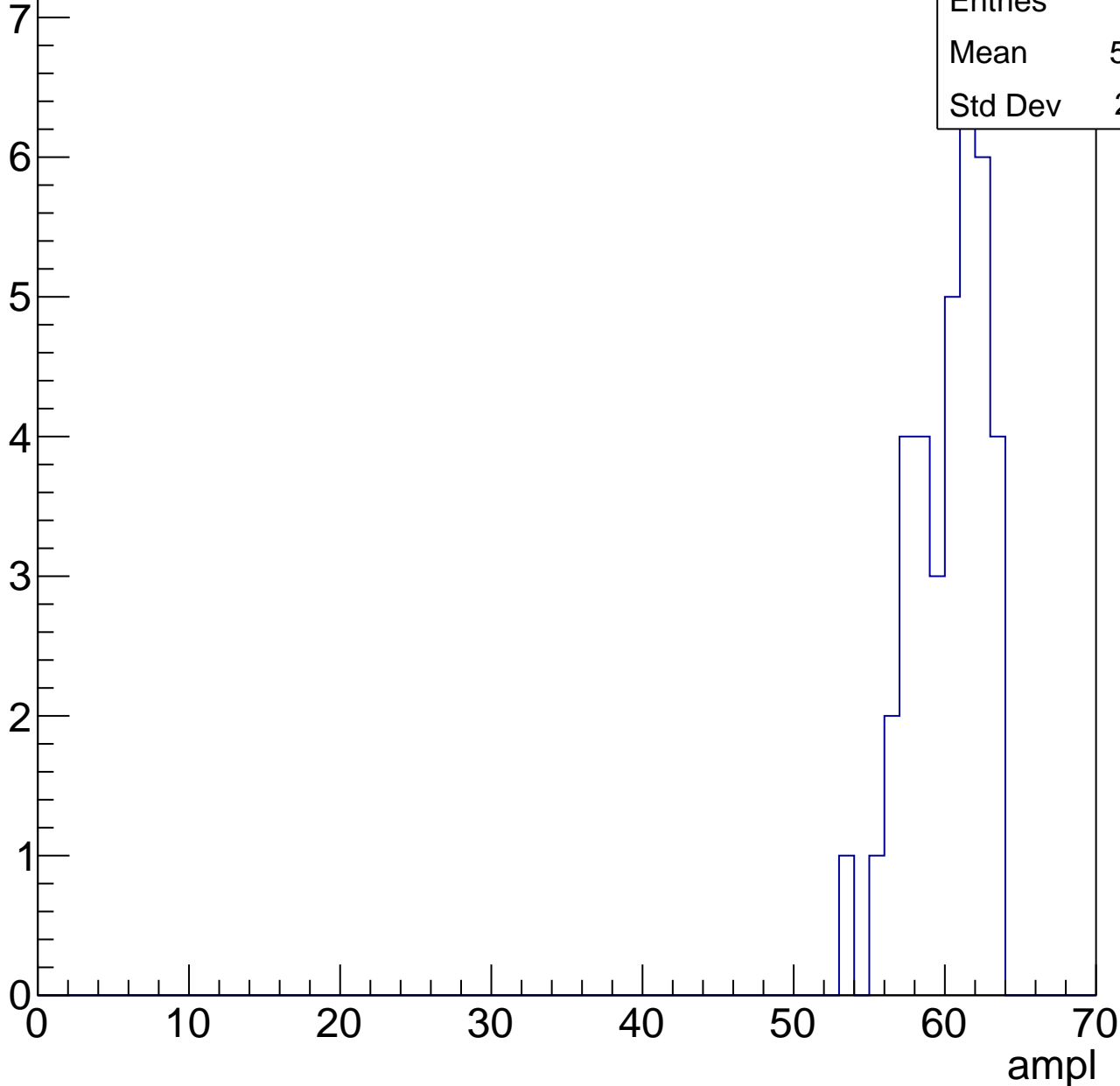


B0L001S, U14-ch49, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	37
Mean	59.68
Std Dev	2.461



B0L001S, U14-ch49, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch49, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch50, adc0

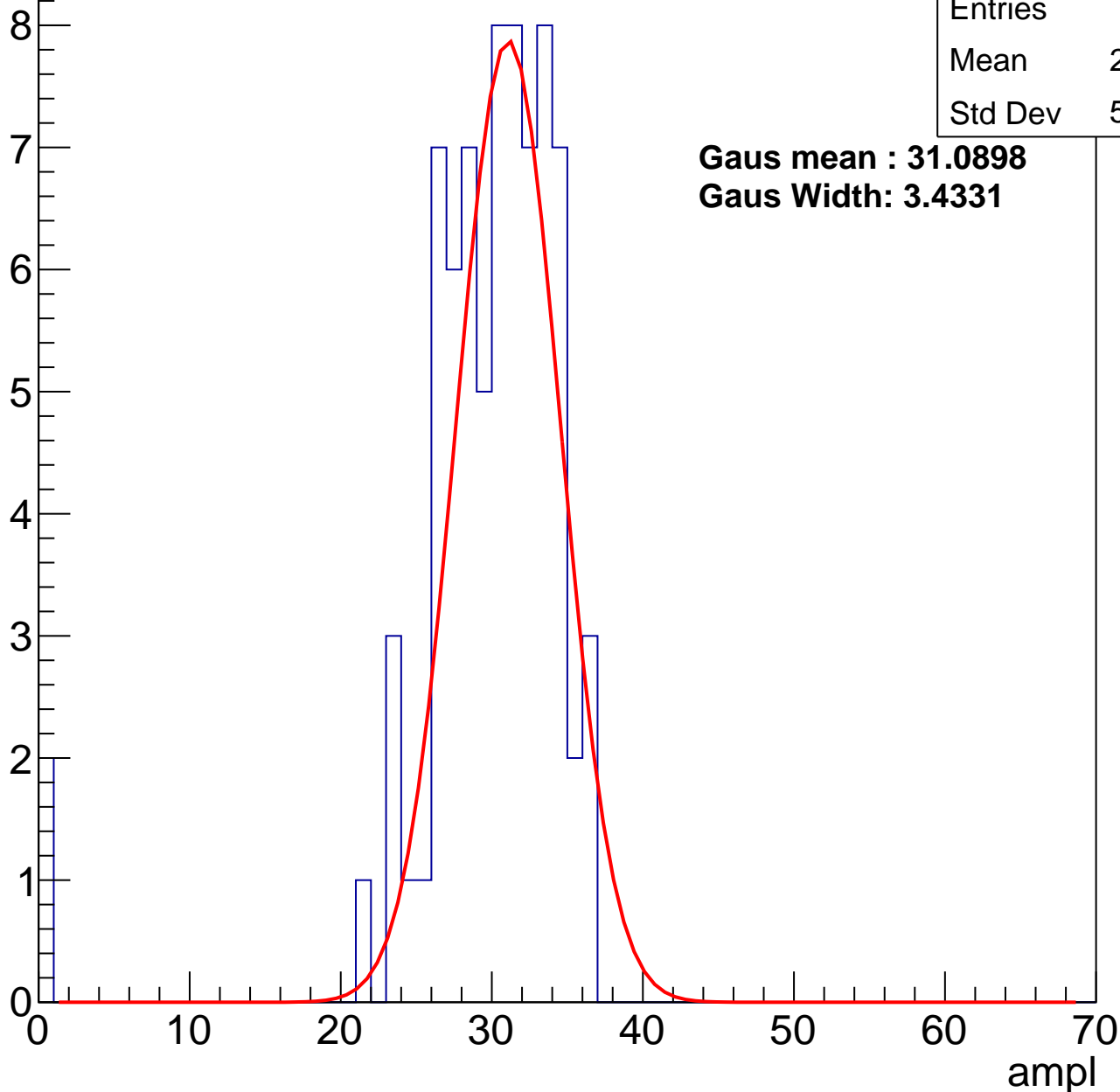
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	29.16
Std Dev	5.863

Gaus mean : 31.0898

Gaus Width: 3.4331



B0L001S, U14-ch50, adc1

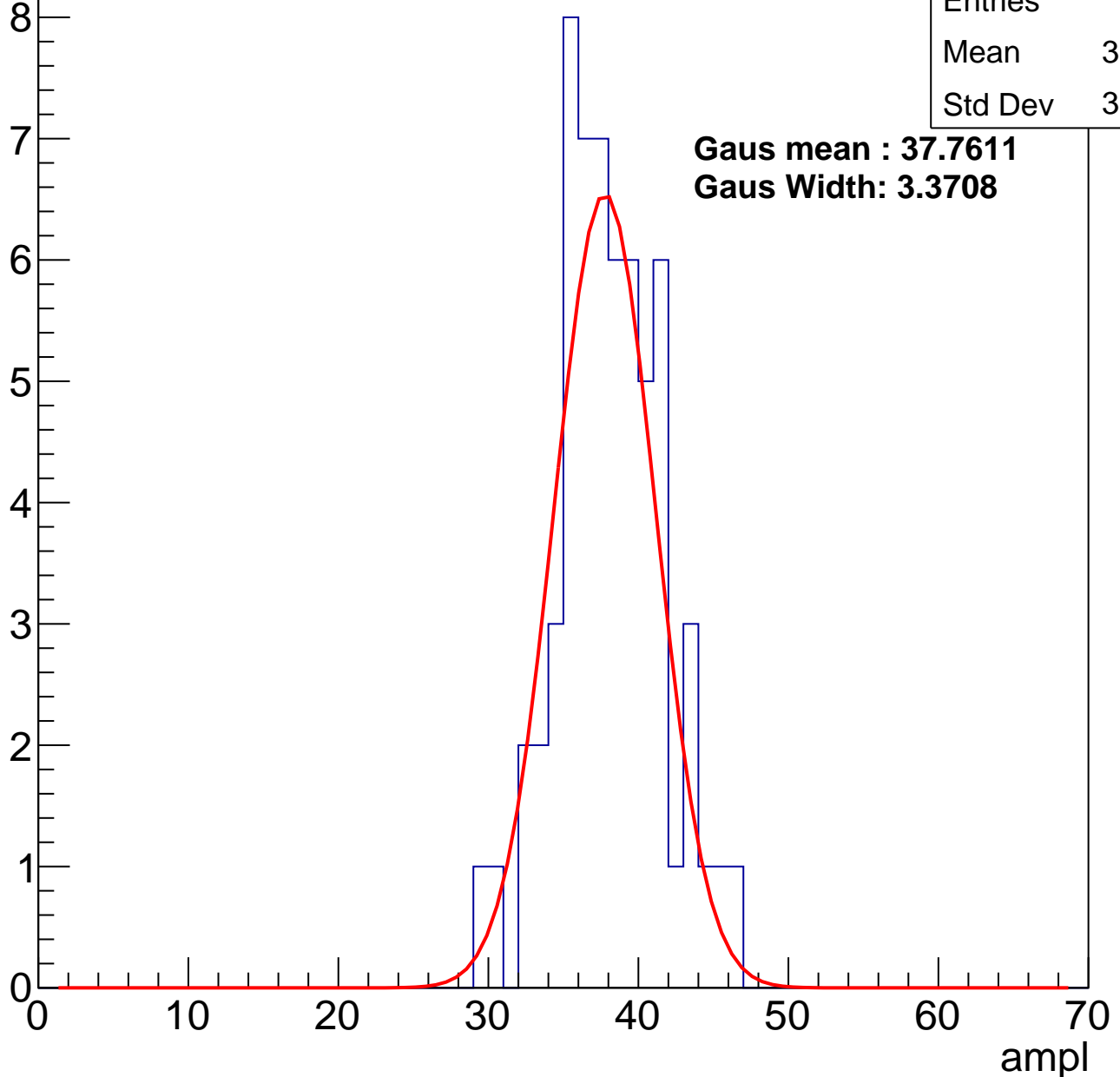
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	37.64
Std Dev	3.469

Gaus mean : 37.7611

Gaus Width: 3.3708



B0L001S, U14-ch50, adc2

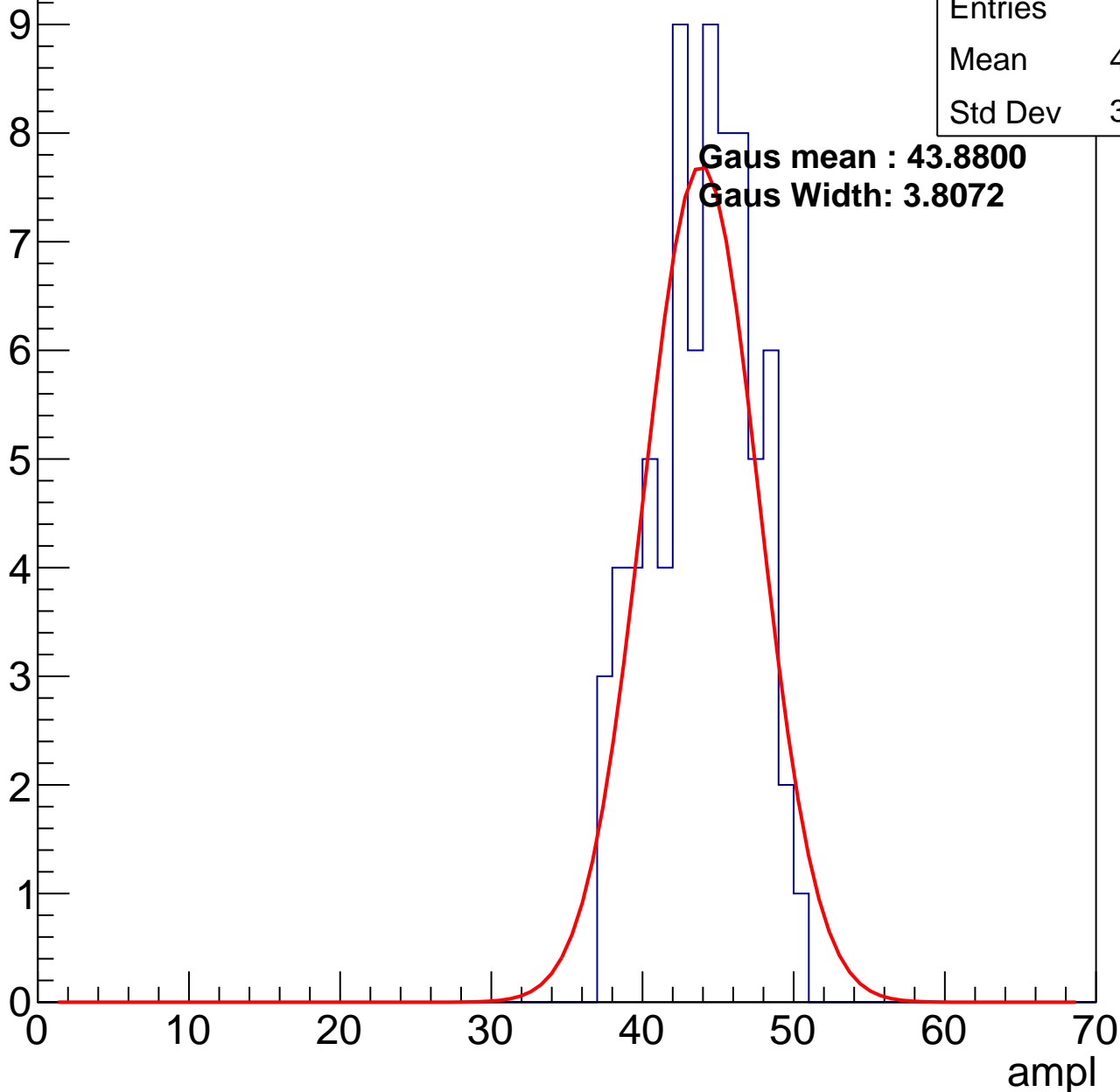
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	43.43
Std Dev	3.272

Gaus mean : 43.8800

Gaus Width: 3.8072

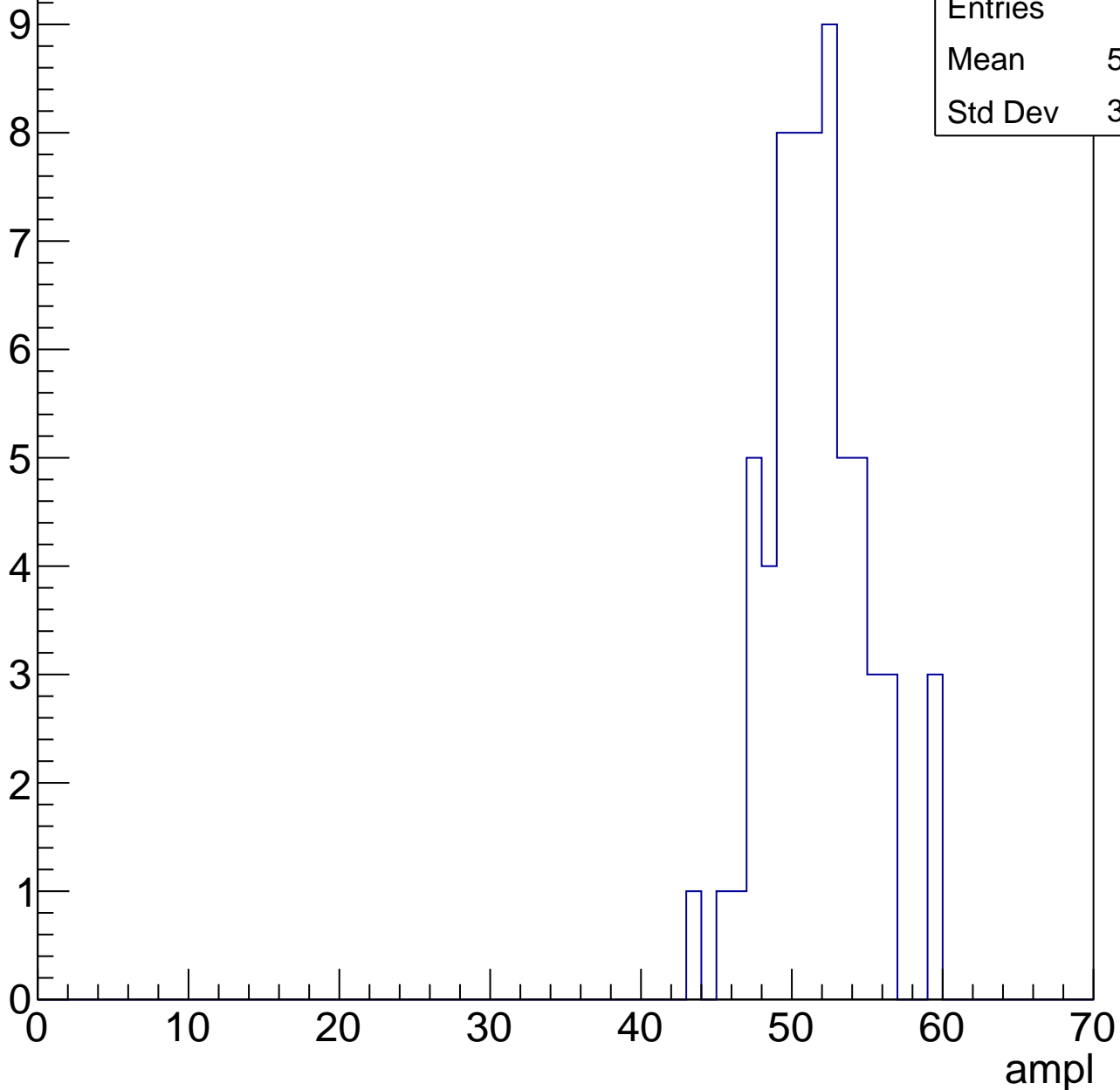


B0L001S, U14-ch50, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	51.16
Std Dev	3.232

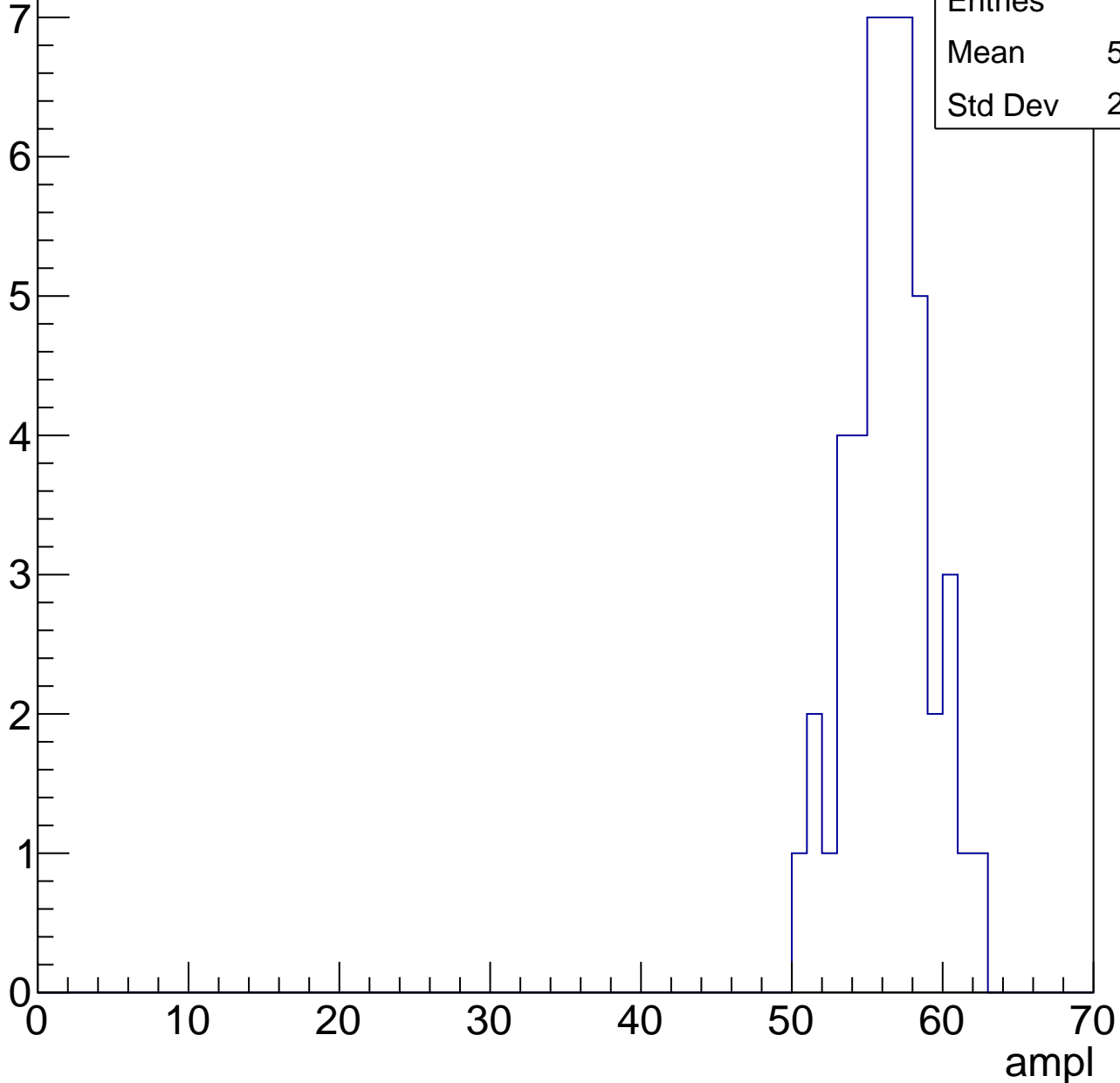


B0L001S, U14-ch50, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	55.98
Std Dev	2.646

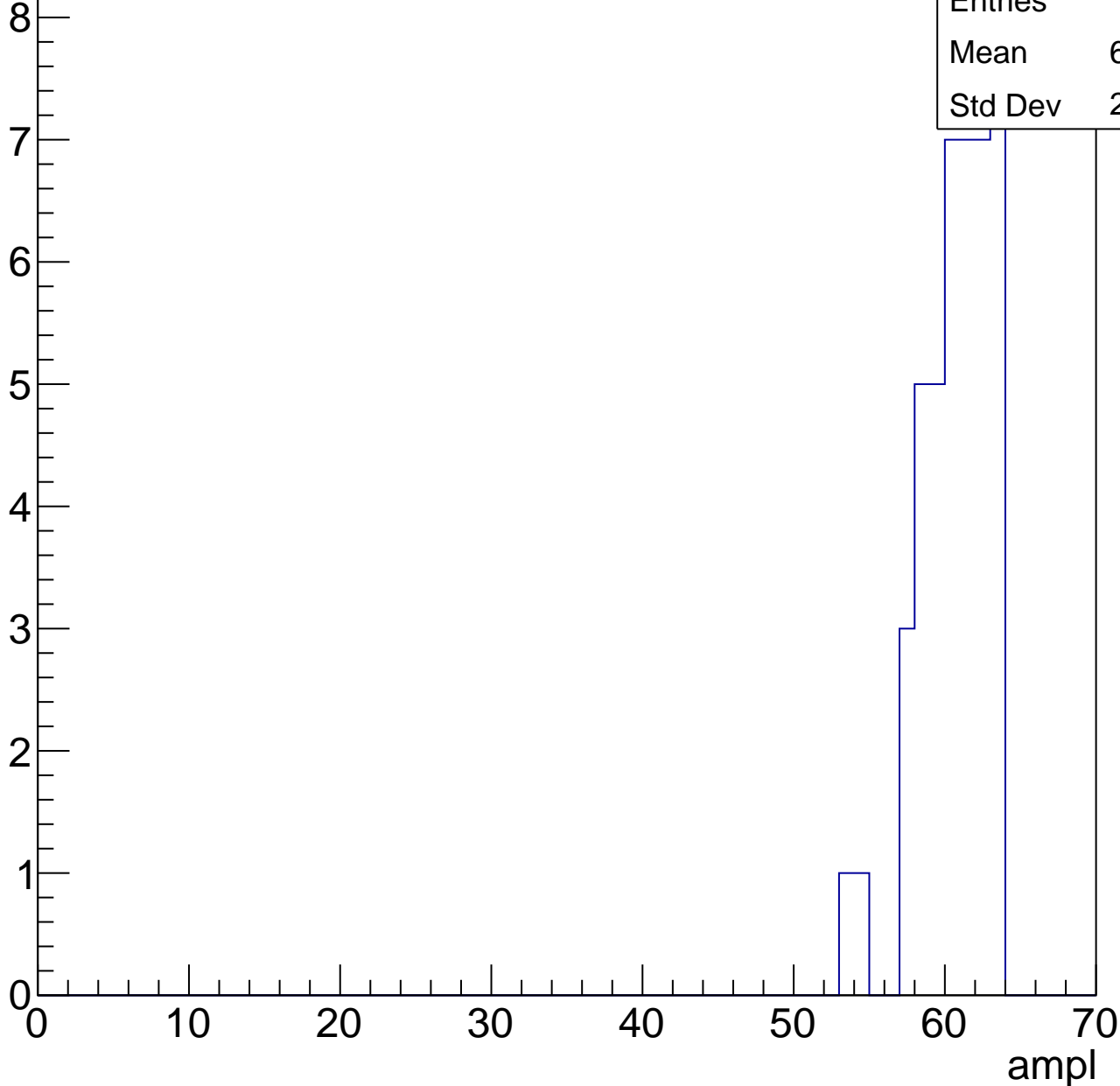


B0L001S, U14-ch50, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

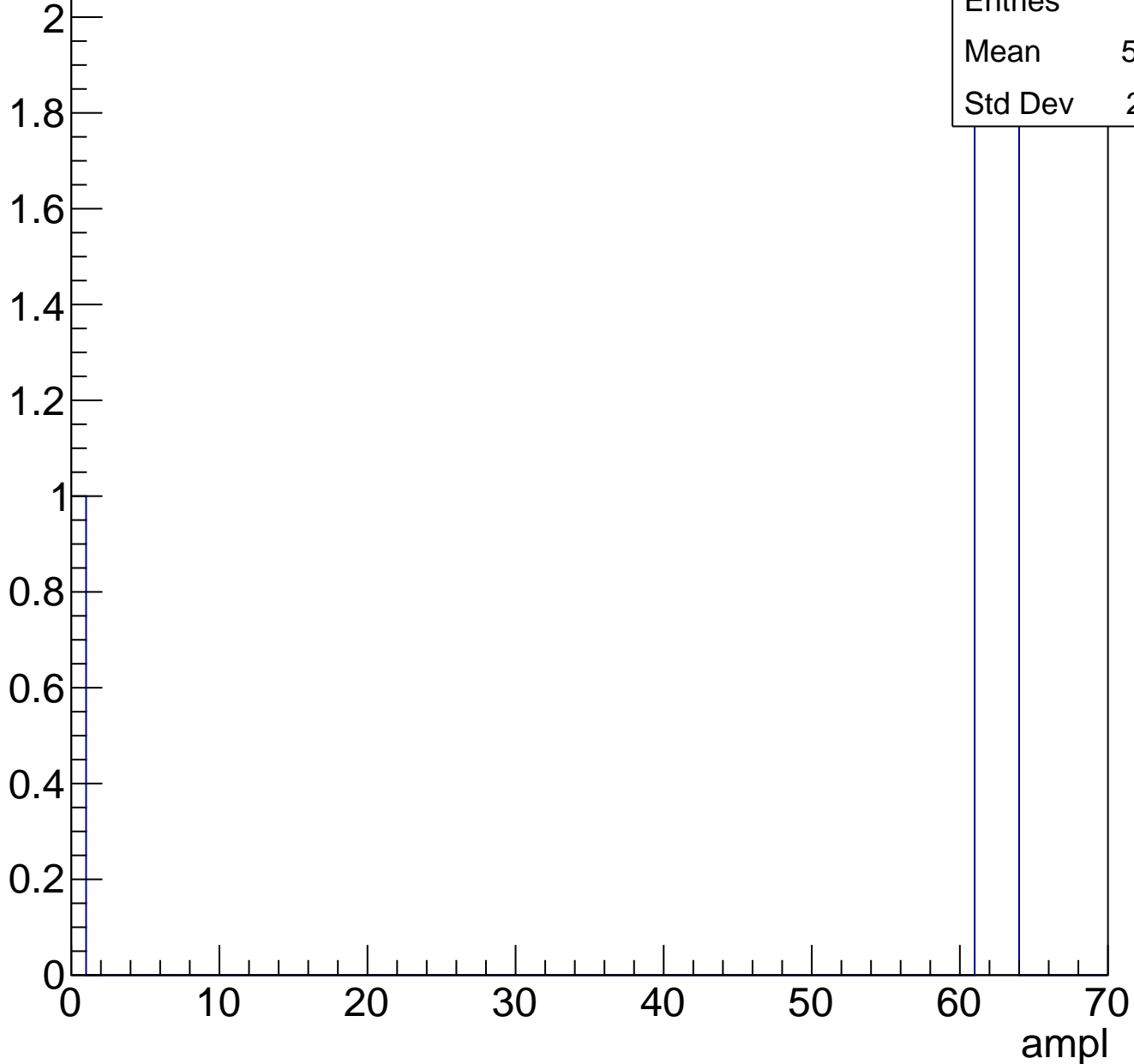
Entries	44
Mean	60.18
Std Dev	2.348



B0L001S, U14-ch50, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch50, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch51, adc0

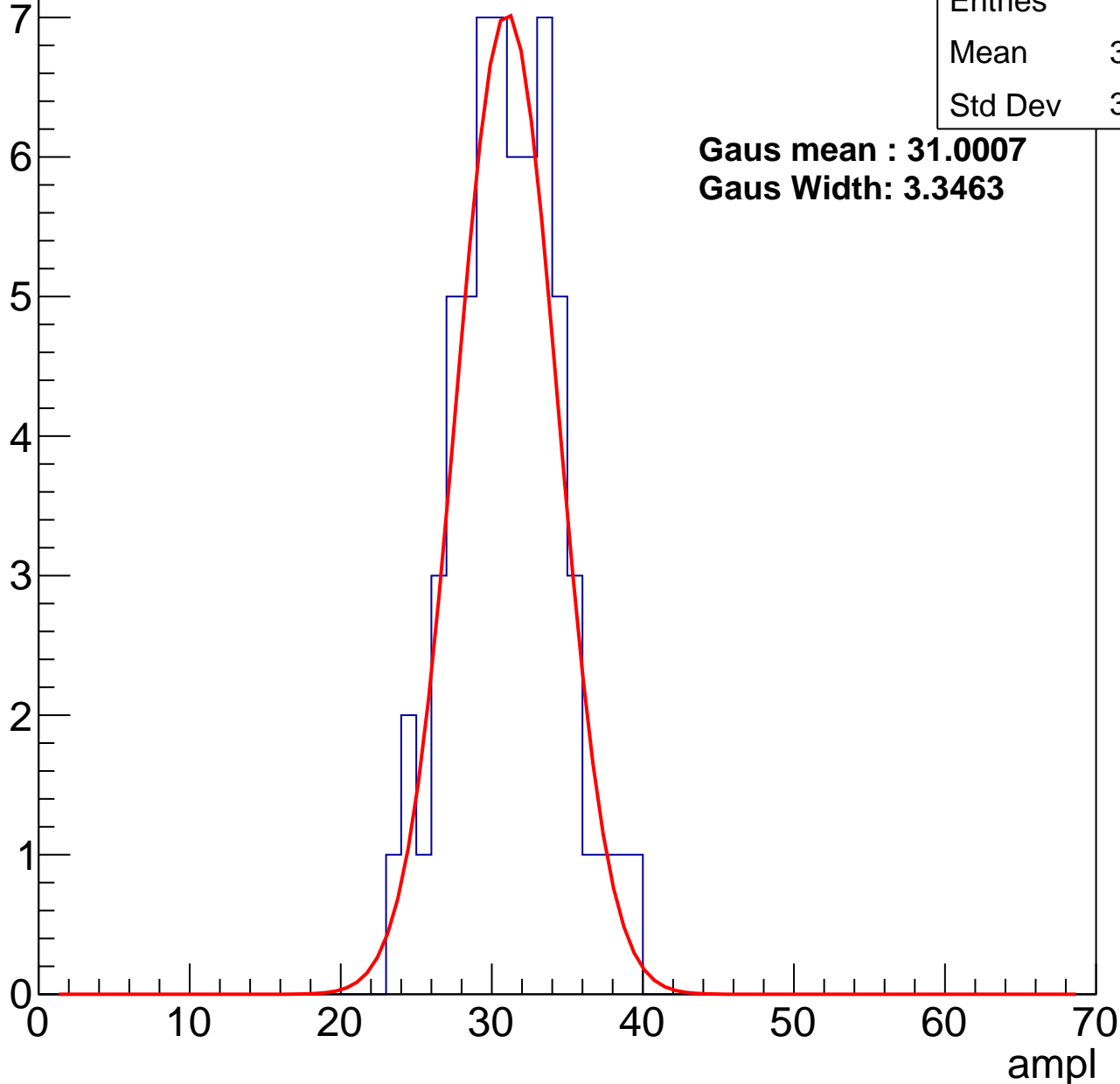
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.58
Std Dev	3.415

Gaus mean : 31.0007

Gaus Width: 3.3463



B0L001S, U14-ch51, adc1

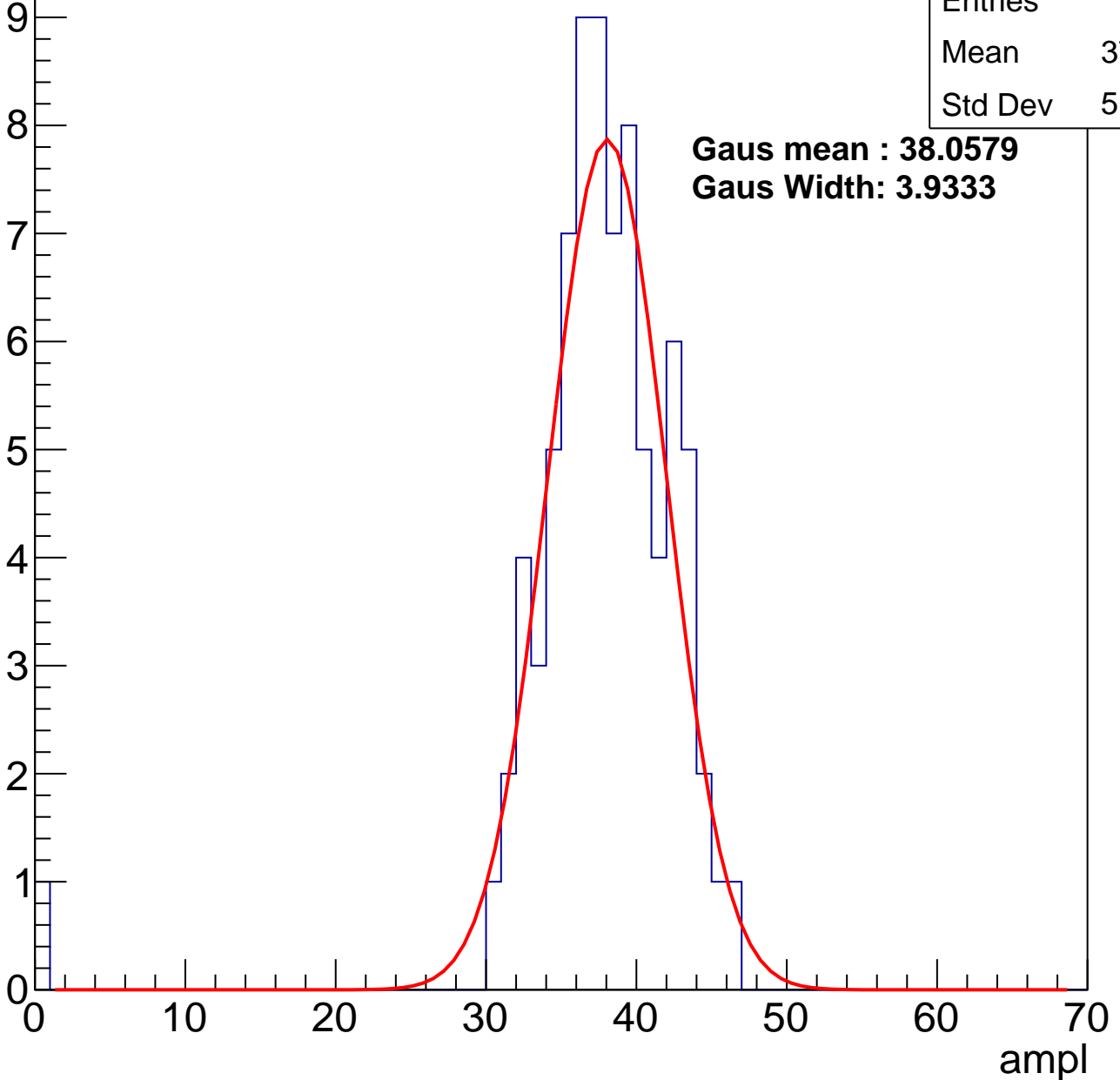
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	80
Mean	37.24
Std Dev	5.519

Gaus mean : 38.0579

Gaus Width: 3.9333



B0L001S, U14-ch51, adc2

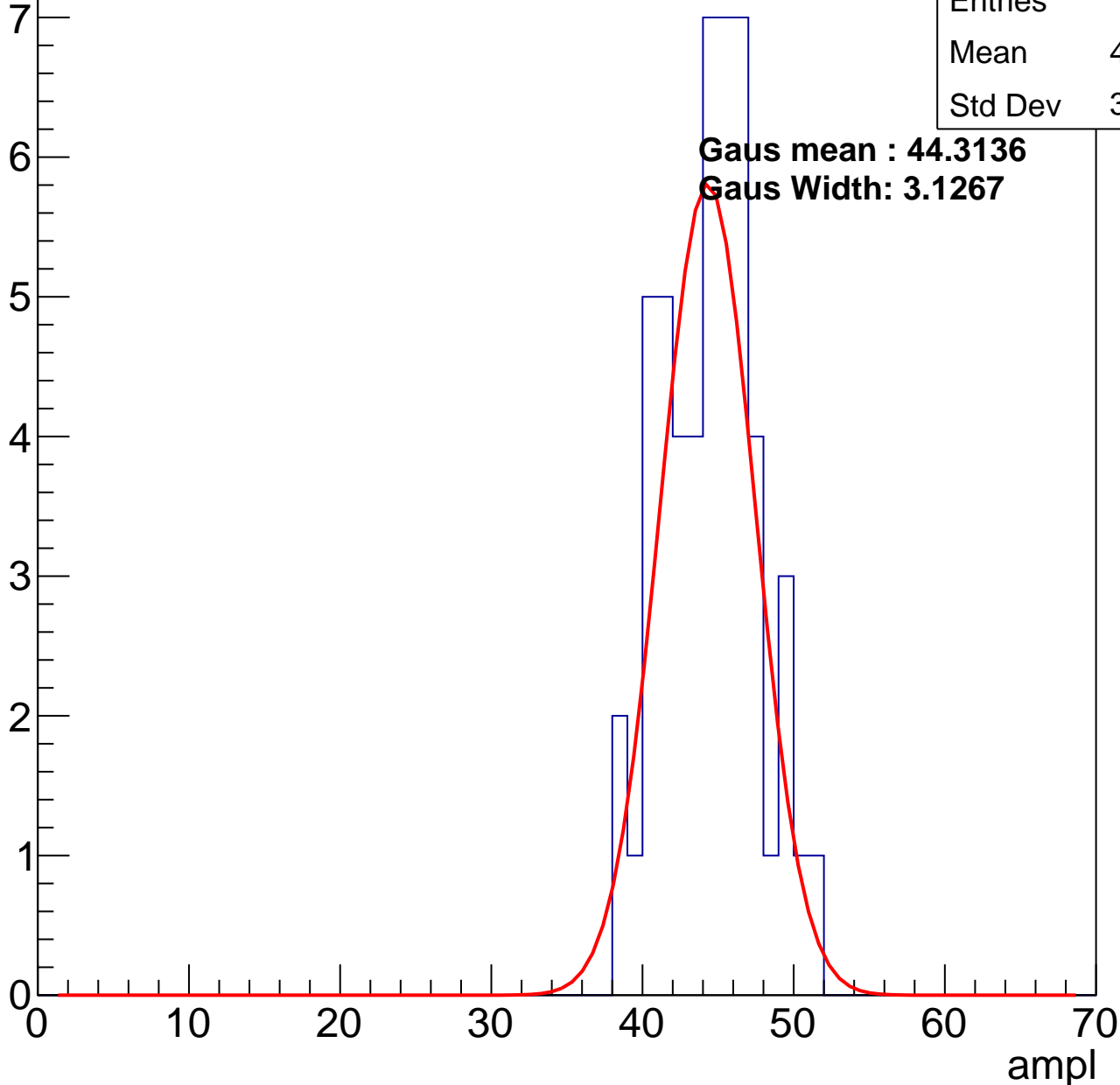
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	44.02
Std Dev	3.067

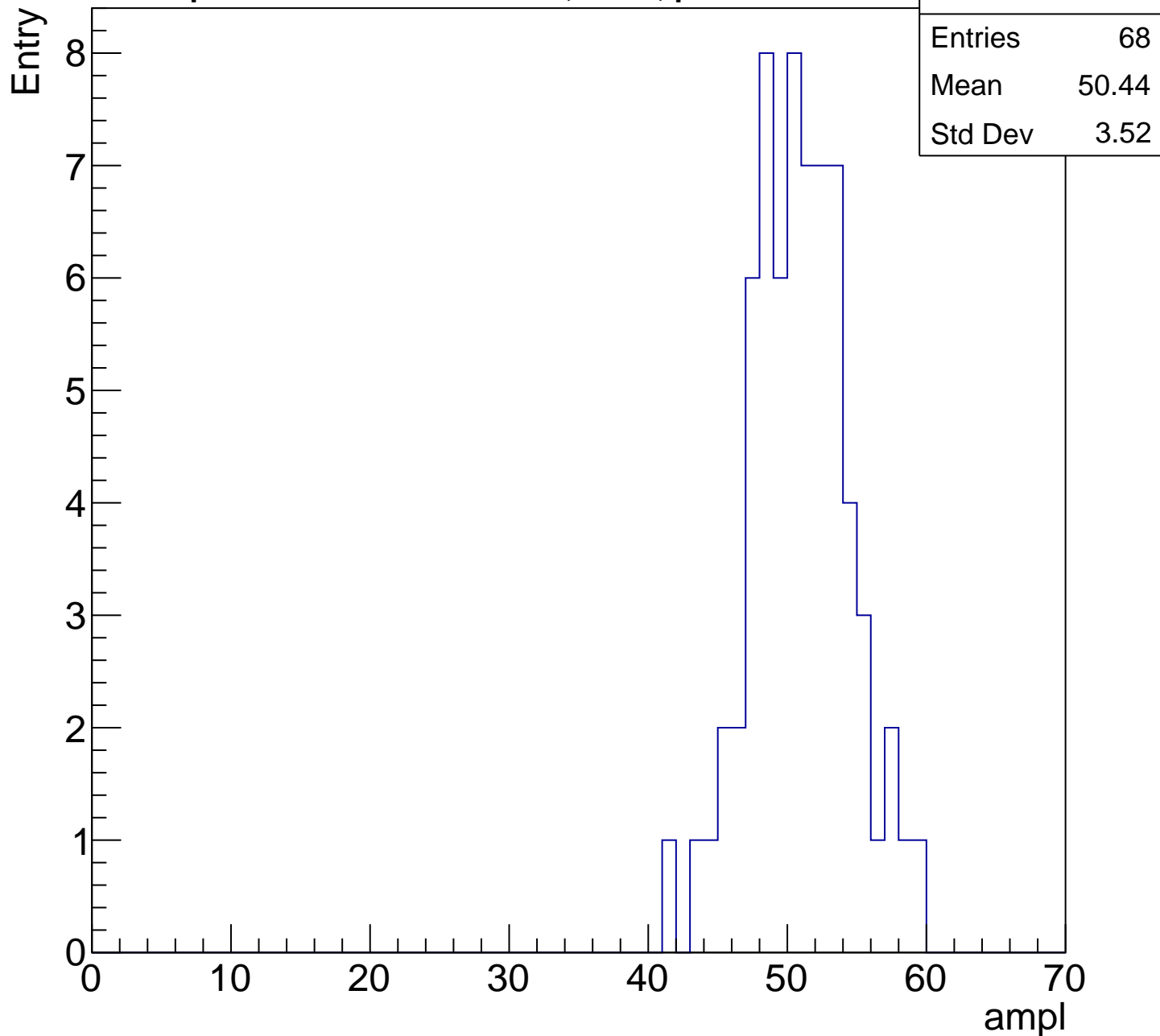
Gaus mean : 44.3136

Gaus Width: 3.1267



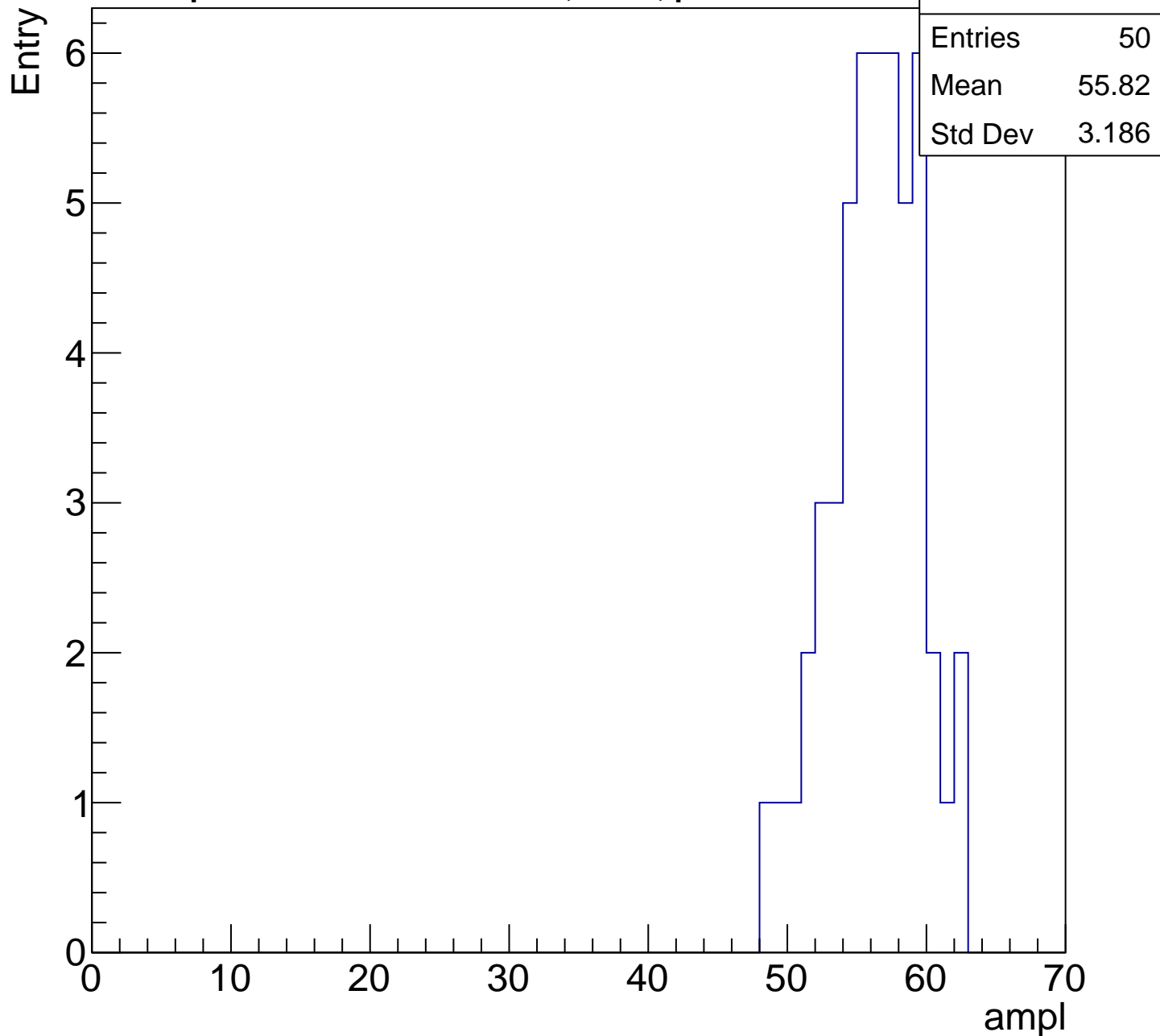
B0L001S, U14-ch51, adc3

calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U14-ch51, adc4

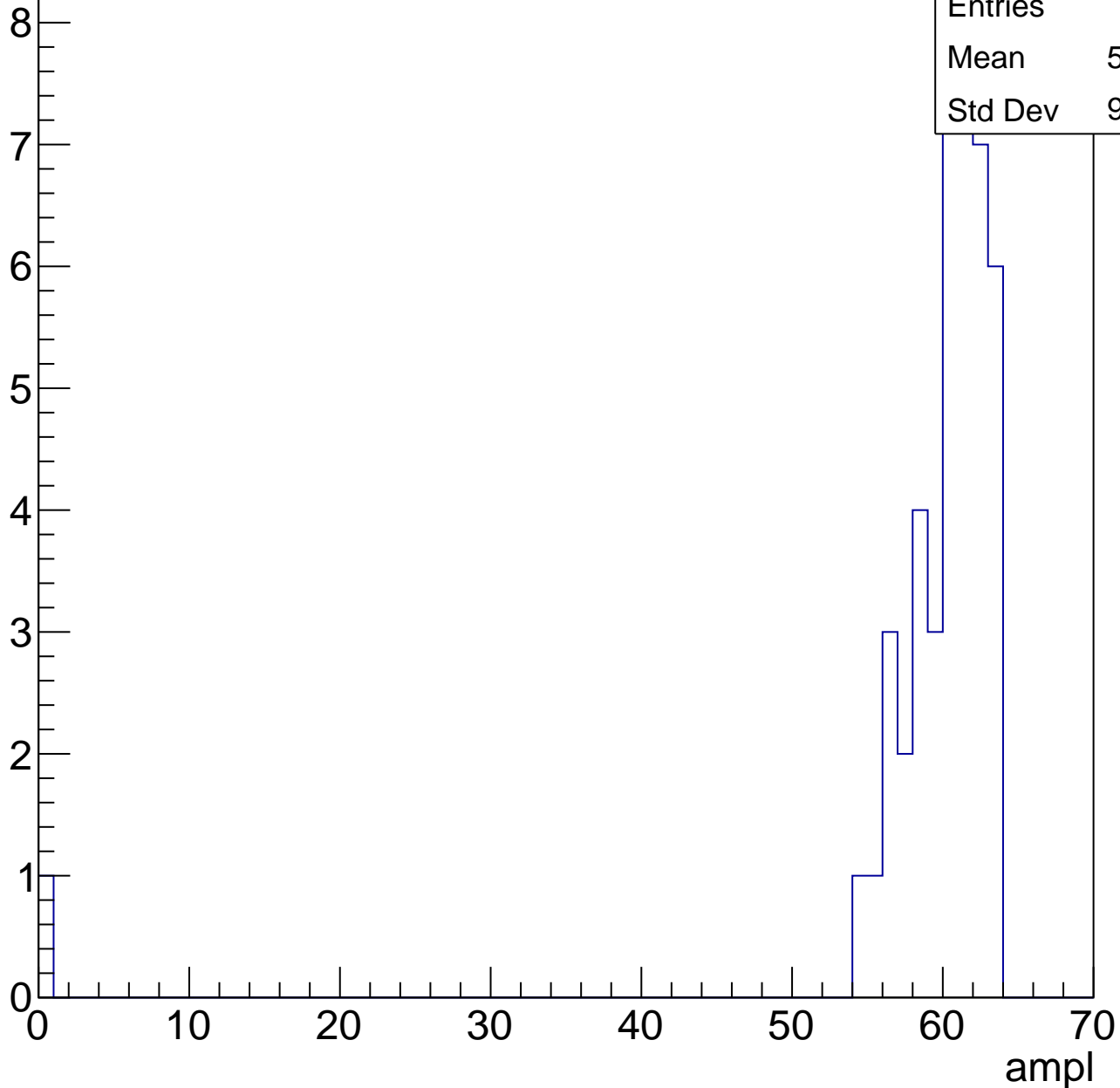
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U14-ch51, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch51, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch51, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch52, adc0

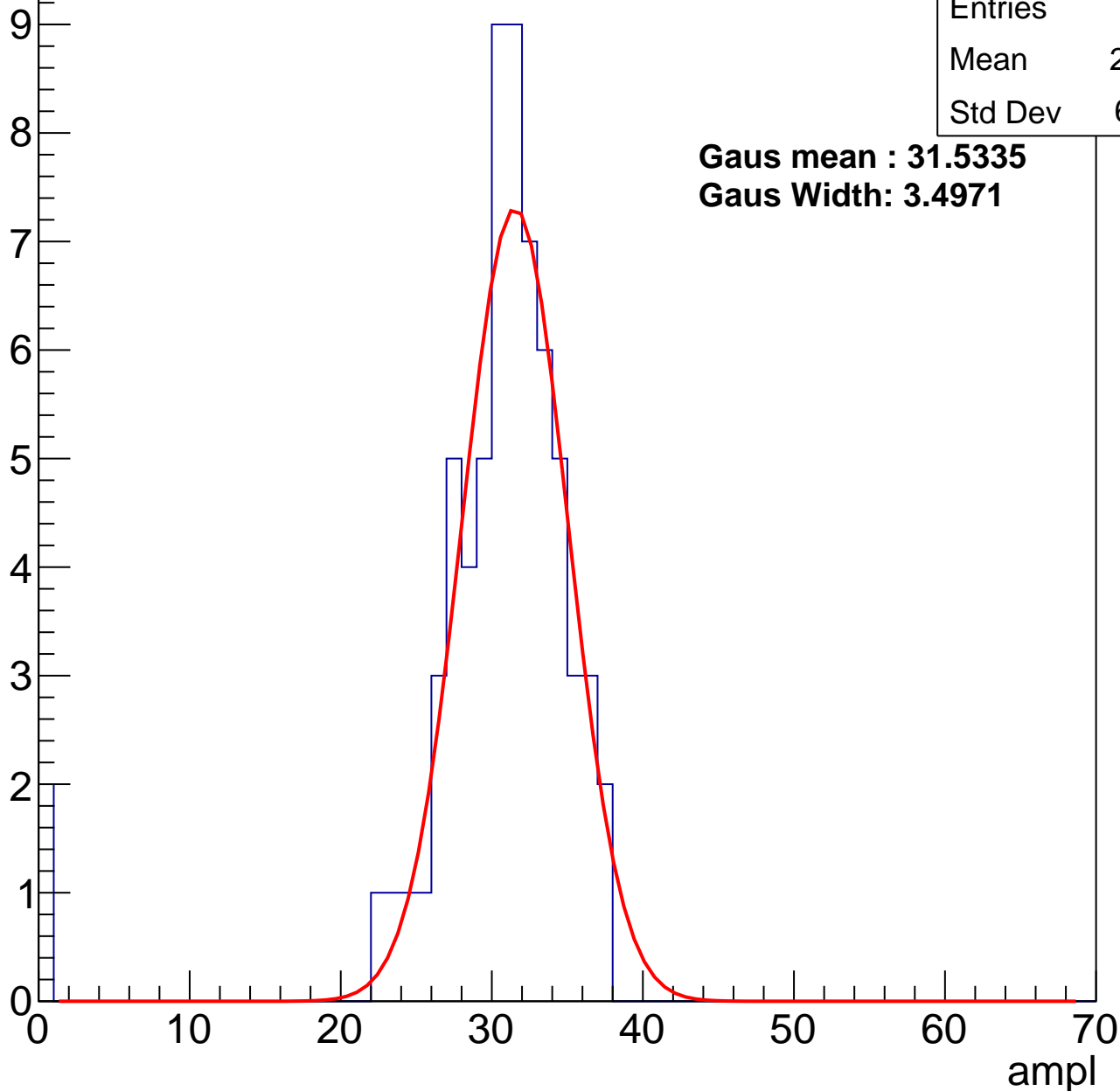
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.73
Std Dev	6.151

Gaus mean : 31.5335

Gaus Width: 3.4971



B0L001S, U14-ch52, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	37.34
Std Dev	3.695

Gaus mean : 38.1786

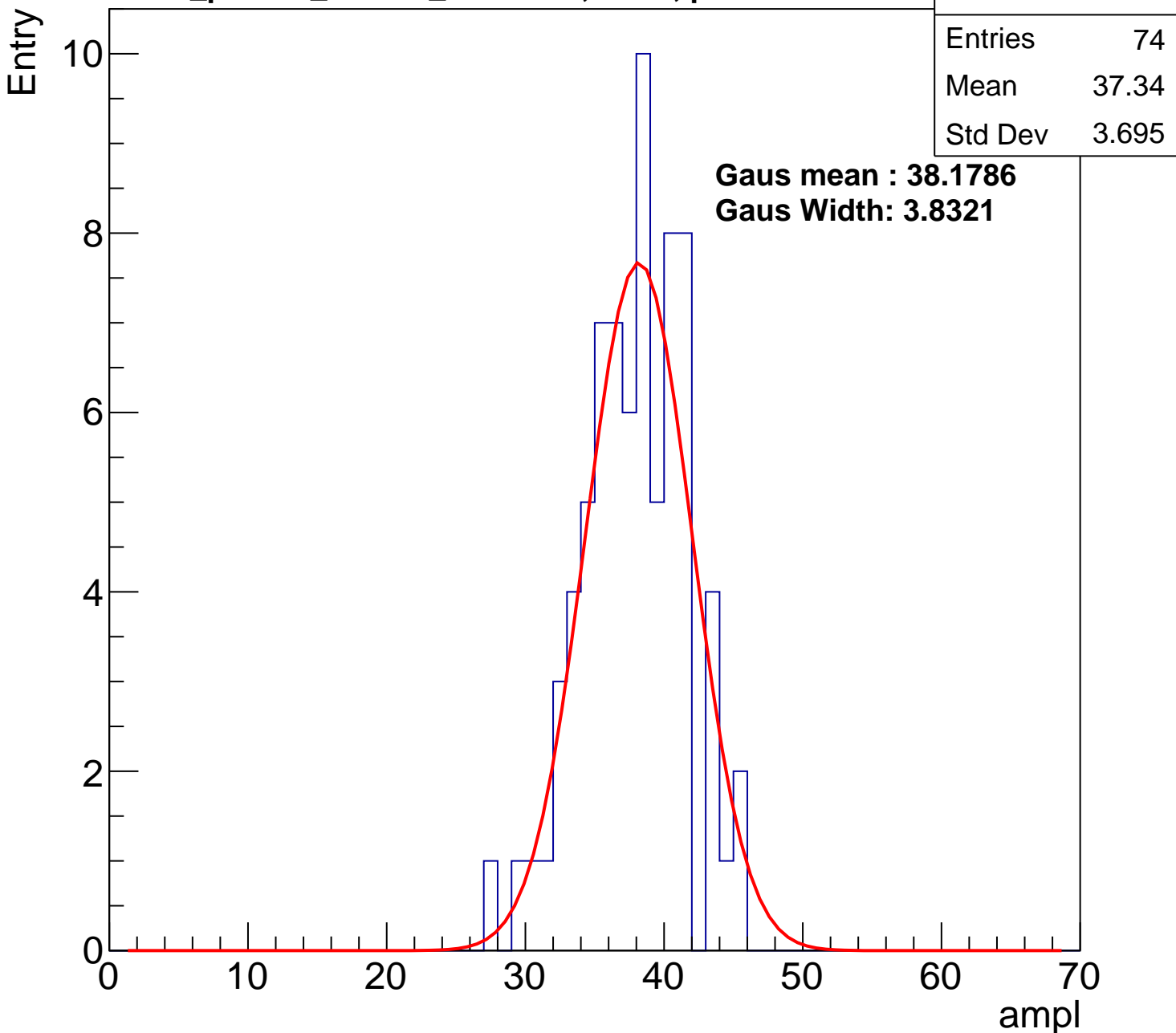
Gaus Width: 3.8321

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

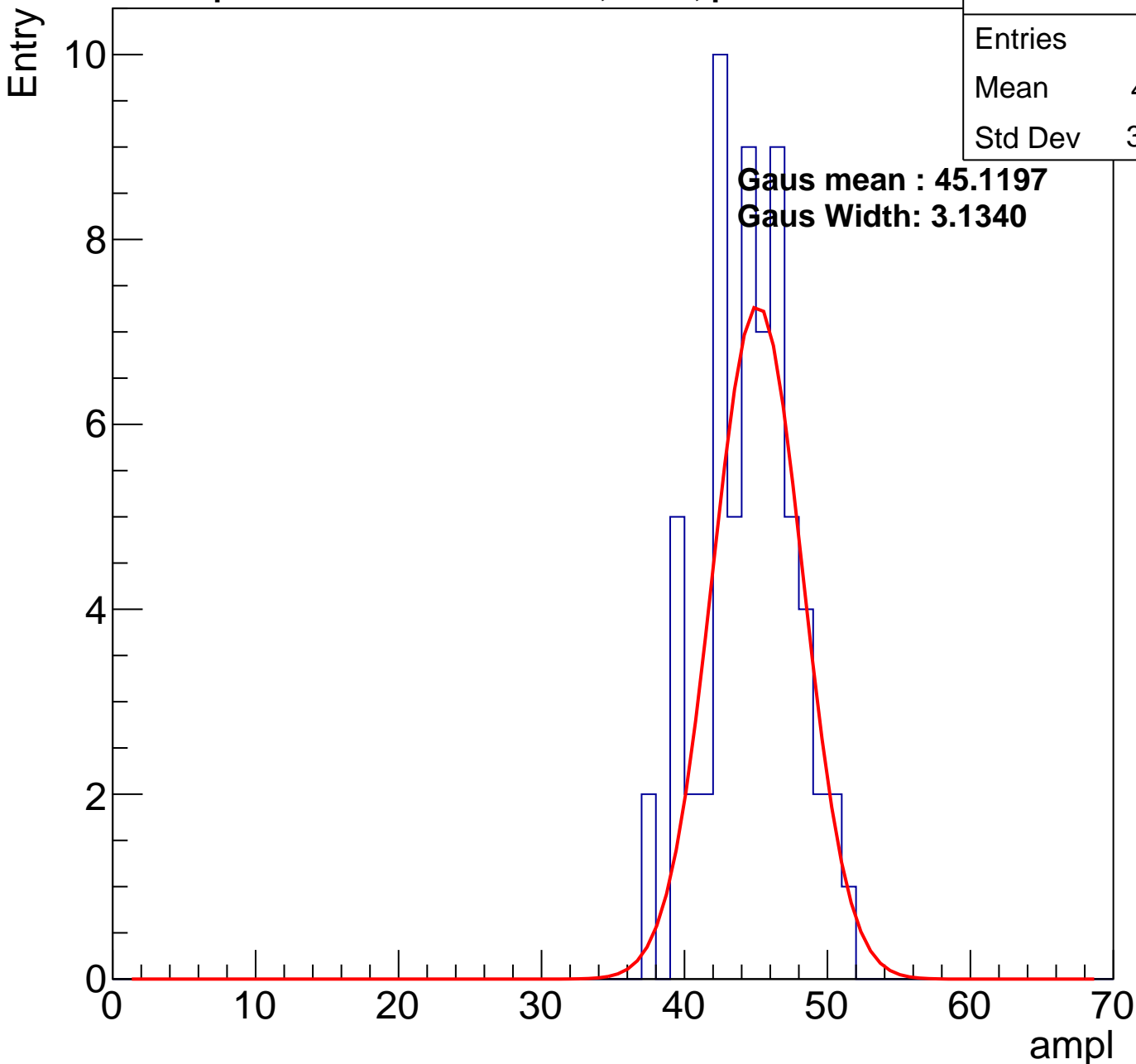


B0L001S, U14-ch52, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	44.11
Std Dev	3.138

Gaus mean : 45.1197
Gaus Width: 3.1340

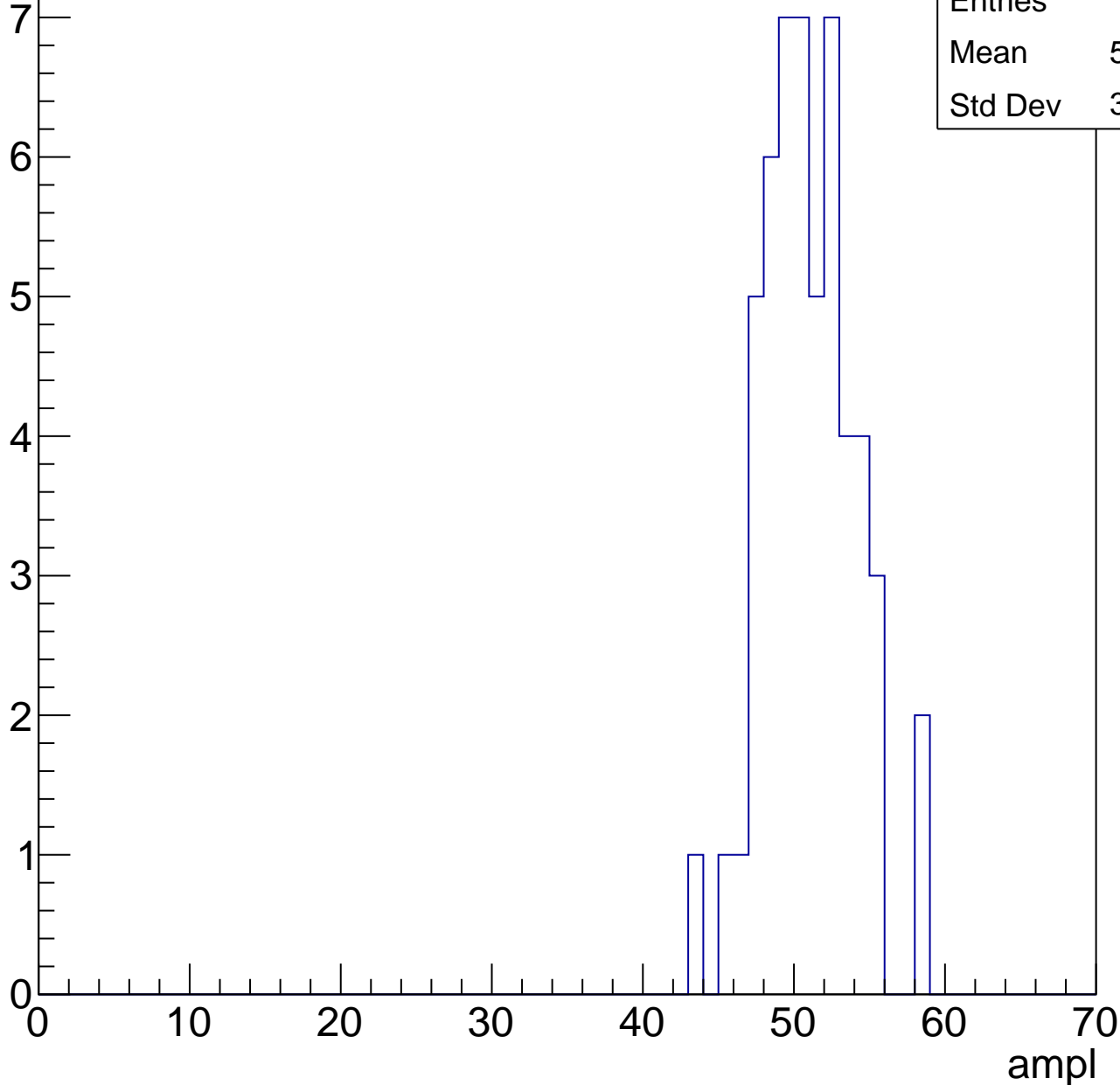


B0L001S, U14-ch52, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	50.53
Std Dev	3.032

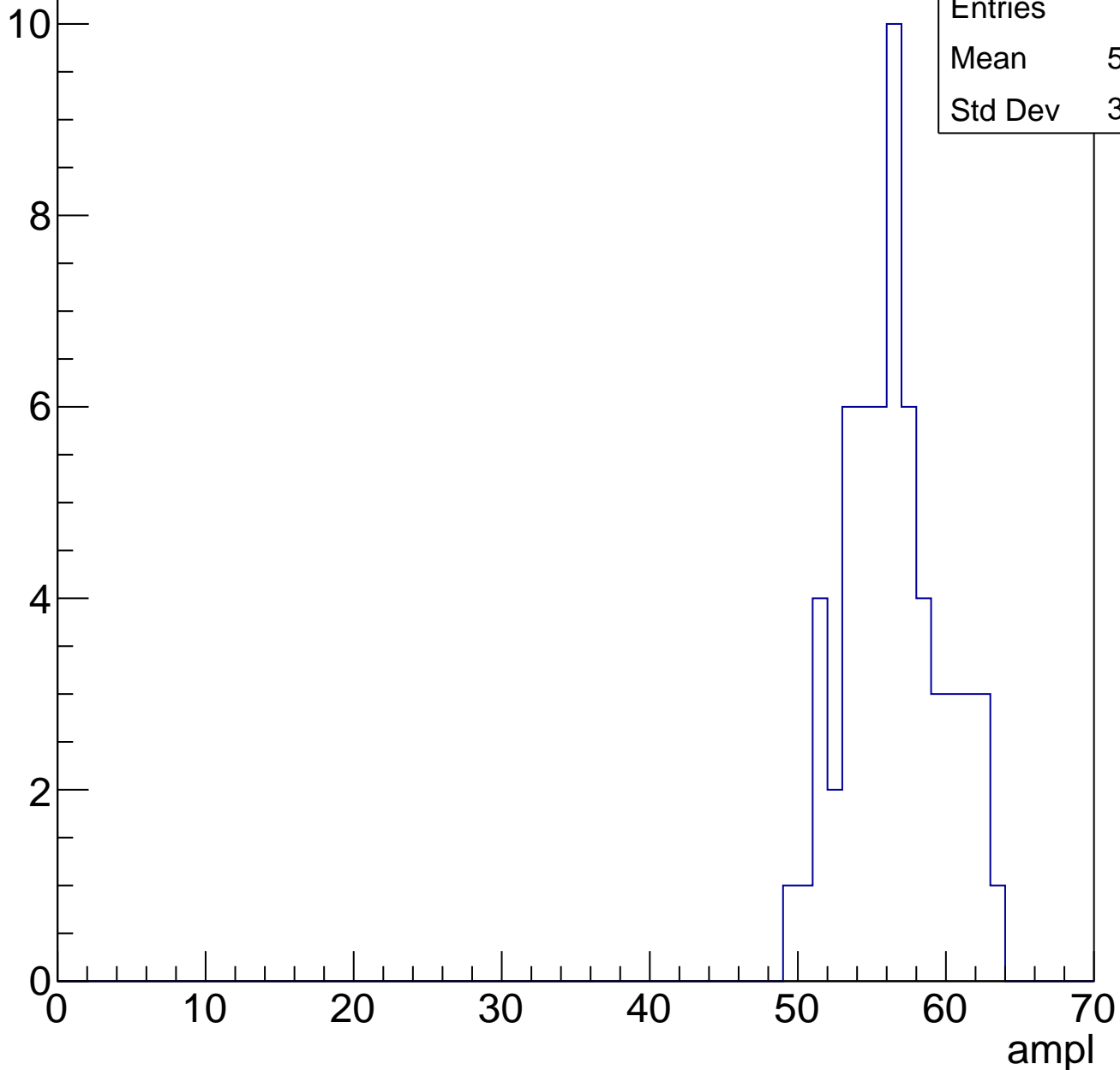


B0L001S, U14-ch52, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	55.97
Std Dev	3.268

Entry

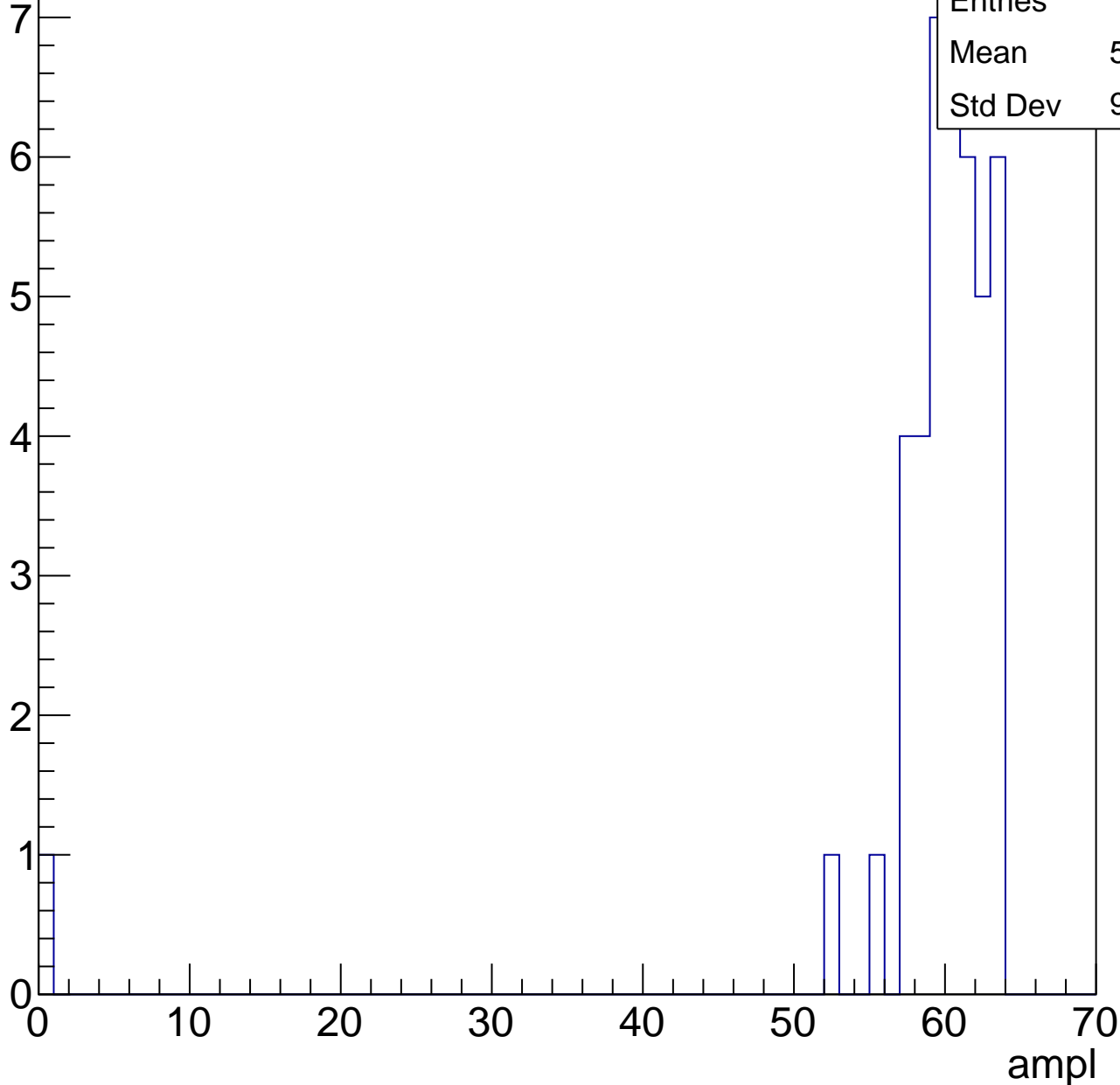


B0L001S, U14-ch52, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

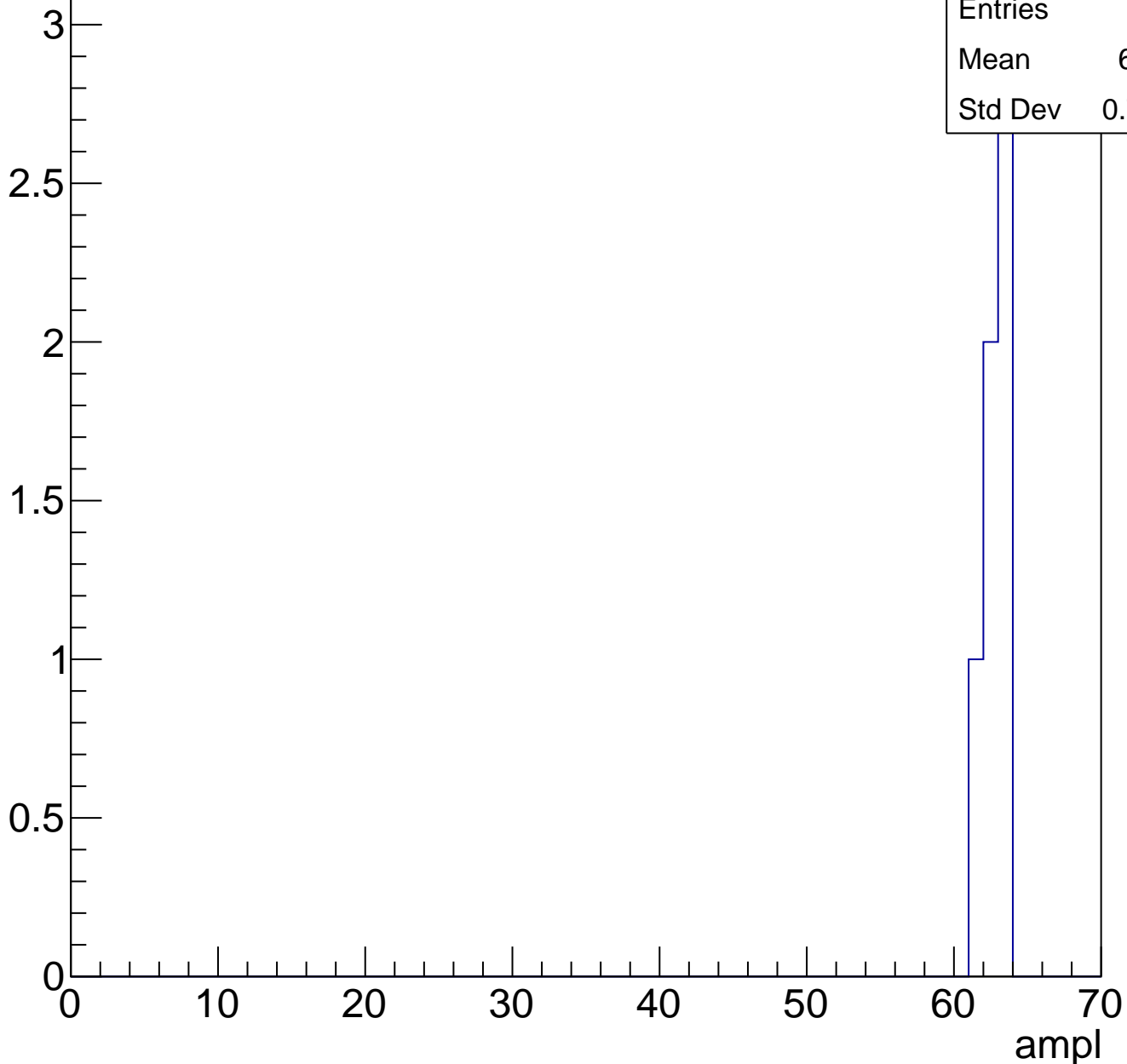
Entries	42
Mean	58.43
Std Dev	9.417



B0L001S, U14-ch52, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch52, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch53, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	30.02
Std Dev	5.059

Gaus mean : 30.4726

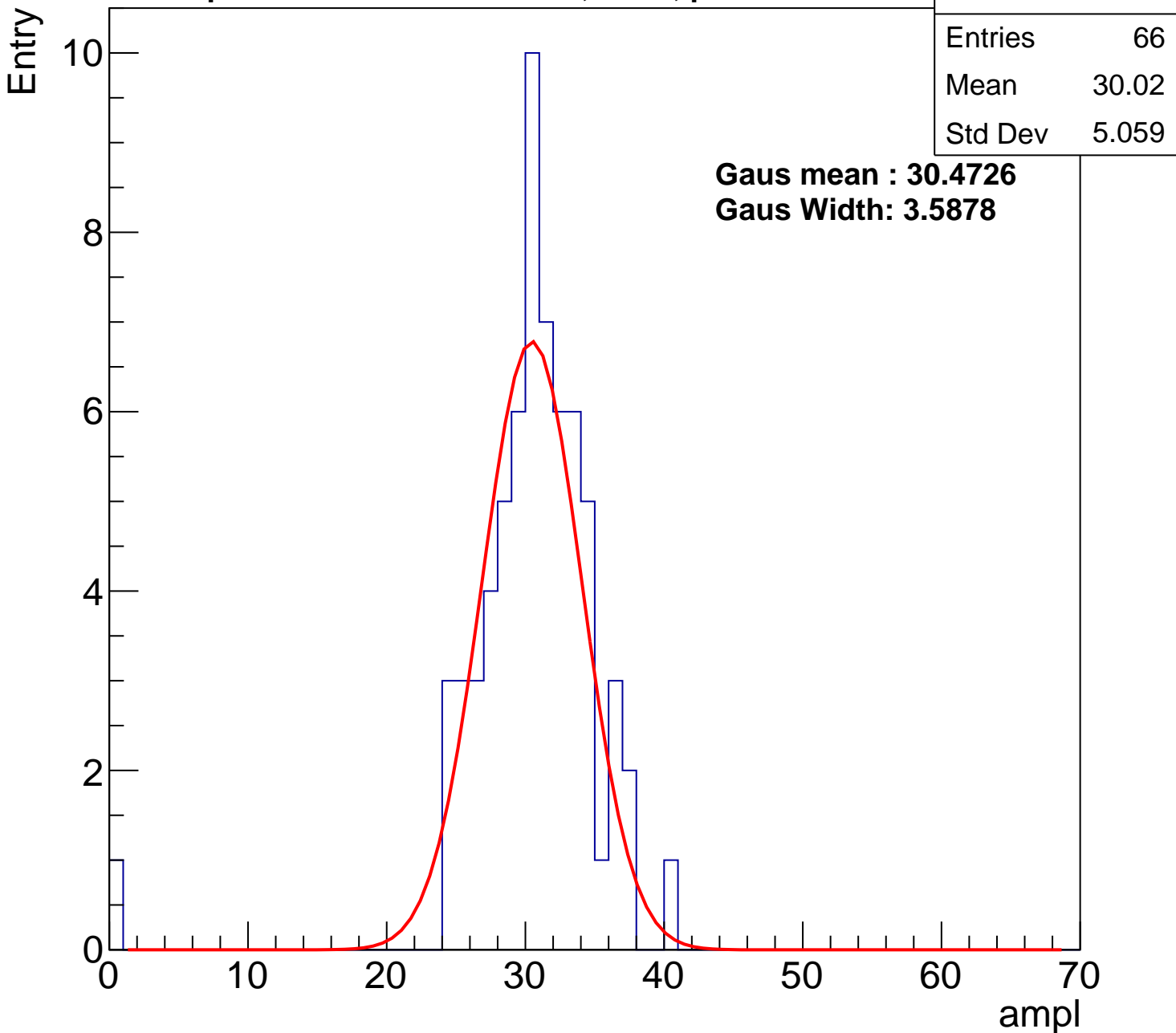
Gaus Width: 3.5878

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch53, adc1

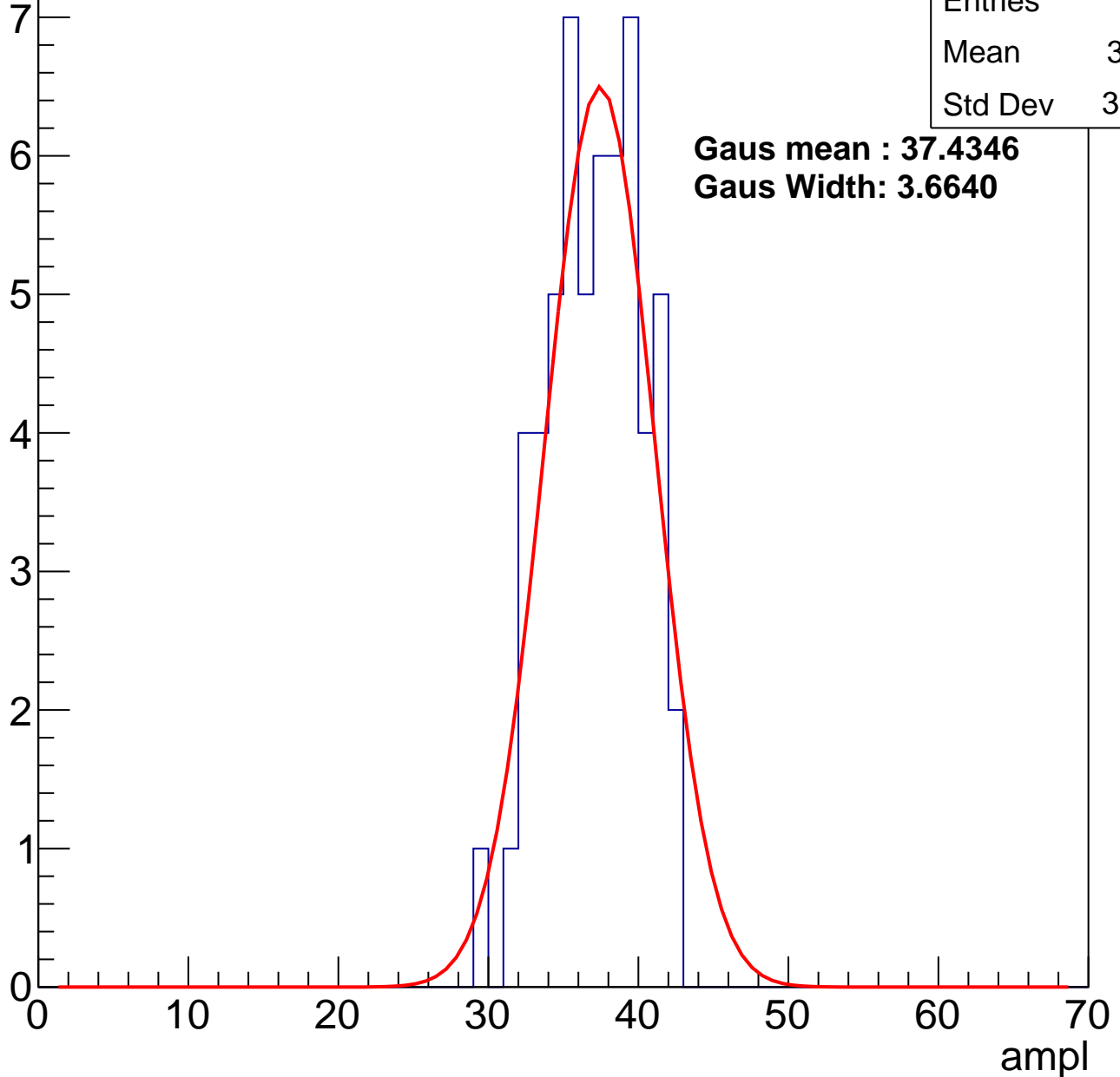
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	36.61
Std Dev	3.059

Gaus mean : 37.4346

Gaus Width: 3.6640



B0L001S, U14-ch53, adc2

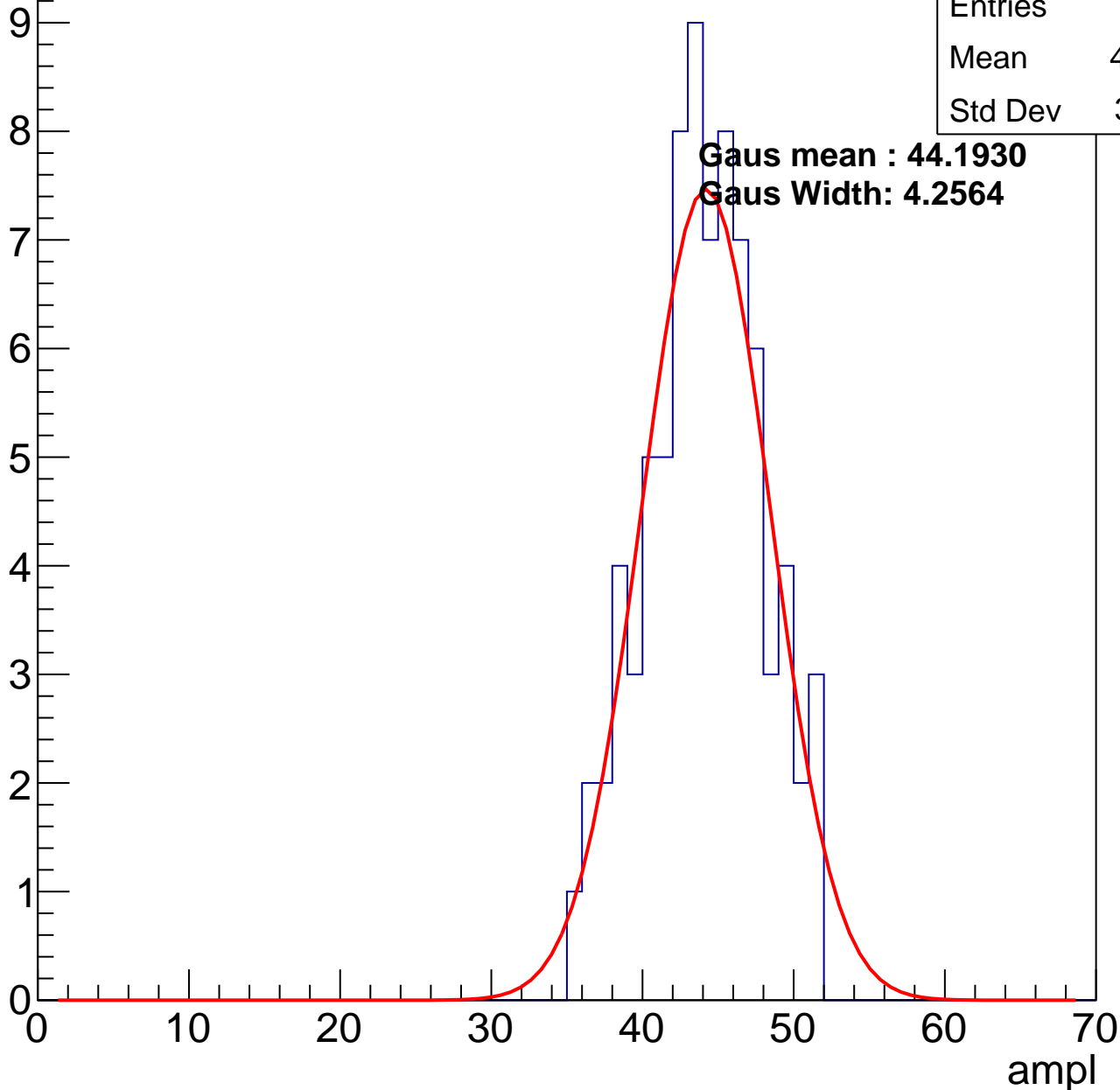
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	43.58
Std Dev	3.791

Gaus mean : 44.1930

Gaus Width: 4.2564

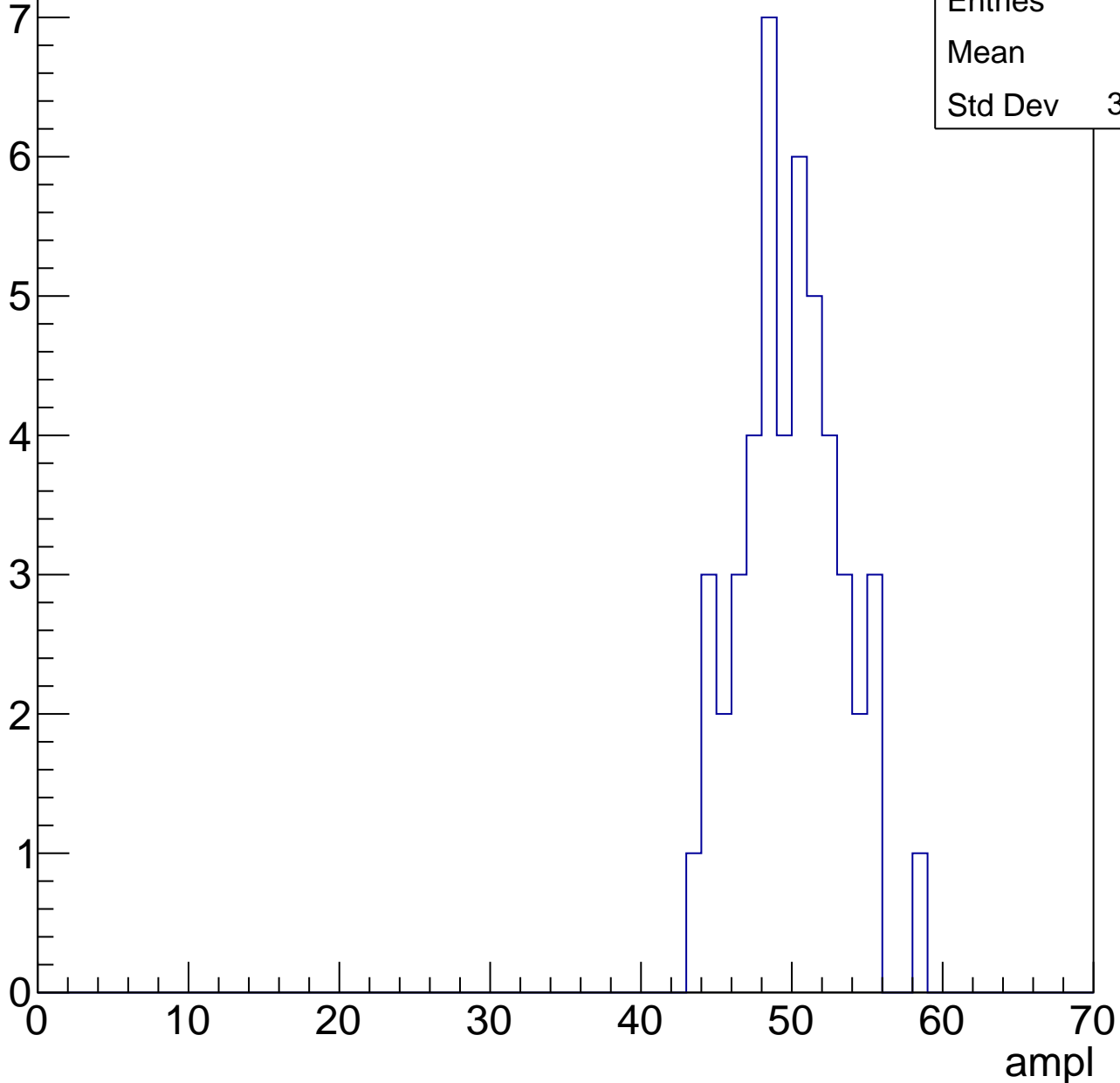


B0L001S, U14-ch53, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

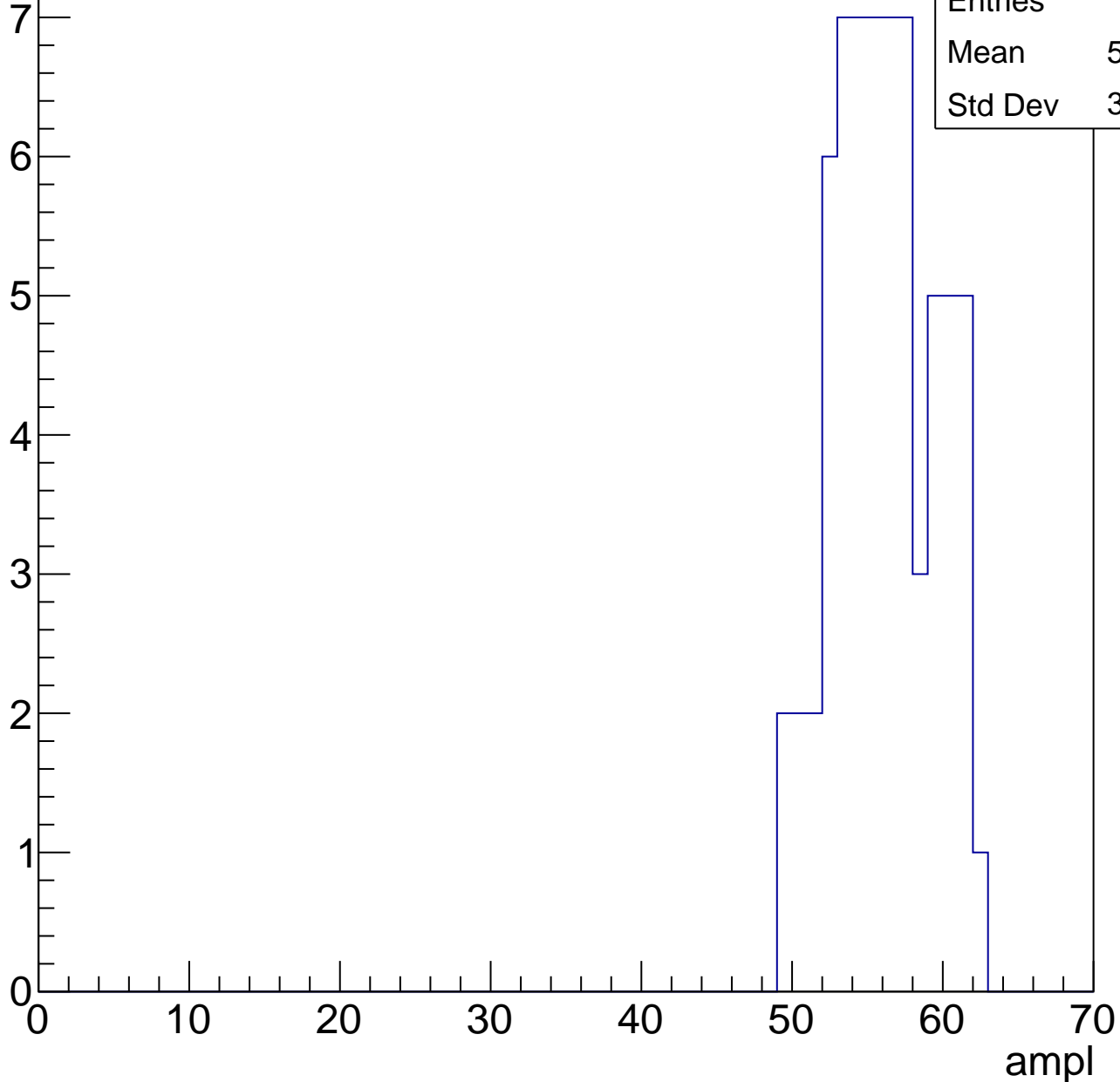
Entries	48
Mean	49.5
Std Dev	3.323



B0L001S, U14-ch53, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



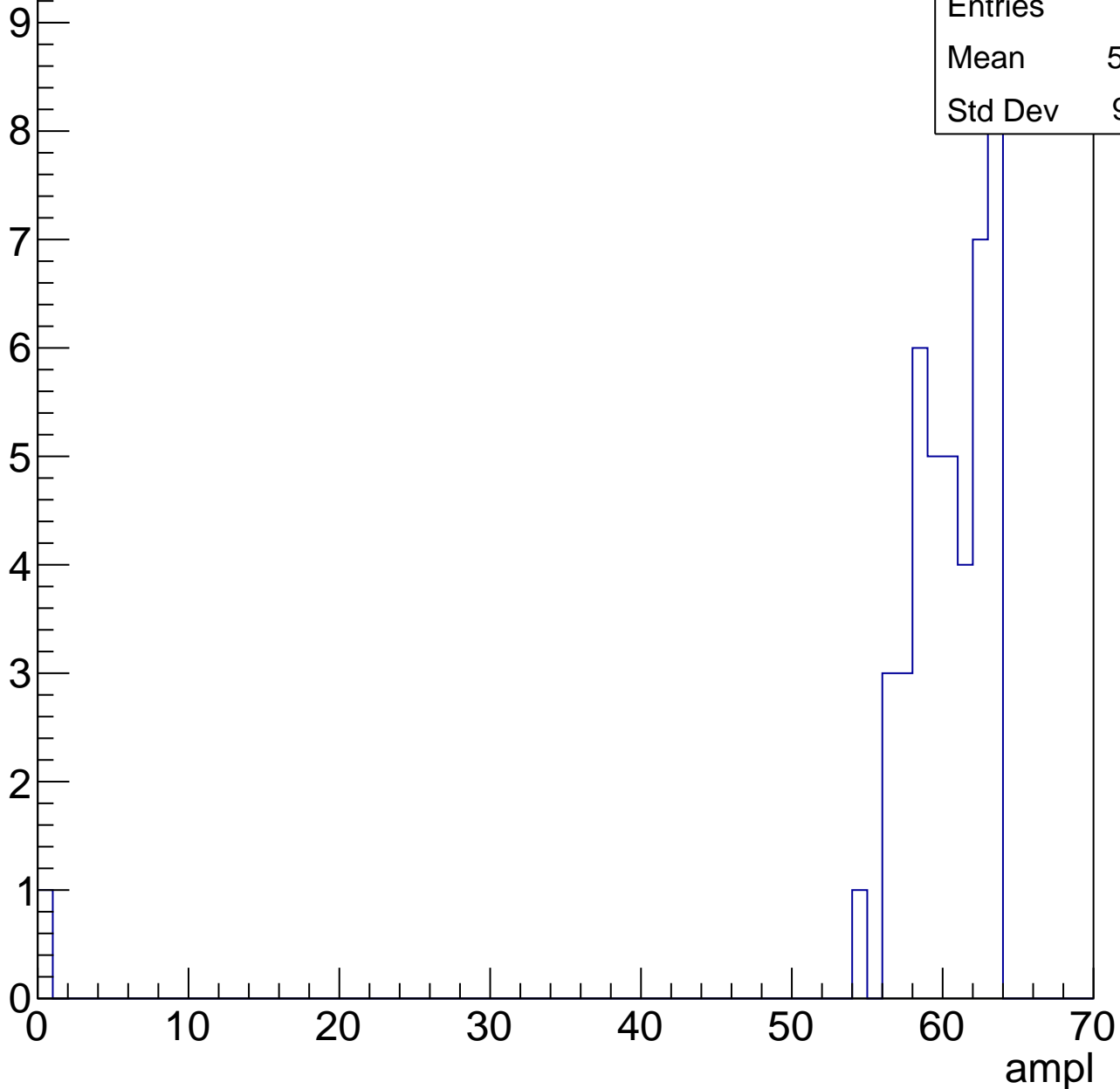
Entries	66
Mean	55.65
Std Dev	3.282

B0L001S, U14-ch53, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.66
Std Dev	9.261



B0L001S, U14-ch53, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch53, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch54, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	28.75
Std Dev	4.651

Gaus mean : 29.8161

Gaus Width: 3.0179

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B0L001S, U14-ch54, adc1

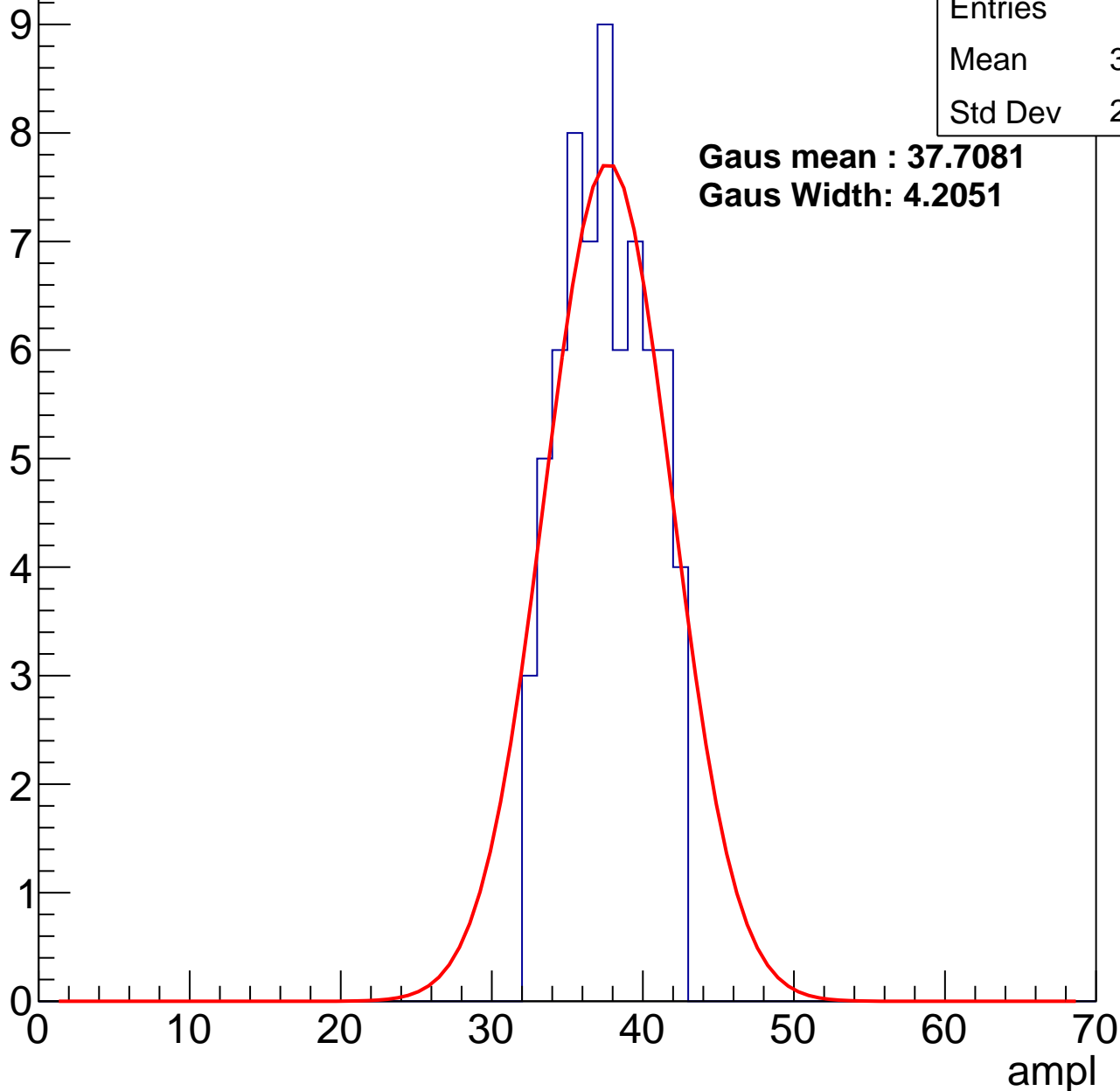
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.09
Std Dev	2.816

Gaus mean : 37.7081

Gaus Width: 4.2051



B0L001S, U14-ch54, adc2

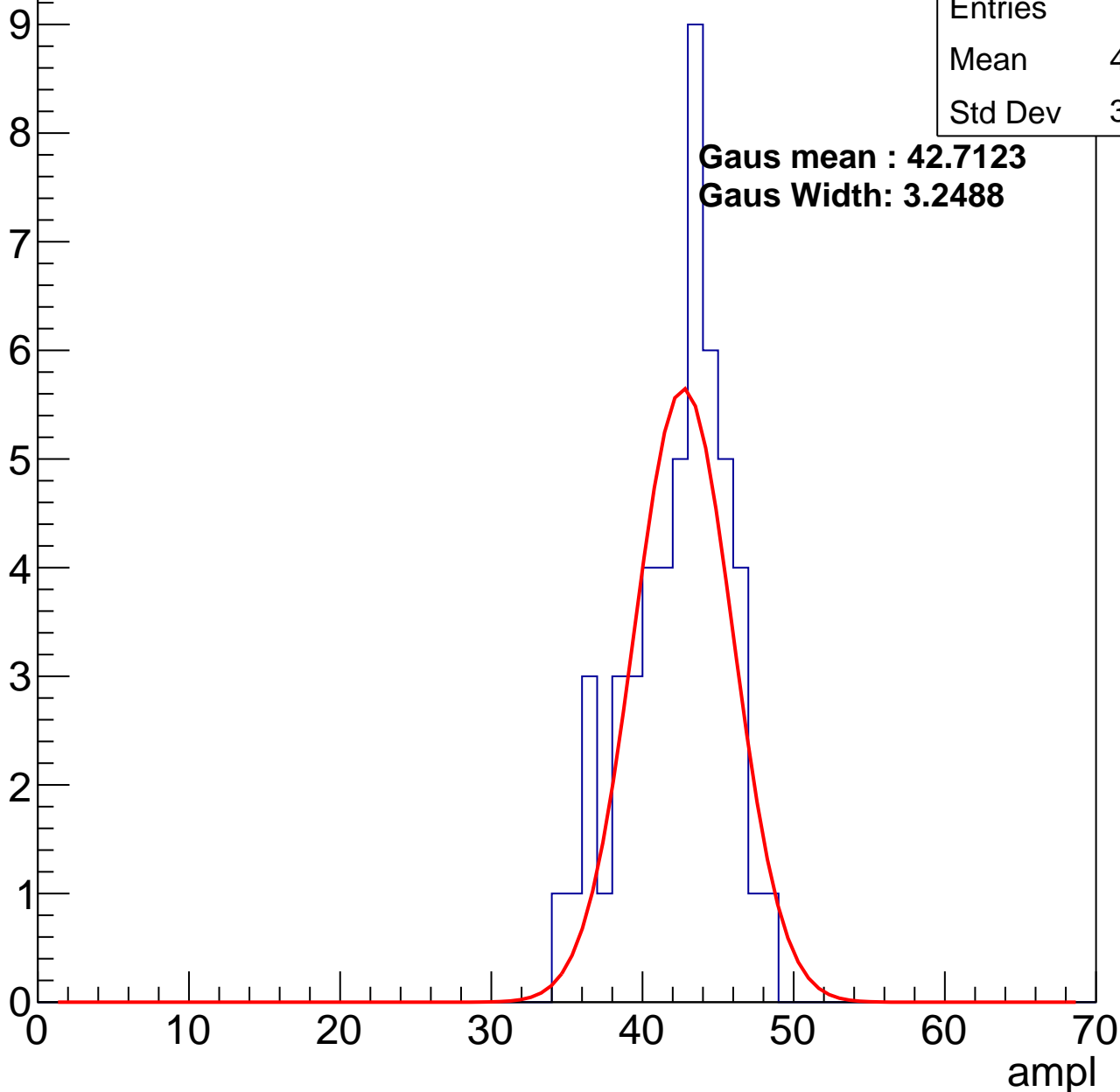
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	41.84
Std Dev	3.262

Gaus mean : 42.7123

Gaus Width: 3.2488



B0L001S, U14-ch54, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	49.11
Std Dev	3.102

Entry

10

8

6

4

2

0

0

10

20

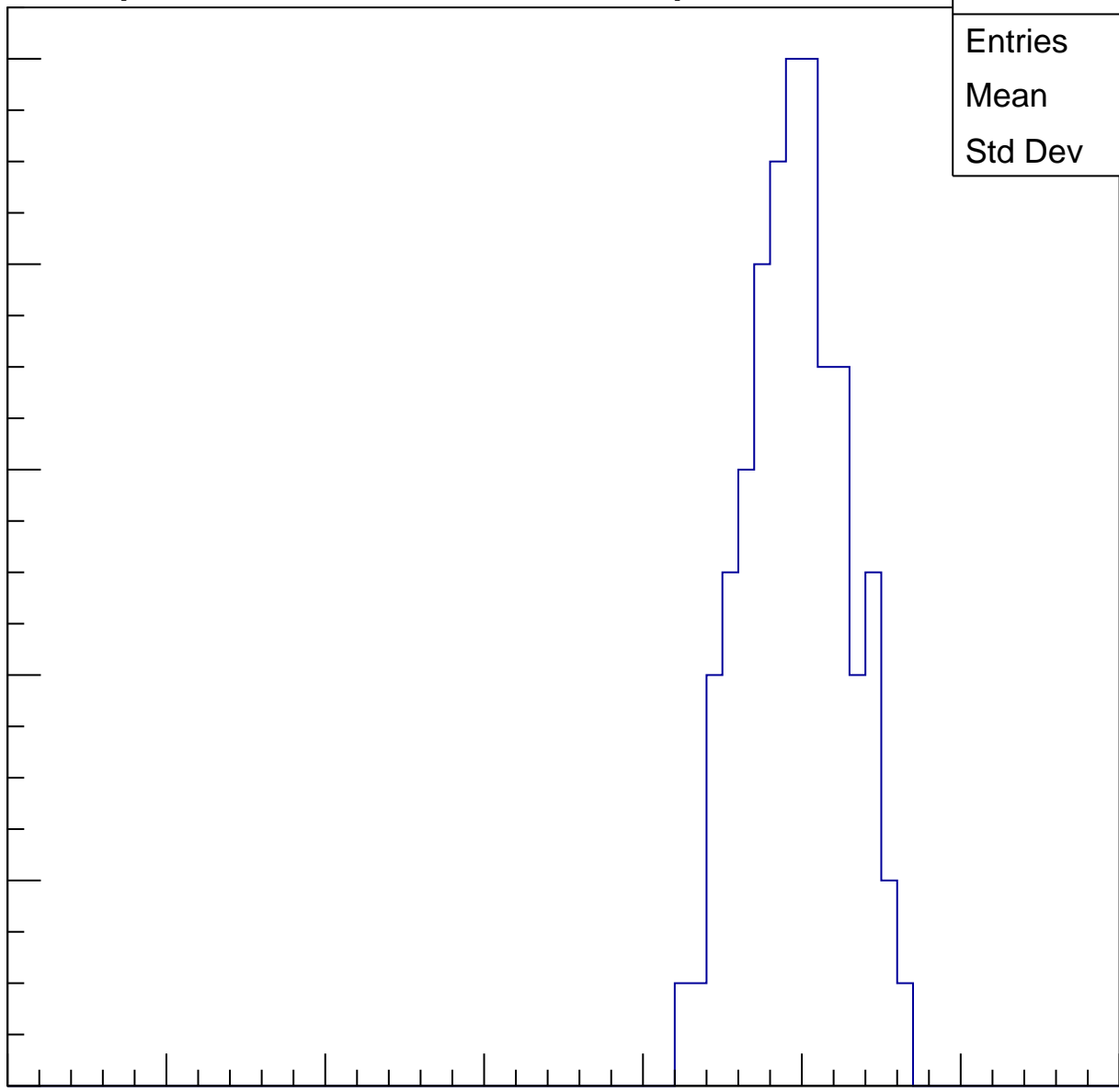
30

40

50

60

ampl

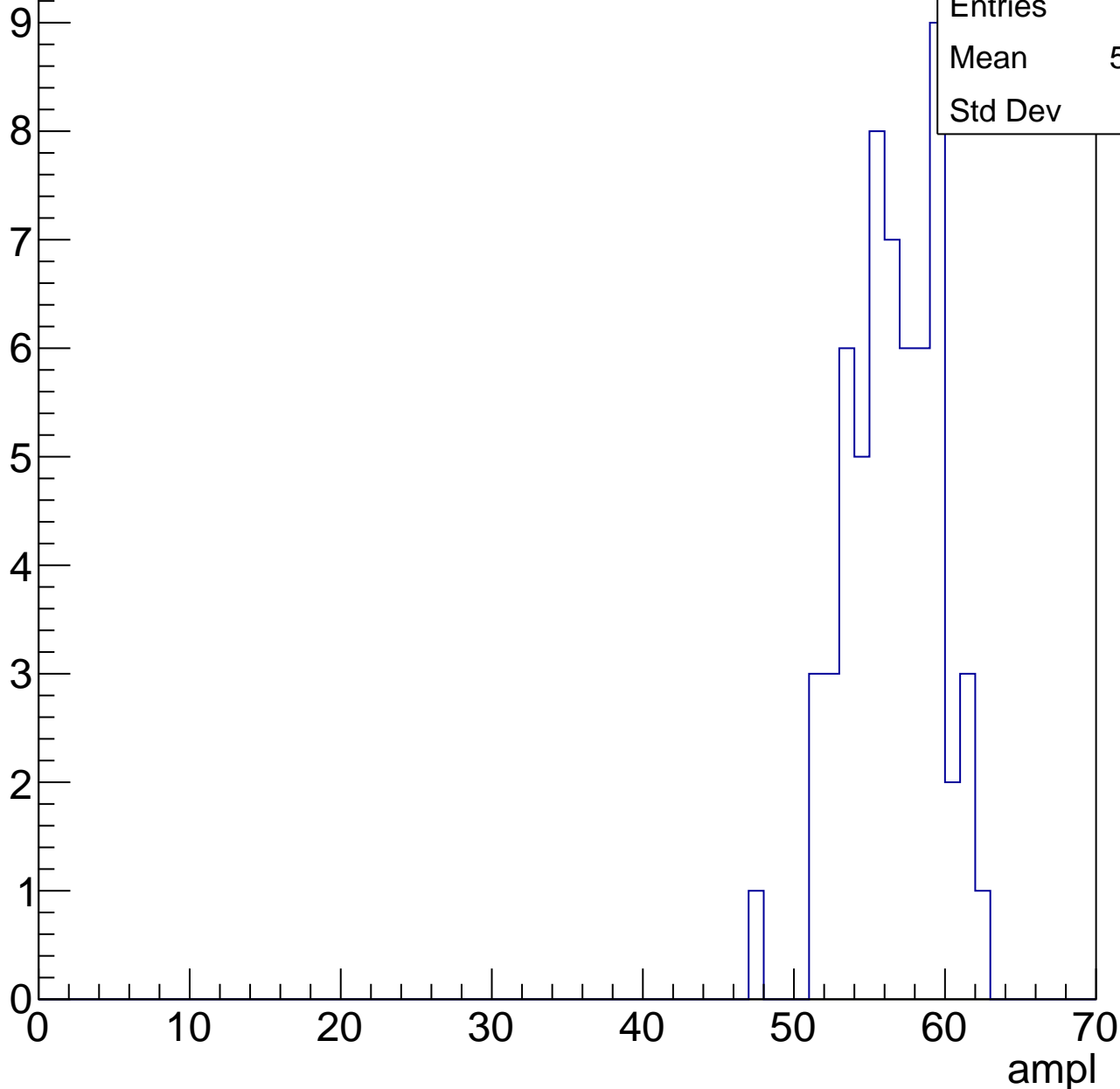


B0L001S, U14-ch54, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	56.03
Std Dev	3

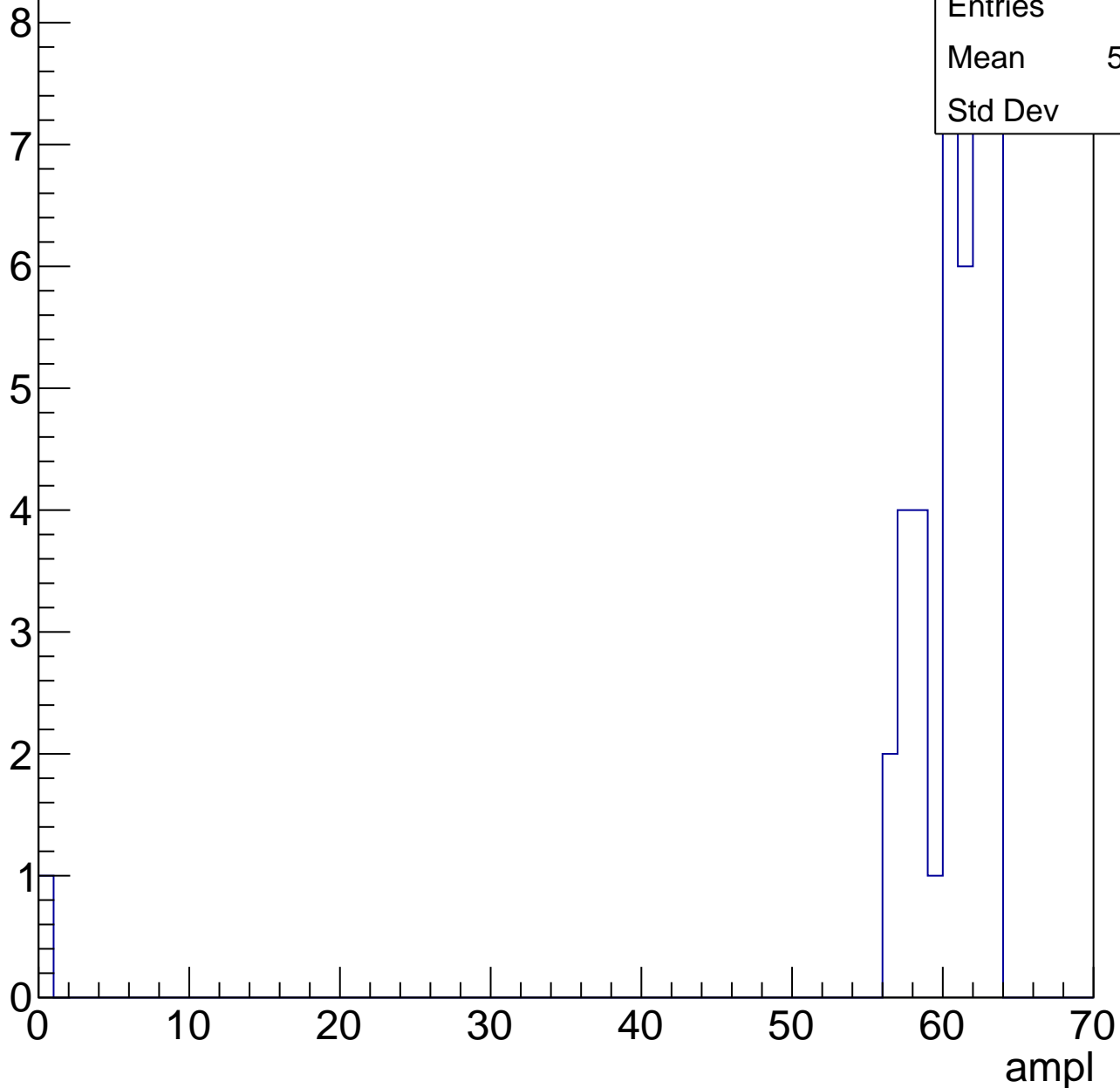


B0L001S, U14-ch54, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	58.98
Std Dev	9.45



B0L001S, U14-ch54, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch54, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch55, adc0

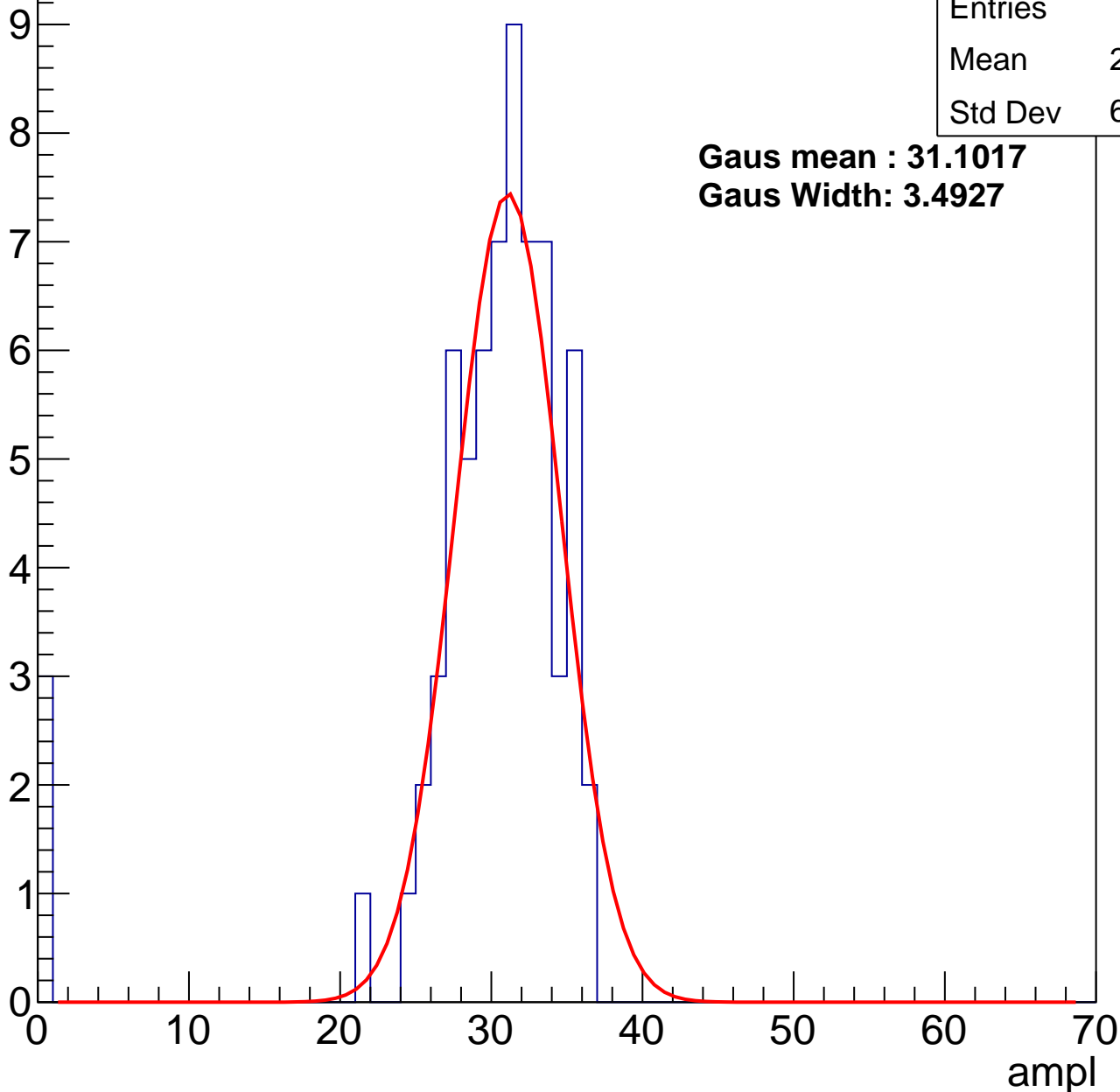
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	29.07
Std Dev	6.973

Gaus mean : 31.1017

Gaus Width: 3.4927



B0L001S, U14-ch55, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	37.06
Std Dev	3.464

Gaus mean : 37.8009

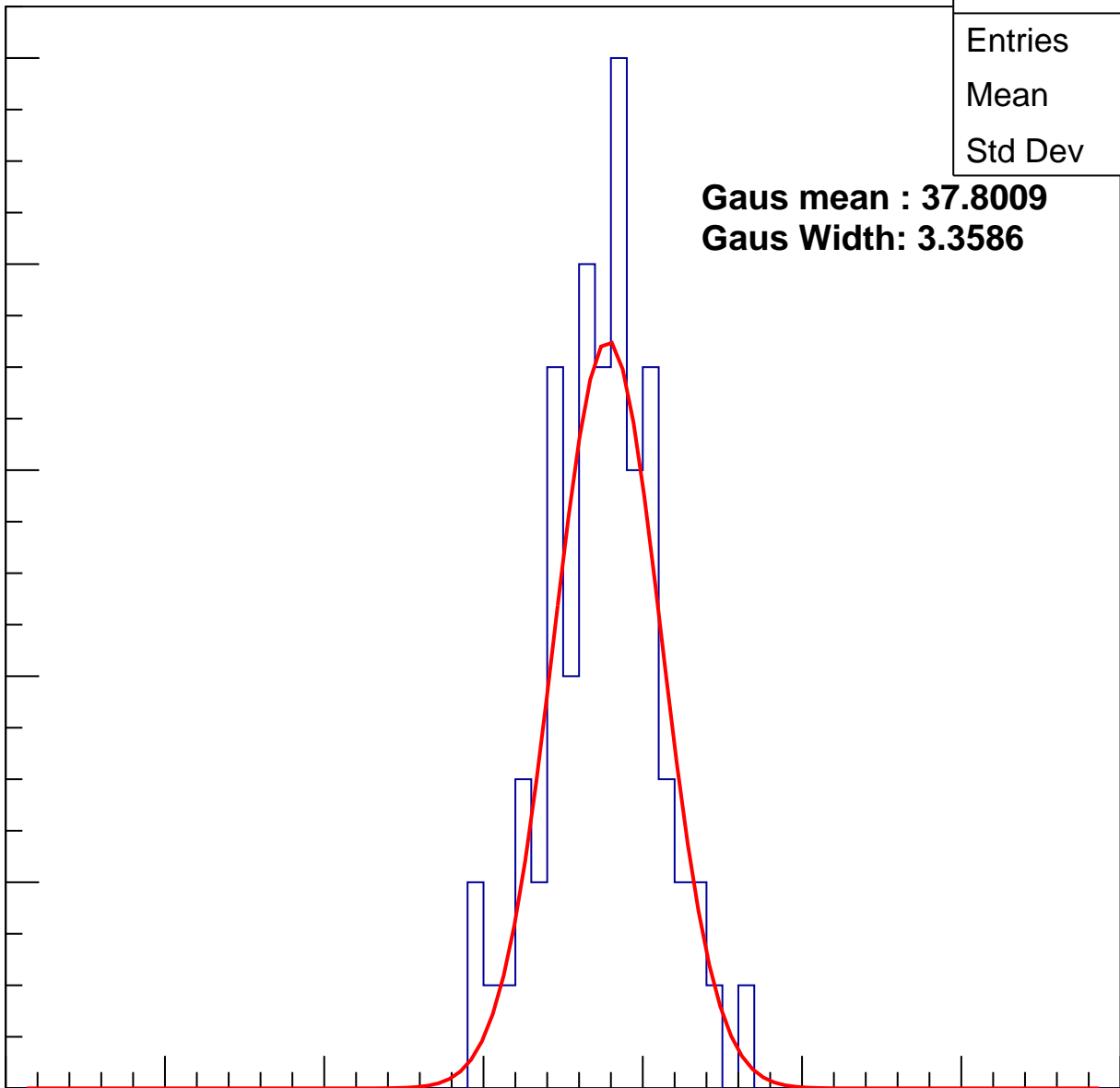
Gaus Width: 3.3586

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch55, adc2

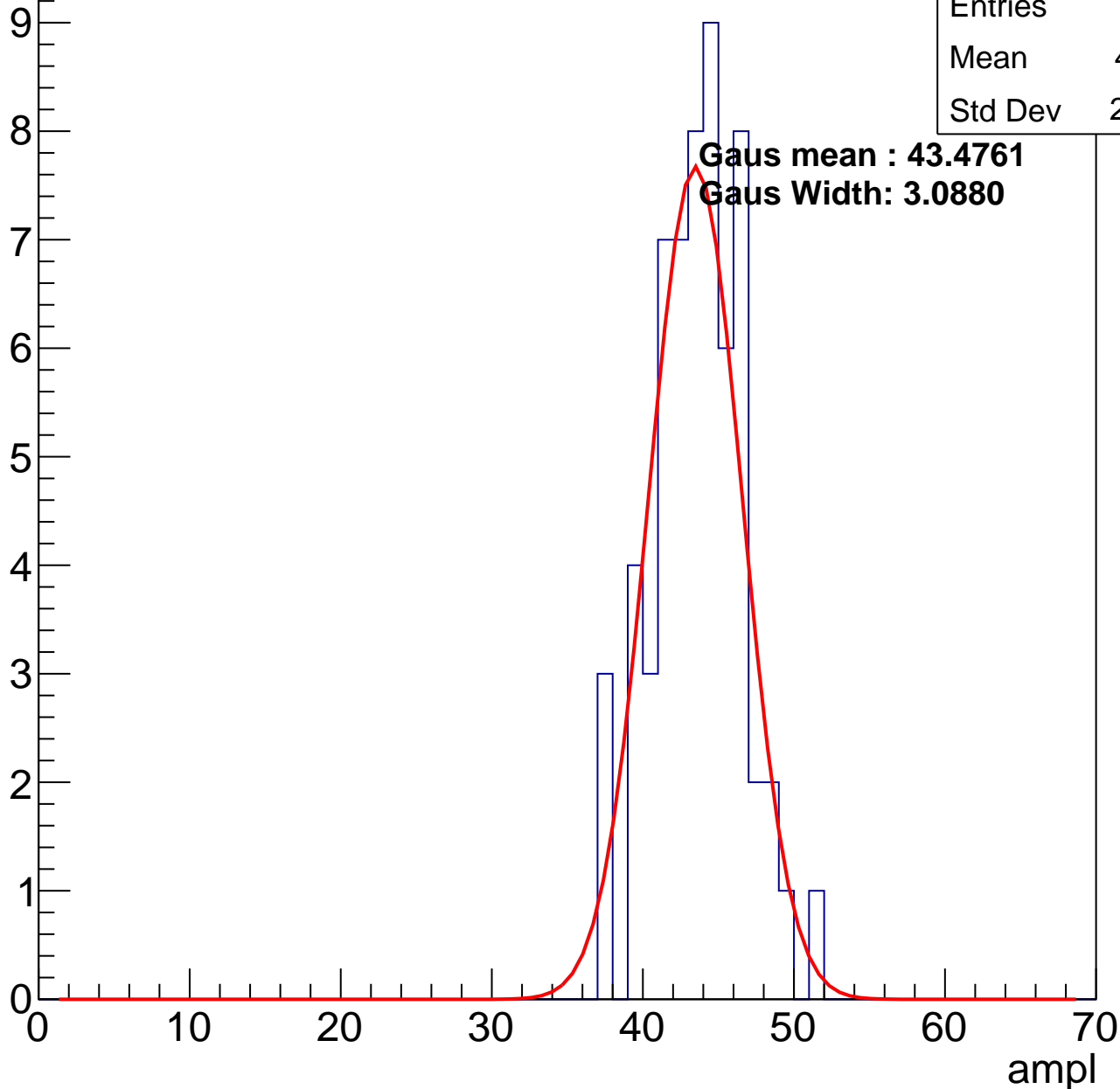
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.21
Std Dev	2.915

Gaus mean : 43.4761

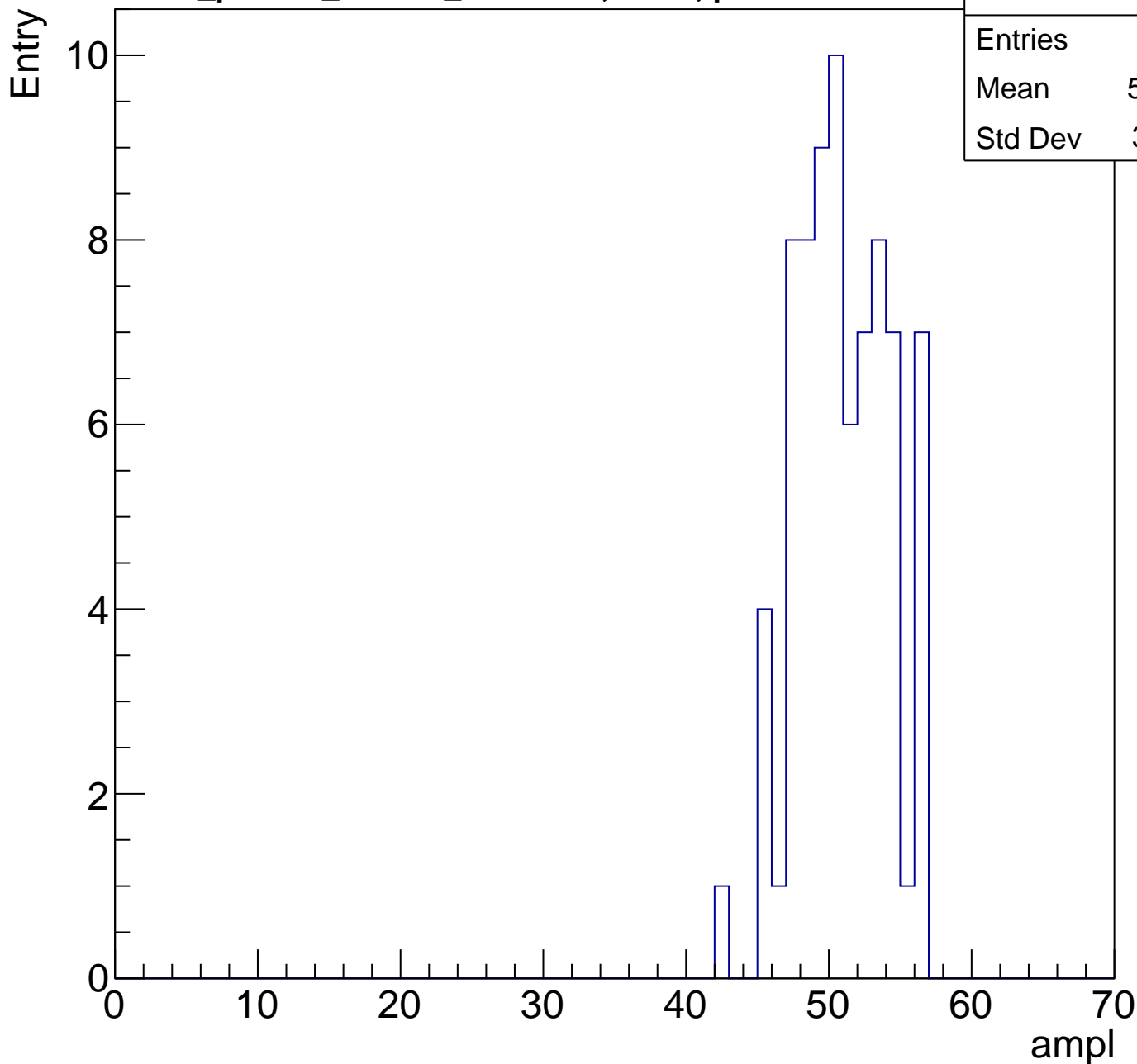
Gaus Width: 3.0880



B0L001S, U14-ch55, adc3

calib_packv5_042523_0143.root, FC#9, port A1

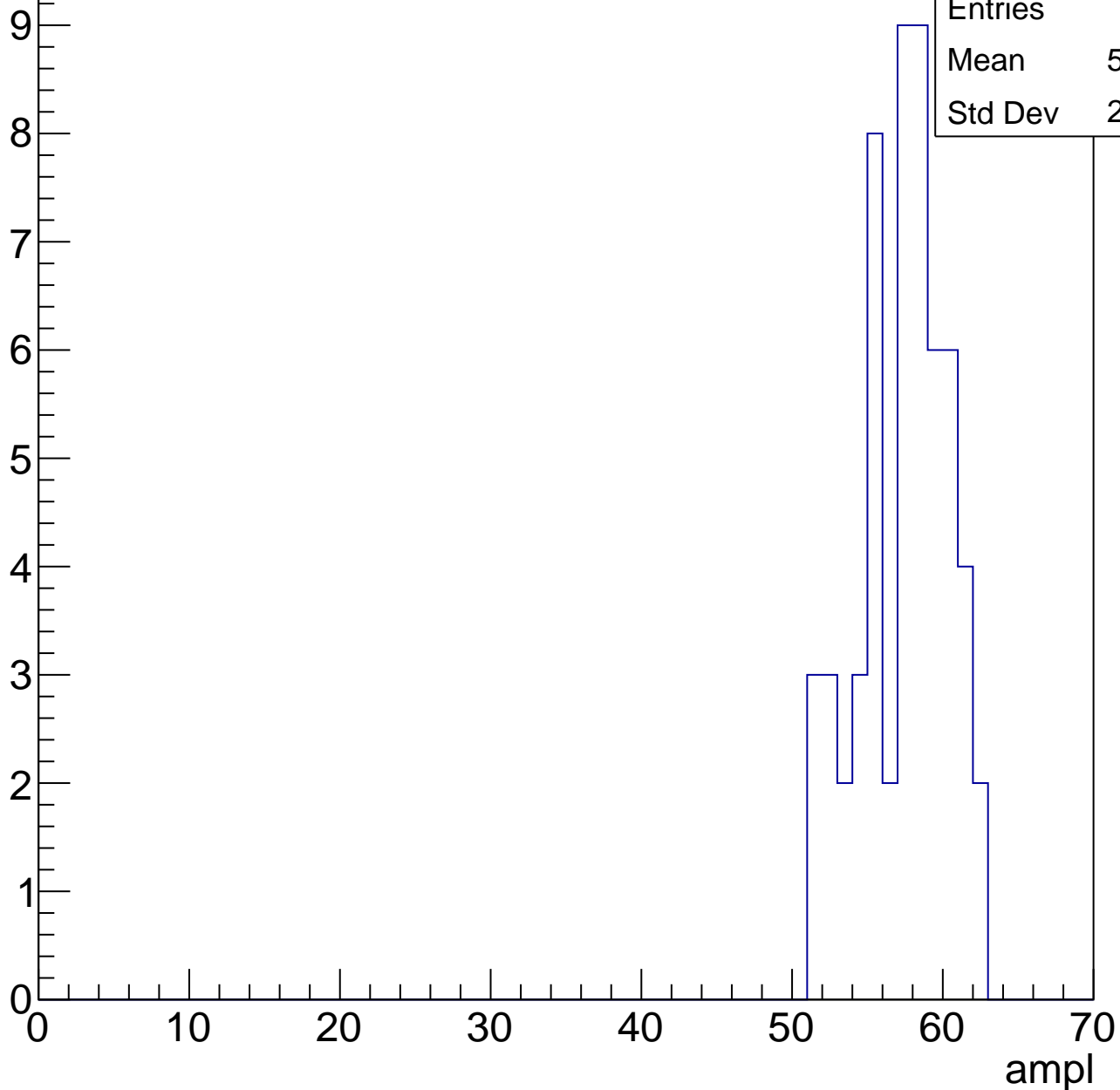
Entries	77
Mean	50.49
Std Dev	3.161



B0L001S, U14-ch55, adc4

calib_packv5_042523_0143.root, FC#9, port A1

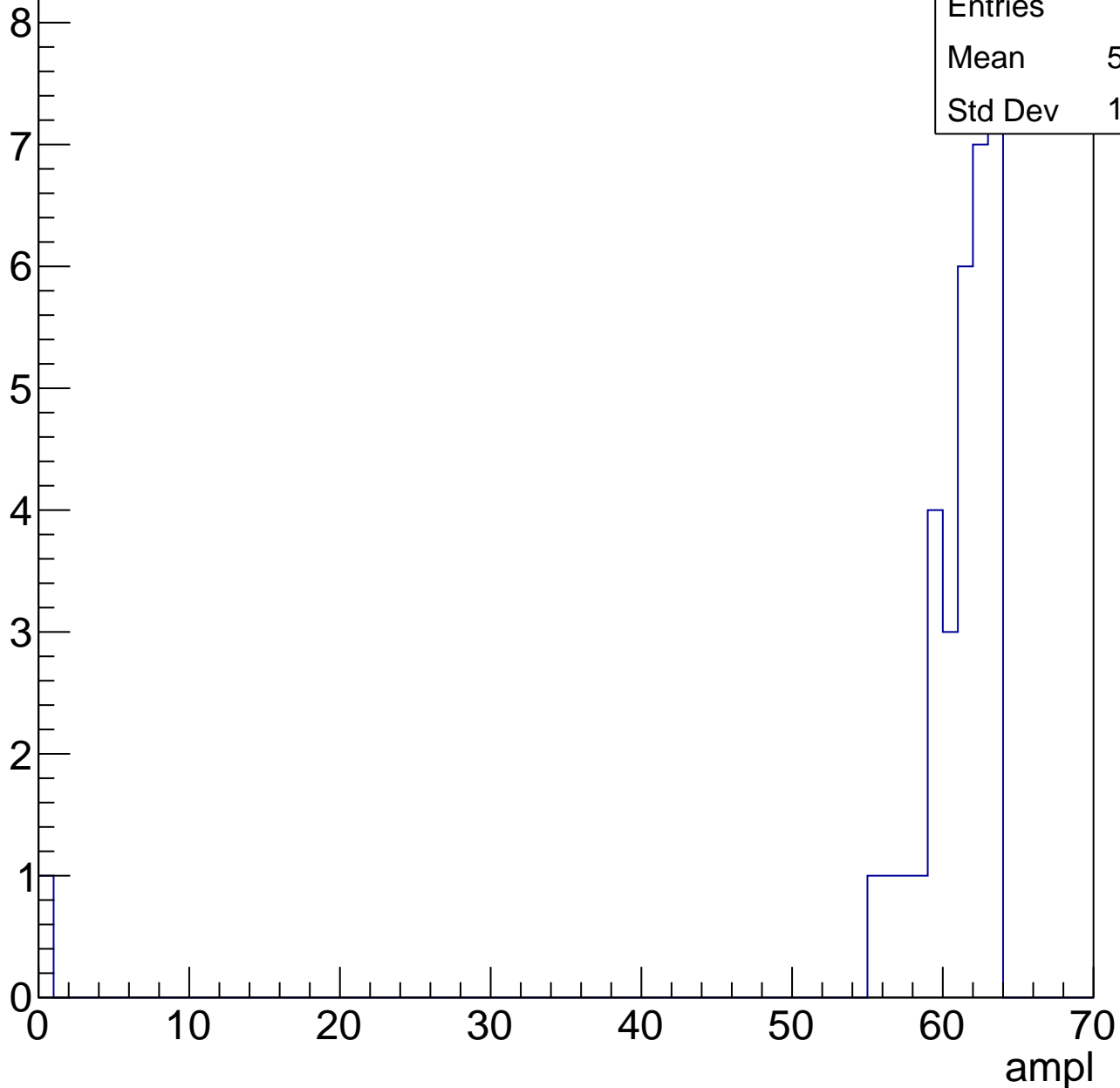
Entry



B0L001S, U14-ch55, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch55, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch55, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch56, adc0

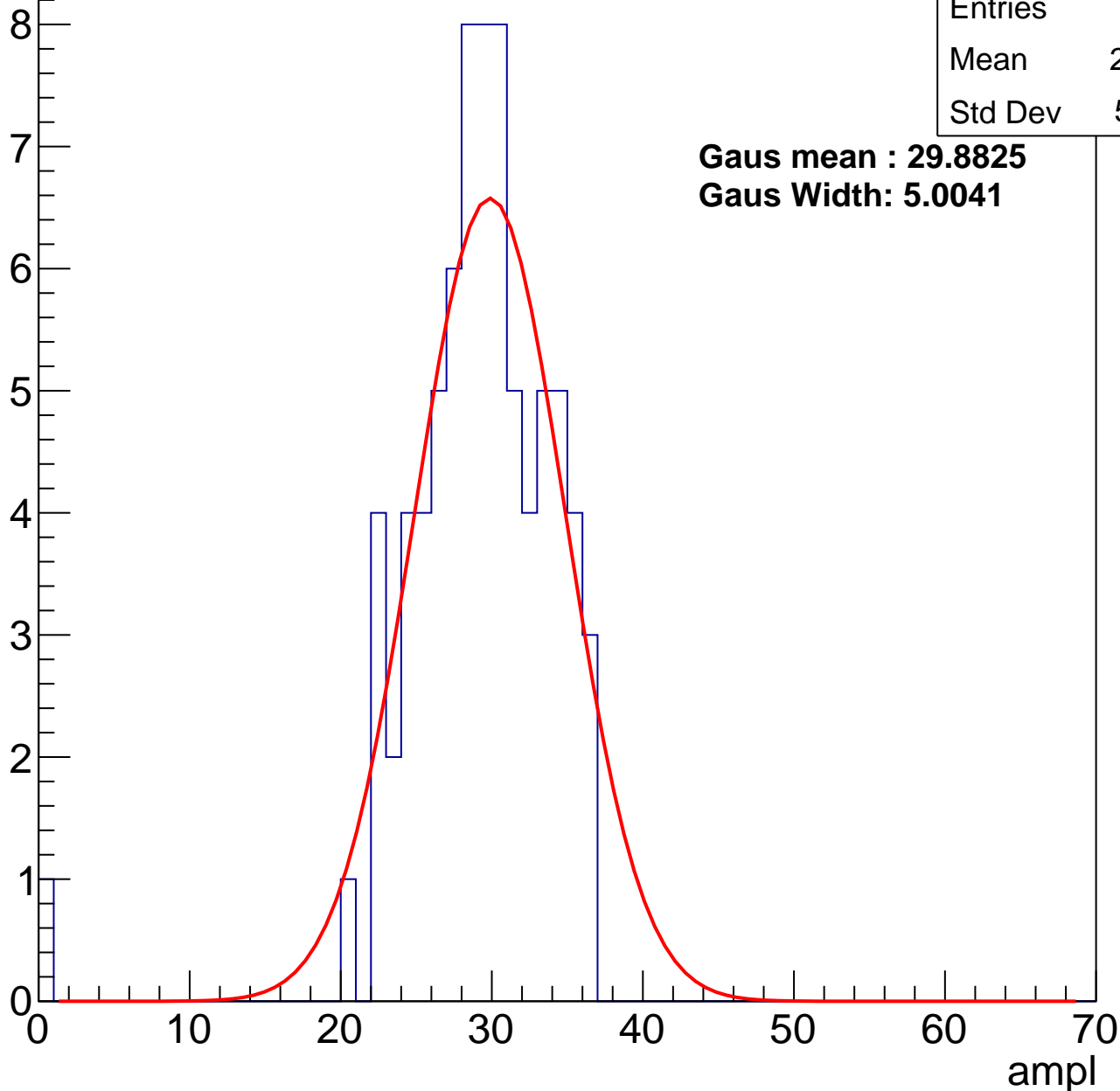
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	28.62
Std Dev	5.071

Gaus mean : 29.8825

Gaus Width: 5.0041



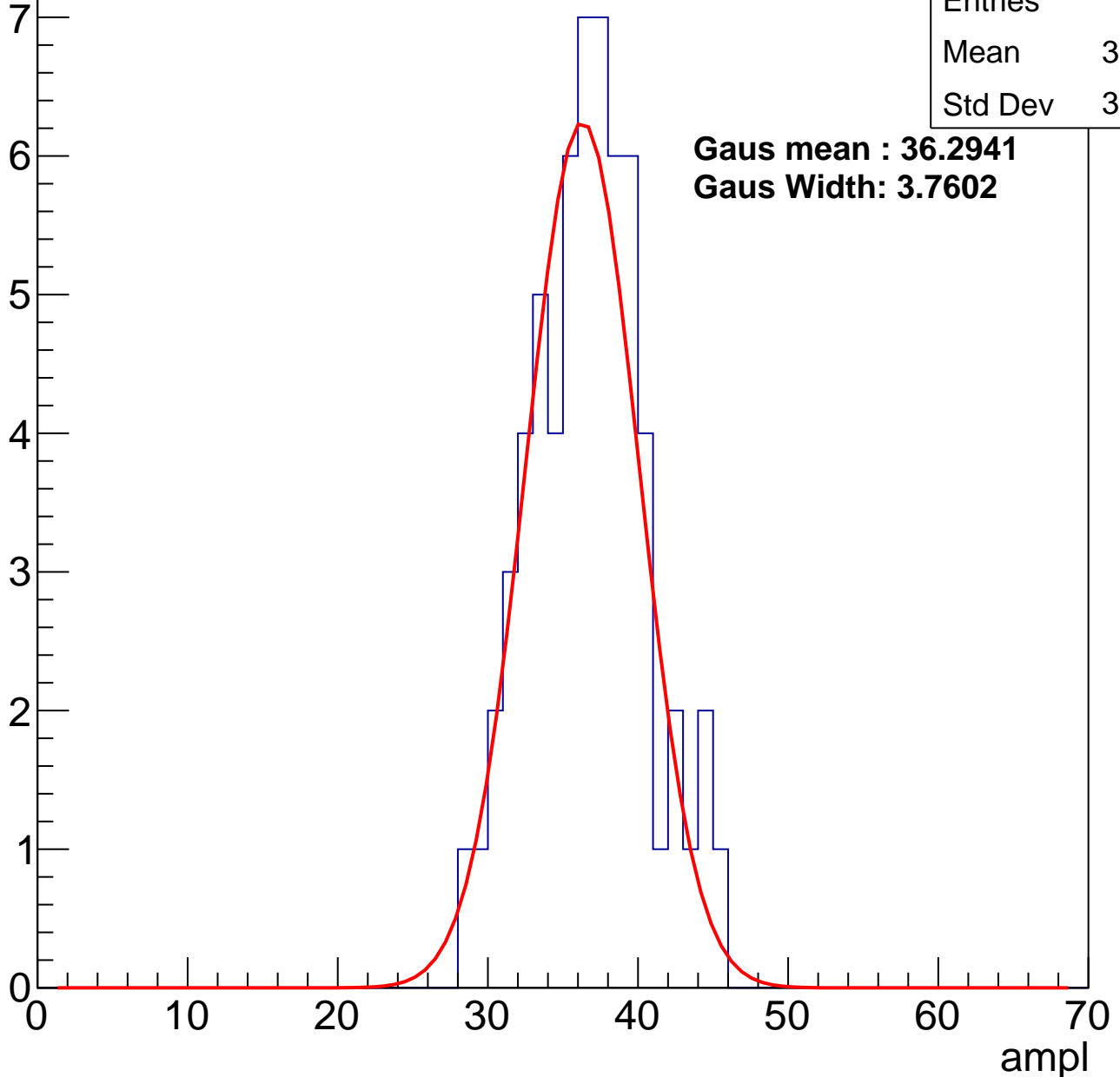
B0L001S, U14-ch56, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	36.24
Std Dev	3.762

Gaus mean : 36.2941
Gaus Width: 3.7602



B0L001S, U14-ch56, adc2

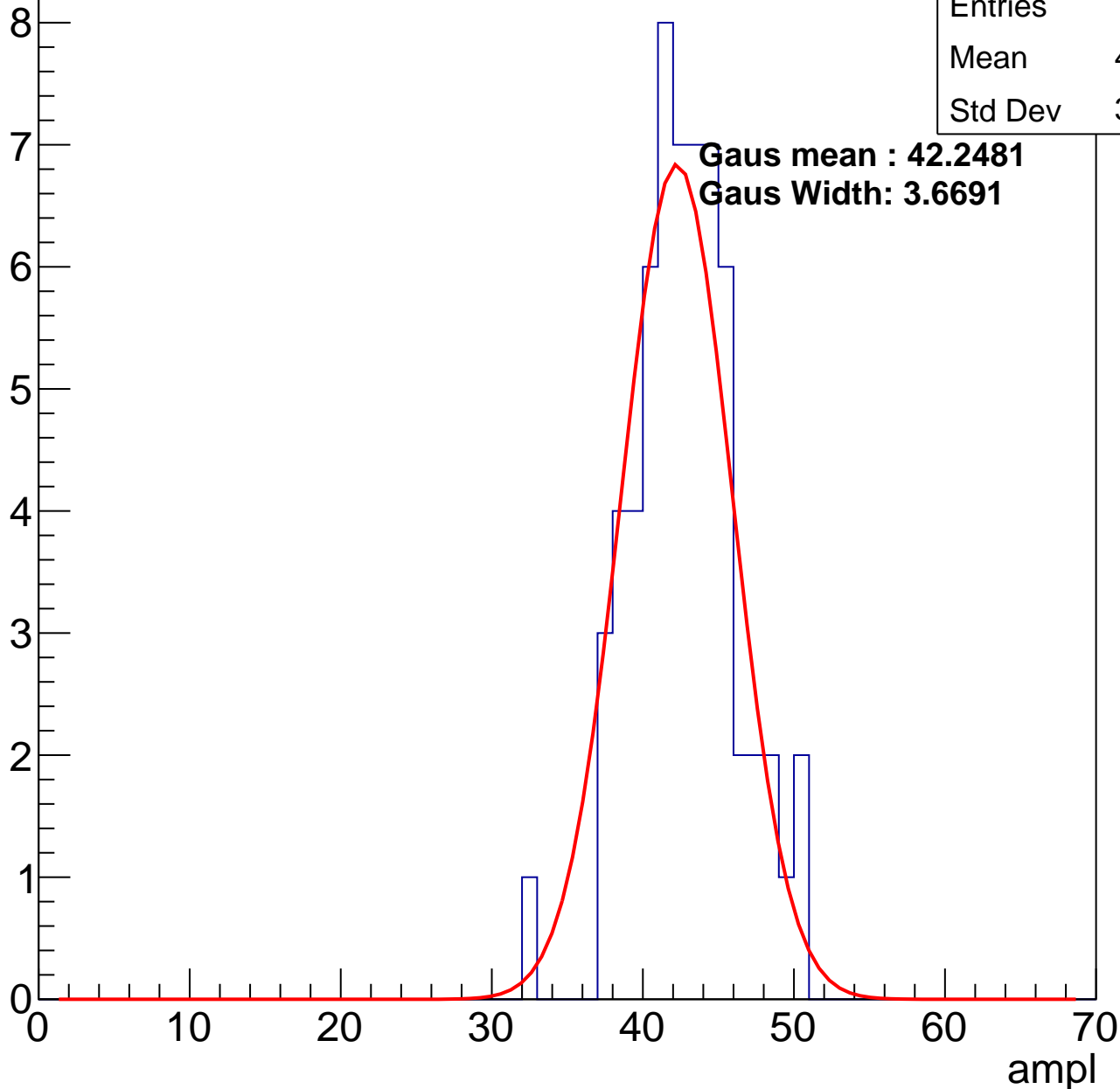
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	42.31
Std Dev	3.411

Gaus mean : 42.2481

Gaus Width: 3.6691

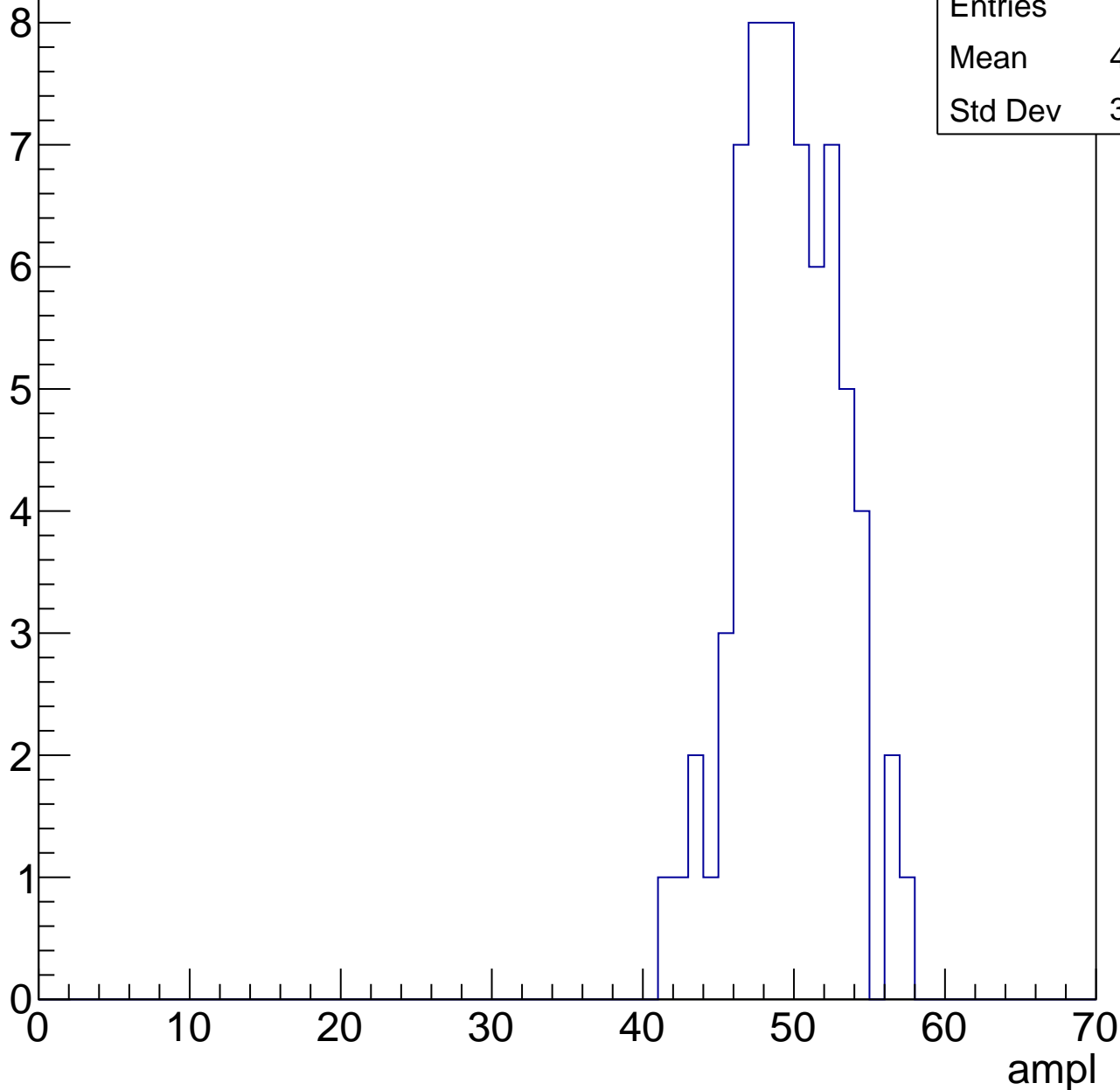


B0L001S, U14-ch56, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.18
Std Dev	3.345

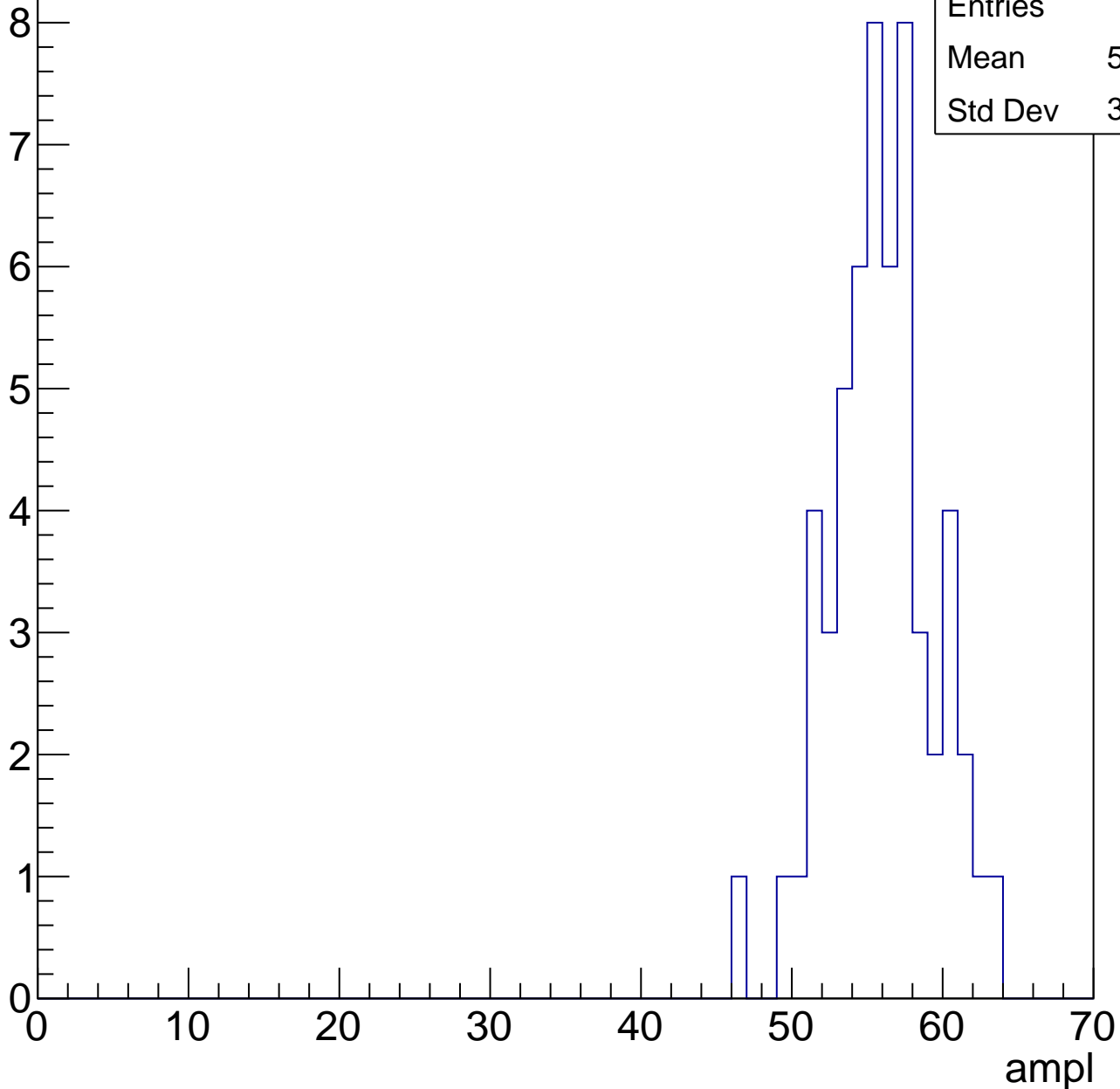


B0L001S, U14-ch56, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

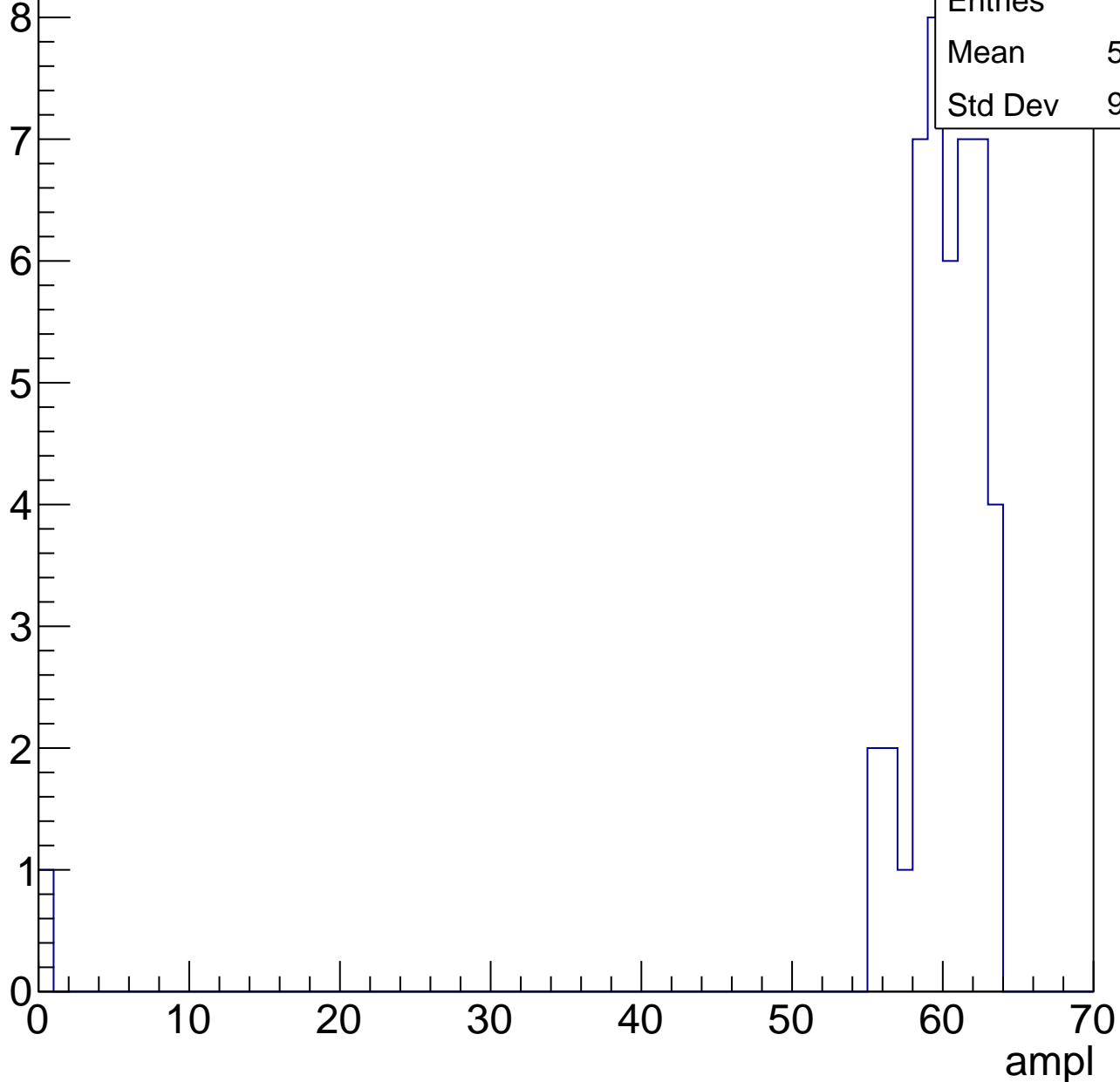
Entries	56
Mean	55.45
Std Dev	3.354



B0L001S, U14-ch56, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

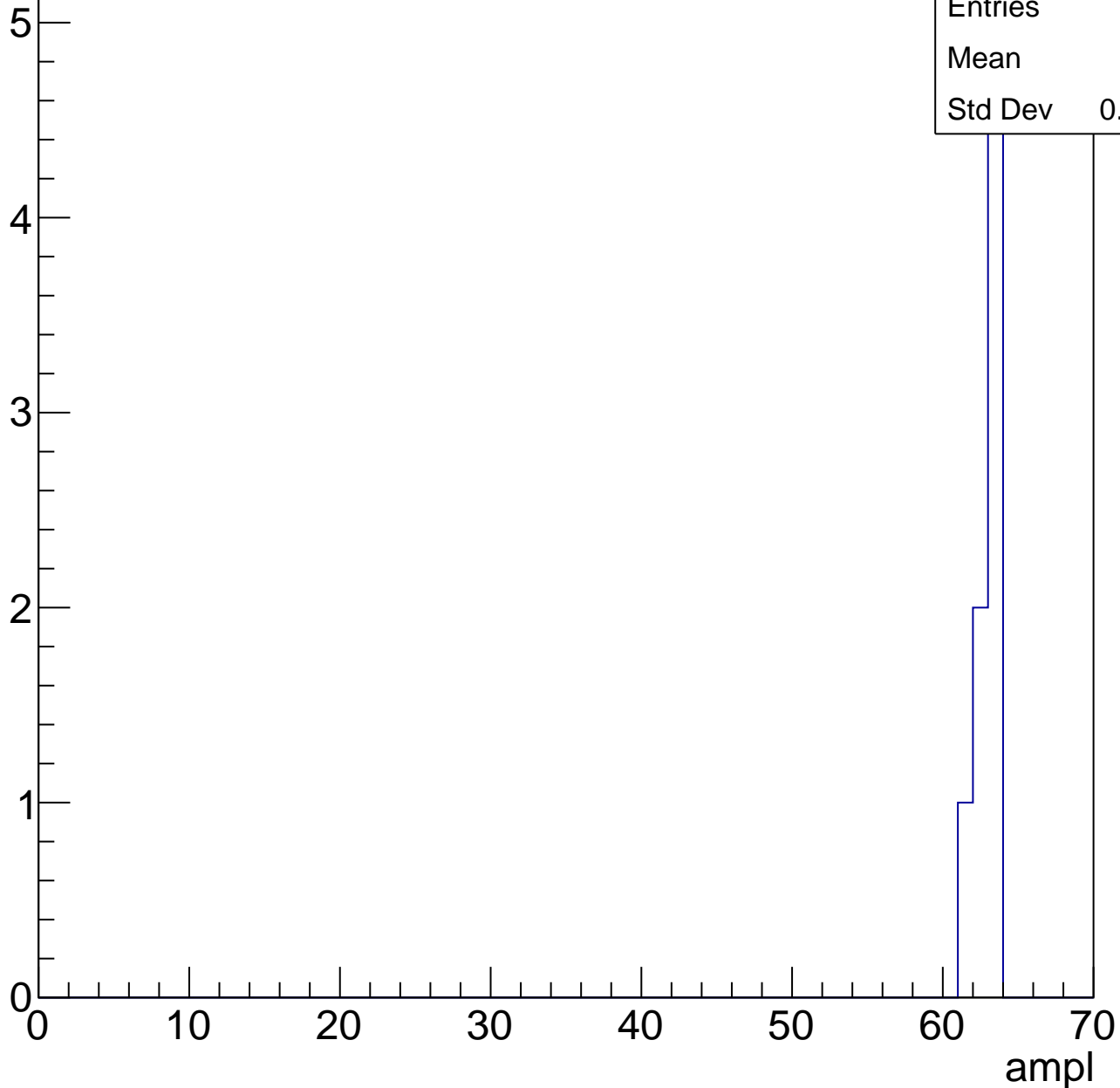


B0L001S, U14-ch56, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071



B0L001S, U14-ch56, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch57, adc0

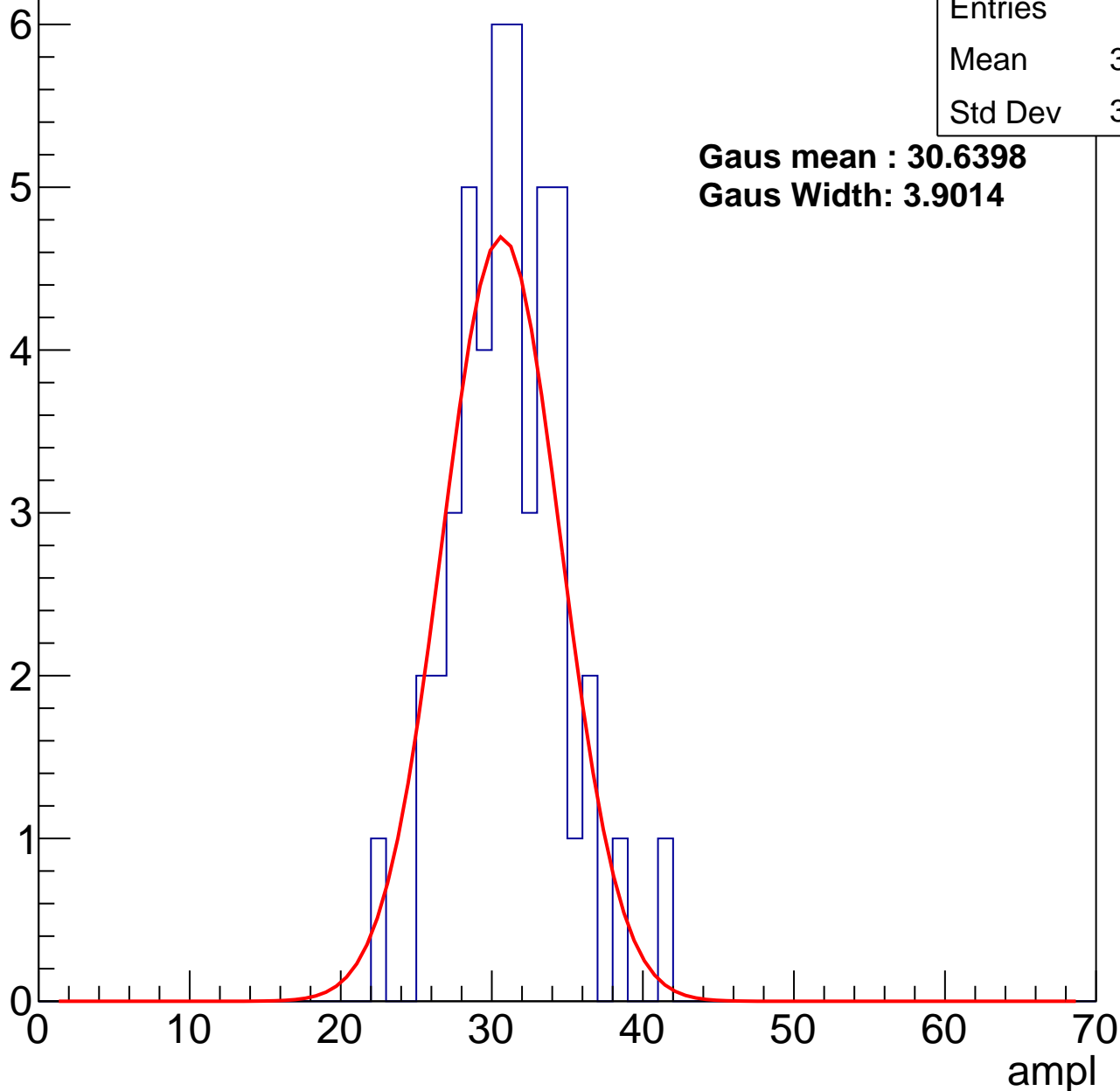
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	30.72
Std Dev	3.565

Gaus mean : 30.6398

Gaus Width: 3.9014



B0L001S, U14-ch57, adc1

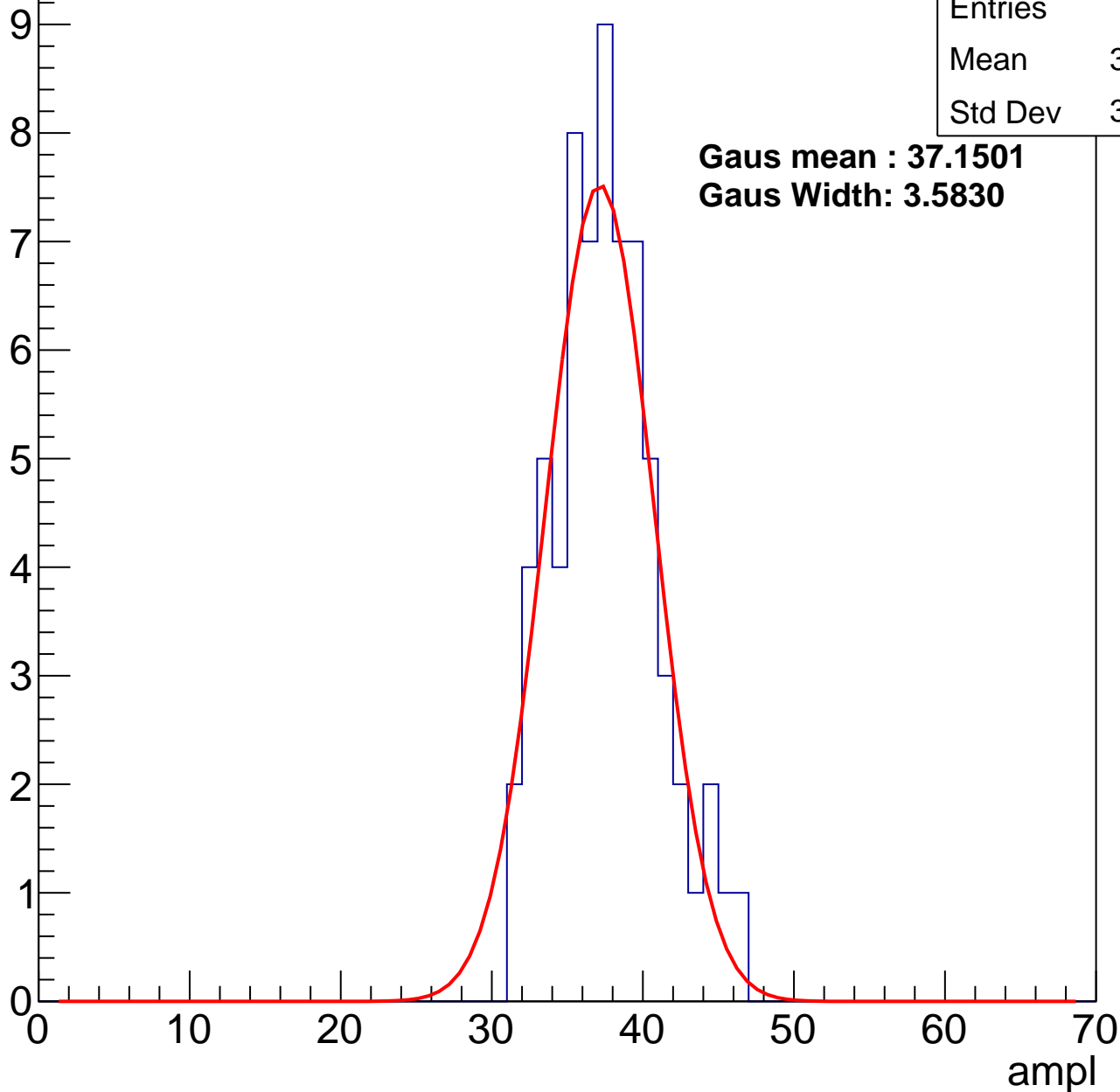
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	37.12
Std Dev	3.393

Gaus mean : 37.1501

Gaus Width: 3.5830



B0L001S, U14-ch57, adc2

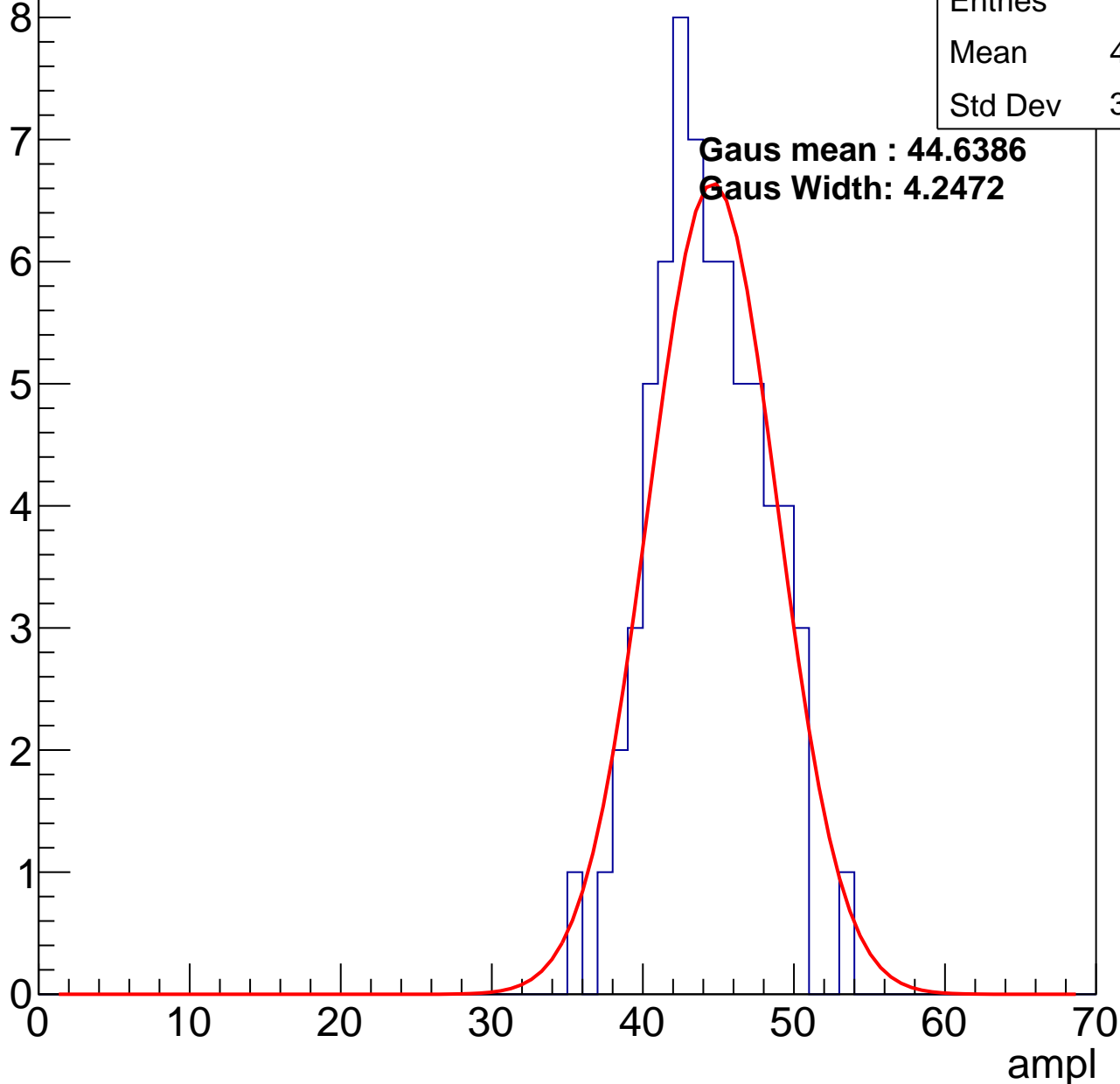
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.85
Std Dev	3.609

Gaus mean : 44.6386

Gaus Width: 4.2472

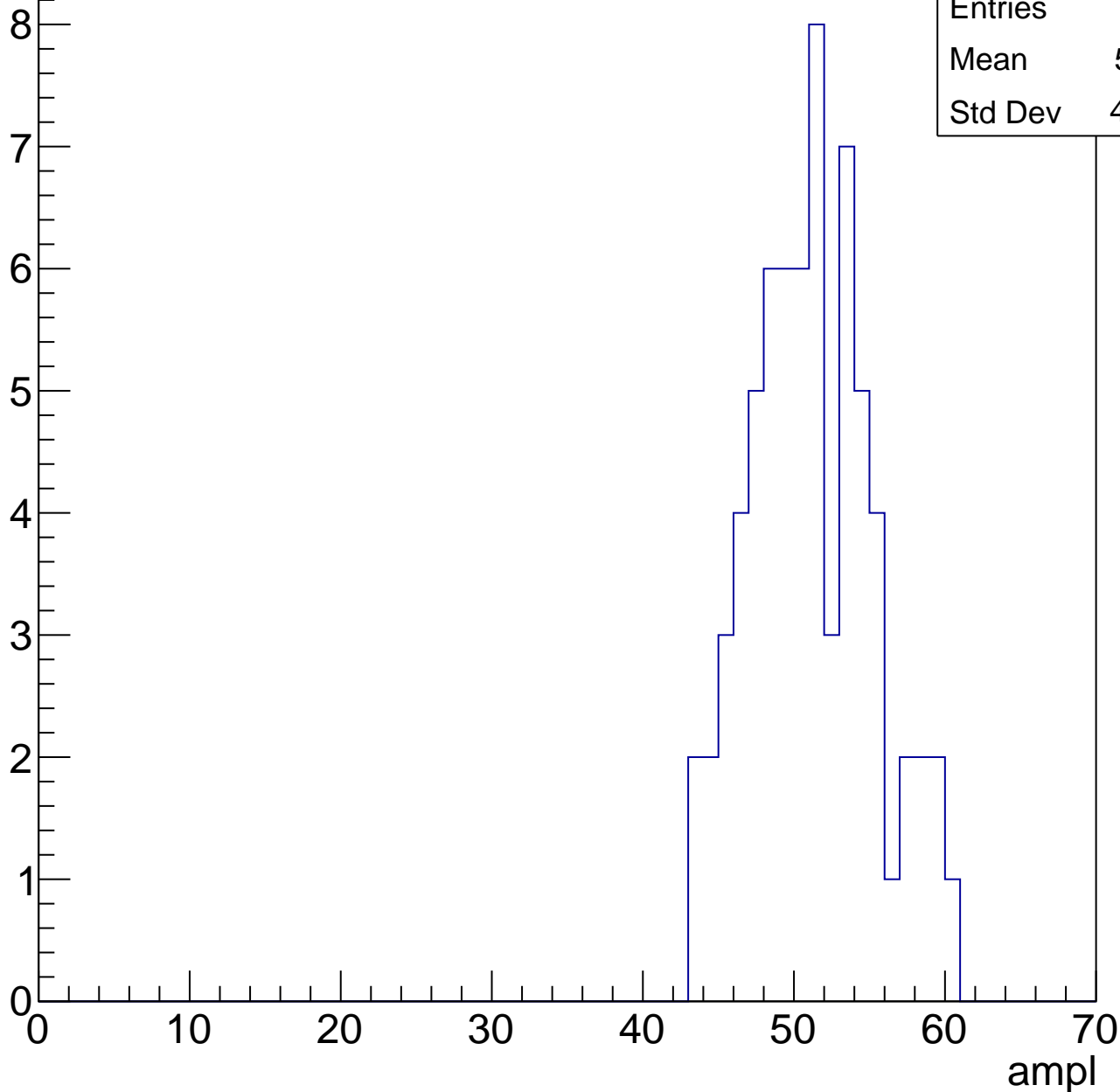


B0L001S, U14-ch57, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	50.71
Std Dev	4.058

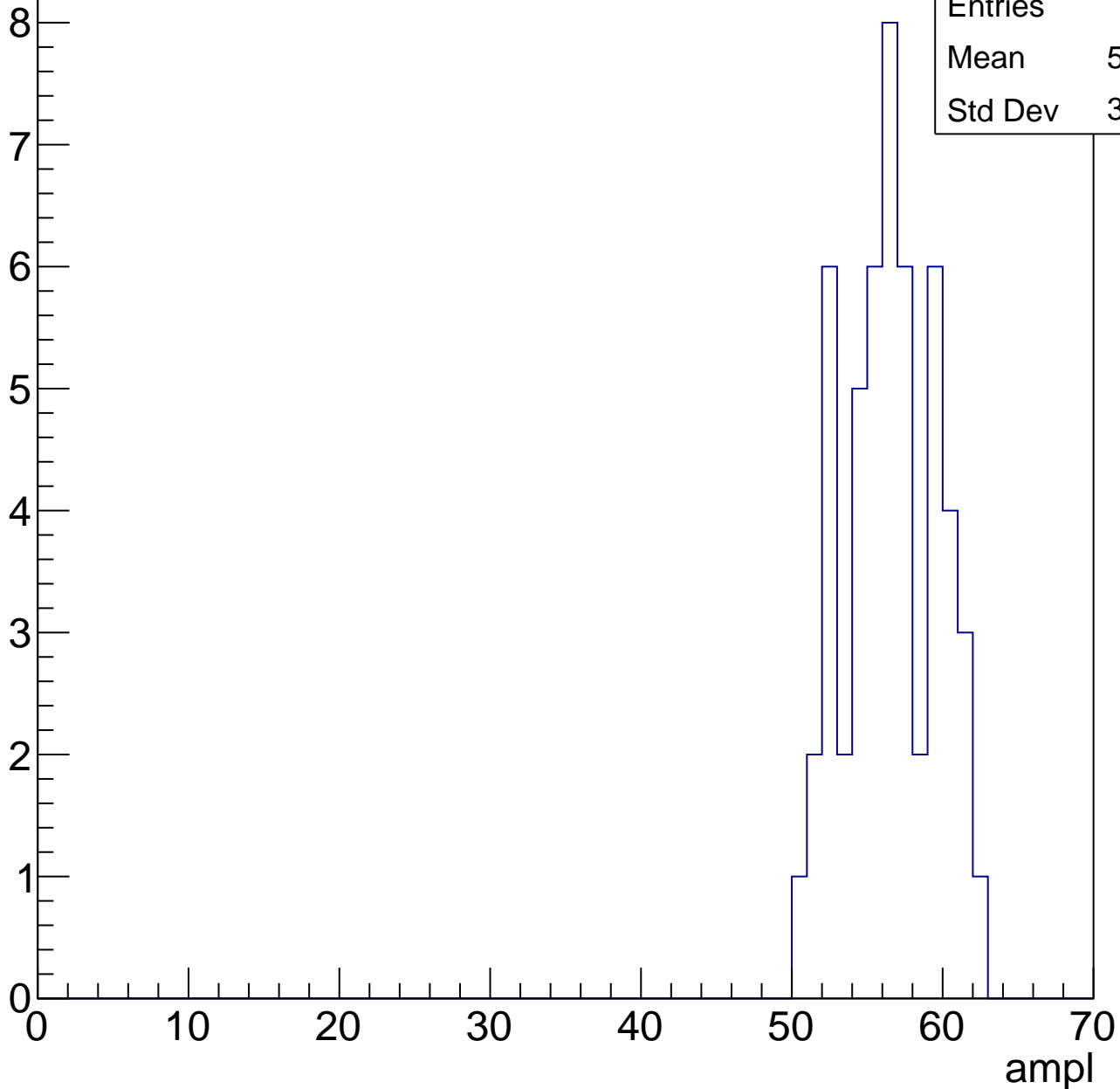


B0L001S, U14-ch57, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	56.06
Std Dev	3.003

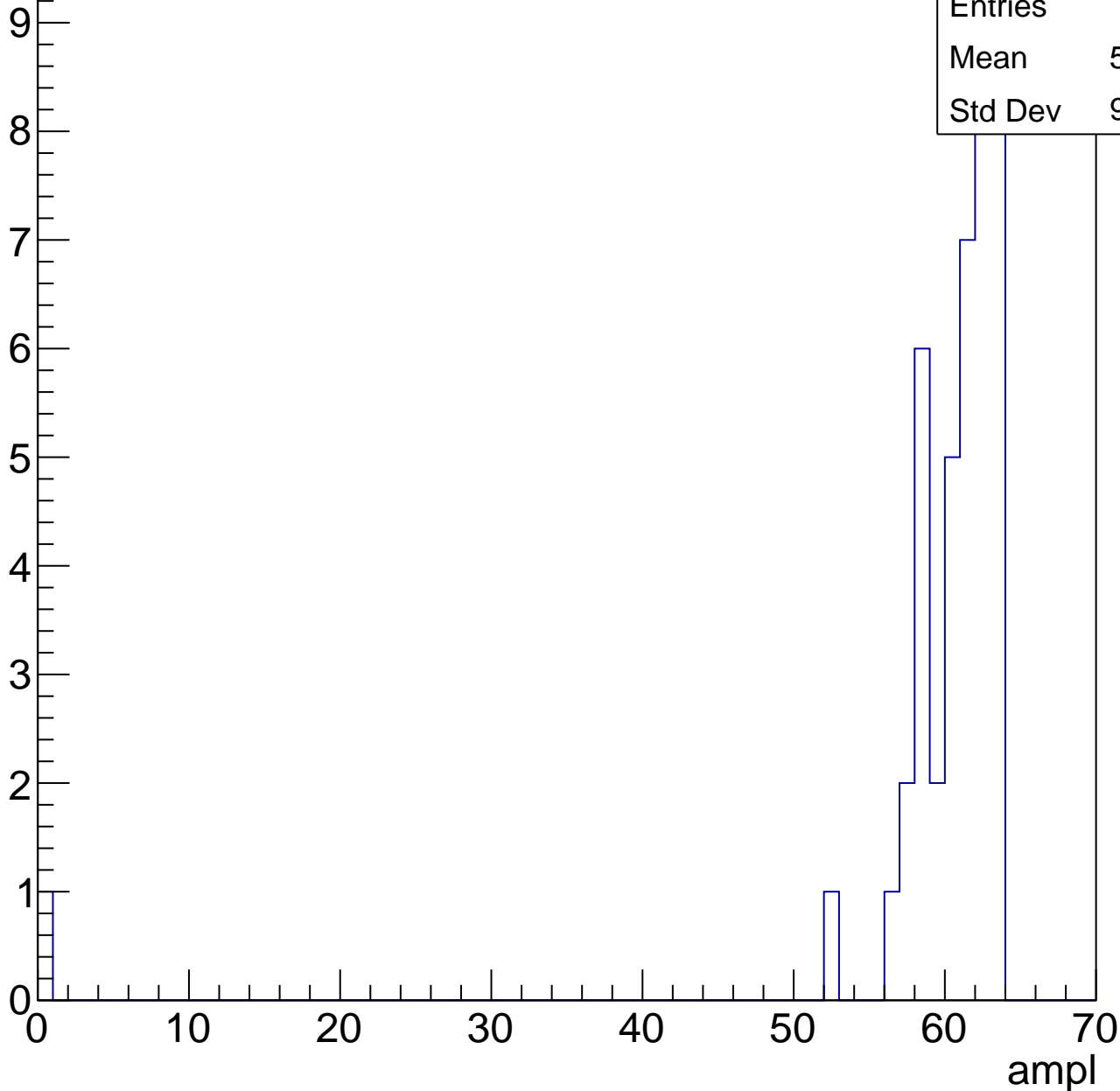


B0L001S, U14-ch57, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	59.07
Std Dev	9.414



B0L001S, U14-ch57, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch57, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch58, adc0

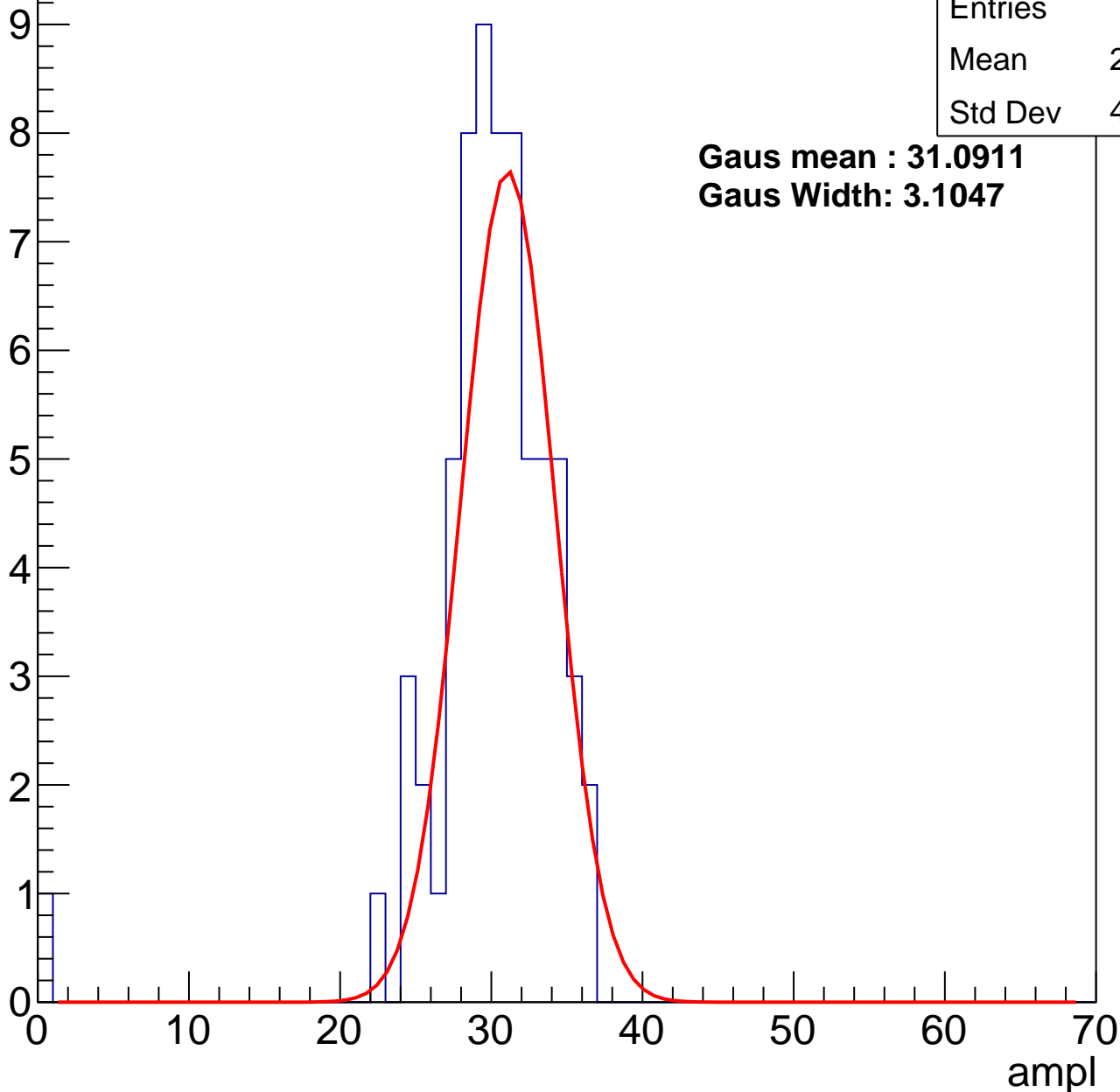
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.55
Std Dev	4.784

Gaus mean : 31.0911

Gaus Width: 3.1047



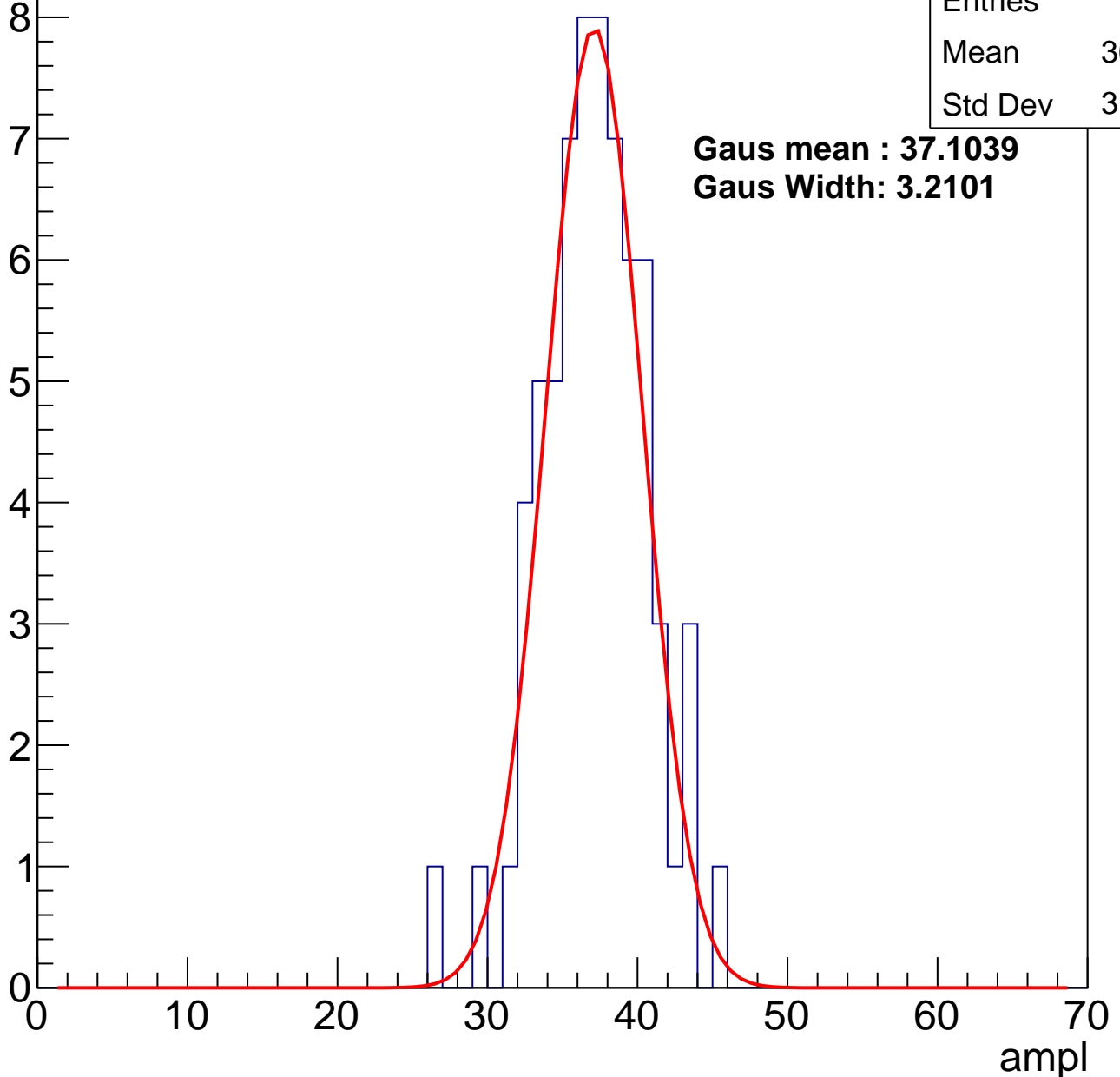
B0L001S, U14-ch58, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	36.67
Std Dev	3.466

Gaus mean : 37.1039
Gaus Width: 3.2101



B0L001S, U14-ch58, adc2

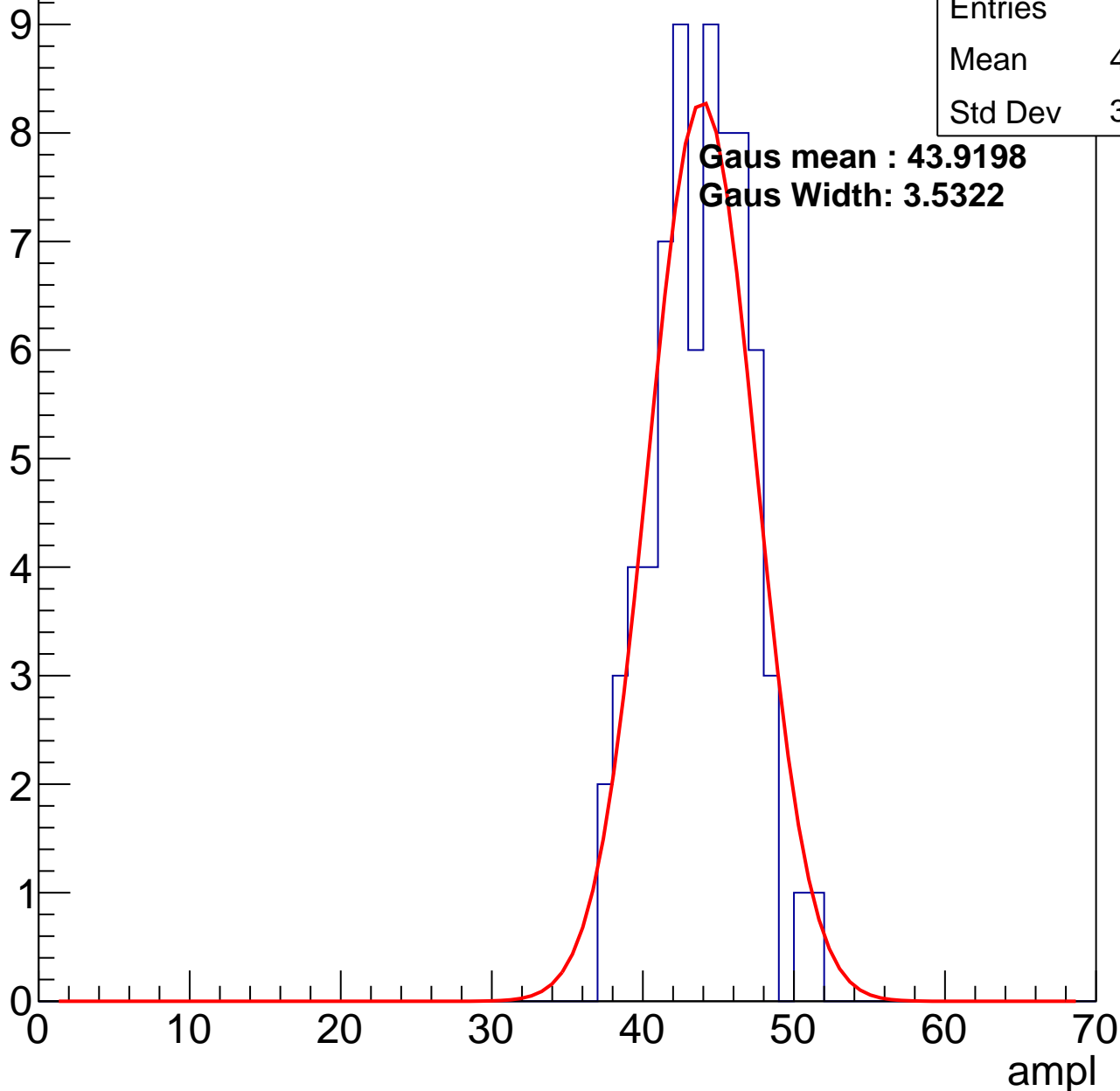
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.35
Std Dev	3.077

Gaus mean : 43.9198

Gaus Width: 3.5322

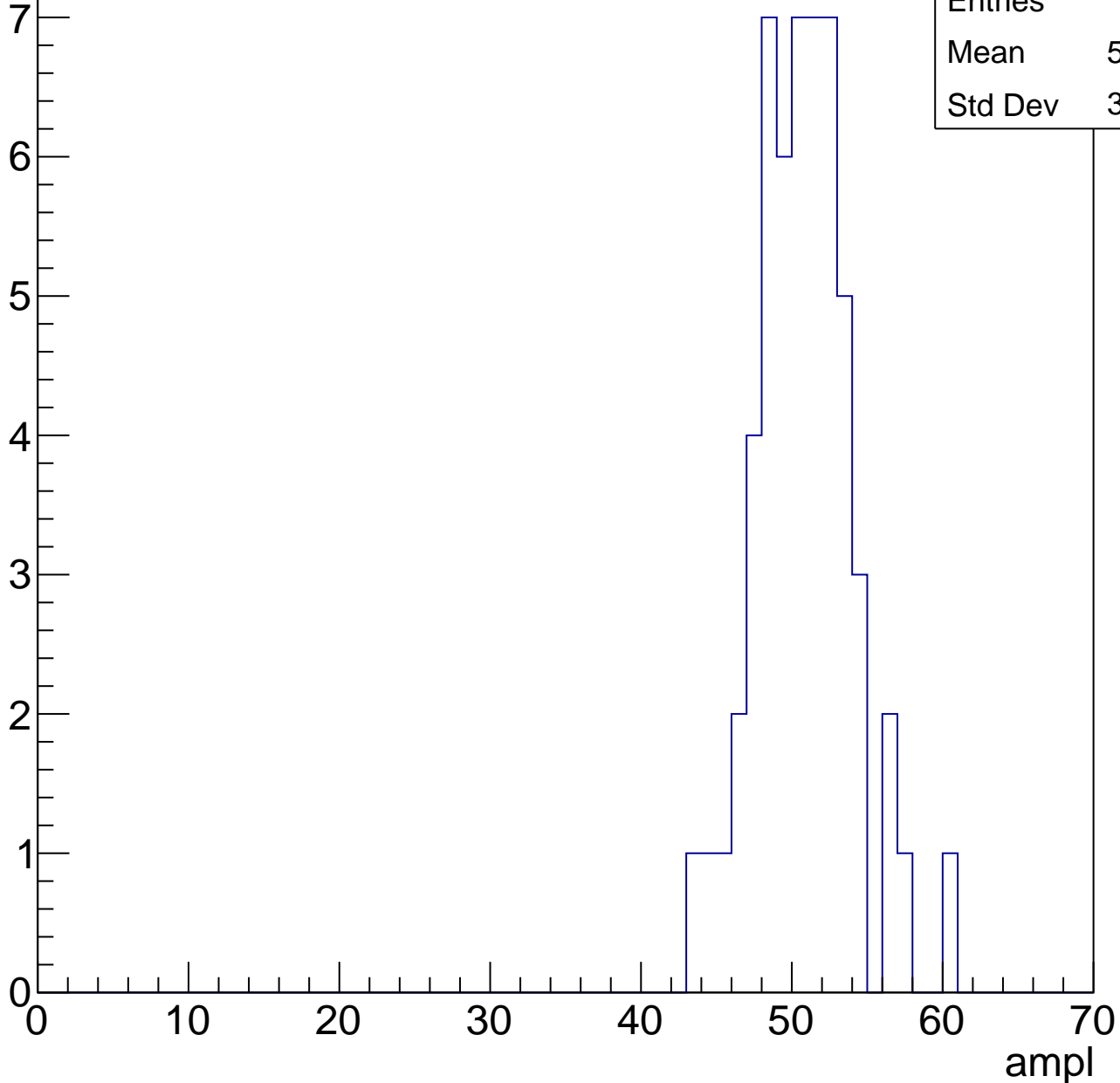


B0L001S, U14-ch58, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	50.35
Std Dev	3.175

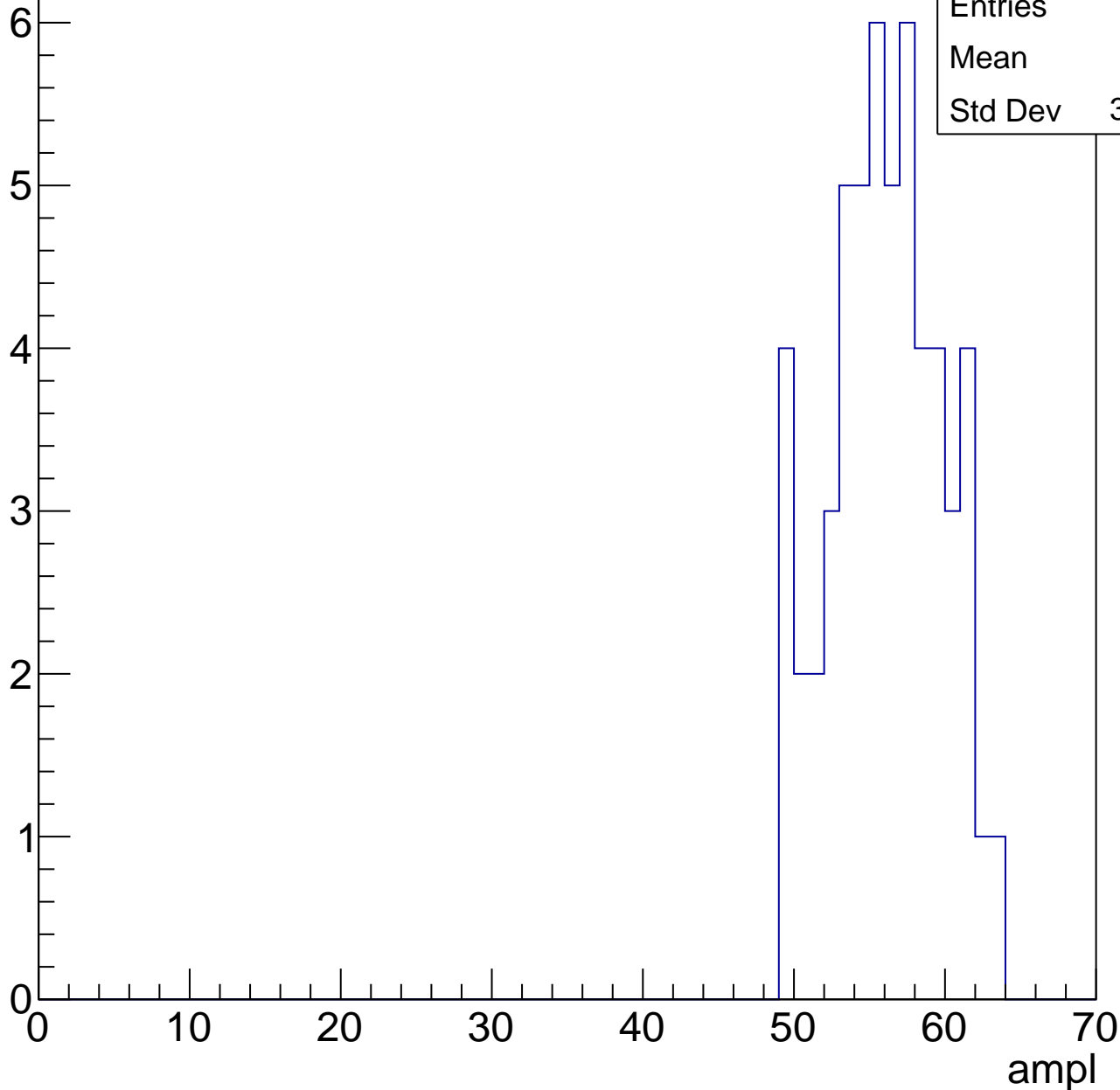


B0L001S, U14-ch58, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	55.6
Std Dev	3.616

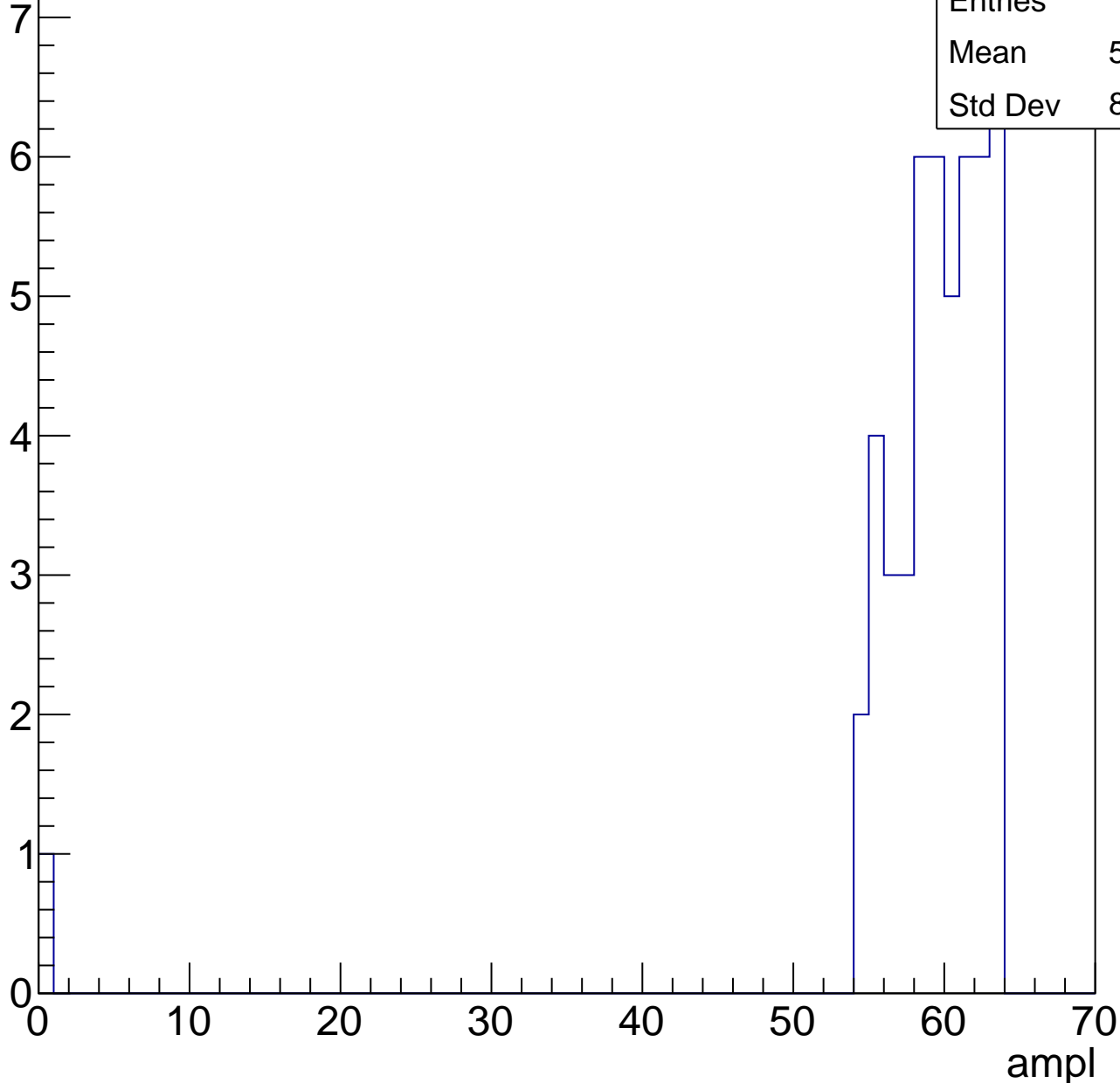


B0L001S, U14-ch58, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.12
Std Dev	8.803



B0L001S, U14-ch58, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

ampl

Entries

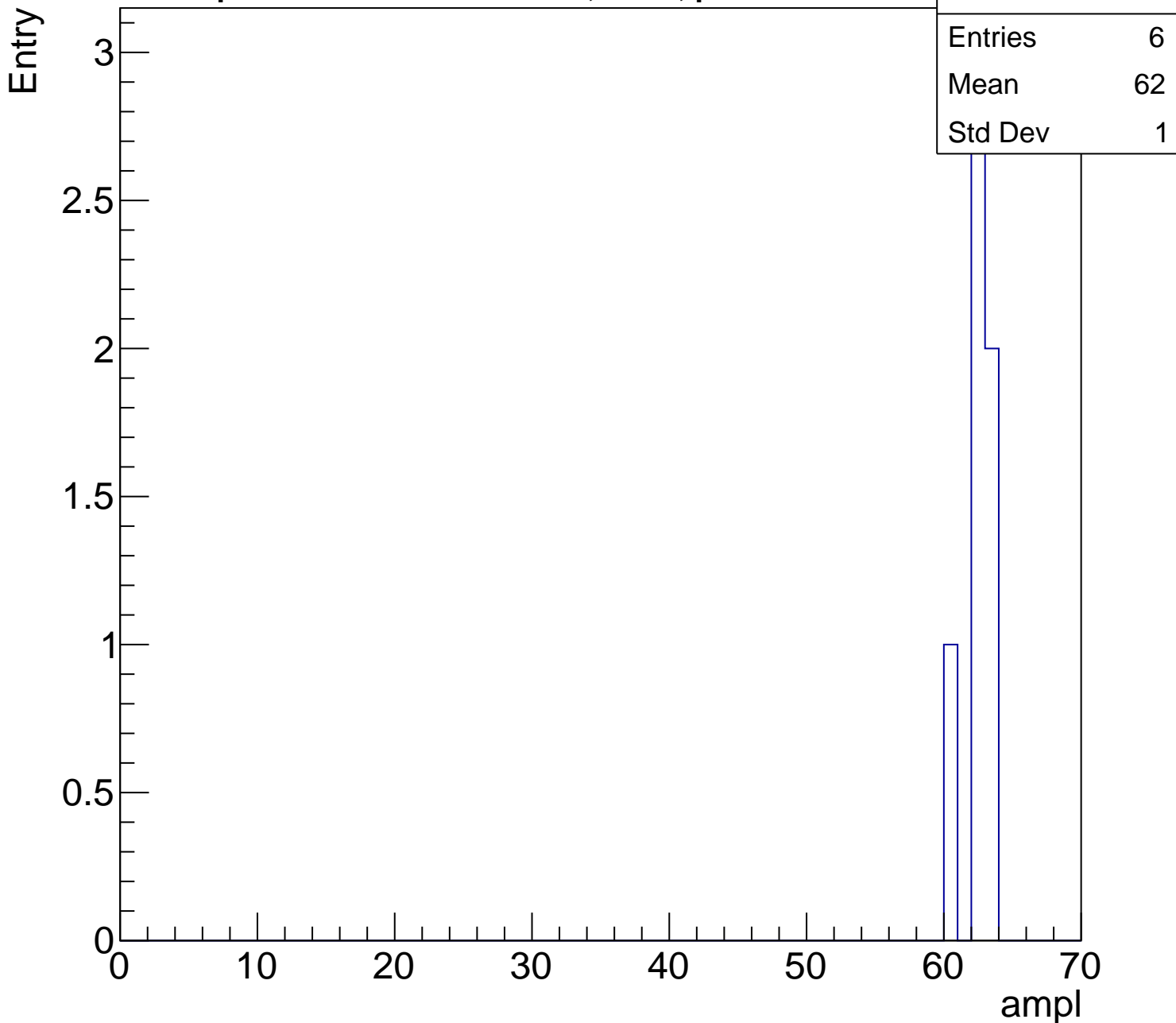
6

Mean

62

Std Dev

1



B0L001S, U14-ch58, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch59, adc0

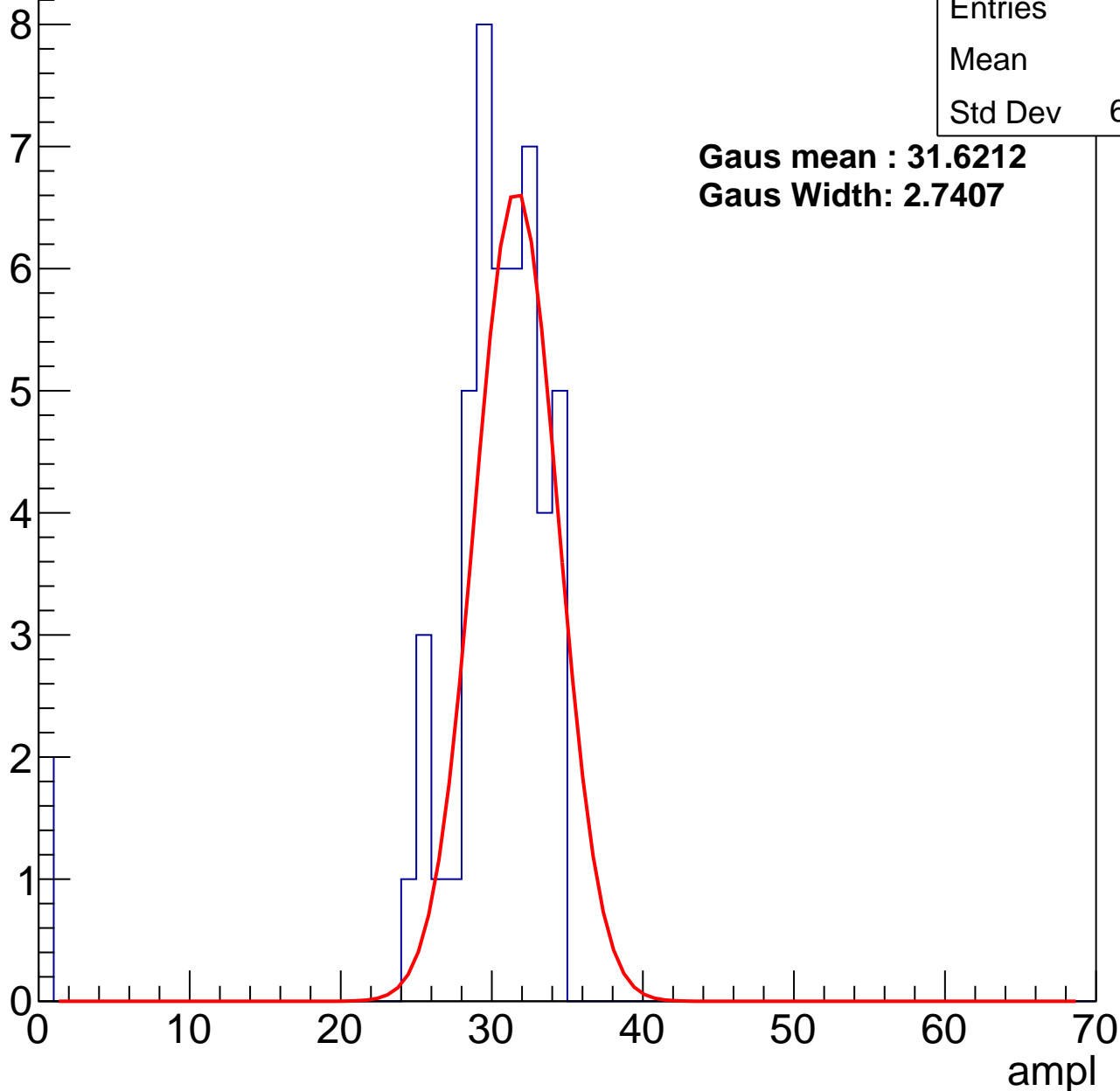
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	28.9
Std Dev	6.475

Gaus mean : 31.6212

Gaus Width: 2.7407



B0L001S, U14-ch59, adc1

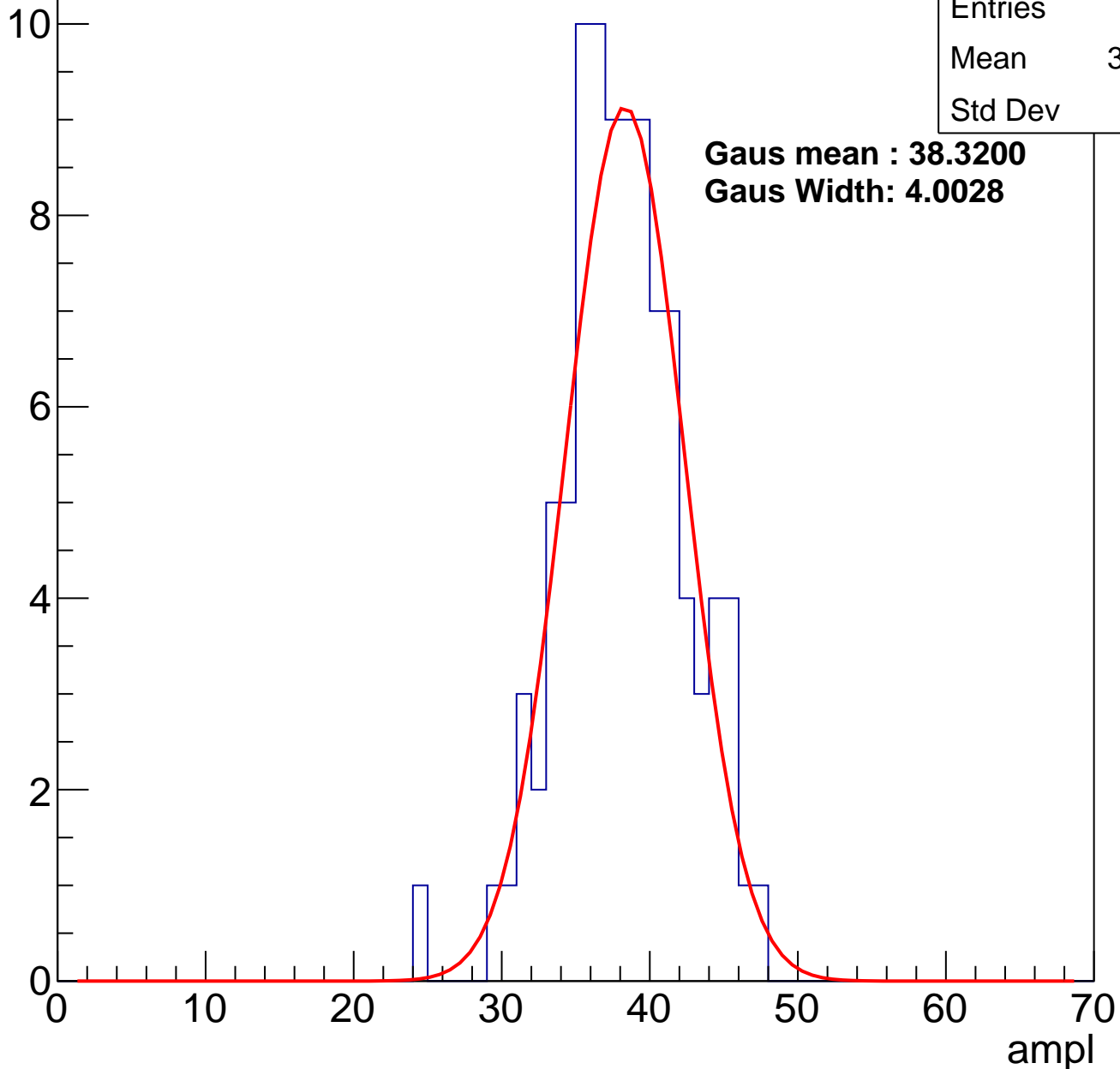
calib_packv5_042523_0143.root, FC#9, port A1

Entries	96
Mean	37.75
Std Dev	4.11

Gaus mean : 38.3200

Gaus Width: 4.0028

Entry



B0L001S, U14-ch59, adc2

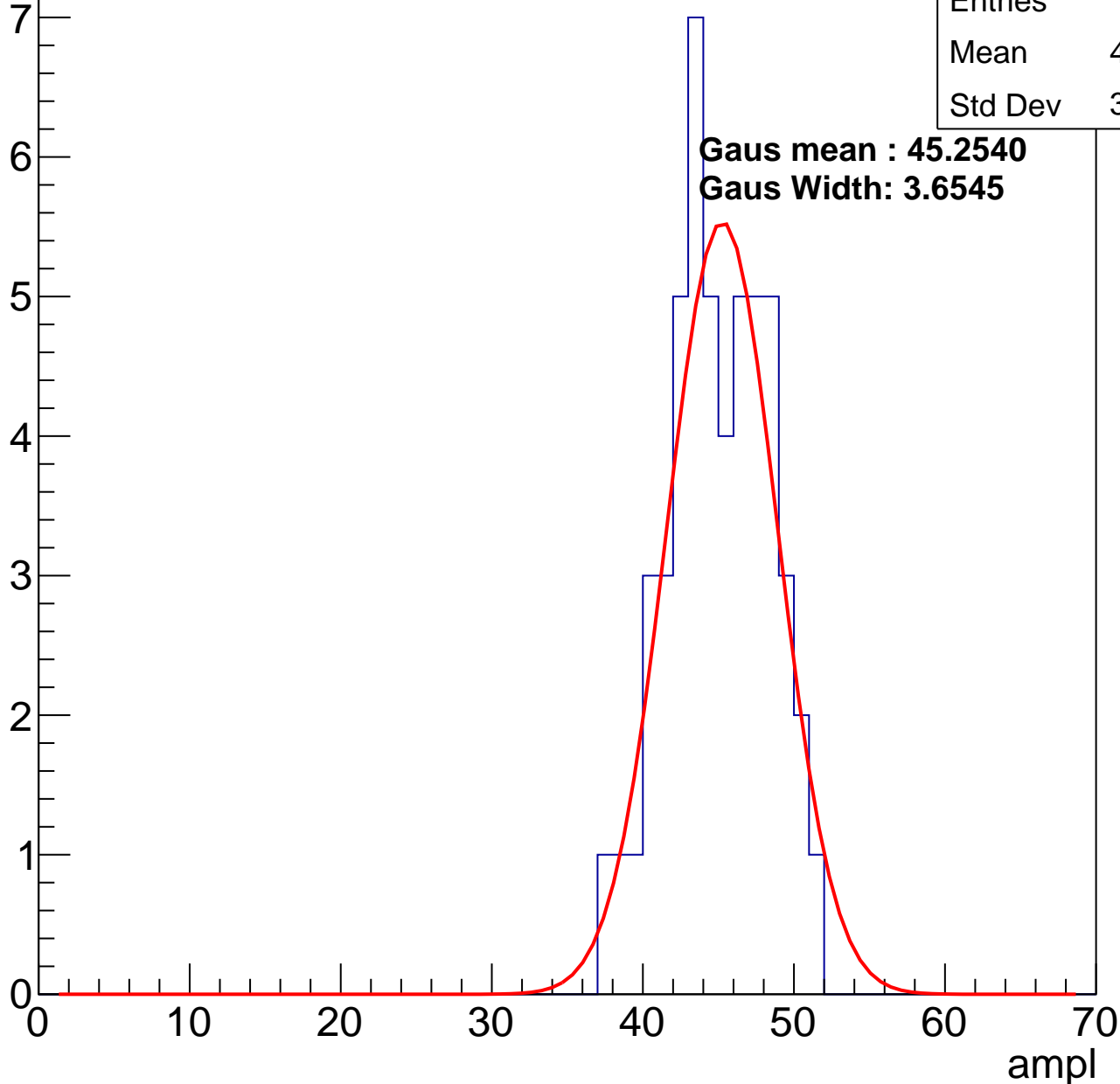
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	44.53
Std Dev	3.262

Gaus mean : 45.2540

Gaus Width: 3.6545

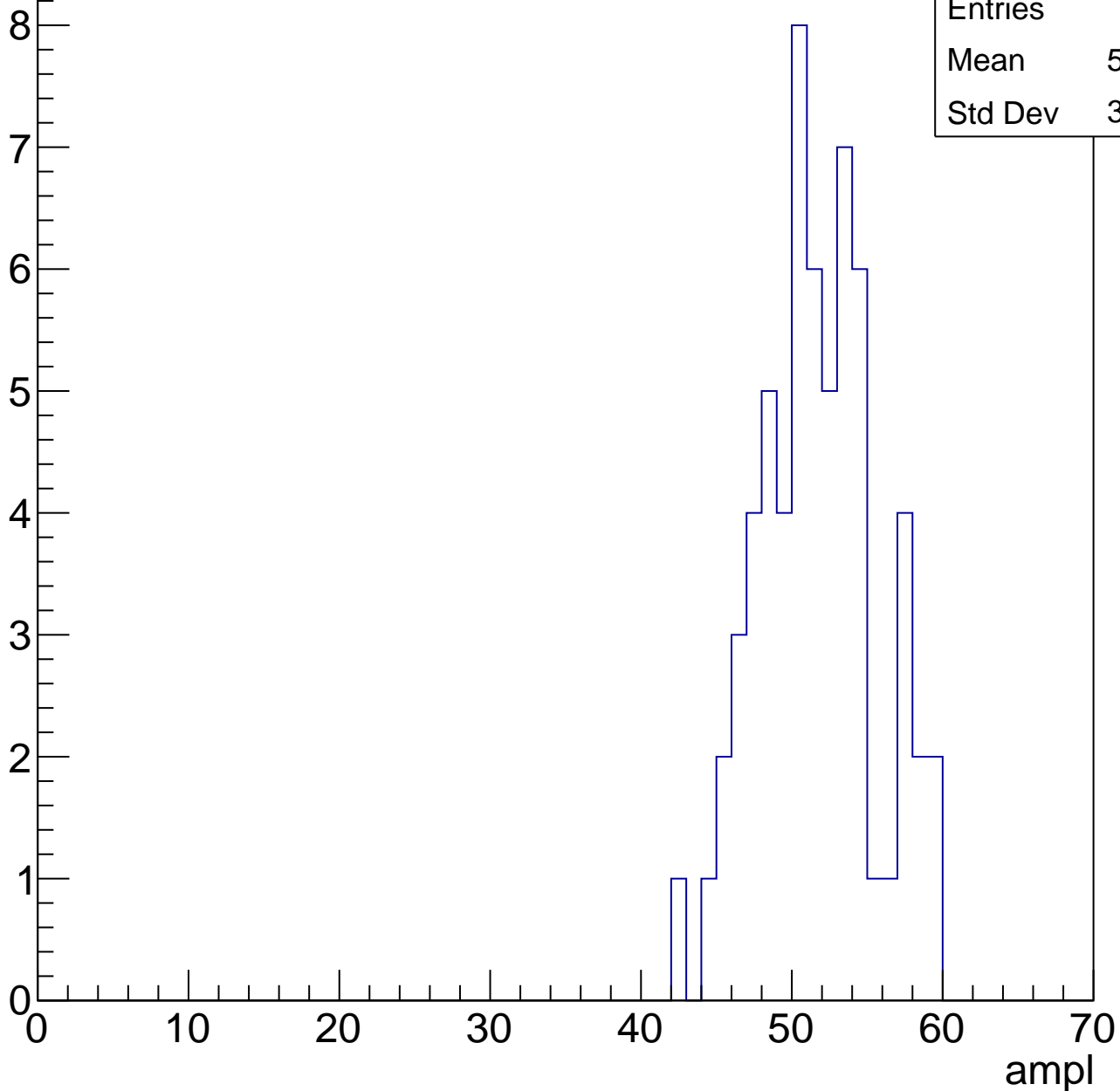


B0L001S, U14-ch59, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	51.16
Std Dev	3.823

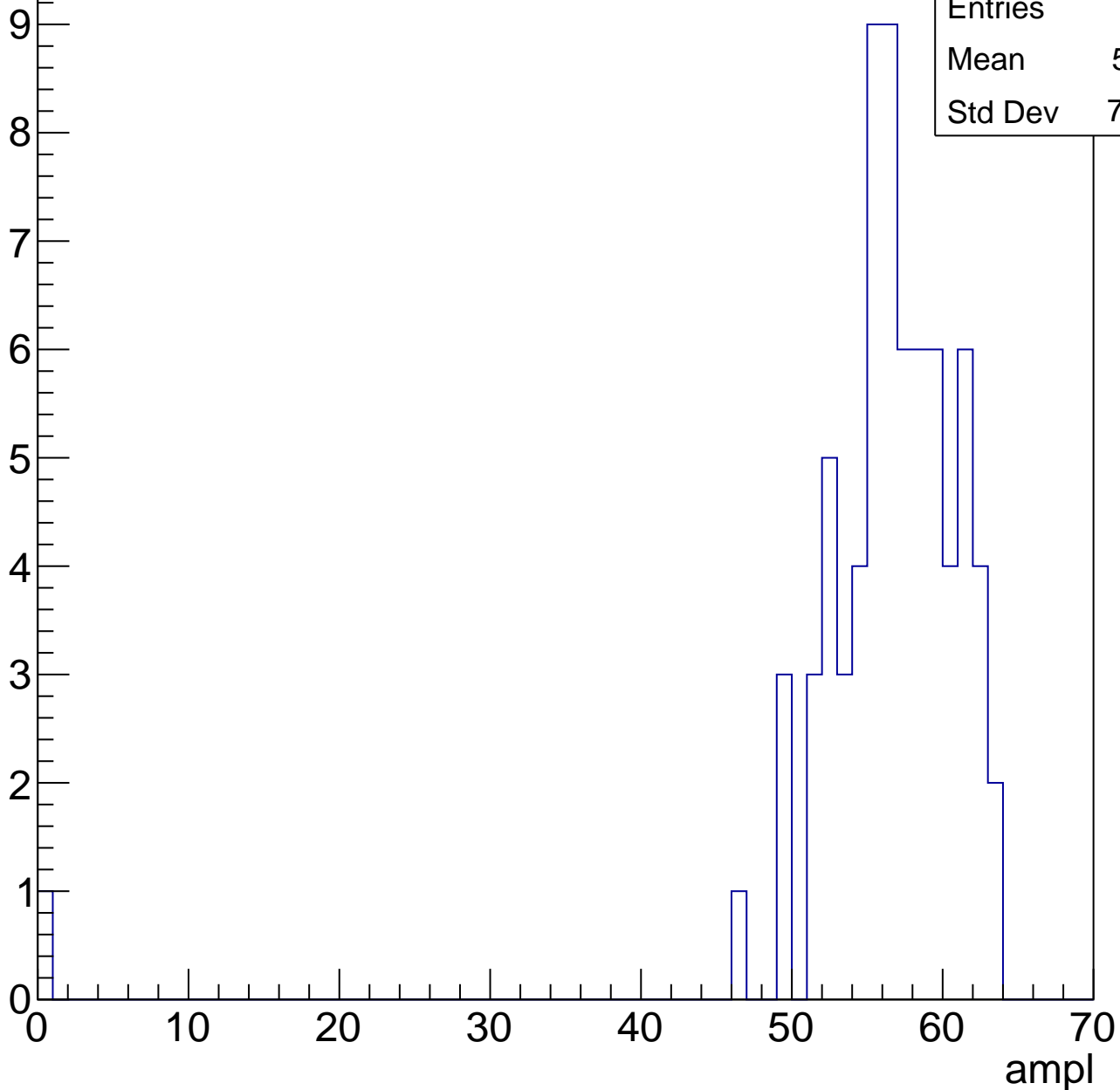


B0L001S, U14-ch59, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

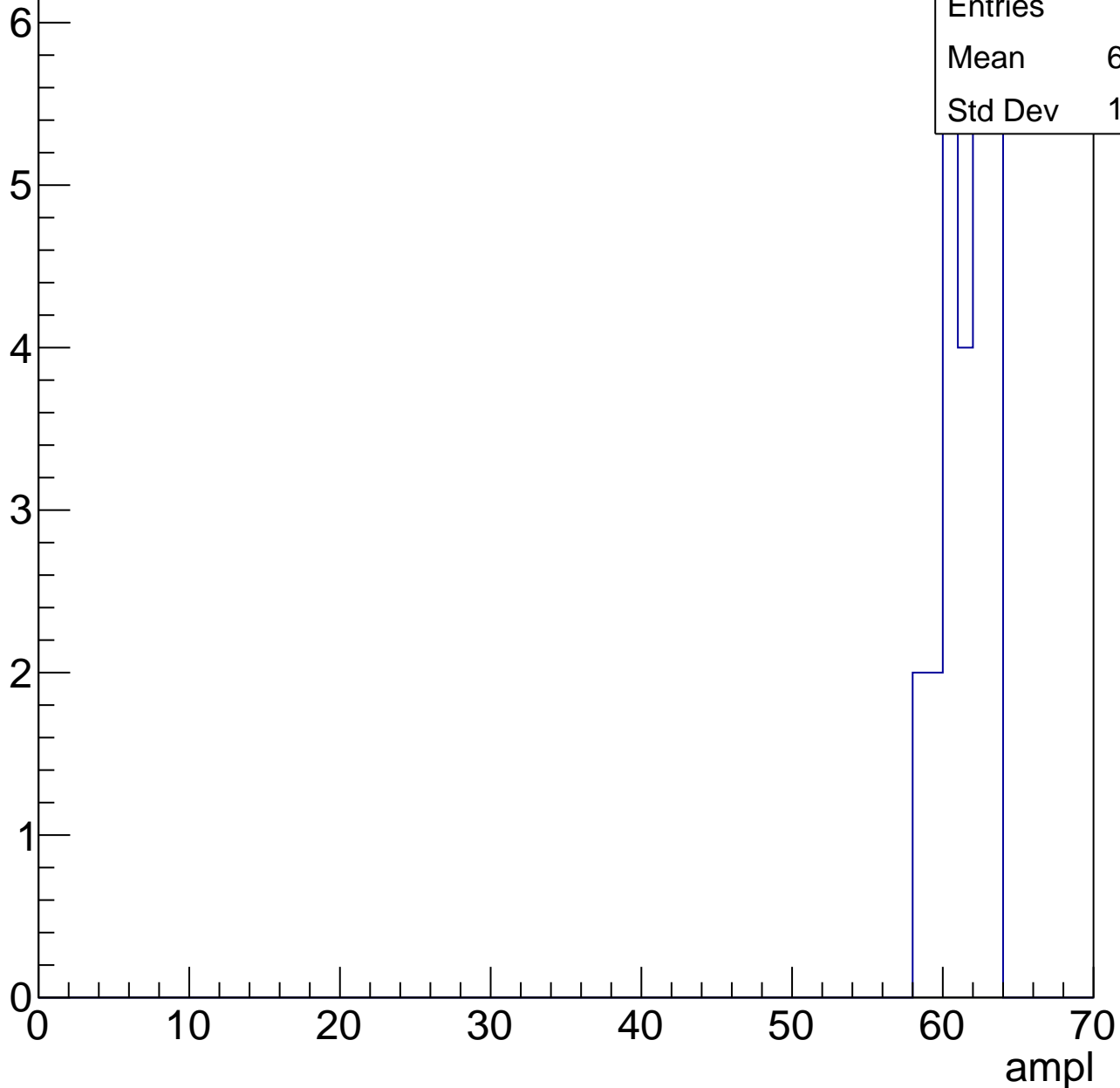
Entries	72
Mean	55.61
Std Dev	7.564



B0L001S, U14-ch59, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch59, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch59, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	22
Std Dev	0

ampl

B0L001S, U14-ch60, adc0

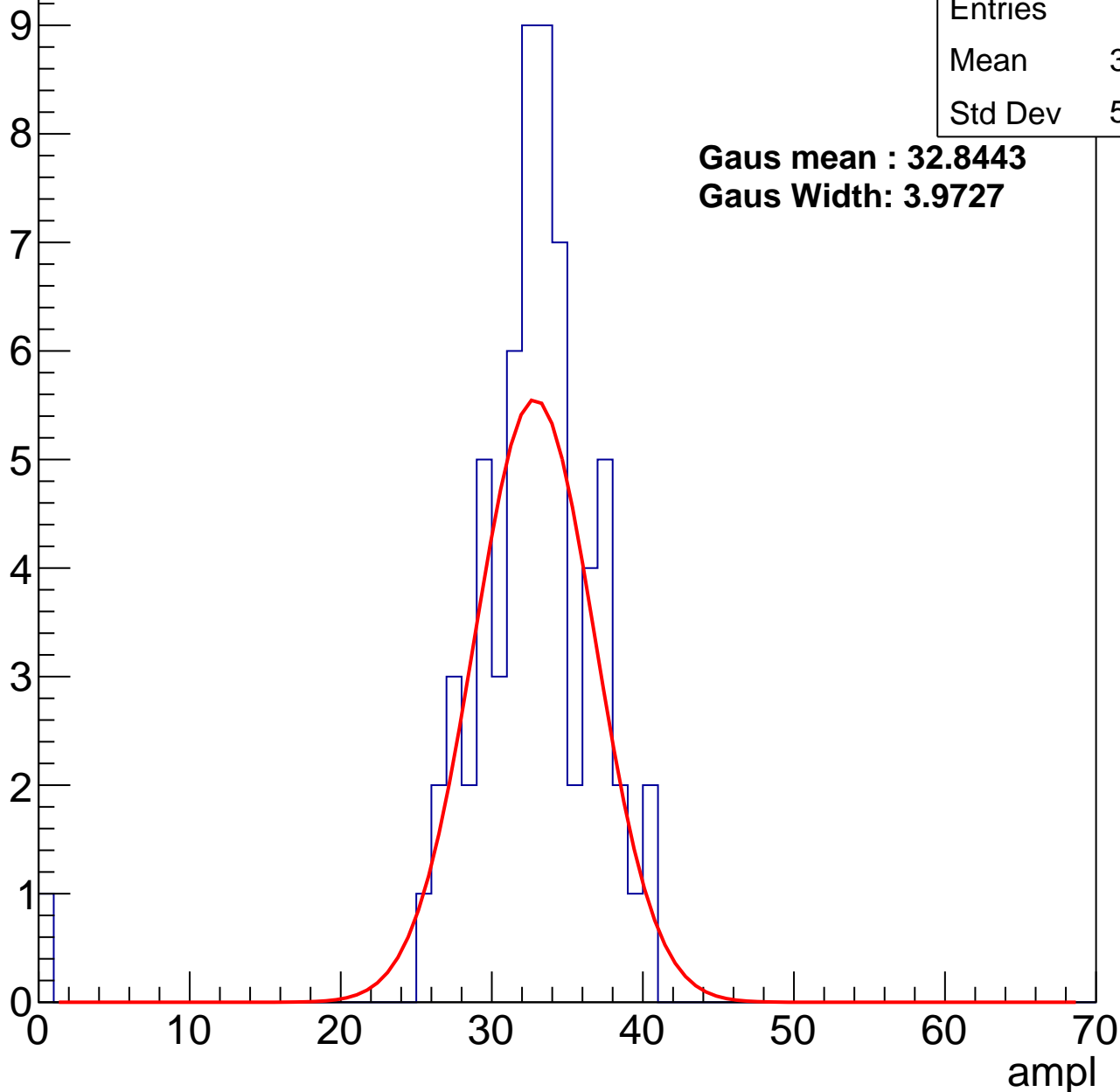
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	32.06
Std Dev	5.315

Gaus mean : 32.8443

Gaus Width: 3.9727



B0L001S, U14-ch60, adc1

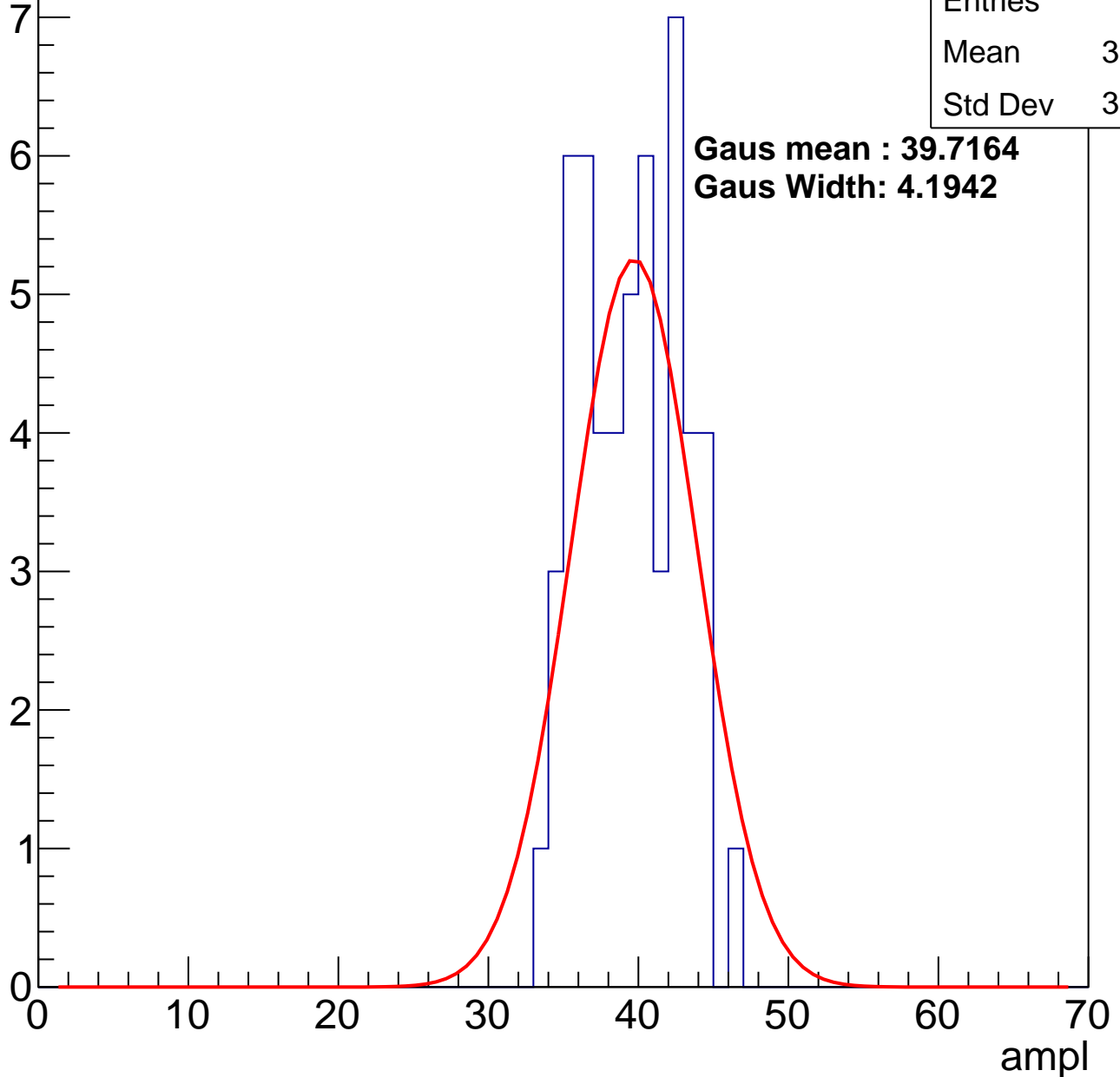
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	39.02
Std Dev	3.263

Gaus mean : 39.7164

Gaus Width: 4.1942



B0L001S, U14-ch60, adc2

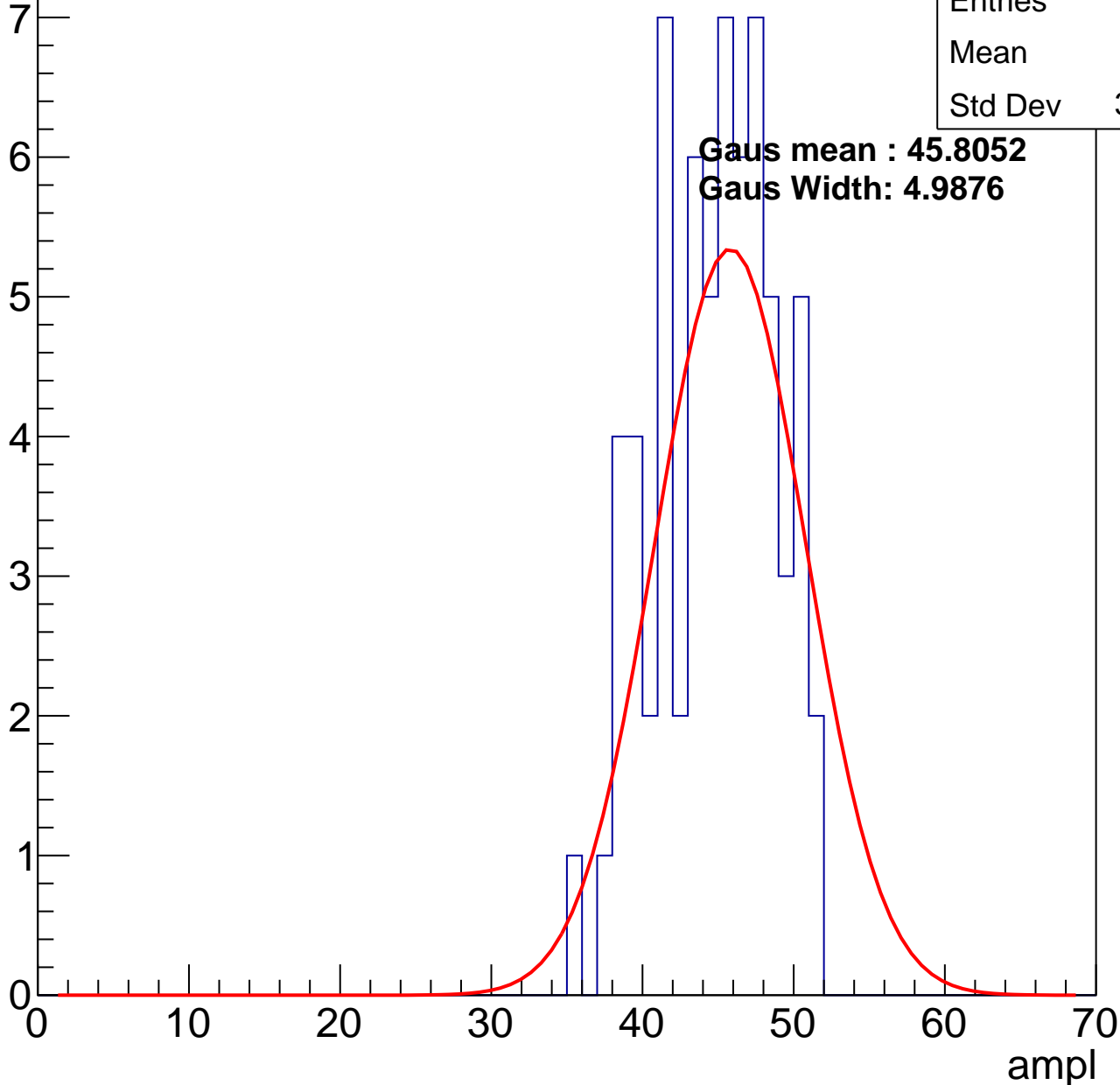
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	44.3
Std Dev	3.871

Gaus mean : 45.8052

Gaus Width: 4.9876

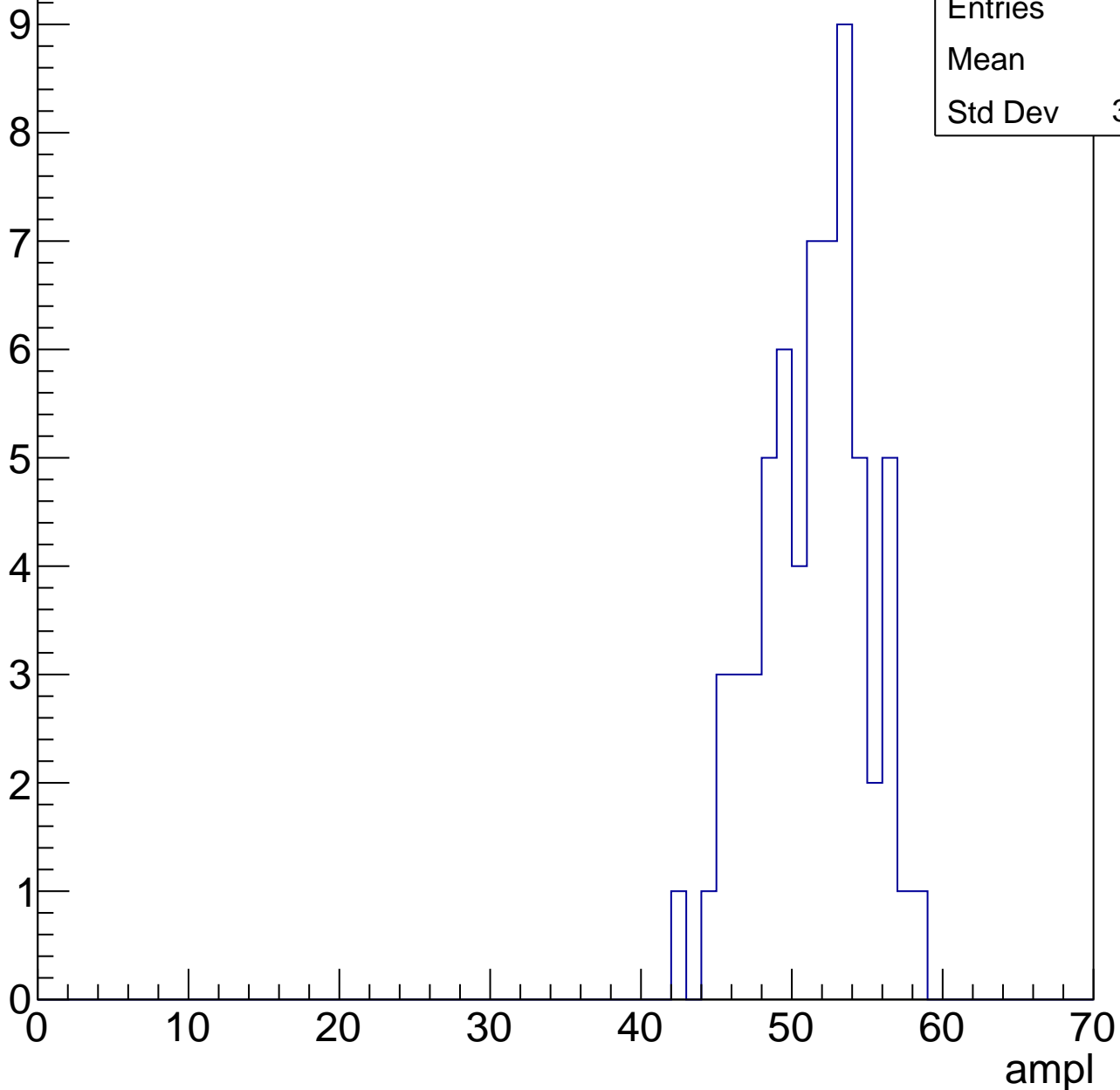


B0L001S, U14-ch60, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.9
Std Dev	3.481

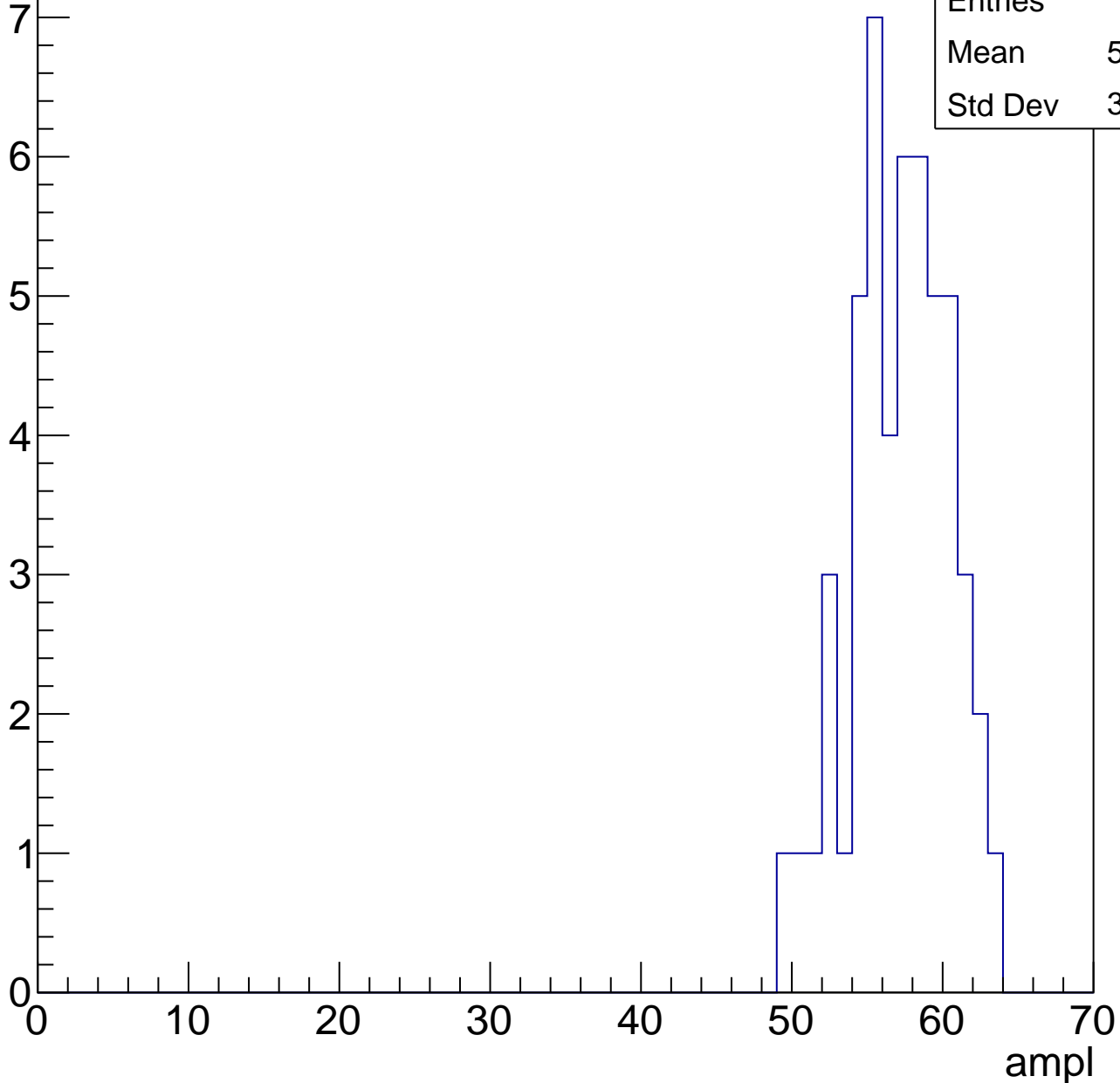


B0L001S, U14-ch60, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	56.73
Std Dev	3.187

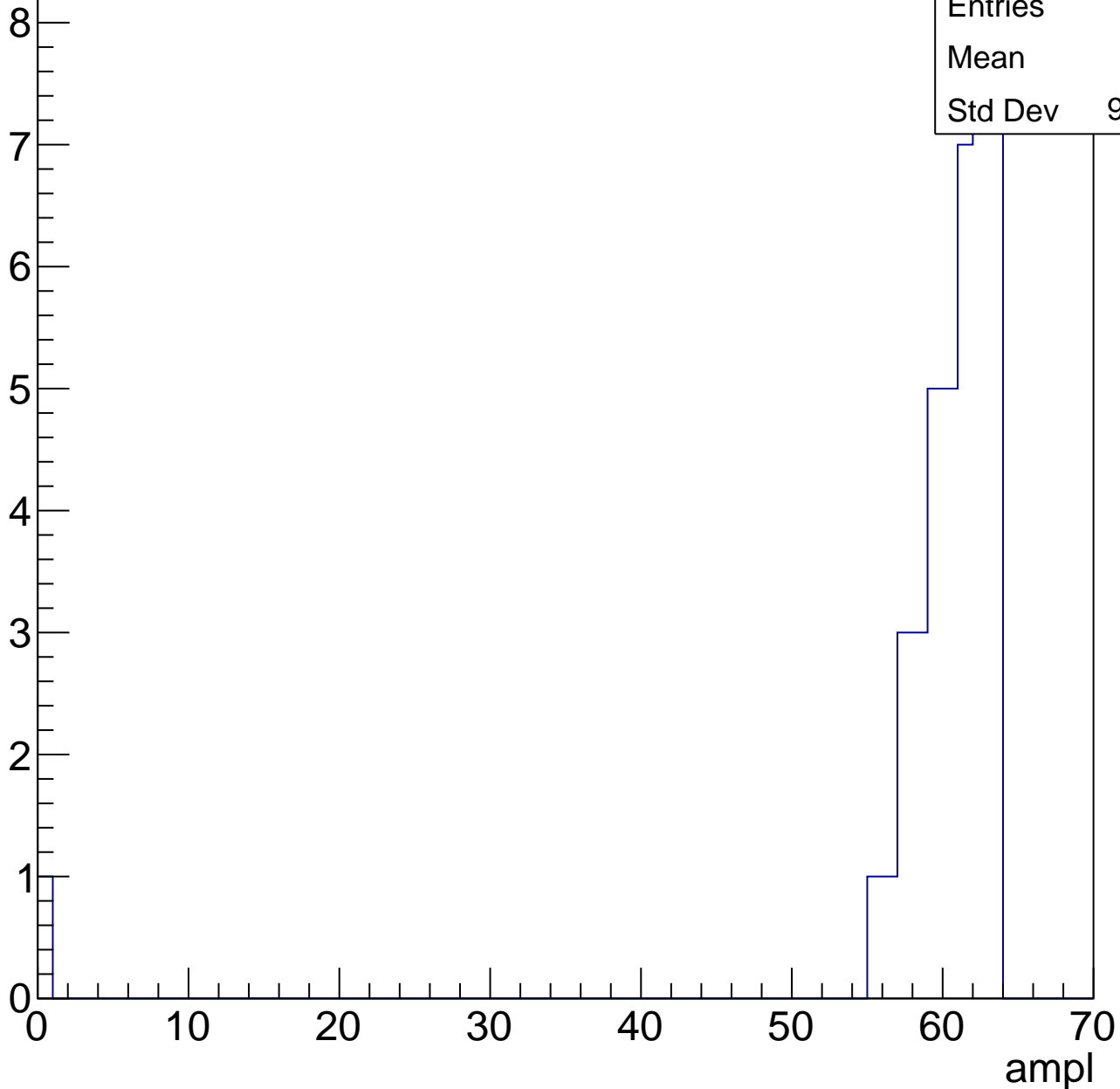


B0L001S, U14-ch60, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	42
Mean	59
Std Dev	9.454



B0L001S, U14-ch60, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch60, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch61, adc0

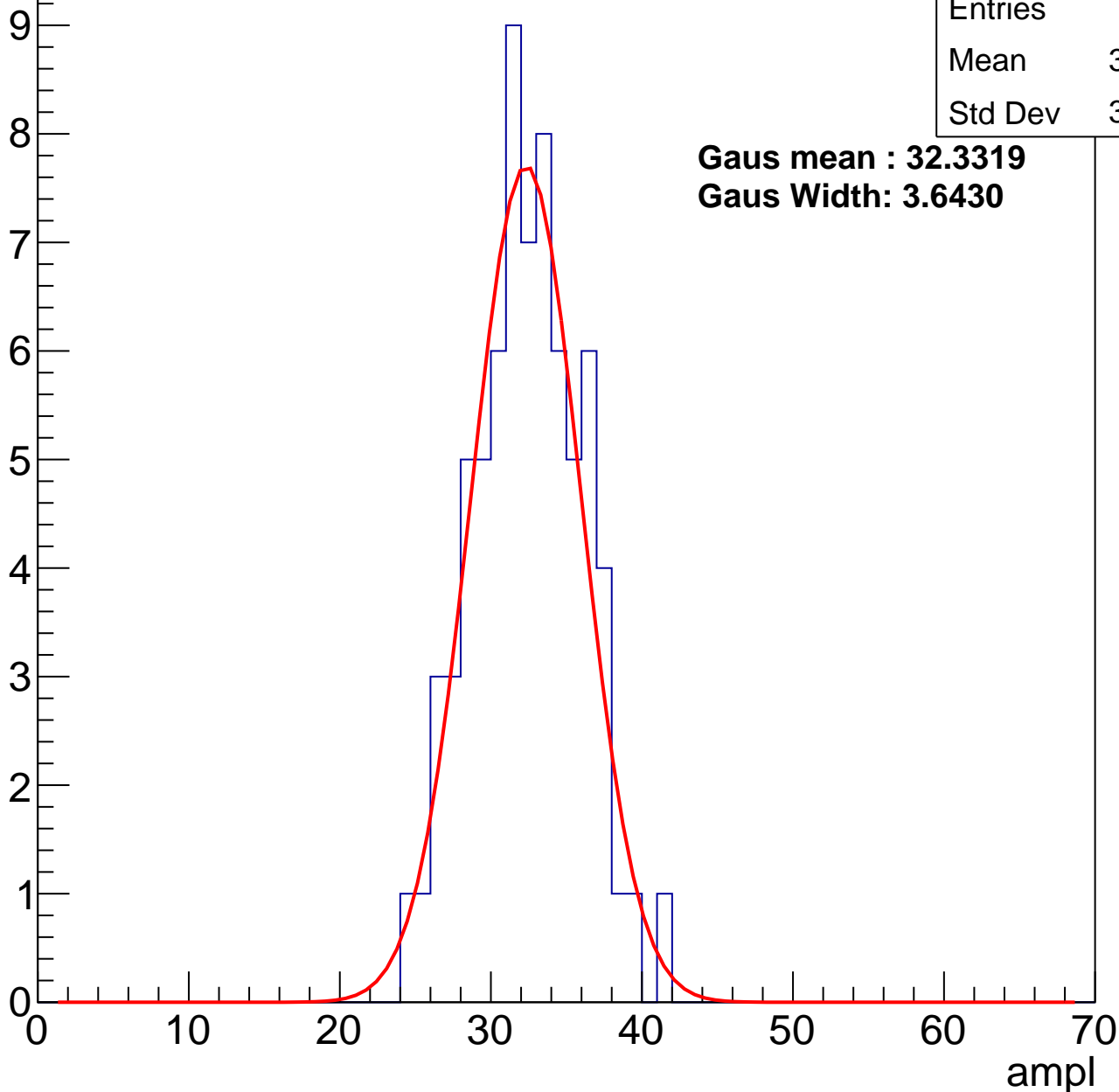
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	31.96
Std Dev	3.522

Gaus mean : 32.3319

Gaus Width: 3.6430



B0L001S, U14-ch61, adc1

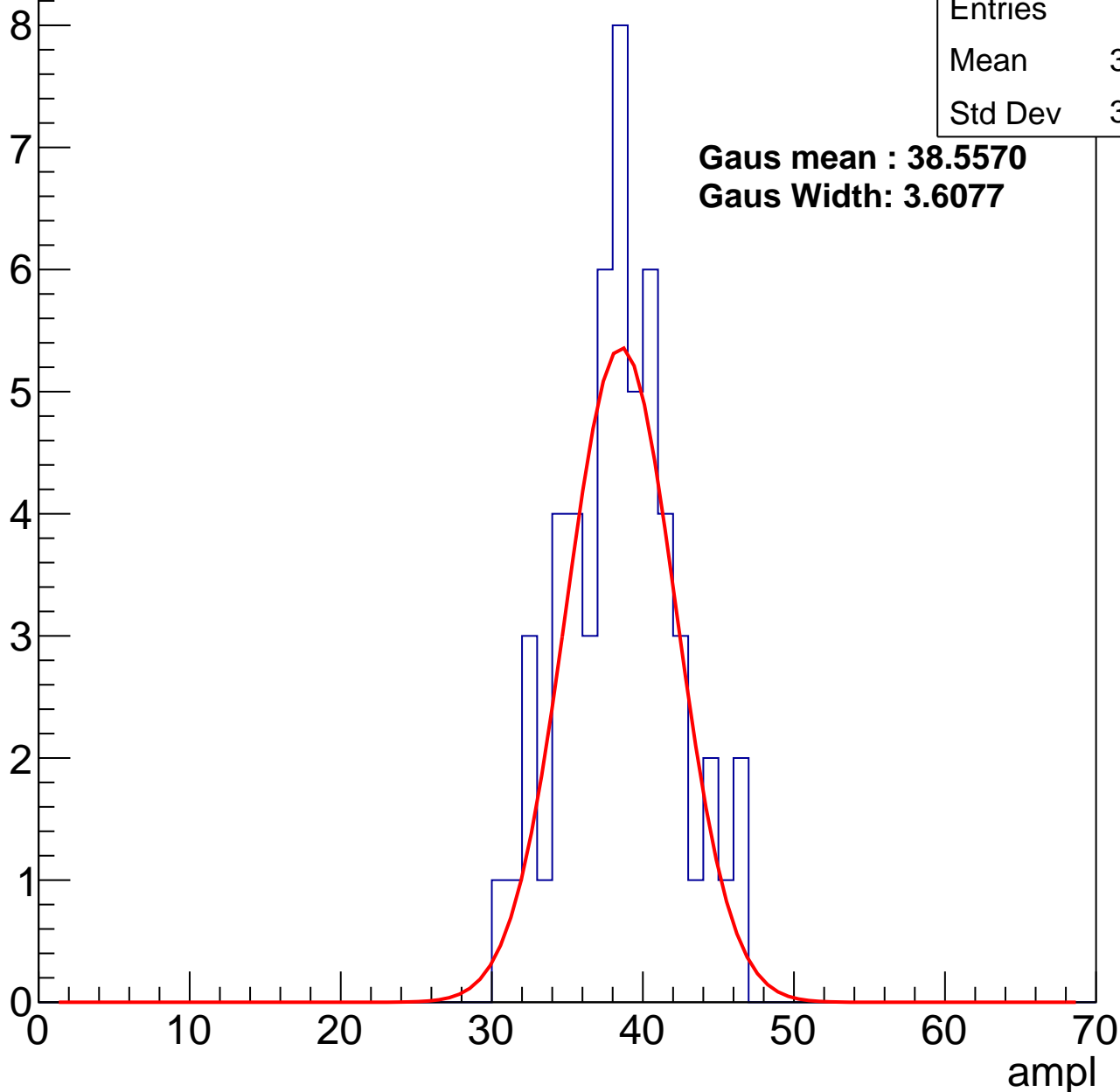
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	38.05
Std Dev	3.695

Gaus mean : 38.5570

Gaus Width: 3.6077



B0L001S, U14-ch61, adc2

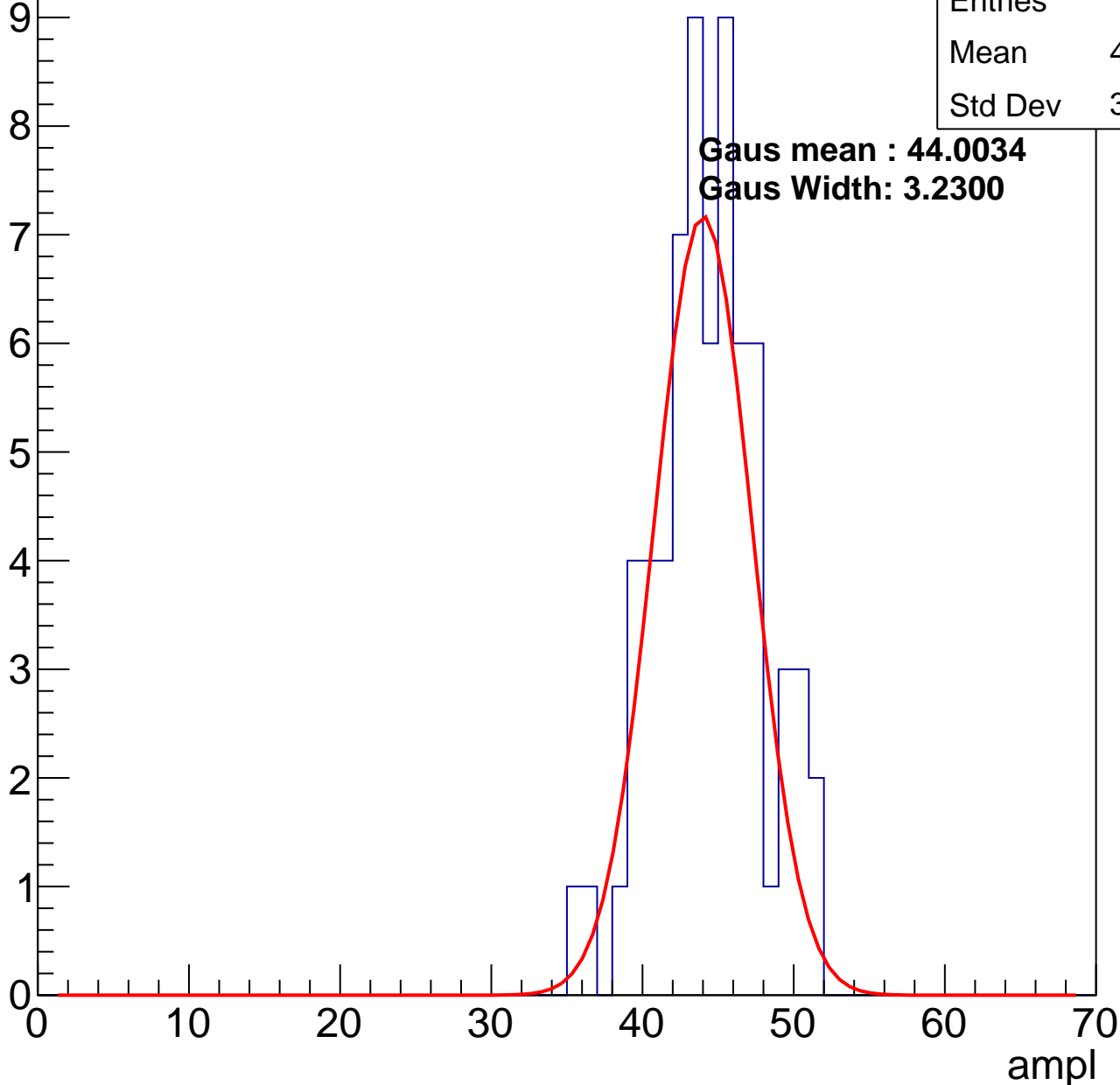
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.94
Std Dev	3.459

Gaus mean : 44.0034

Gaus Width: 3.2300

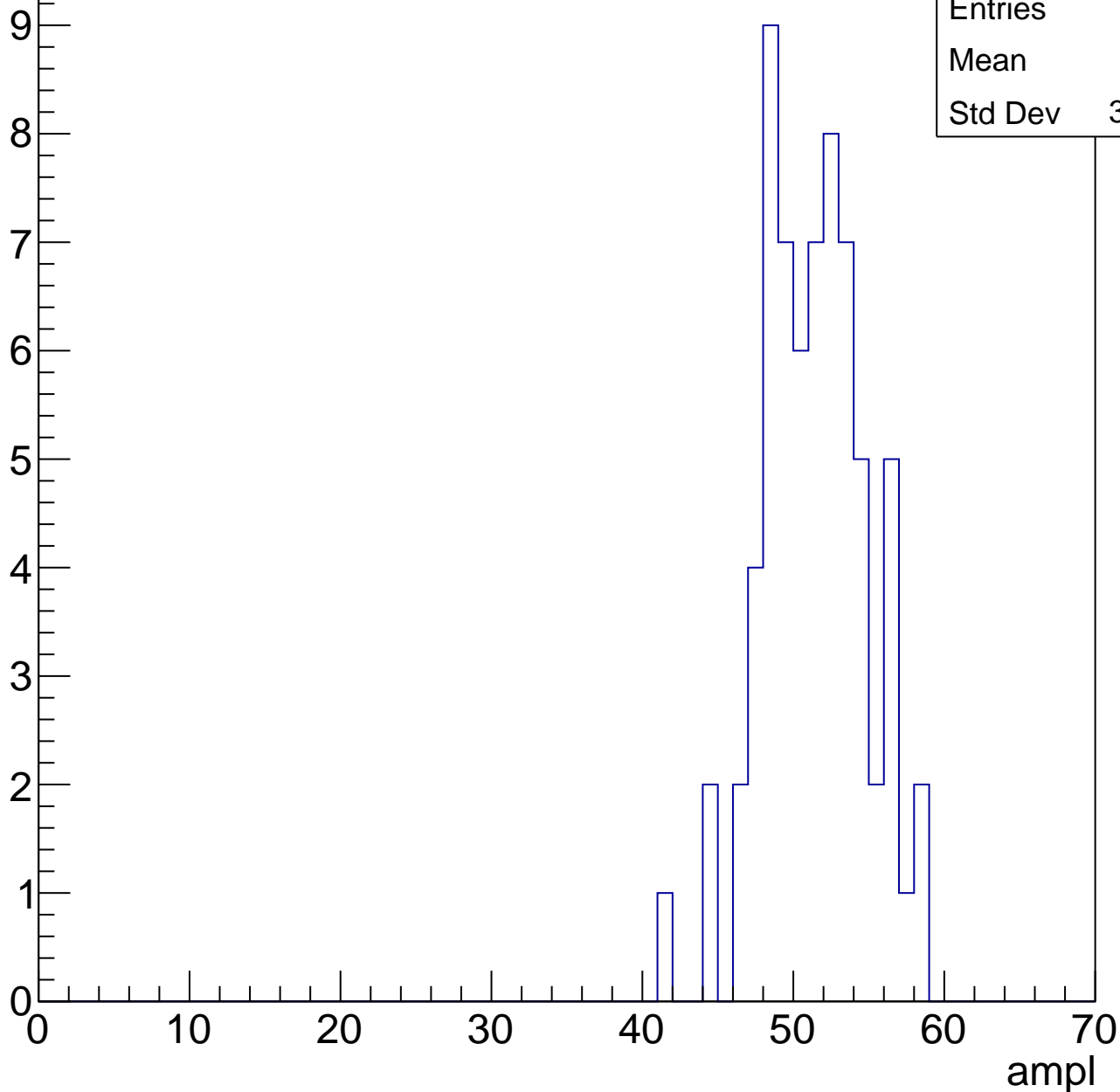


B0L001S, U14-ch61, adc3

calib_packv5_042523_0143.root, FC#9, port A1

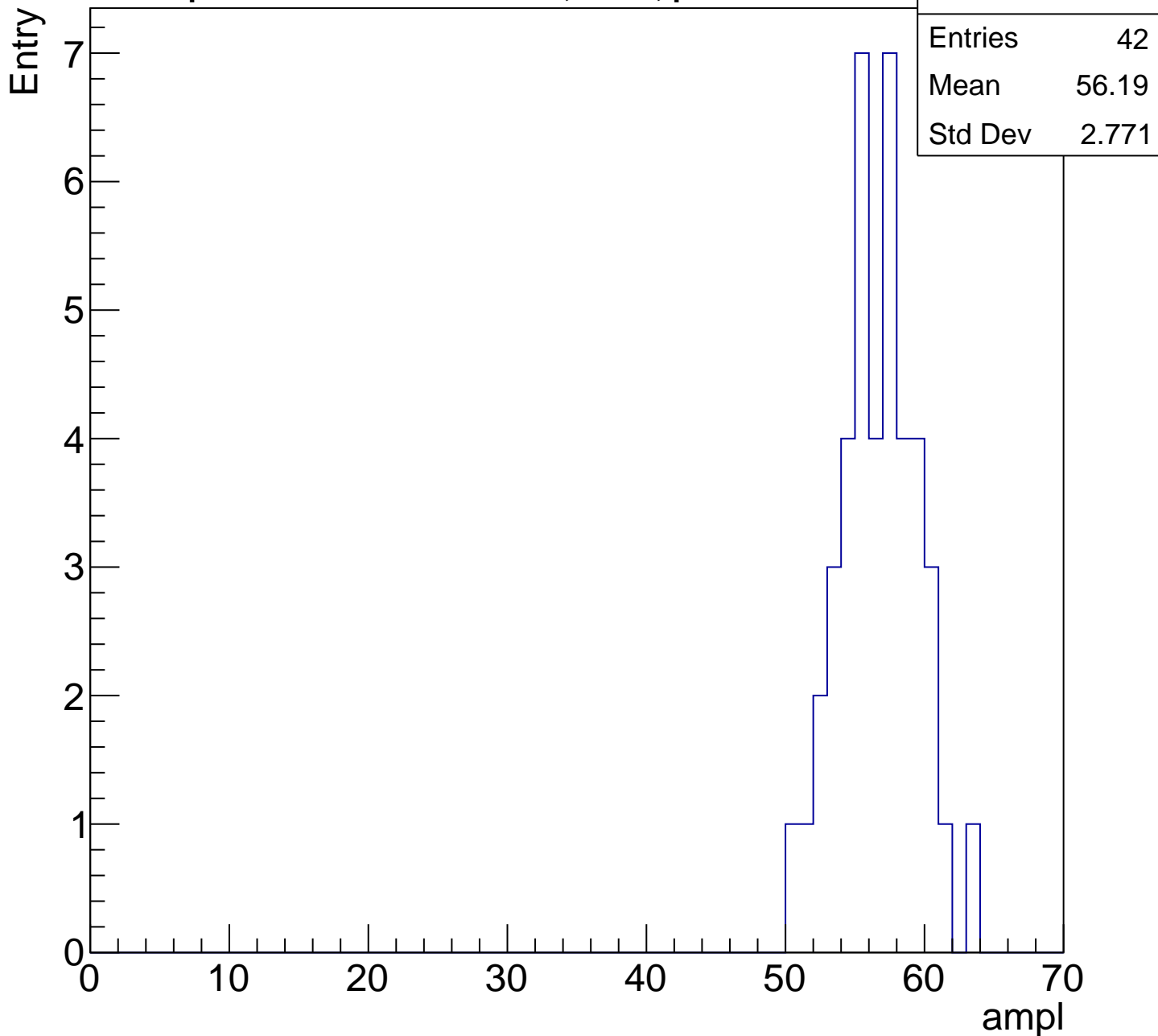
Entry

Entries	68
Mean	50.9
Std Dev	3.426



B0L001S, U14-ch61, adc4

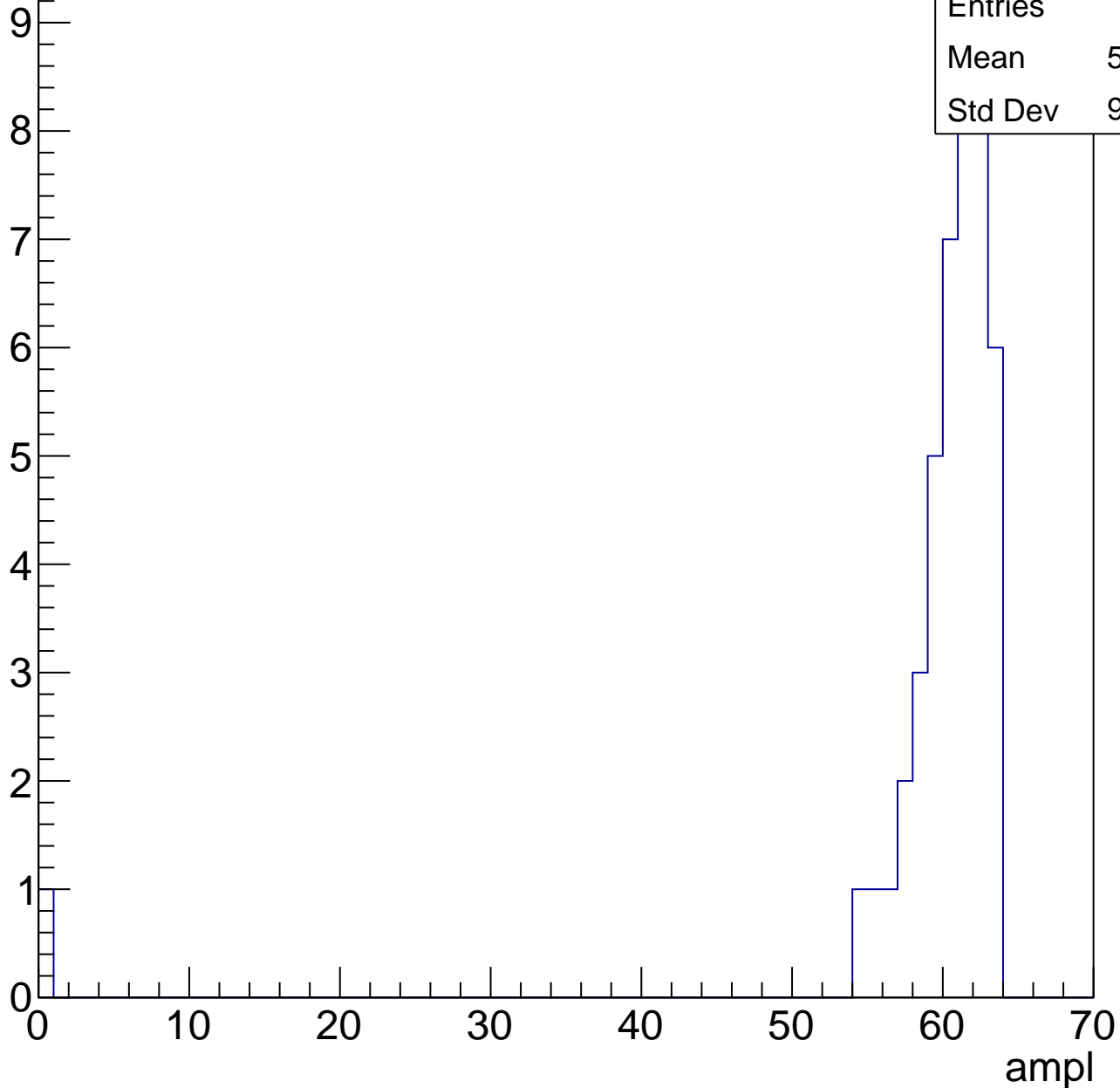
calib_packv5_042523_0143.root, FC#9, port A1



B0L001S, U14-ch61, adc5

calib_packv5_042523_0143.root, FC#9, port A1

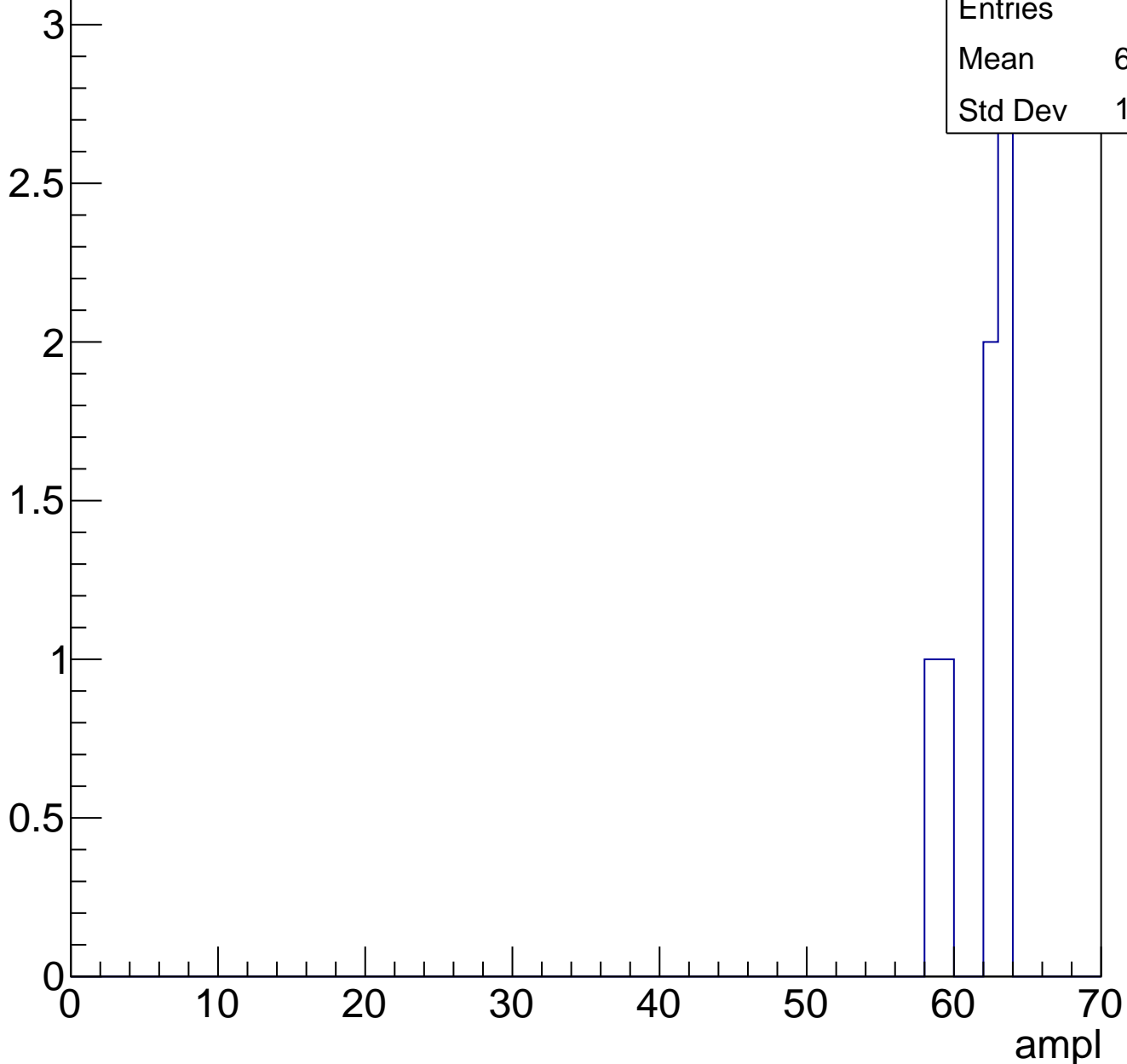
Entry



B0L001S, U14-ch61, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch61, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch62, adc0

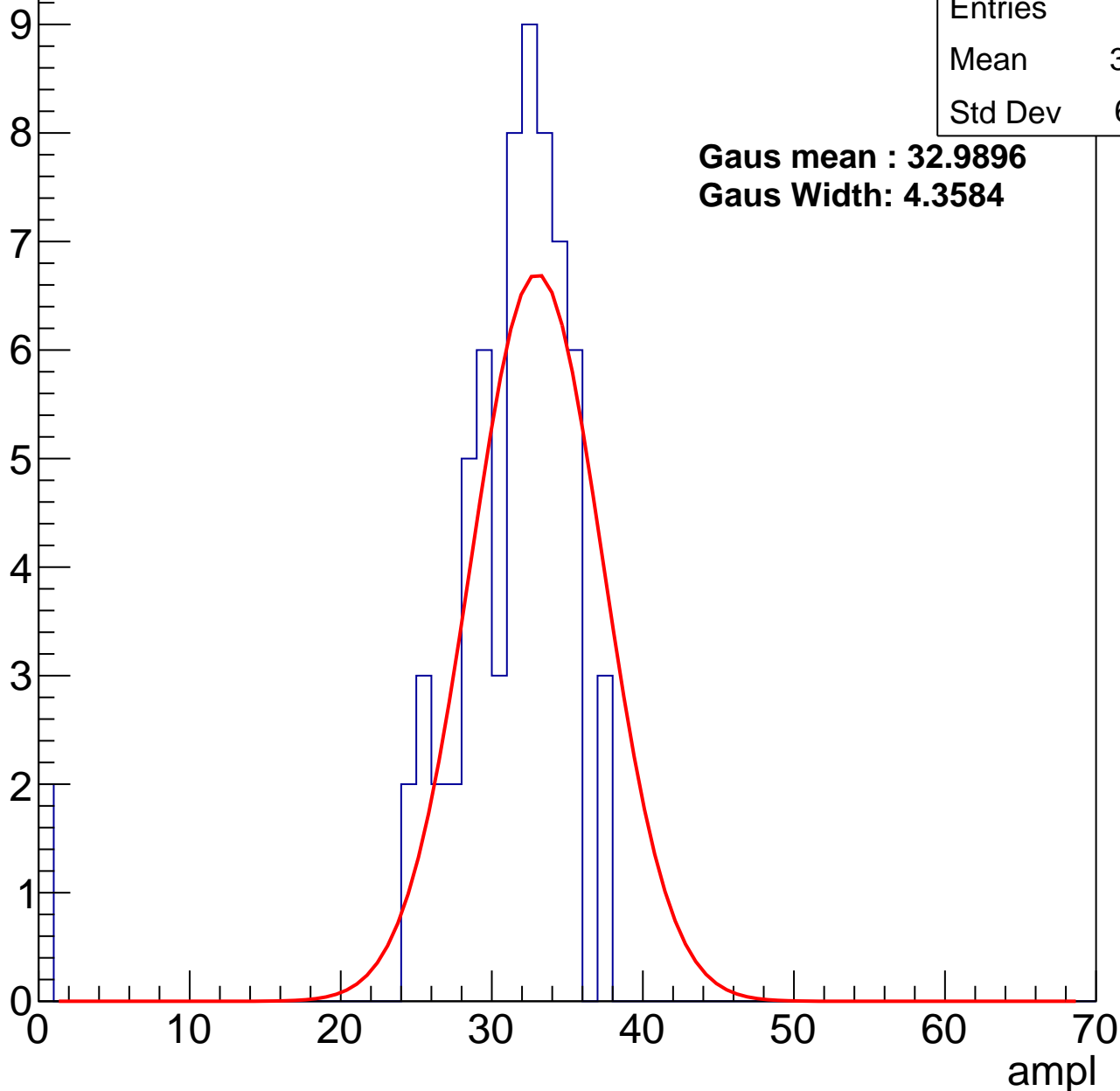
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.18
Std Dev	6.211

Gaus mean : 32.9896

Gaus Width: 4.3584



B0L001S, U14-ch62, adc1

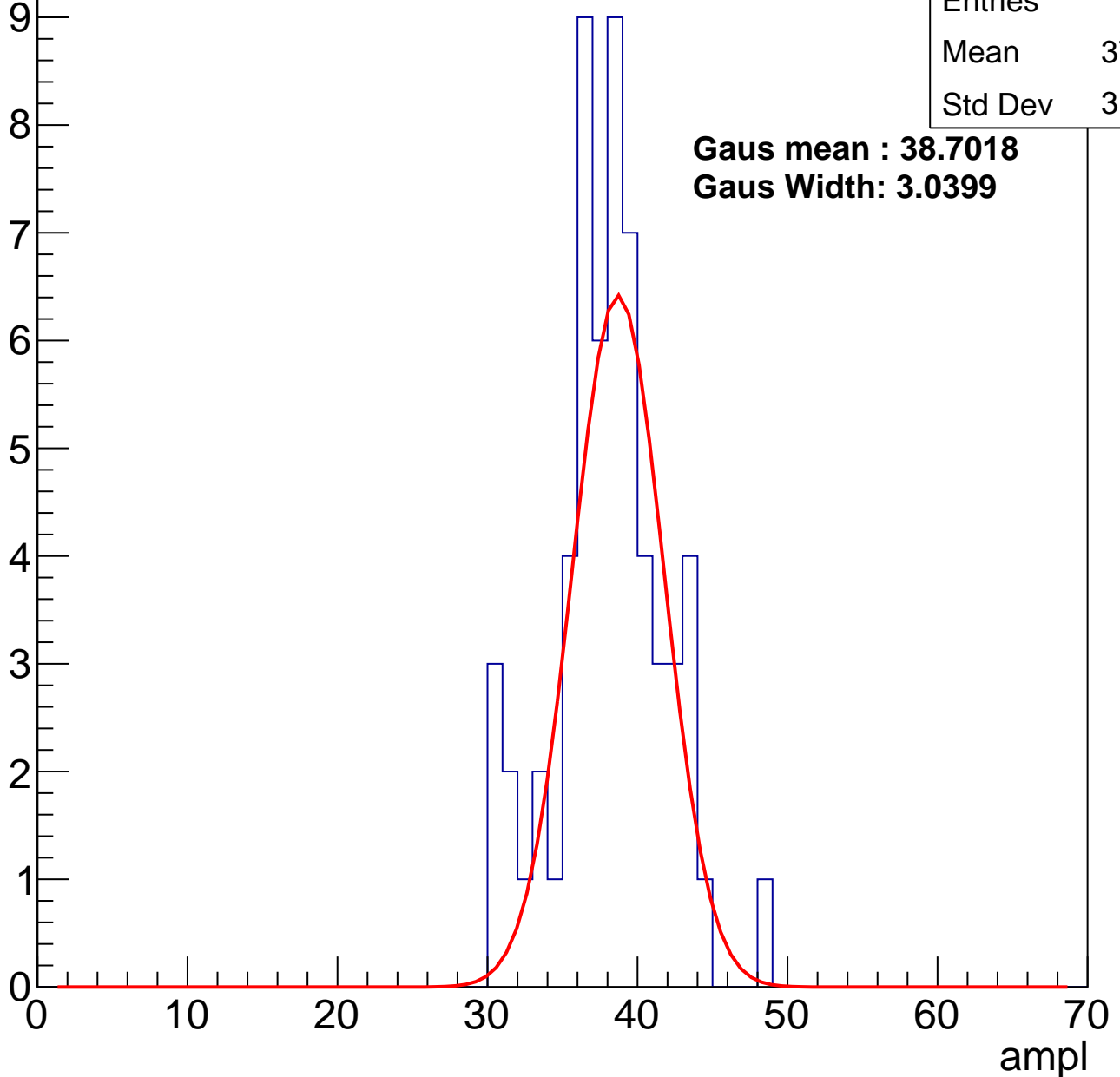
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.63
Std Dev	3.642

Gaus mean : 38.7018

Gaus Width: 3.0399



B0L001S, U14-ch62, adc2

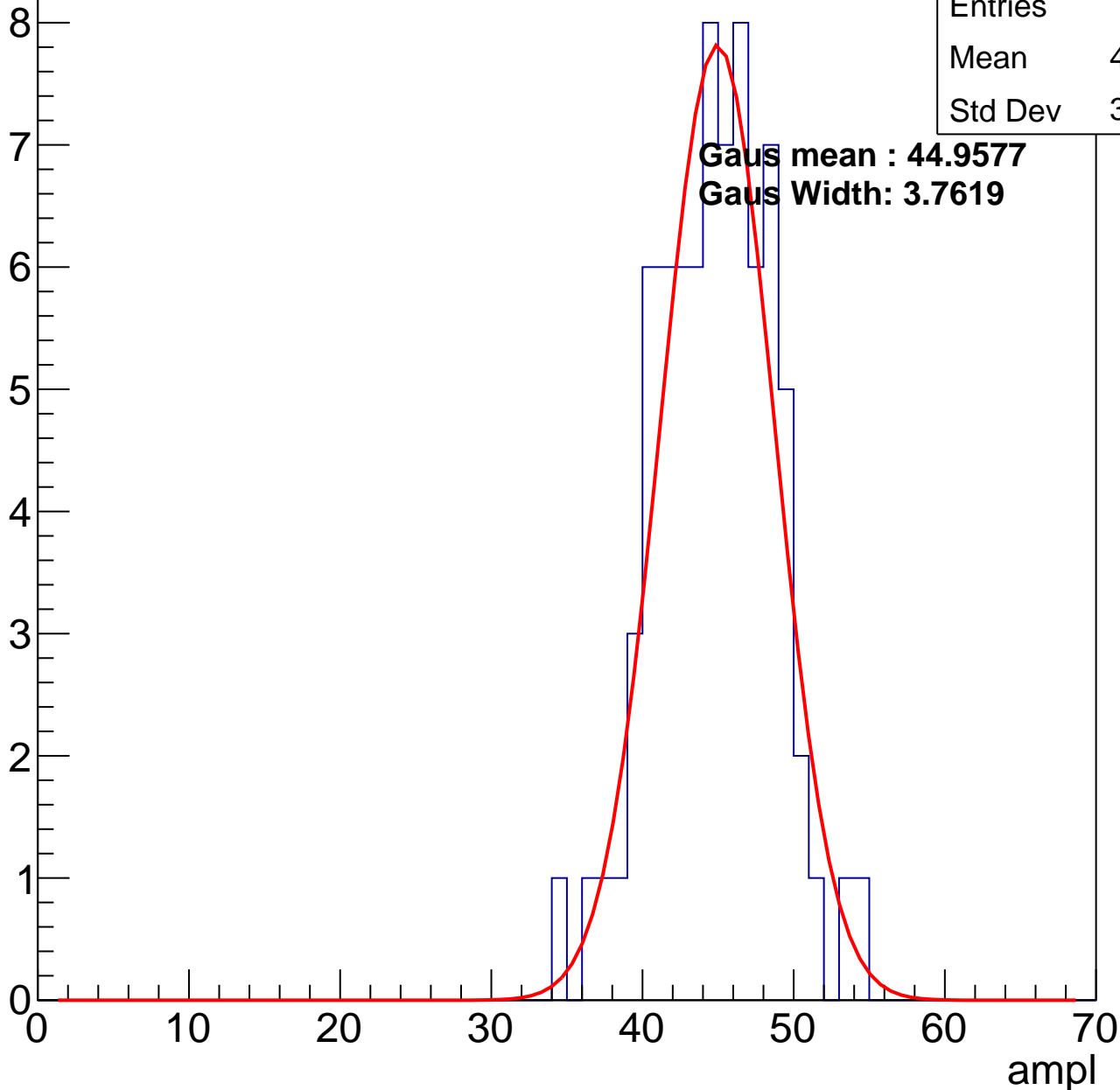
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	44.34
Std Dev	3.826

Gaus mean : 44.9577

Gaus Width: 3.7619

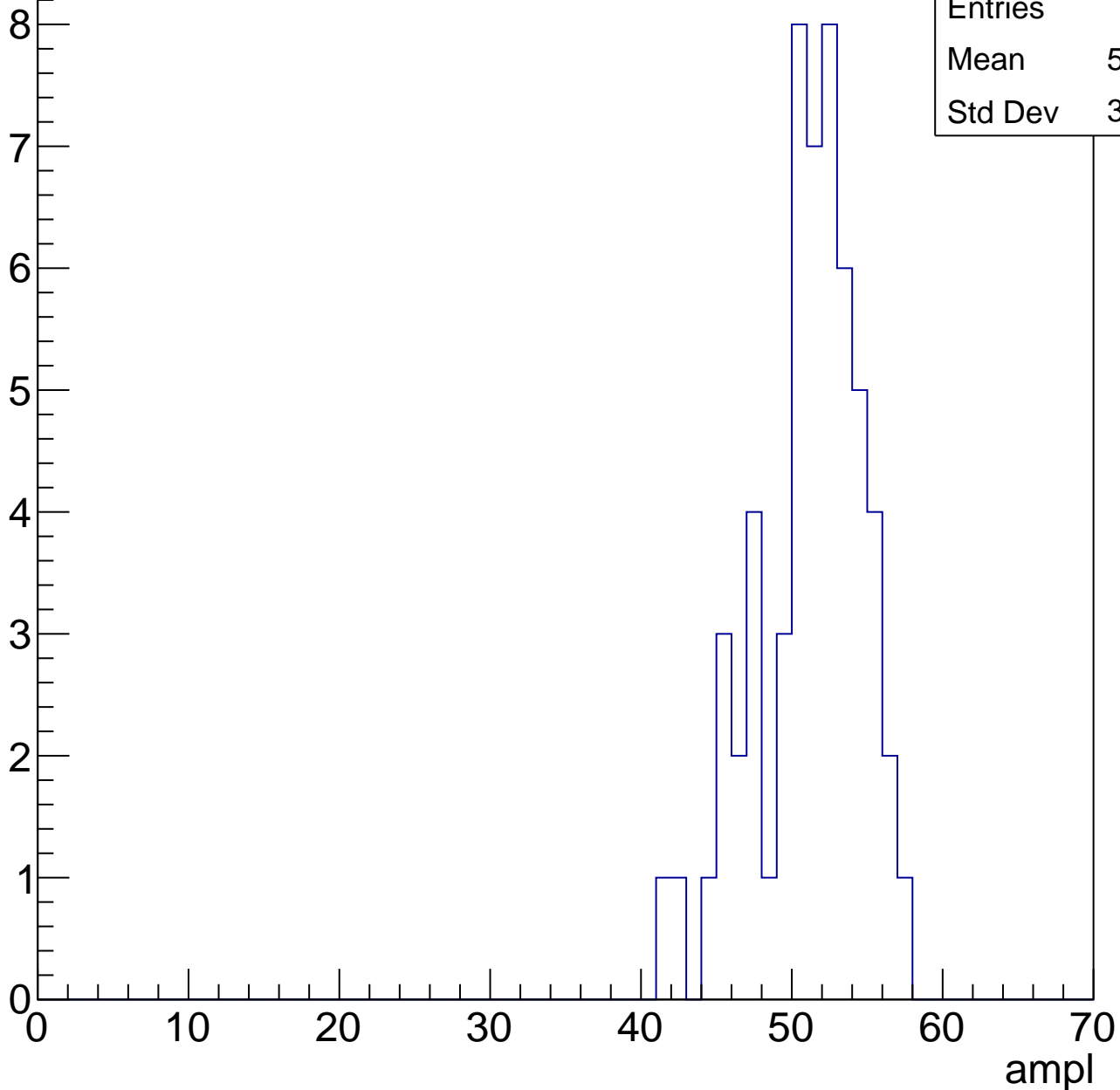


B0L001S, U14-ch62, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.65
Std Dev	3.507

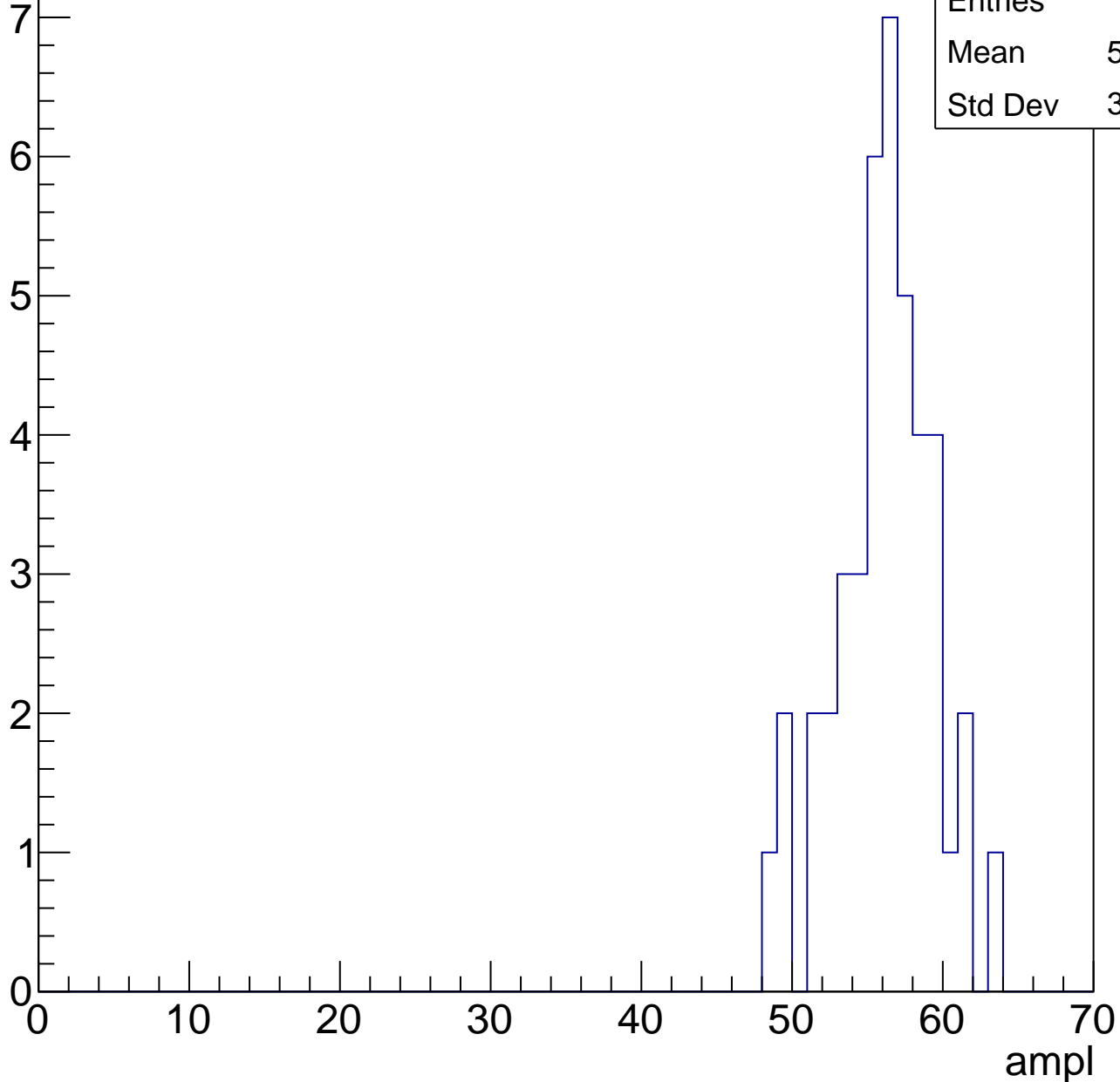


B0L001S, U14-ch62, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	55.65
Std Dev	3.256

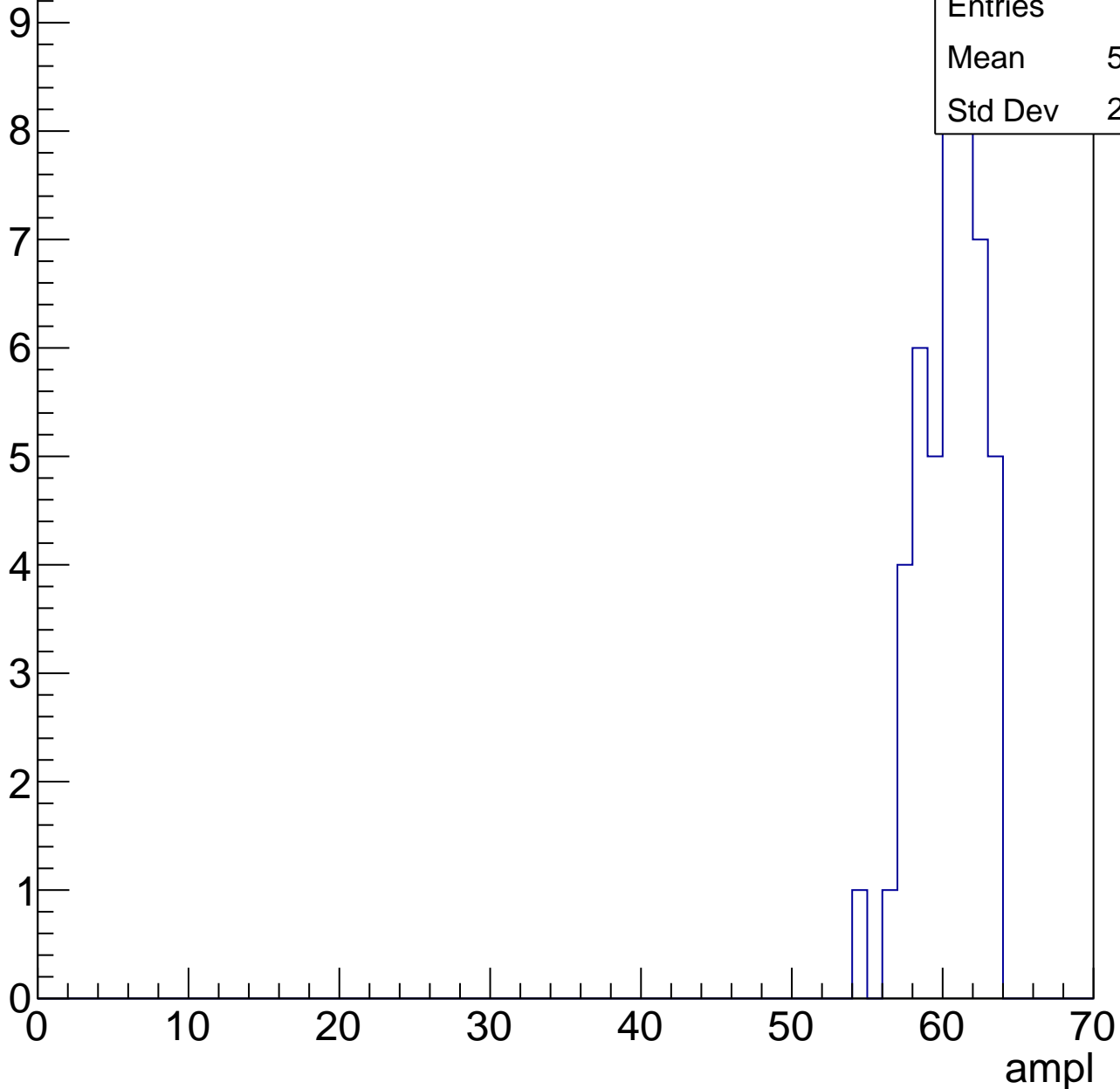


B0L001S, U14-ch62, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

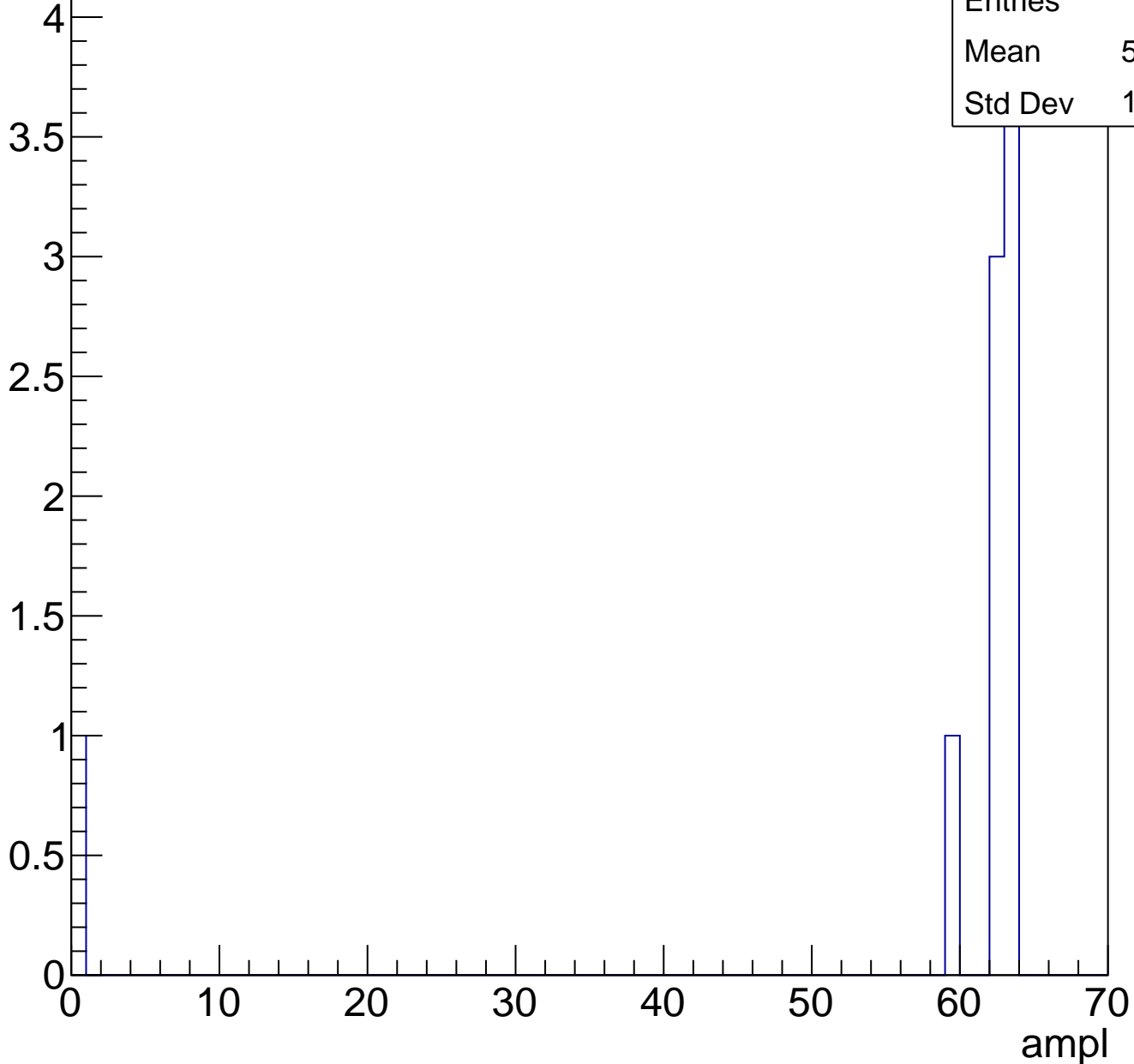
Entries	46
Mean	59.96
Std Dev	2.074



B0L001S, U14-ch62, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch62, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch63, adc0

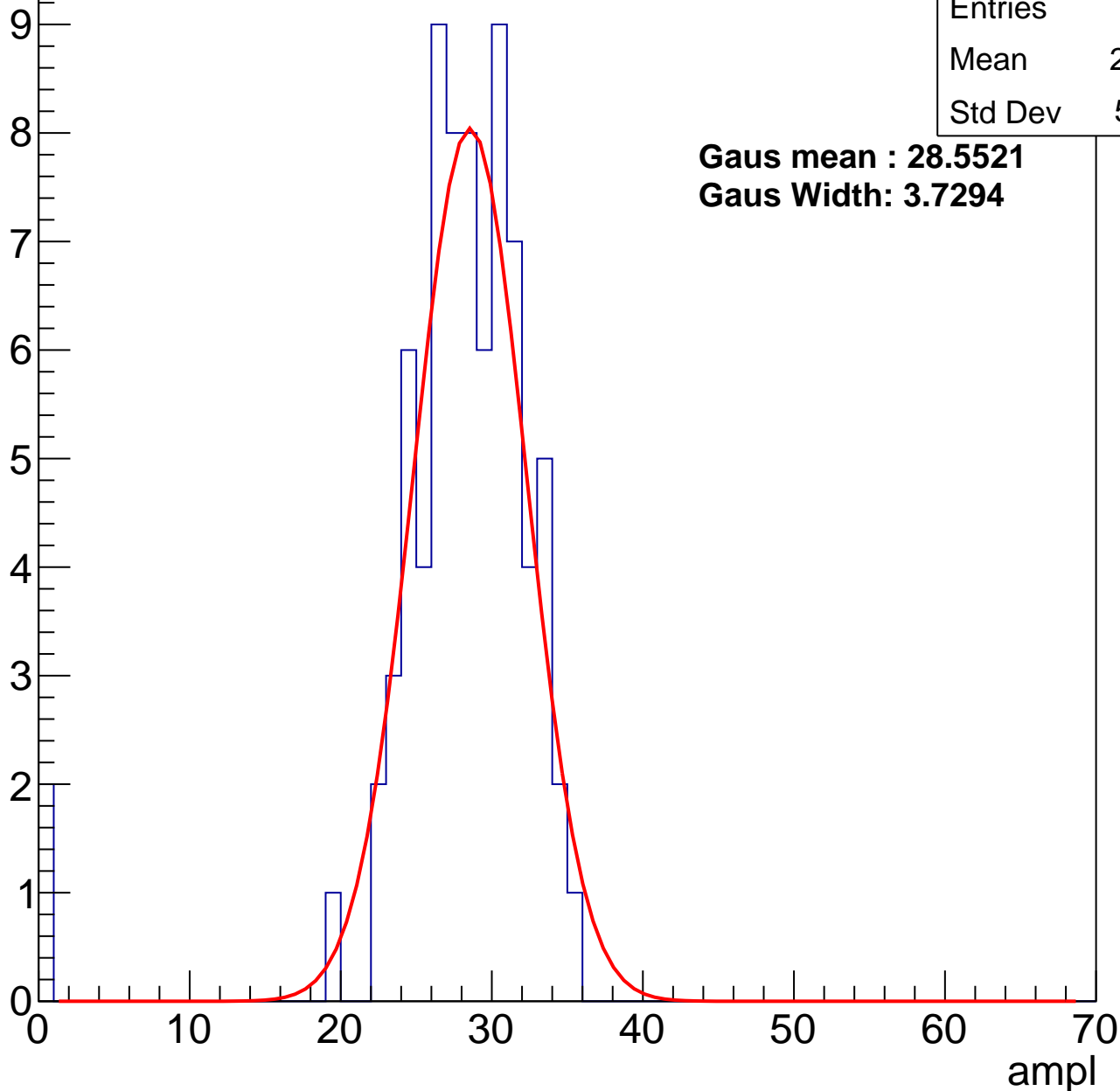
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	27.36
Std Dev	5.531

Gaus mean : 28.5521

Gaus Width: 3.7294



B0L001S, U14-ch63, adc1

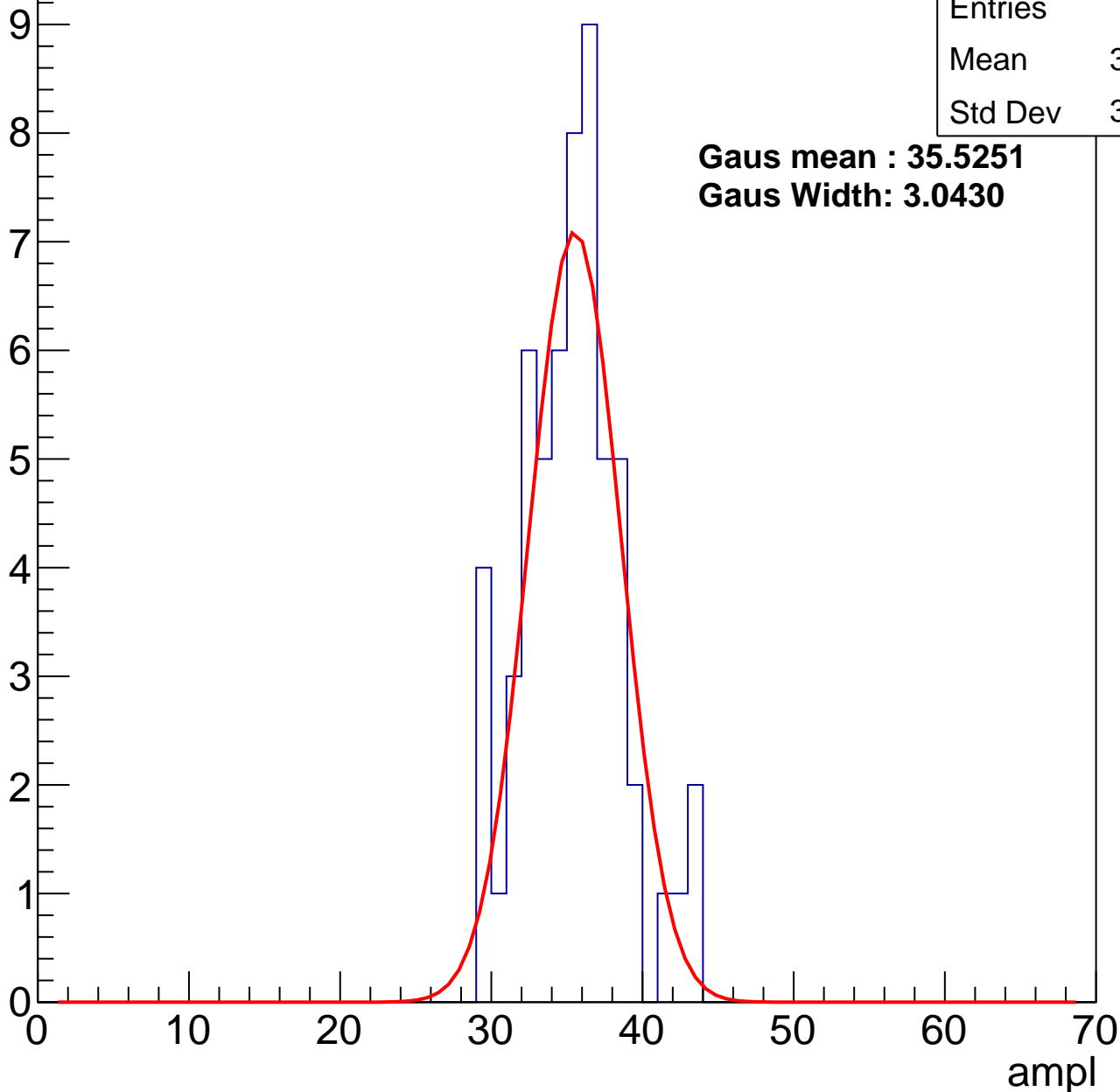
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	34.93
Std Dev	3.258

Gaus mean : 35.5251

Gaus Width: 3.0430



B0L001S, U14-ch63, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	82
Mean	41.78
Std Dev	3.53

Gaus mean : 42.6433

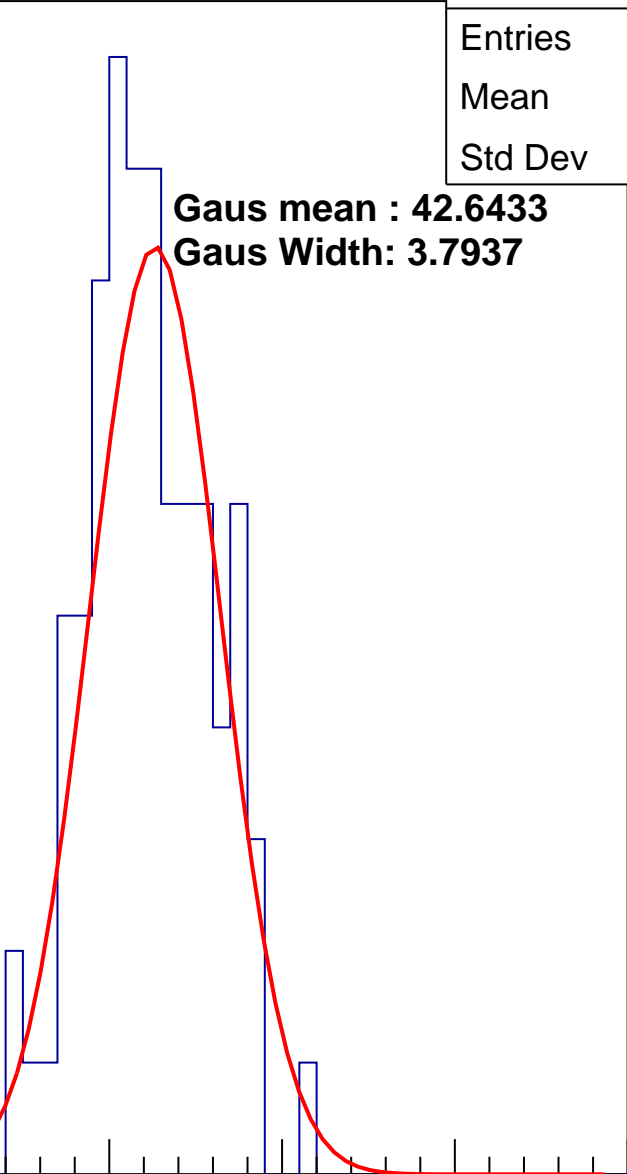
Gaus Width: 3.7937

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

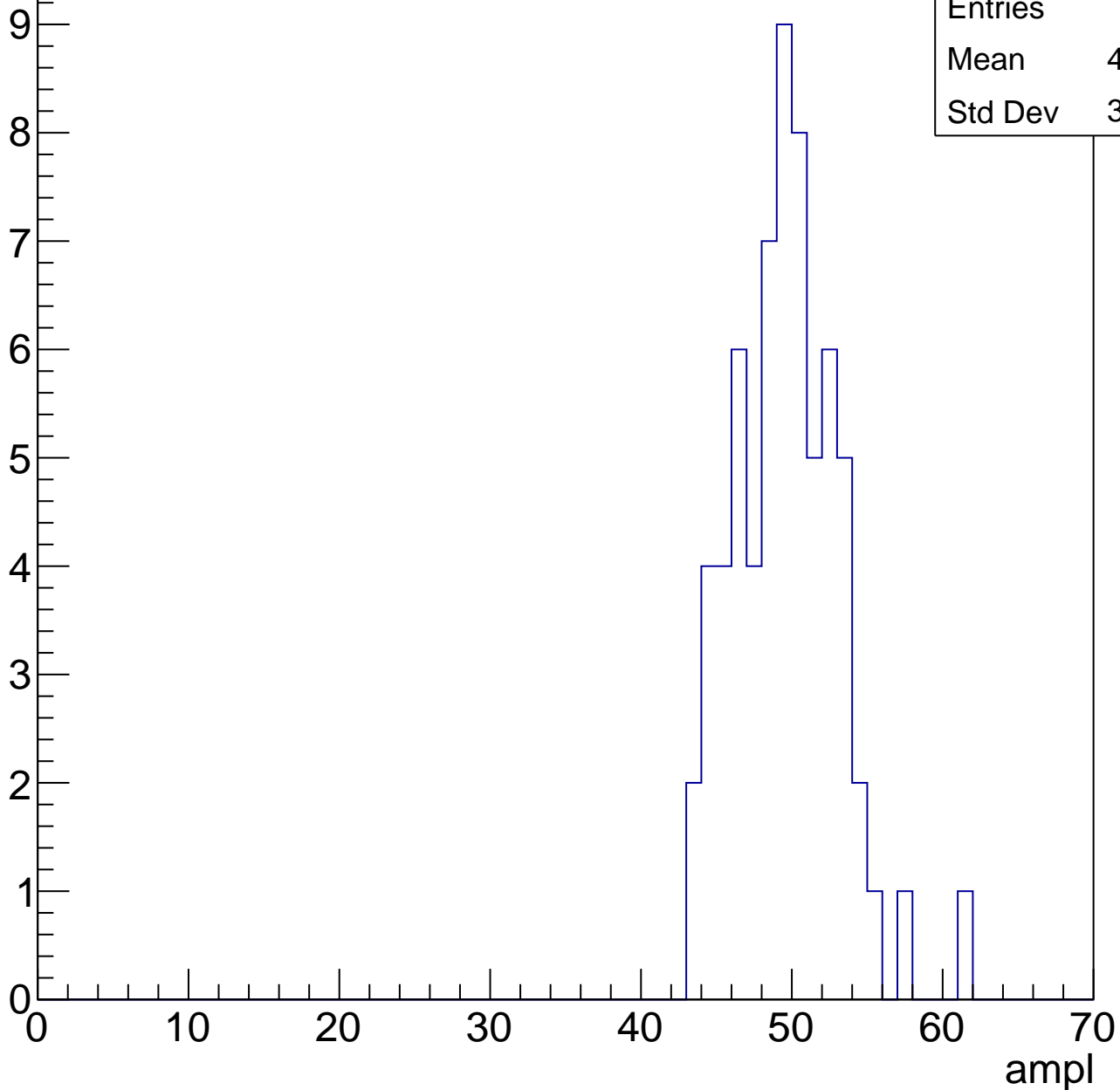


B0L001S, U14-ch63, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.17
Std Dev	3.435

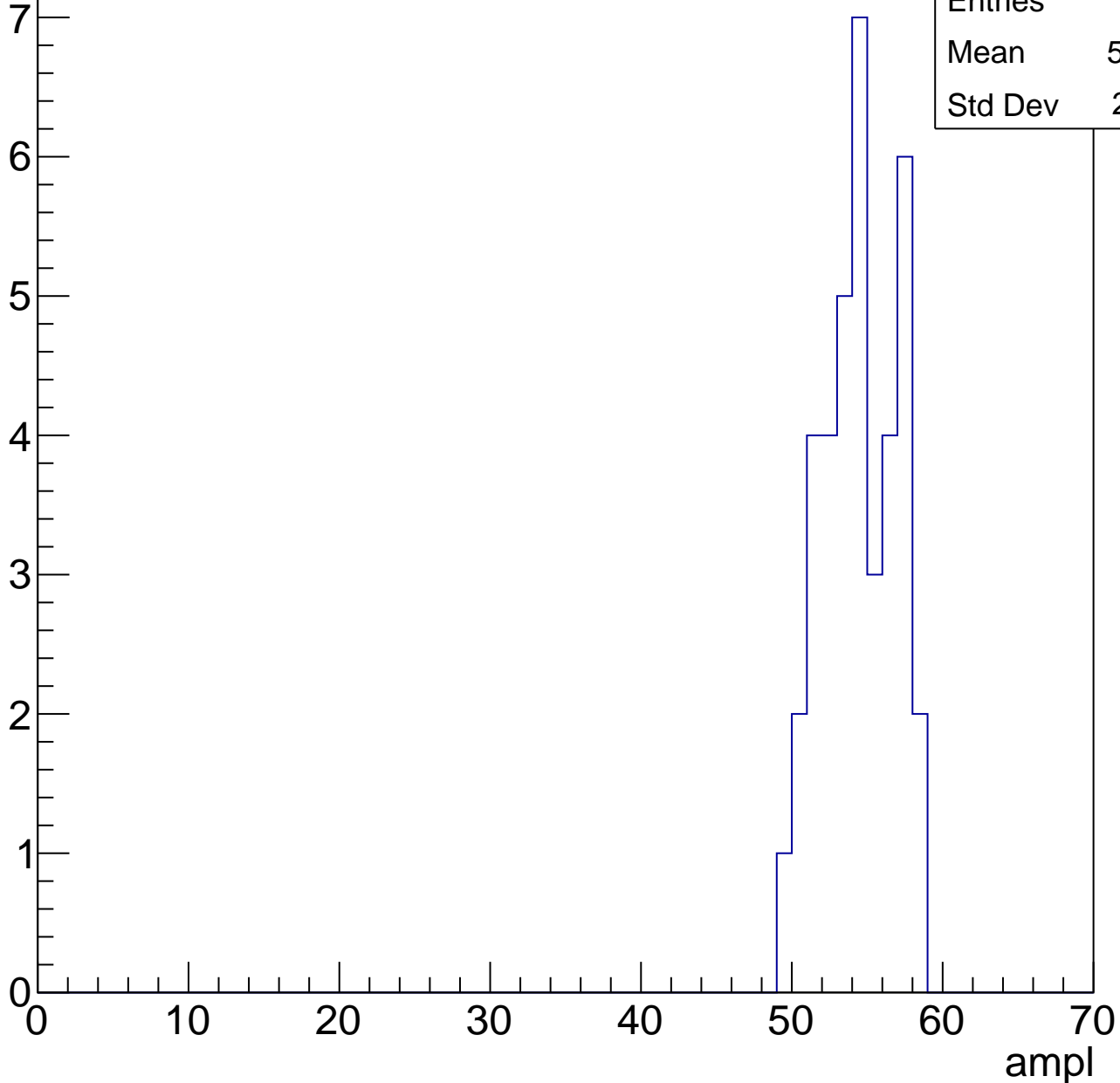


B0L001S, U14-ch63, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	53.97
Std Dev	2.401



B0L001S, U14-ch63, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

Entries

65

Mean

58.26

Std Dev

7.691

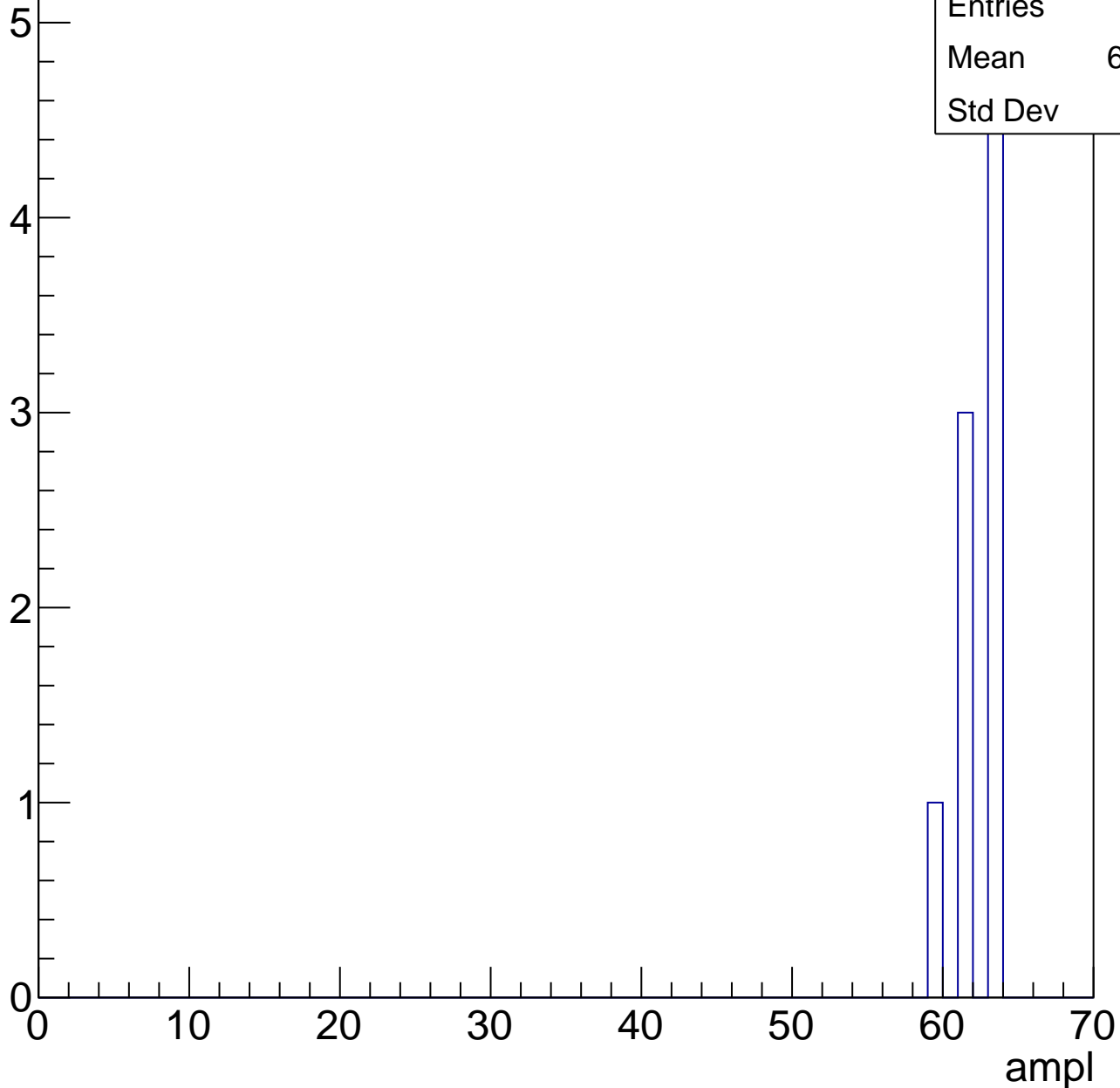
Entries	65
Mean	58.26
Std Dev	7.691

B0L001S, U14-ch63, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	9
Mean	61.89
Std Dev	1.37



B0L001S, U14-ch63, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch64, adc0

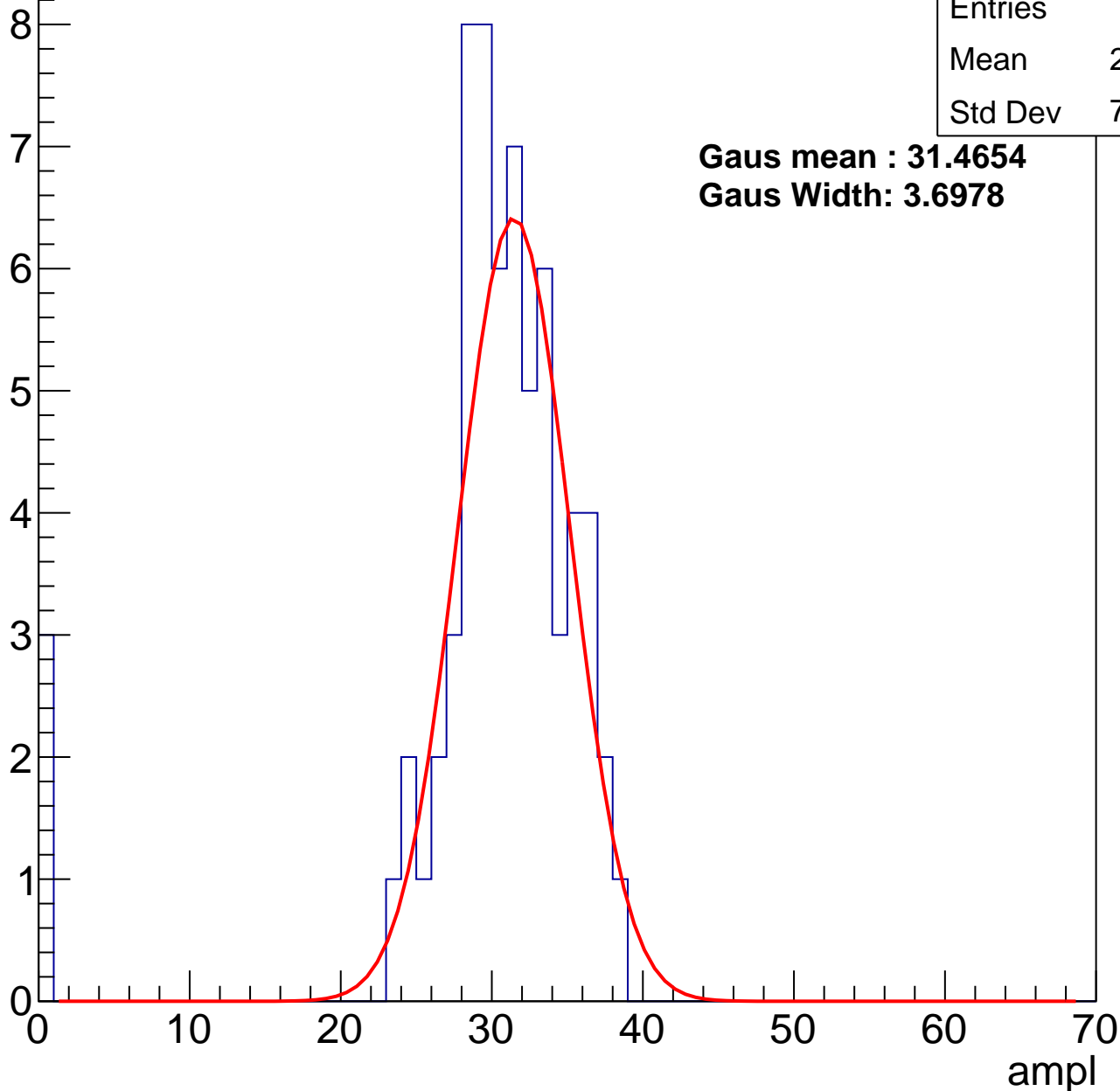
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	29.36
Std Dev	7.233

Gaus mean : 31.4654

Gaus Width: 3.6978



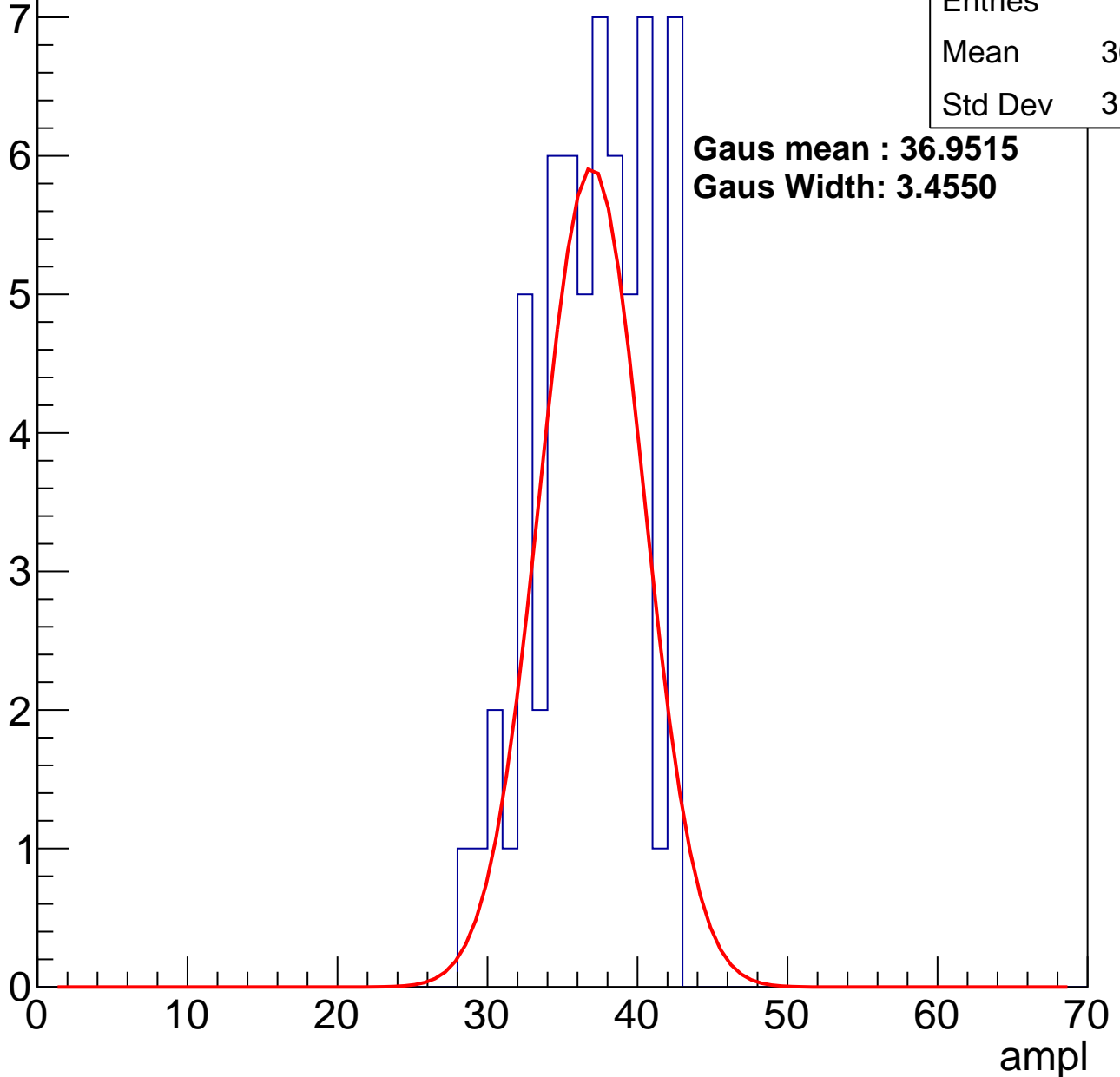
B0L001S, U14-ch64, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	36.53
Std Dev	3.559

Gaus mean : 36.9515
Gaus Width: 3.4550



B0L001S, U14-ch64, adc2

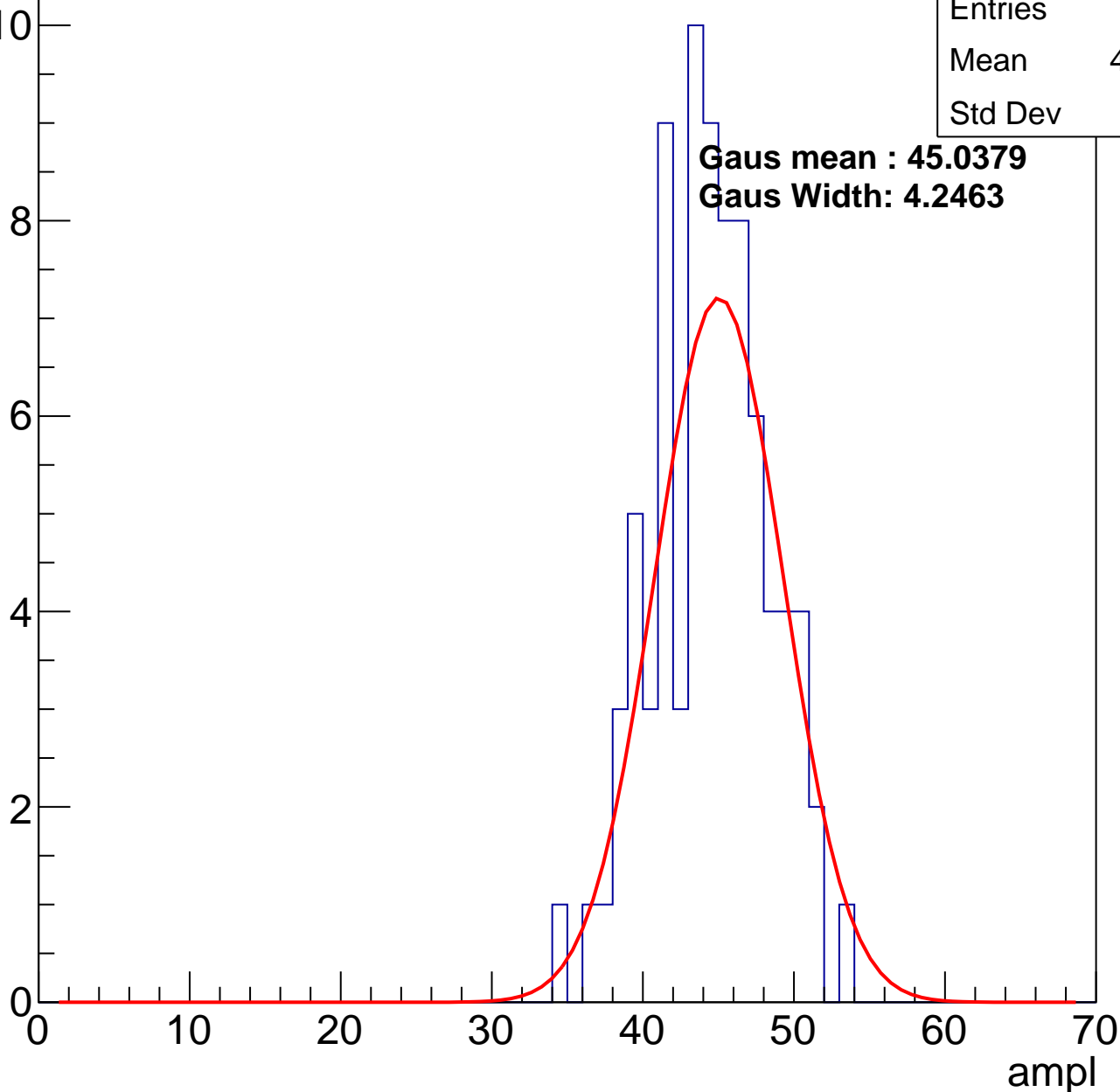
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	44.02
Std Dev	3.79

Gaus mean : 45.0379

Gaus Width: 4.2463

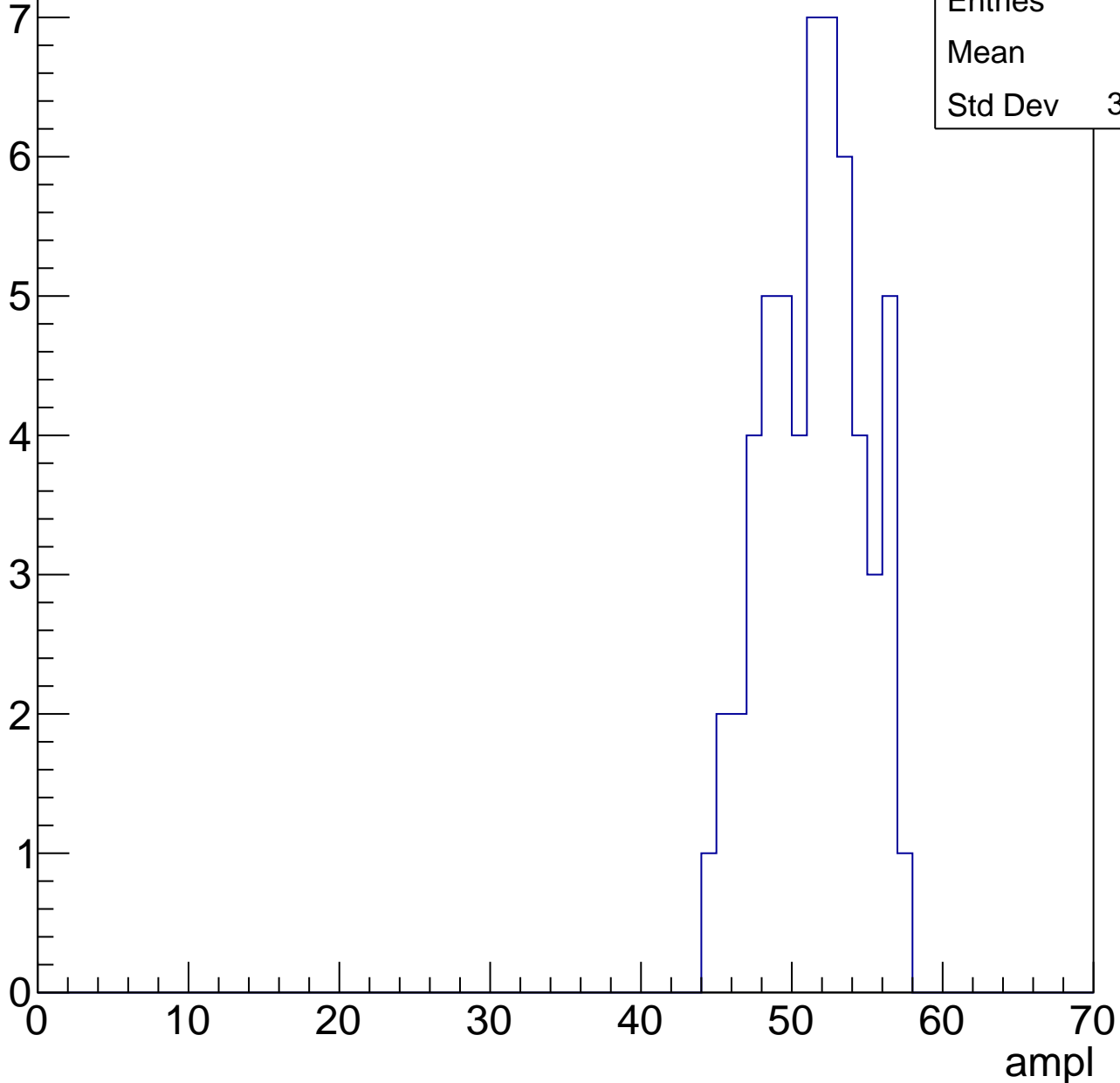


B0L001S, U14-ch64, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	51
Std Dev	3.218

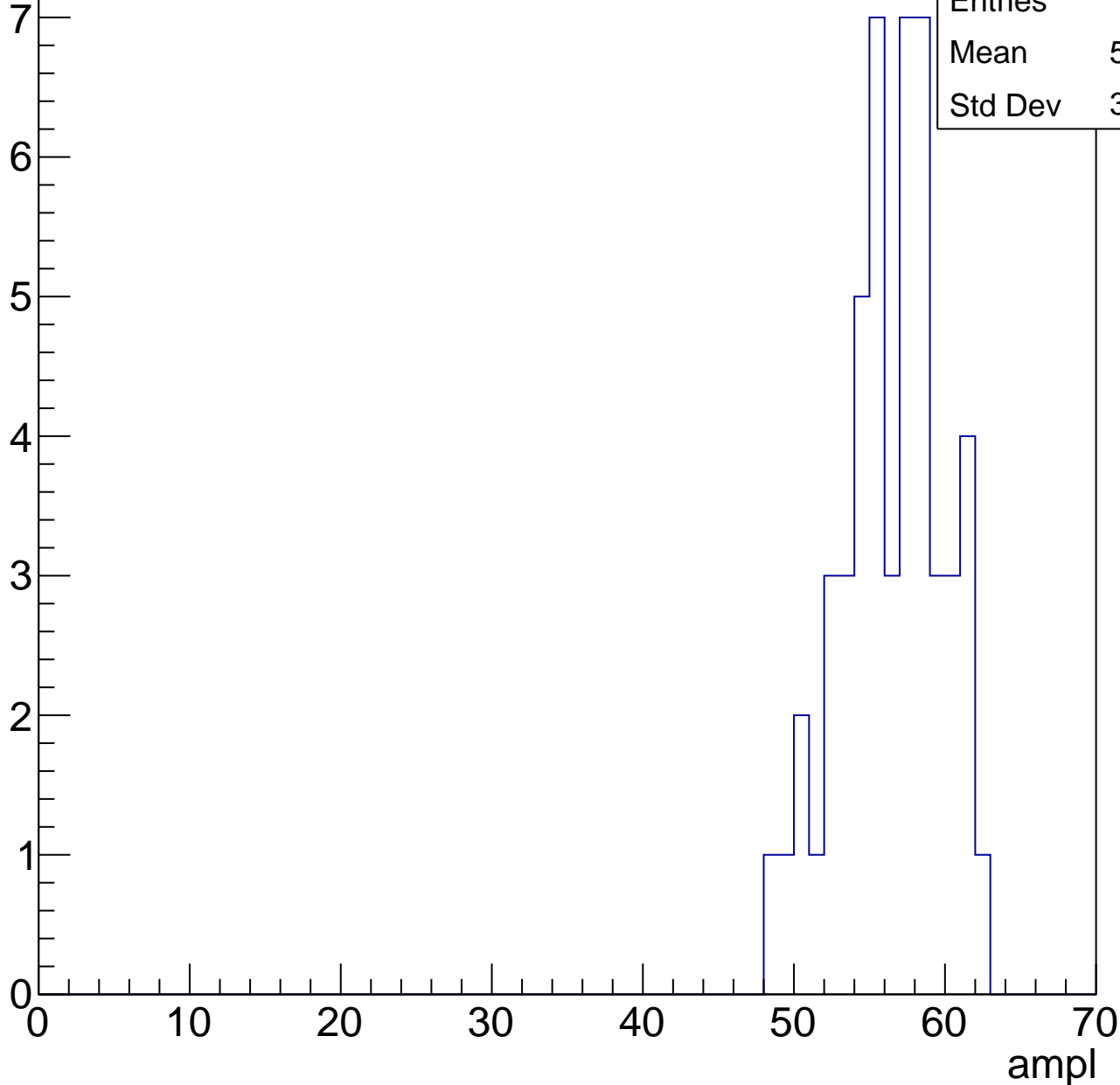


B0L001S, U14-ch64, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	55.96
Std Dev	3.308

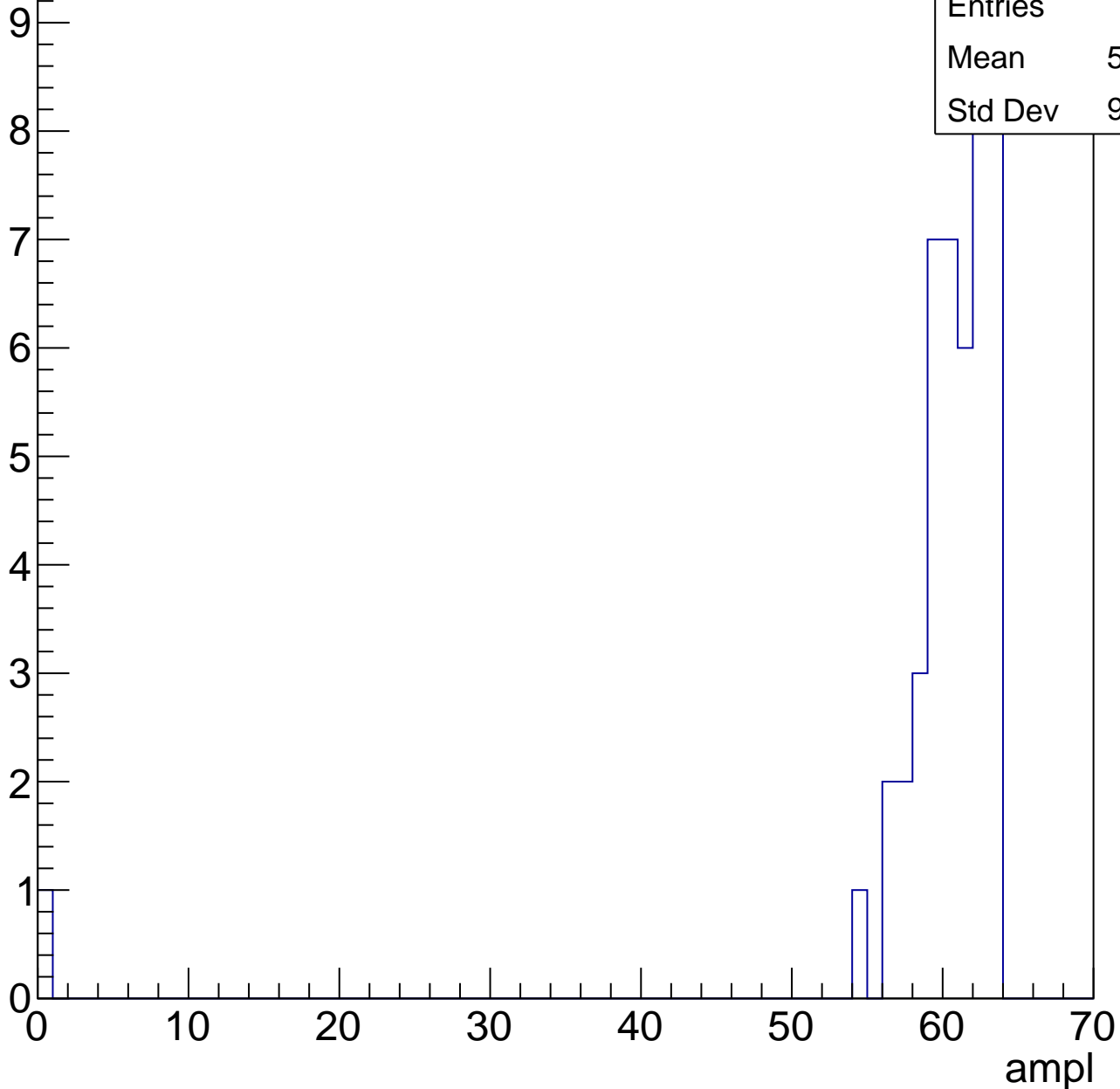


B0L001S, U14-ch64, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	59.02
Std Dev	9.059



B0L001S, U14-ch64, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch64, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch65, adc0

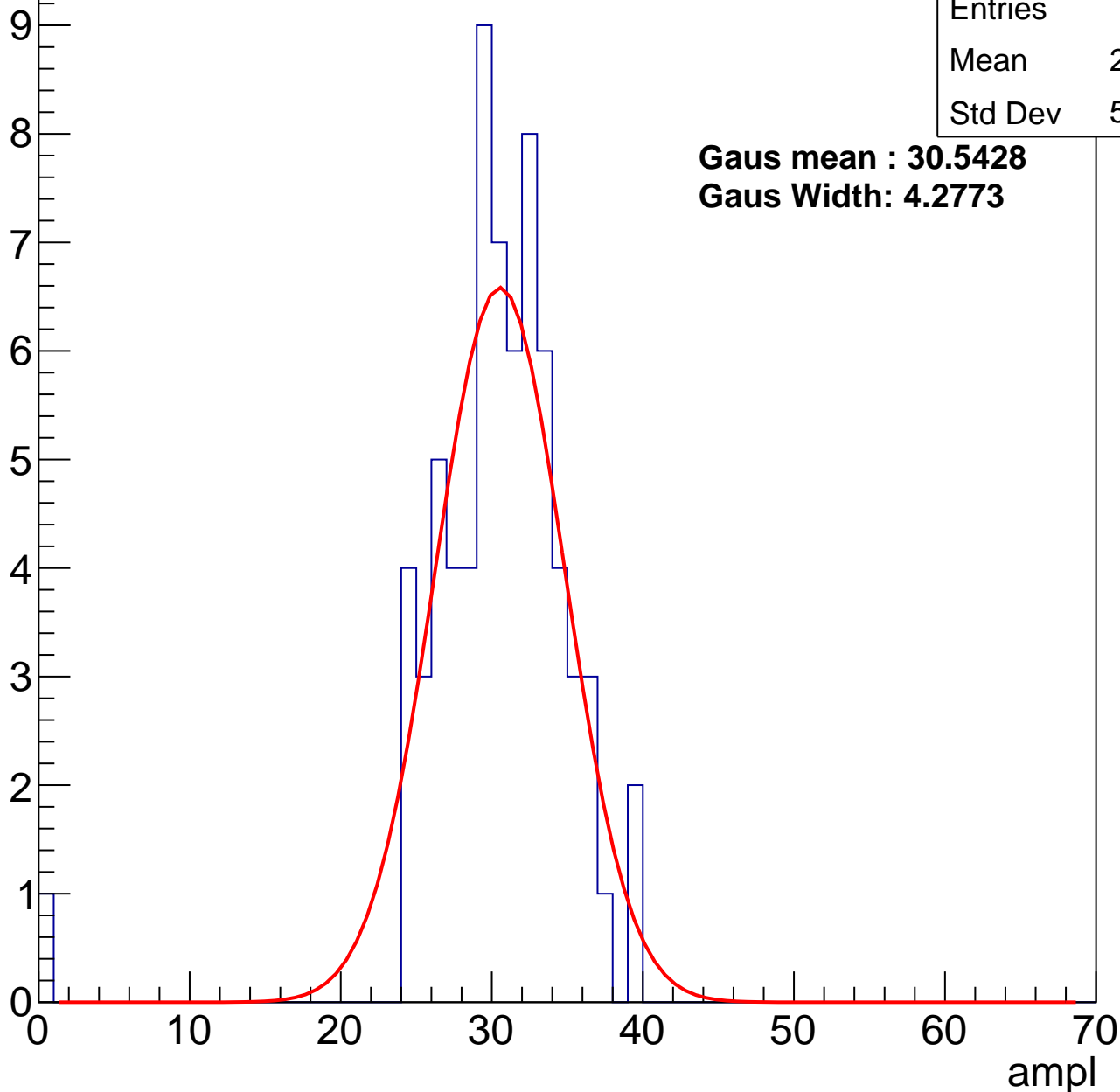
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	29.94
Std Dev	5.085

Gaus mean : 30.5428

Gaus Width: 4.2773



B0L001S, U14-ch65, adc1

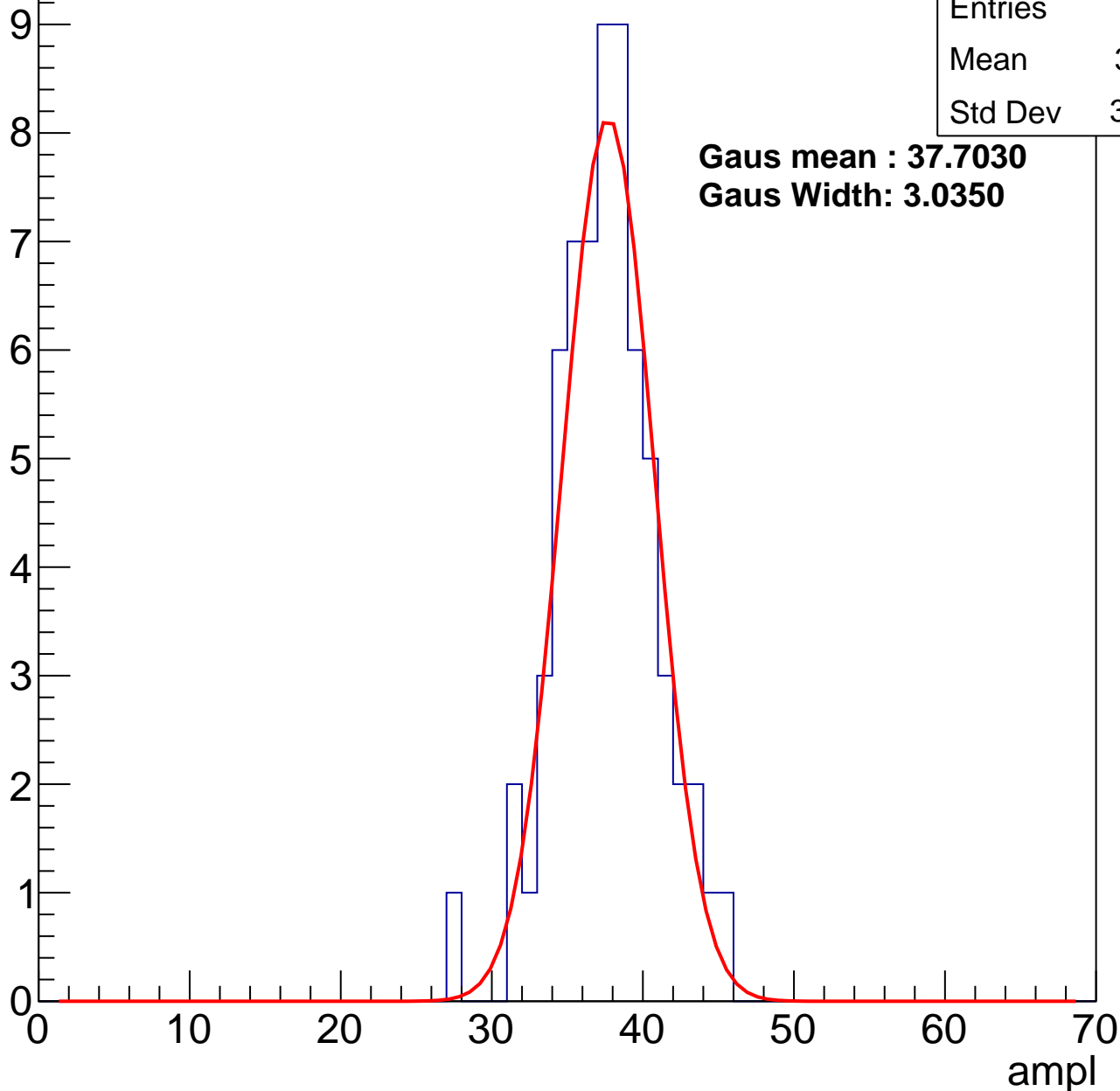
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	37.11
Std Dev	3.268

Gaus mean : 37.7030

Gaus Width: 3.0350



B0L001S, U14-ch65, adc2

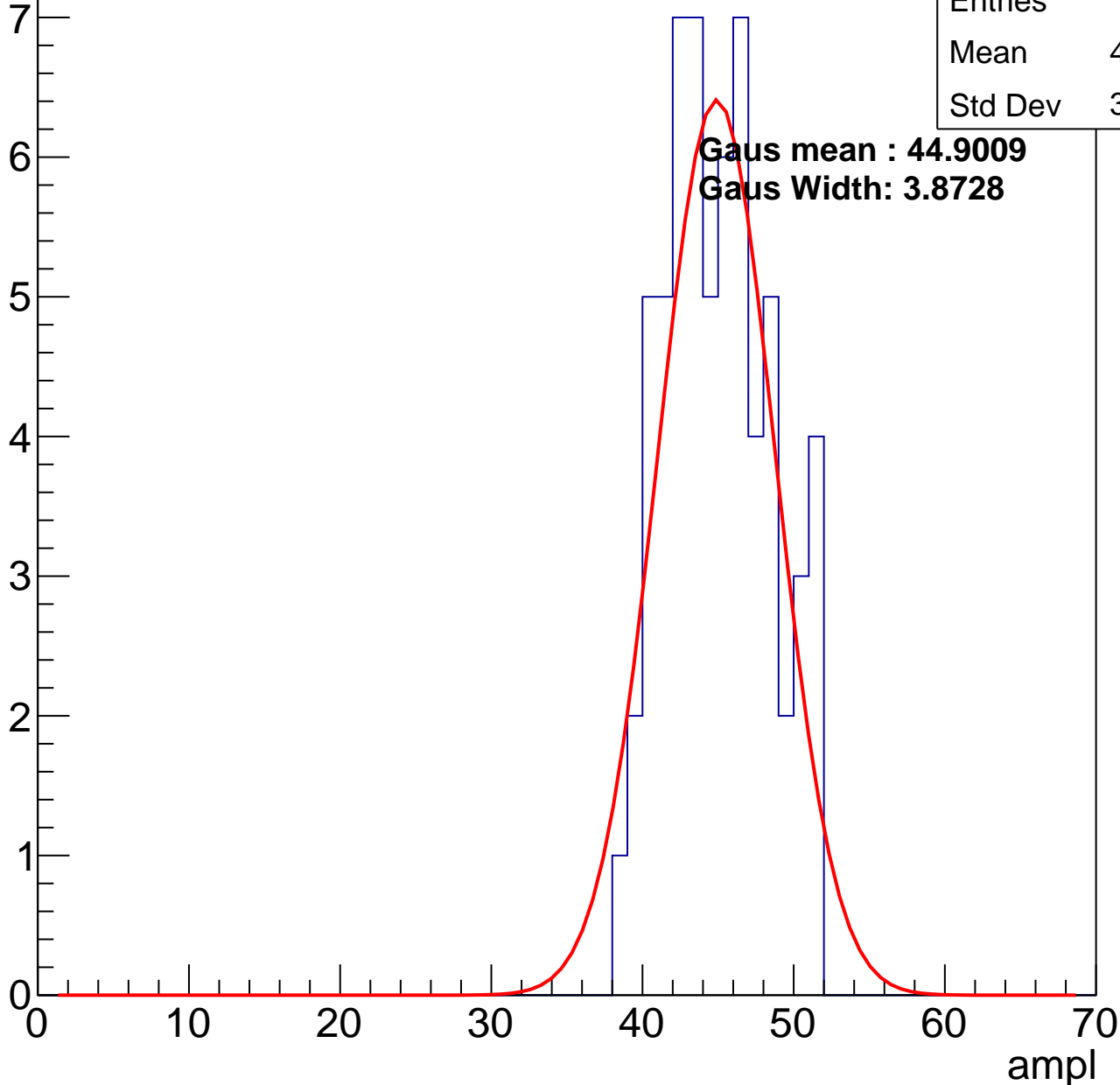
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	44.57
Std Dev	3.403

Gaus mean : 44.9009

Gaus Width: 3.8728

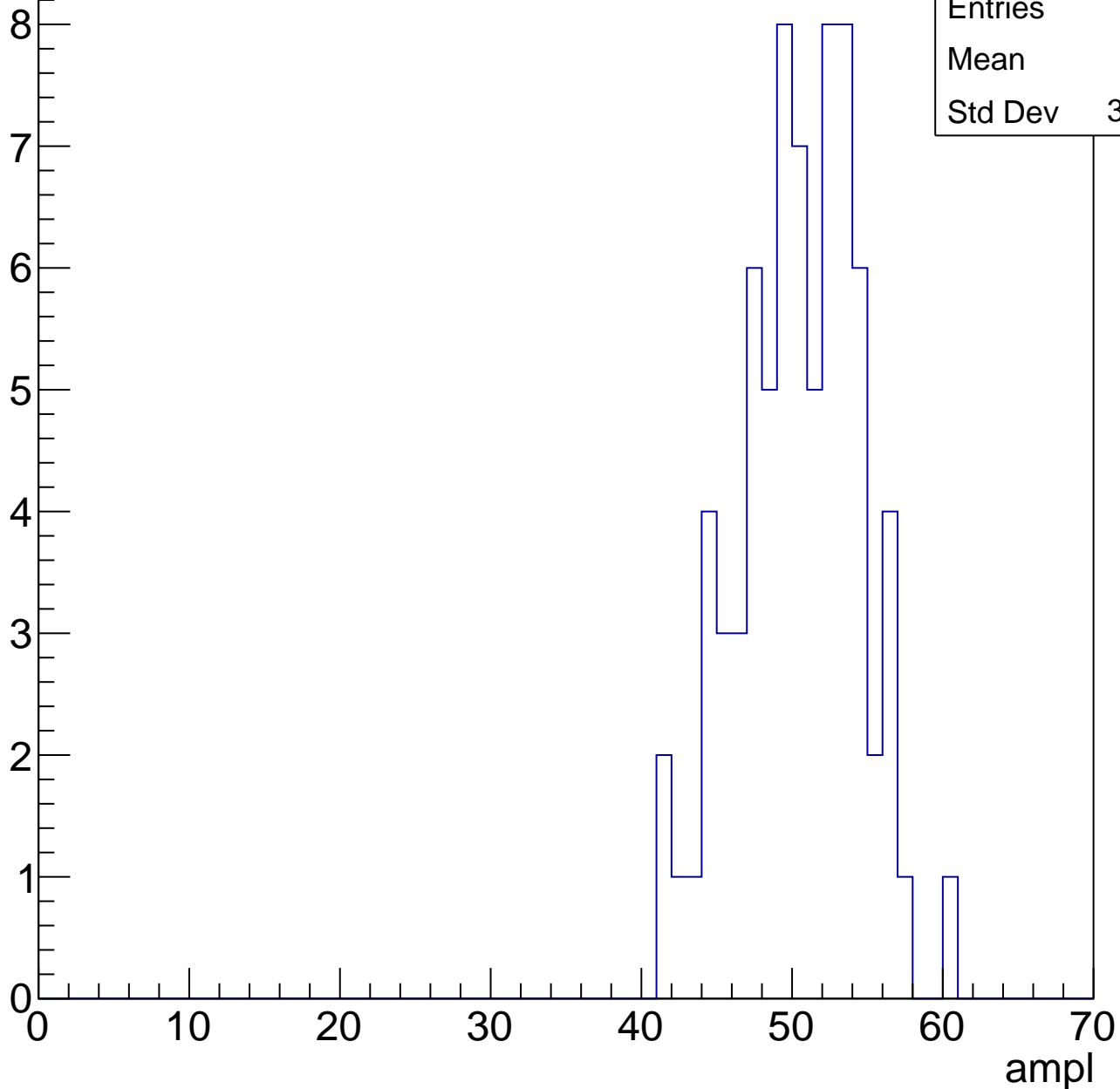


B0L001S, U14-ch65, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

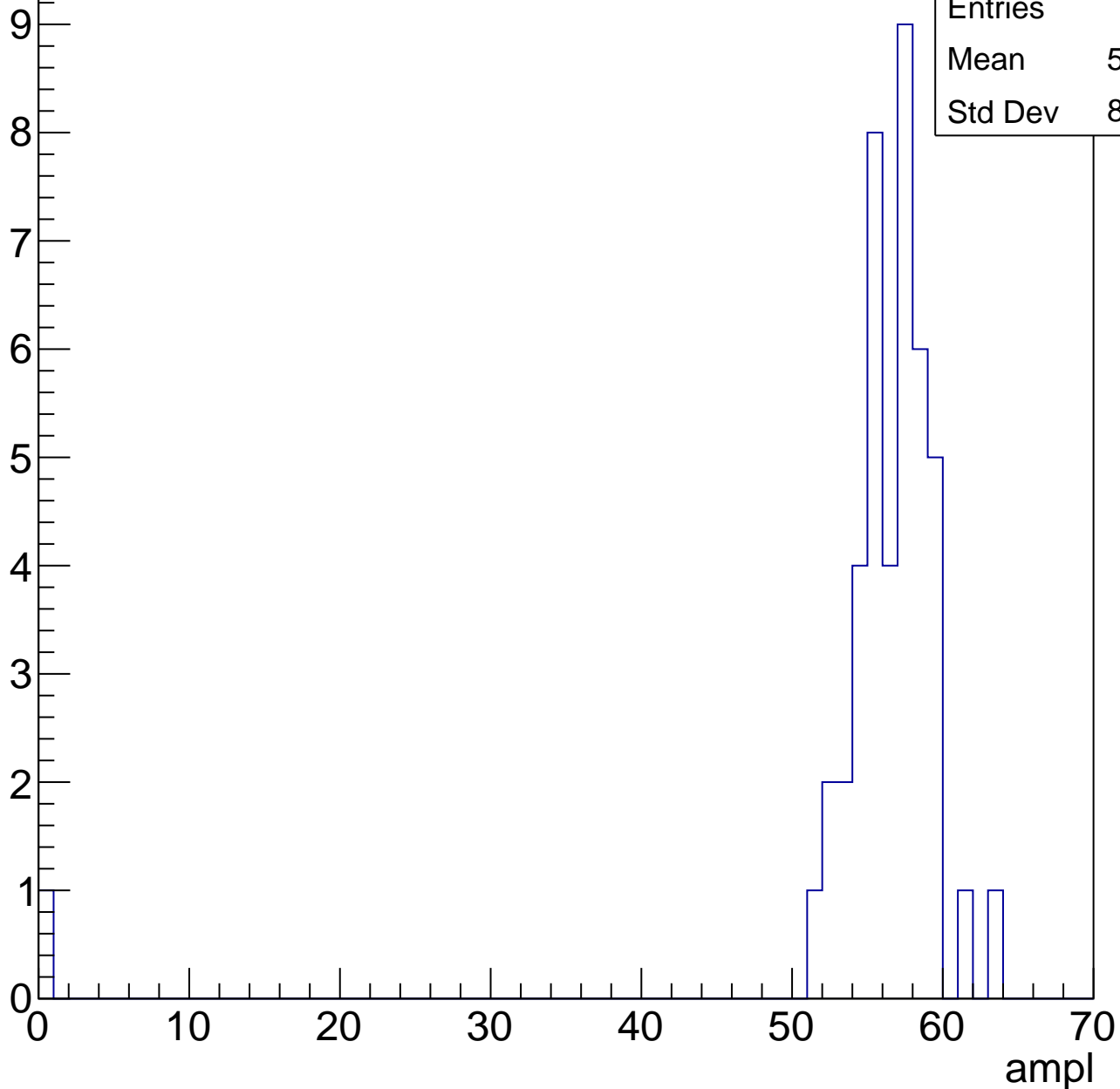
Entries	75
Mean	50
Std Dev	3.953



B0L001S, U14-ch65, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

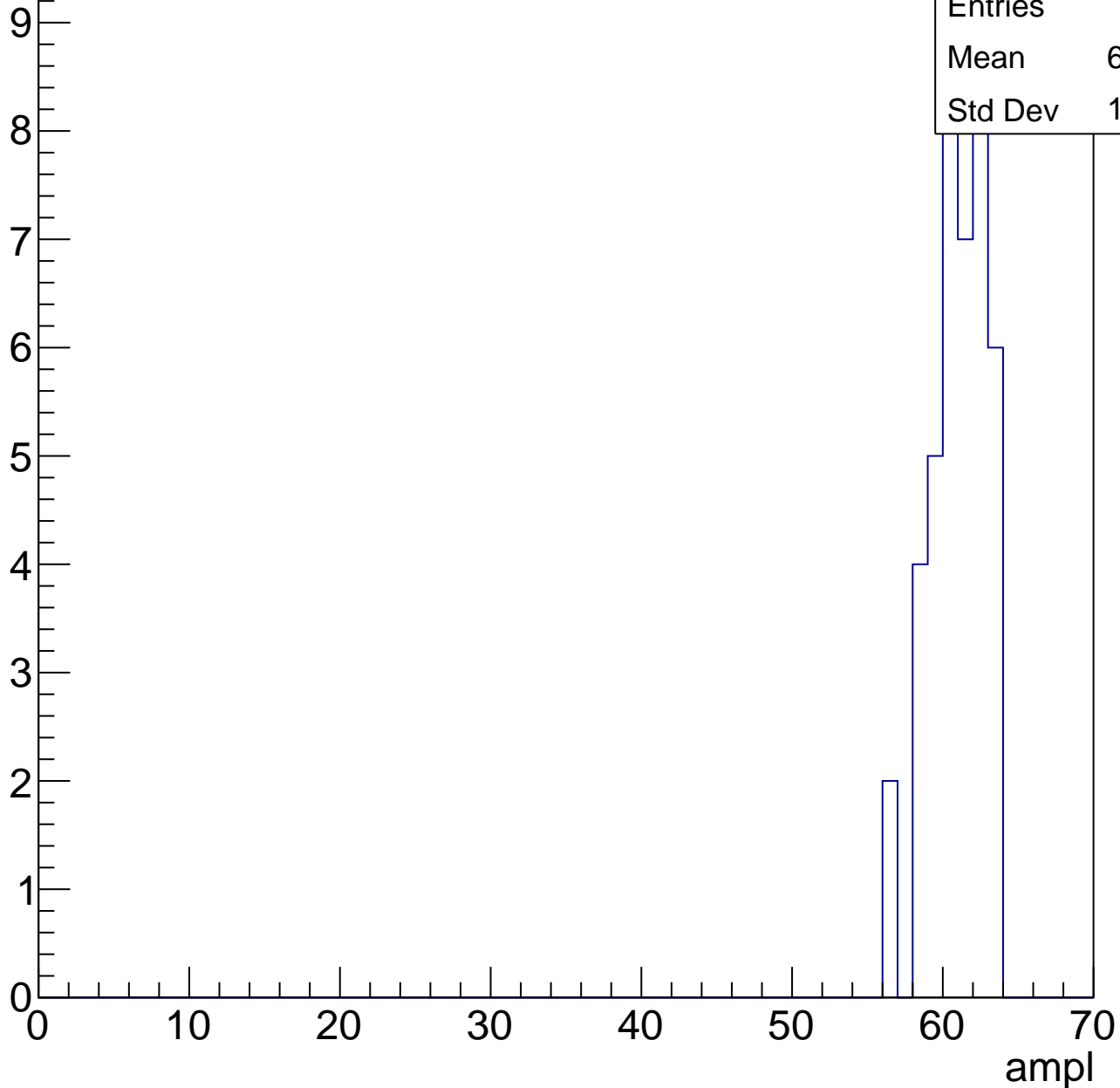


Entries	44
Mean	55.02
Std Dev	8.719

B0L001S, U14-ch65, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch65, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch65, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch66, adc0

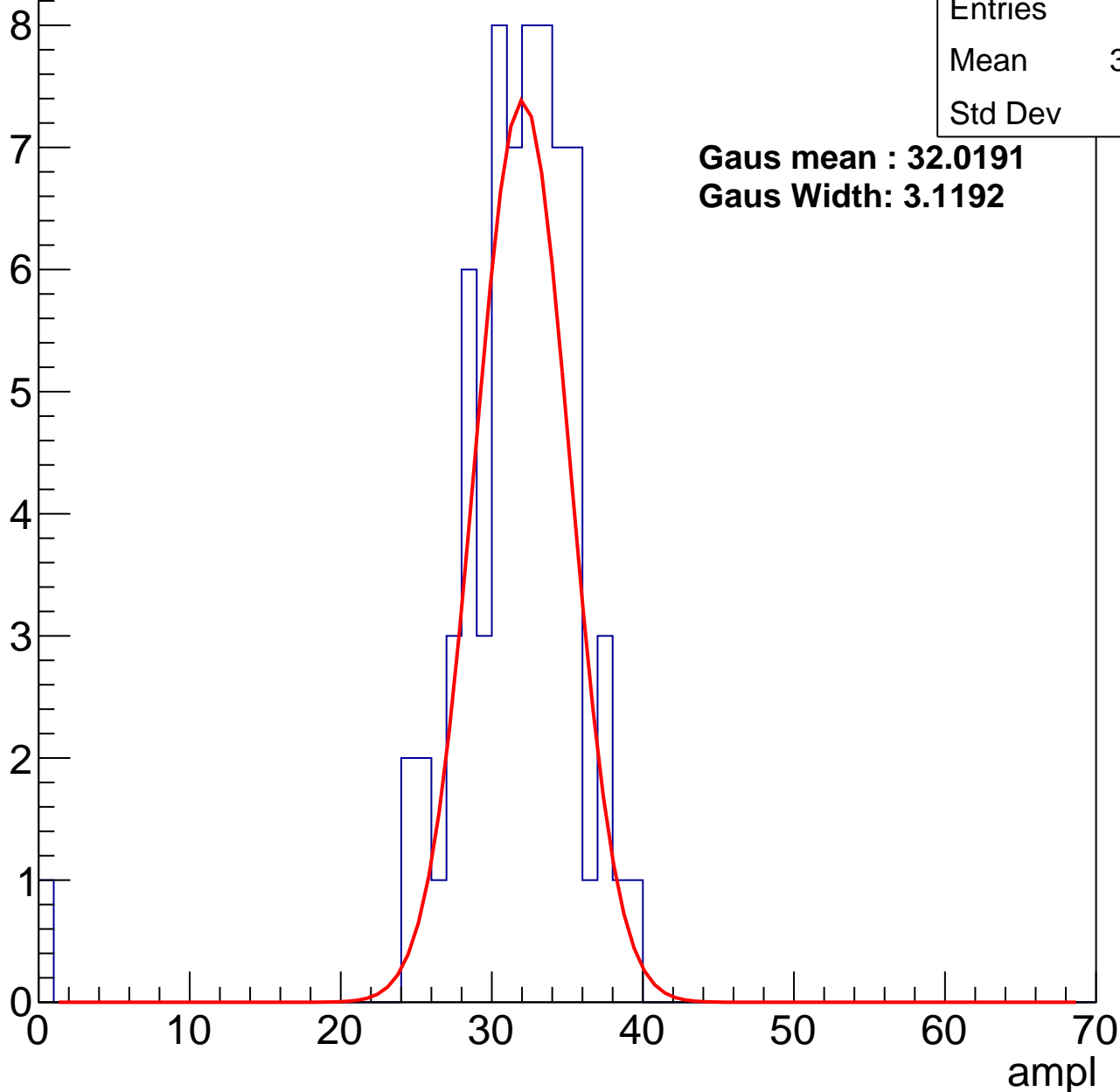
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	31.07
Std Dev	5.02

Gaus mean : 32.0191

Gaus Width: 3.1192



B0L001S, U14-ch66, adc1

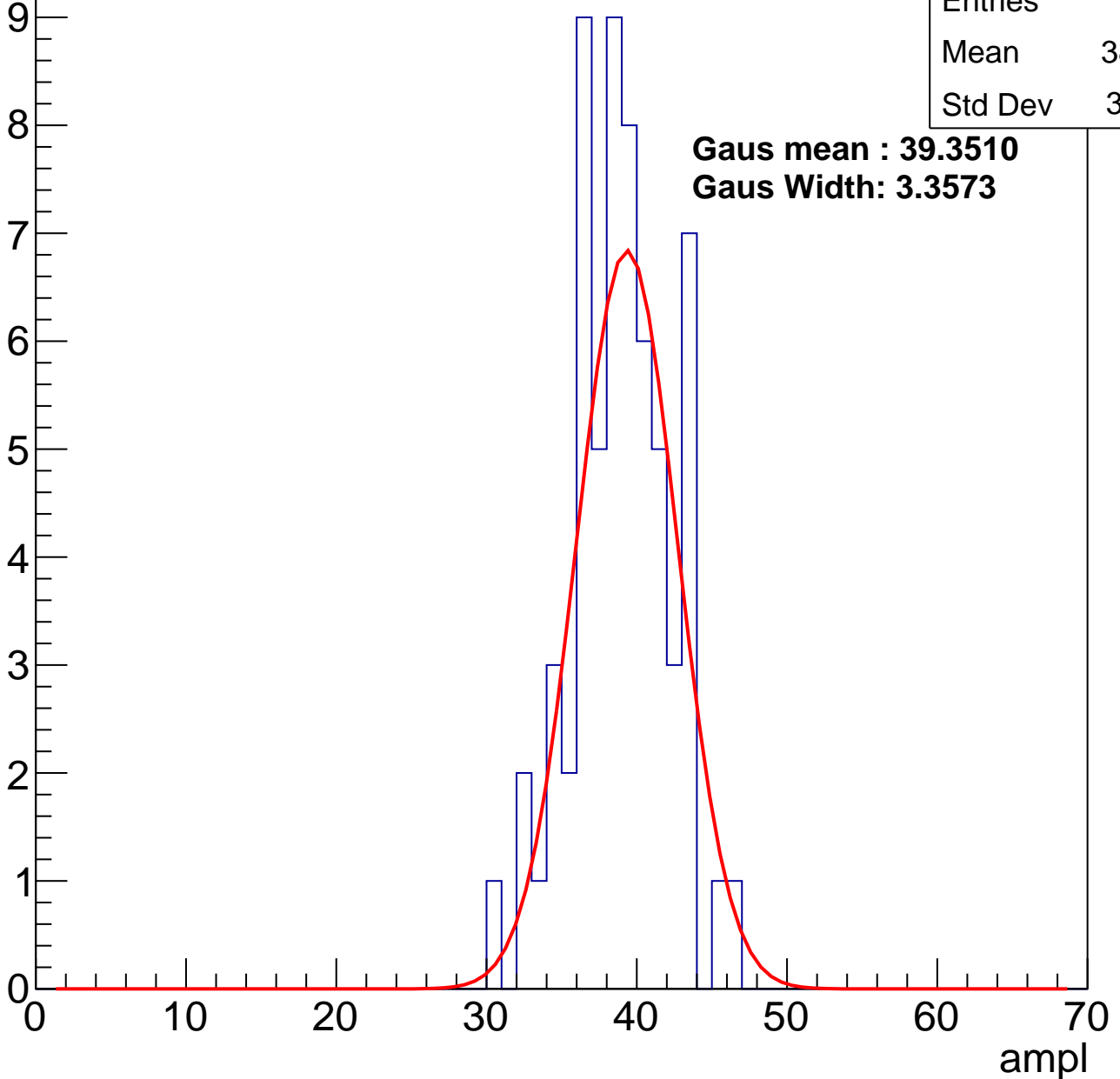
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.49
Std Dev	3.251

Gaus mean : 39.3510

Gaus Width: 3.3573



B0L001S, U14-ch66, adc2

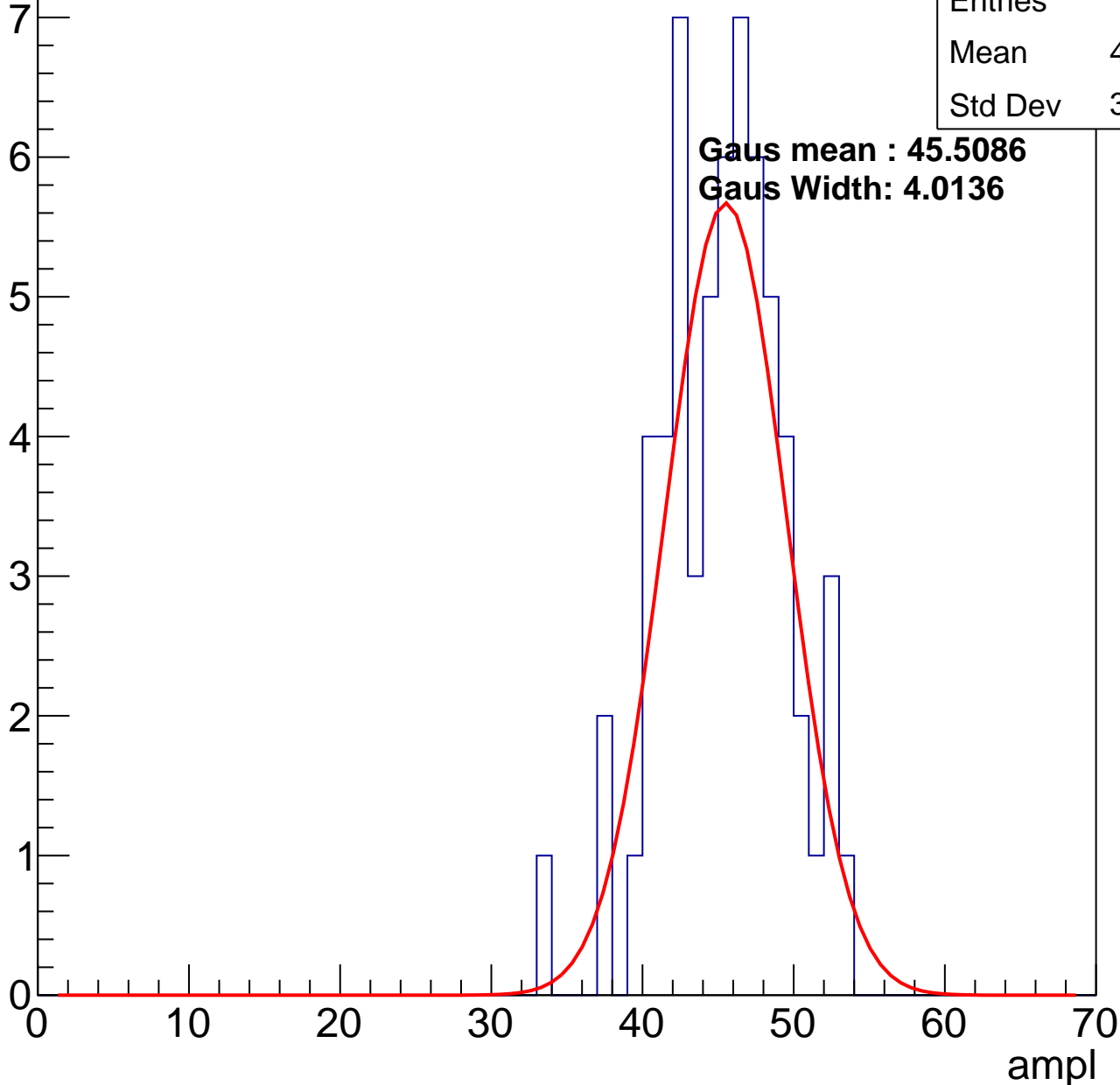
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.89
Std Dev	3.992

Gaus mean : 45.5086

Gaus Width: 4.0136

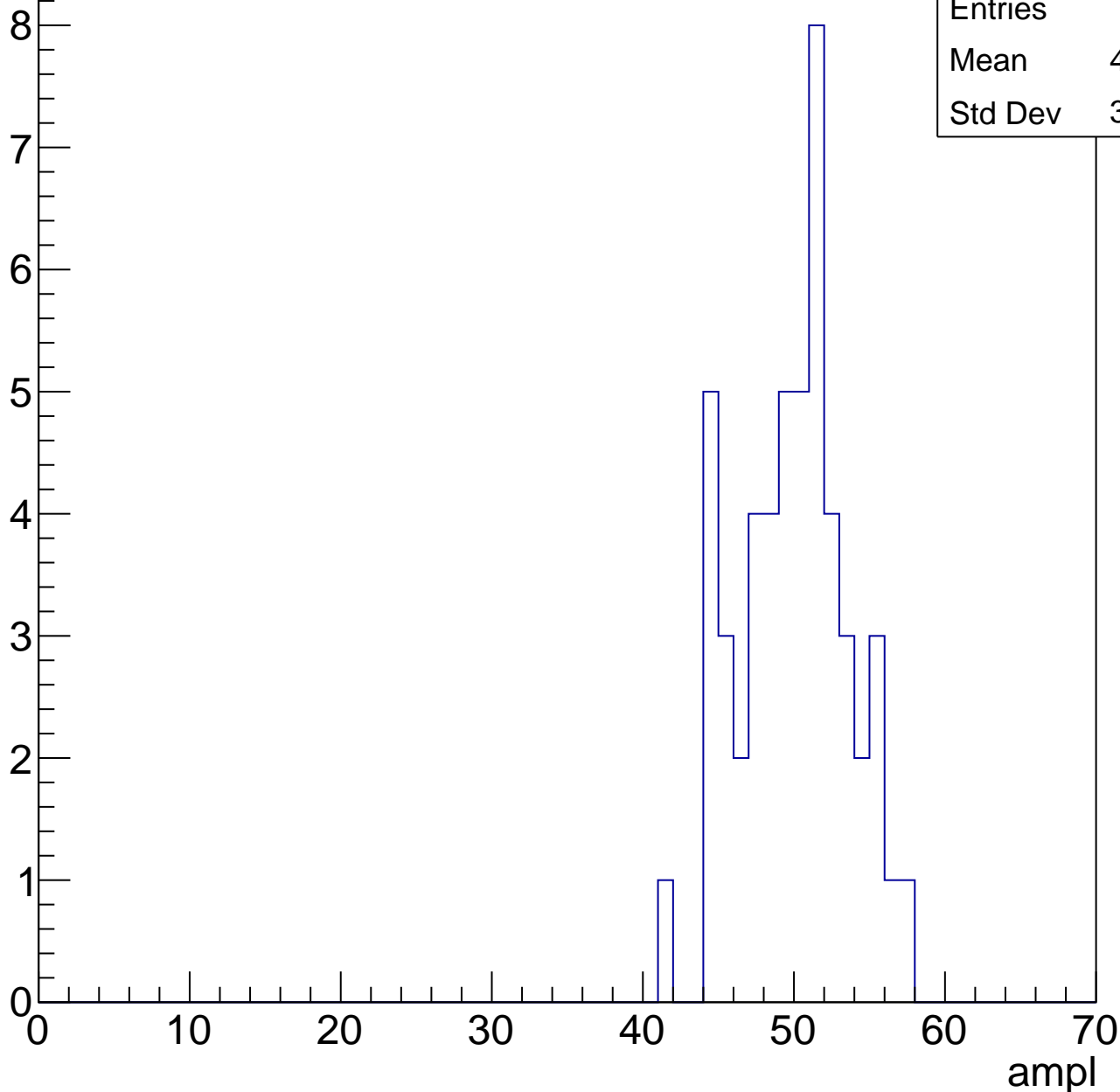


B0L001S, U14-ch66, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

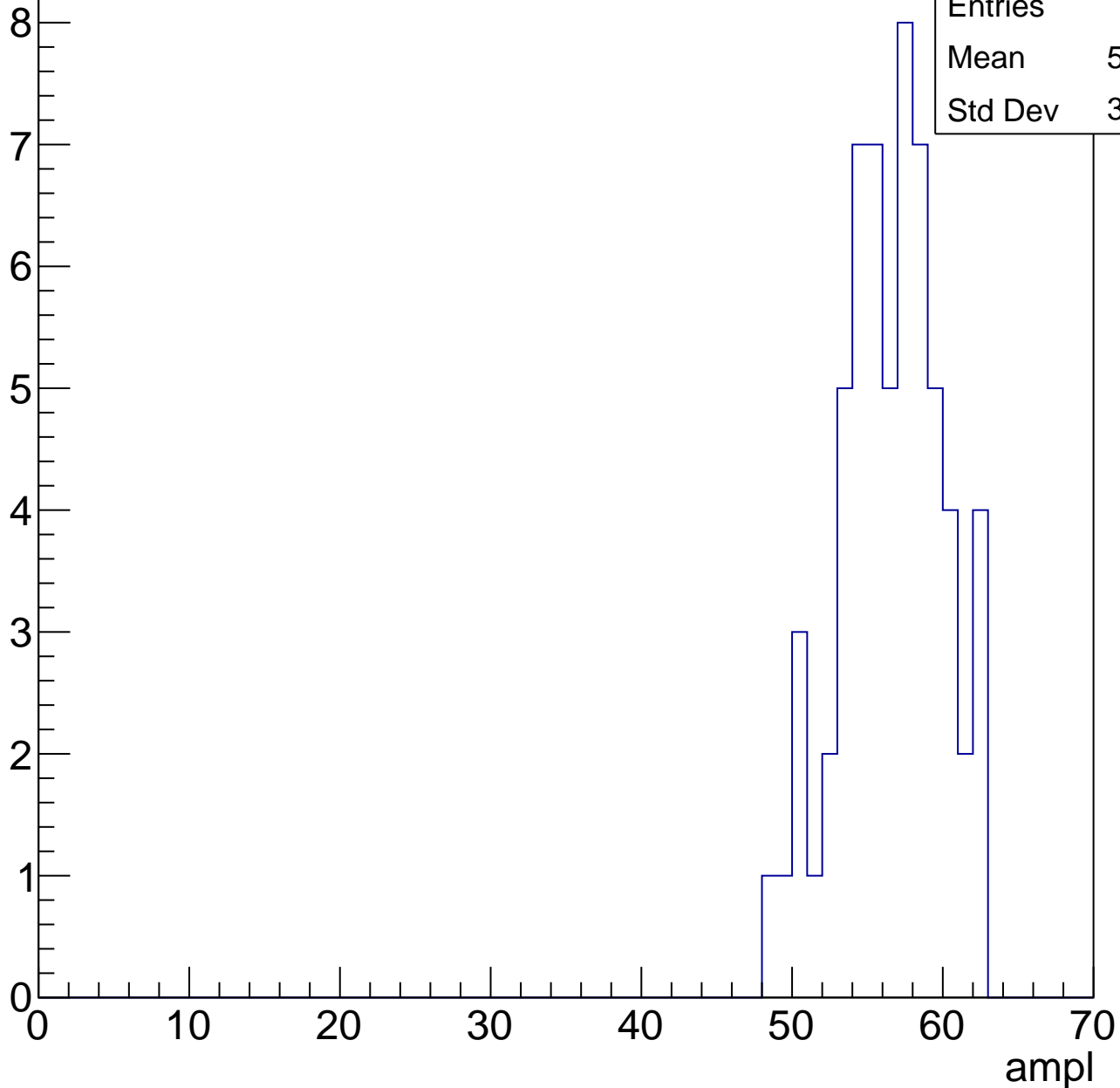
Entries	51
Mean	49.49
Std Dev	3.583



B0L001S, U14-ch66, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

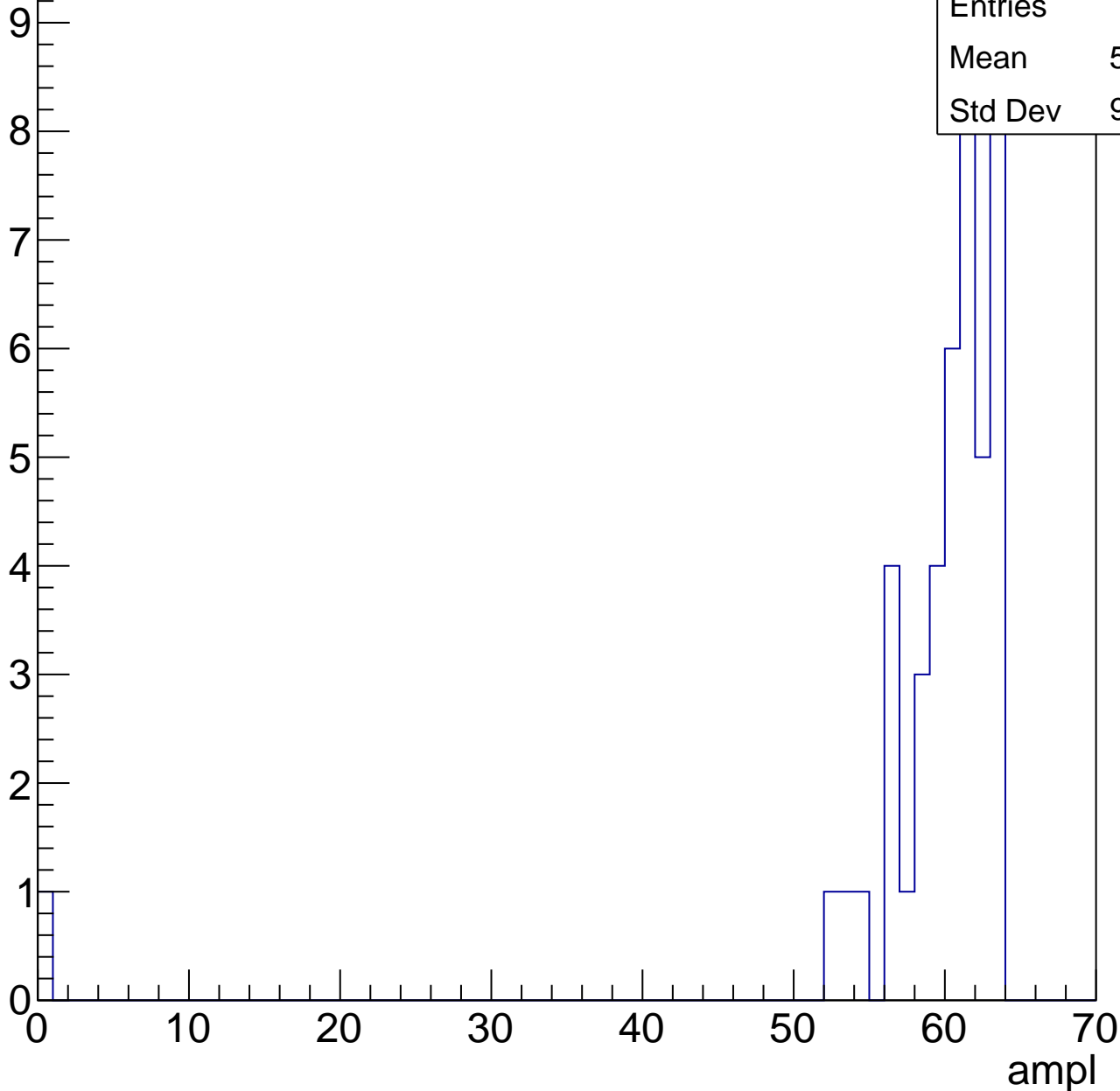


B0L001S, U14-ch66, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

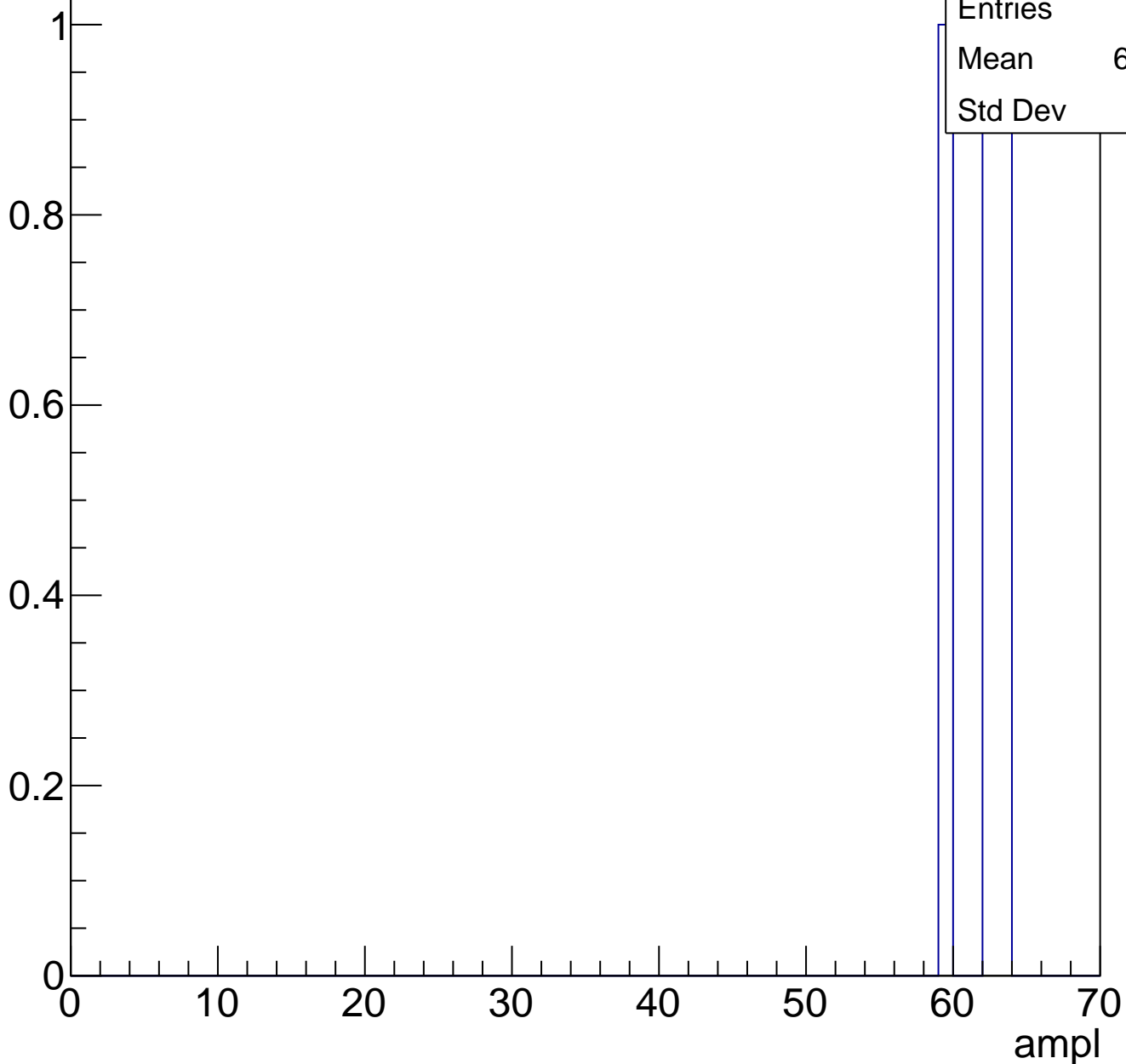
Entries	44
Mean	58.52
Std Dev	9.355



B0L001S, U14-ch66, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch66, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch67, adc0

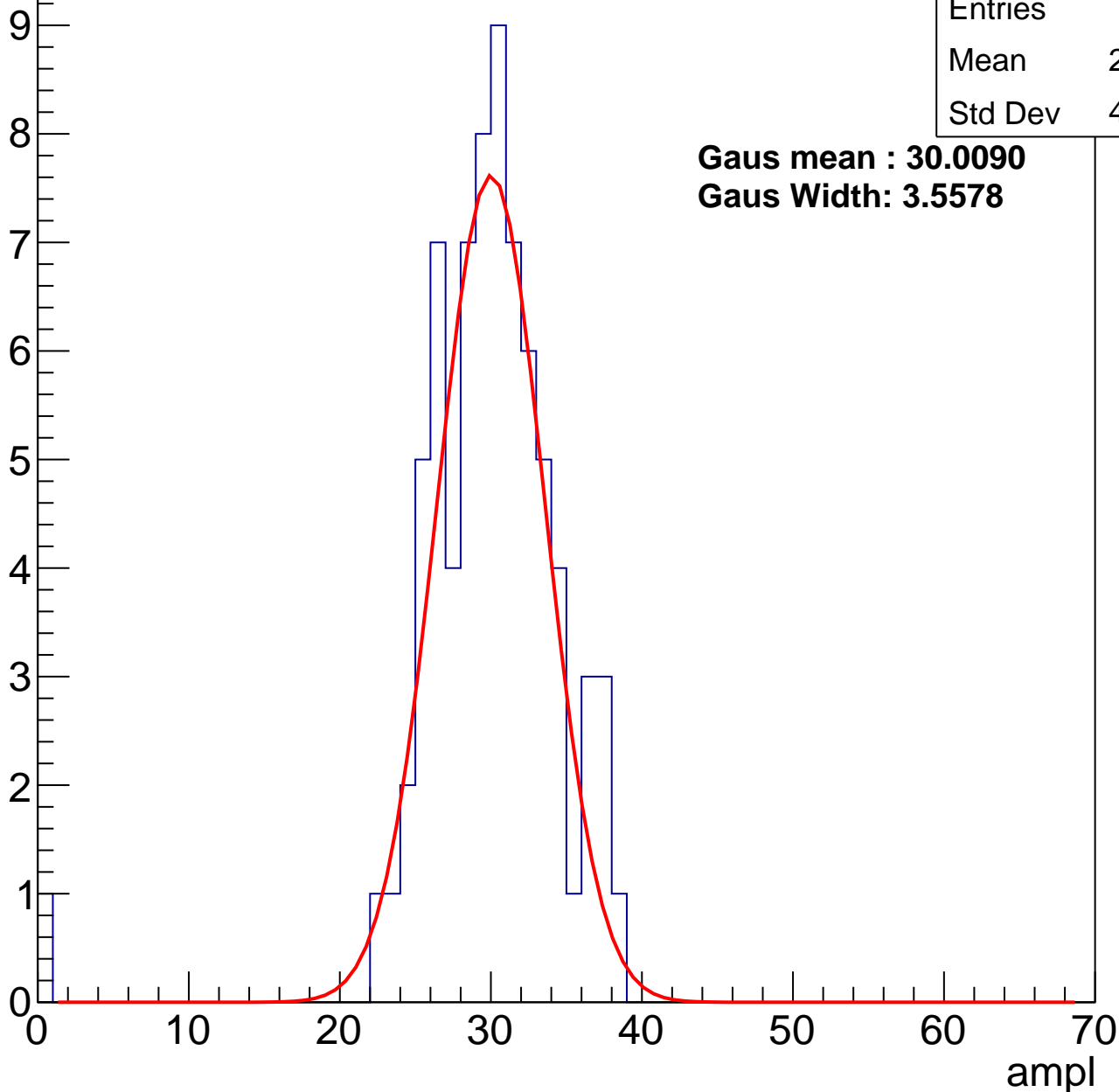
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	29.44
Std Dev	4.973

Gaus mean : 30.0090

Gaus Width: 3.5578



B0L001S, U14-ch67, adc1

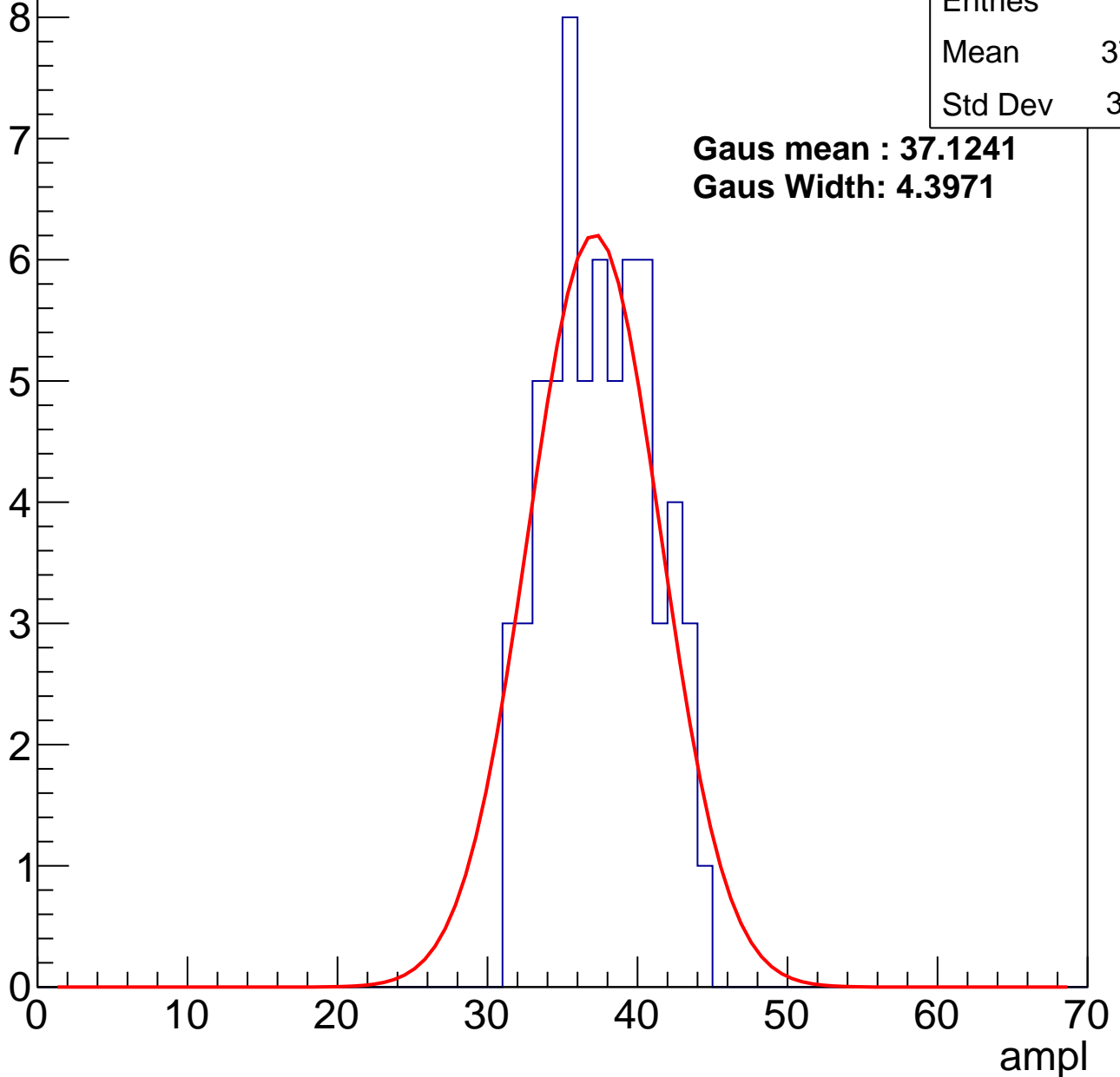
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.05
Std Dev	3.411

Gaus mean : 37.1241

Gaus Width: 4.3971



B0L001S, U14-ch67, adc2

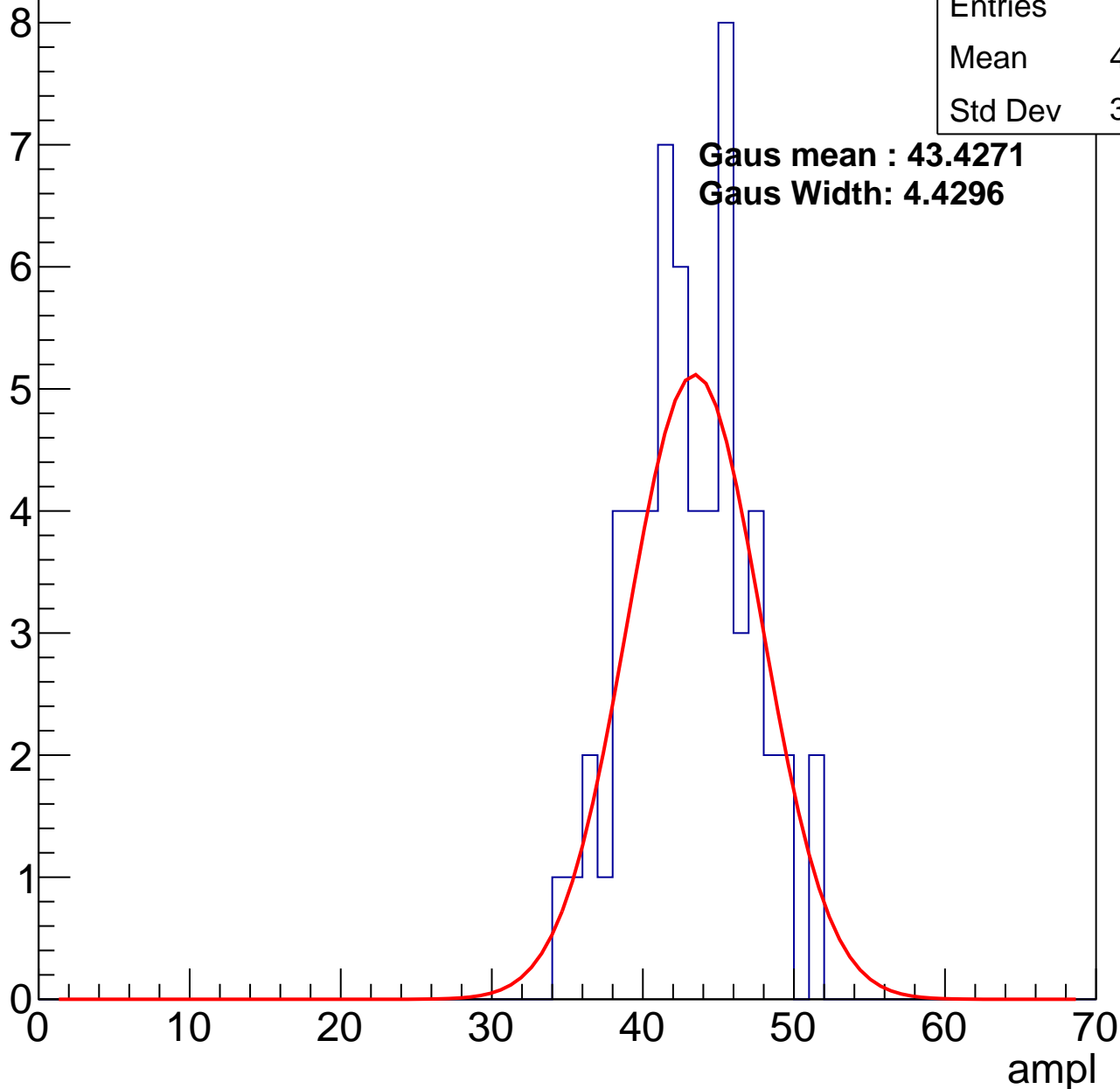
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	42.63
Std Dev	3.866

Gaus mean : 43.4271

Gaus Width: 4.4296

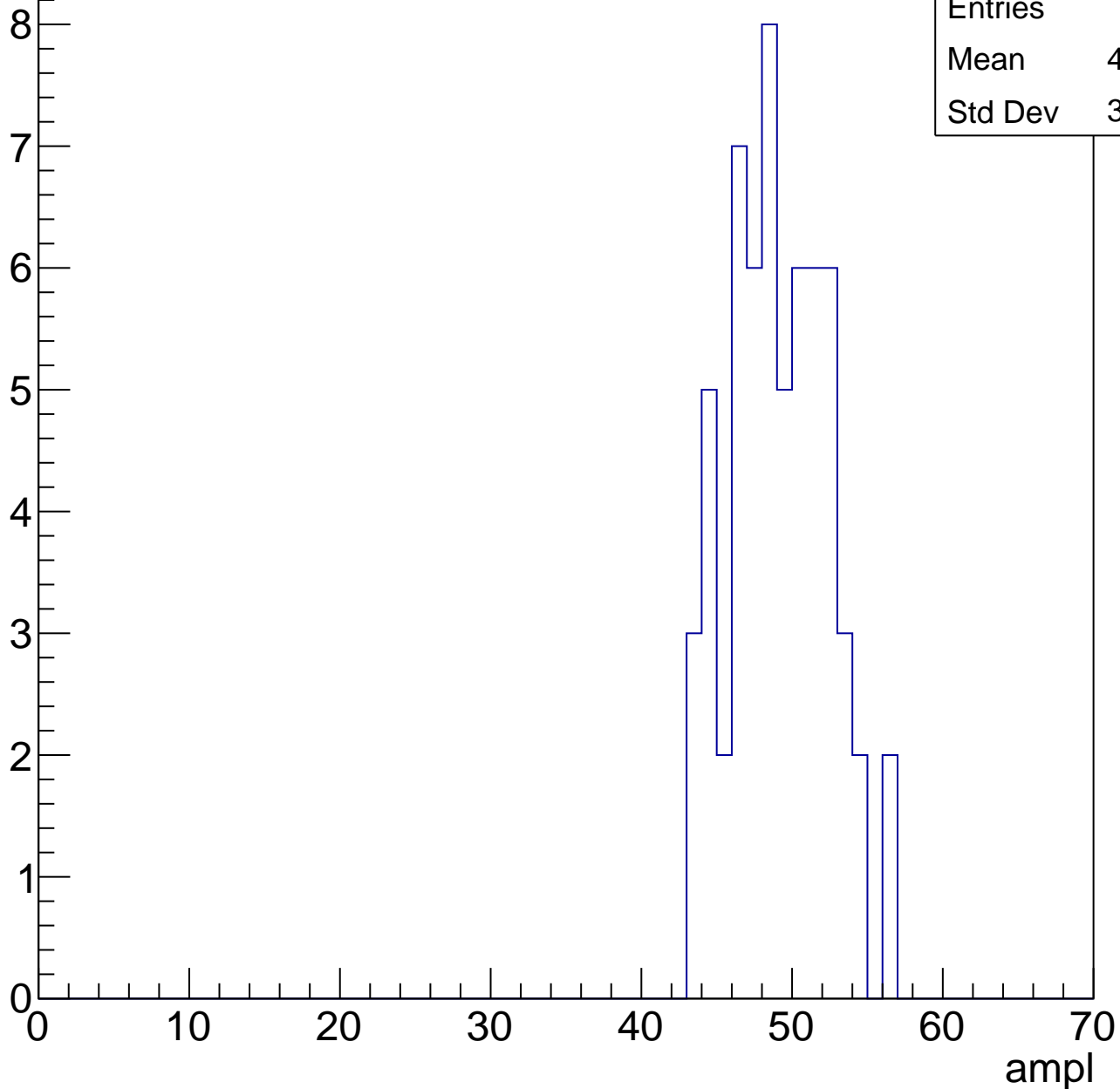


B0L001S, U14-ch67, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

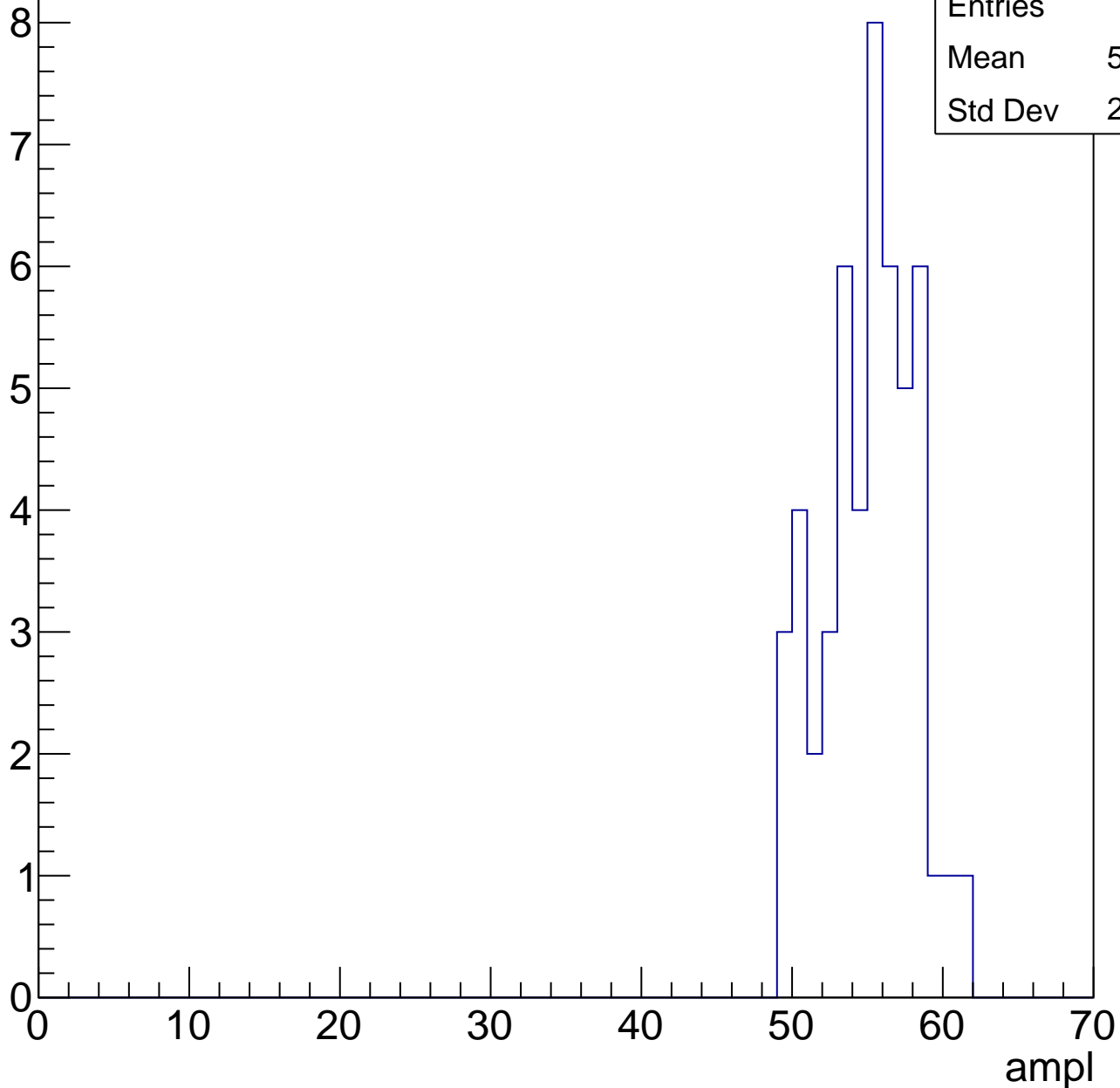
Entries	61
Mean	48.67
Std Dev	3.222



B0L001S, U14-ch67, adc4

calib_packv5_042523_0143.root, FC#9, port A1

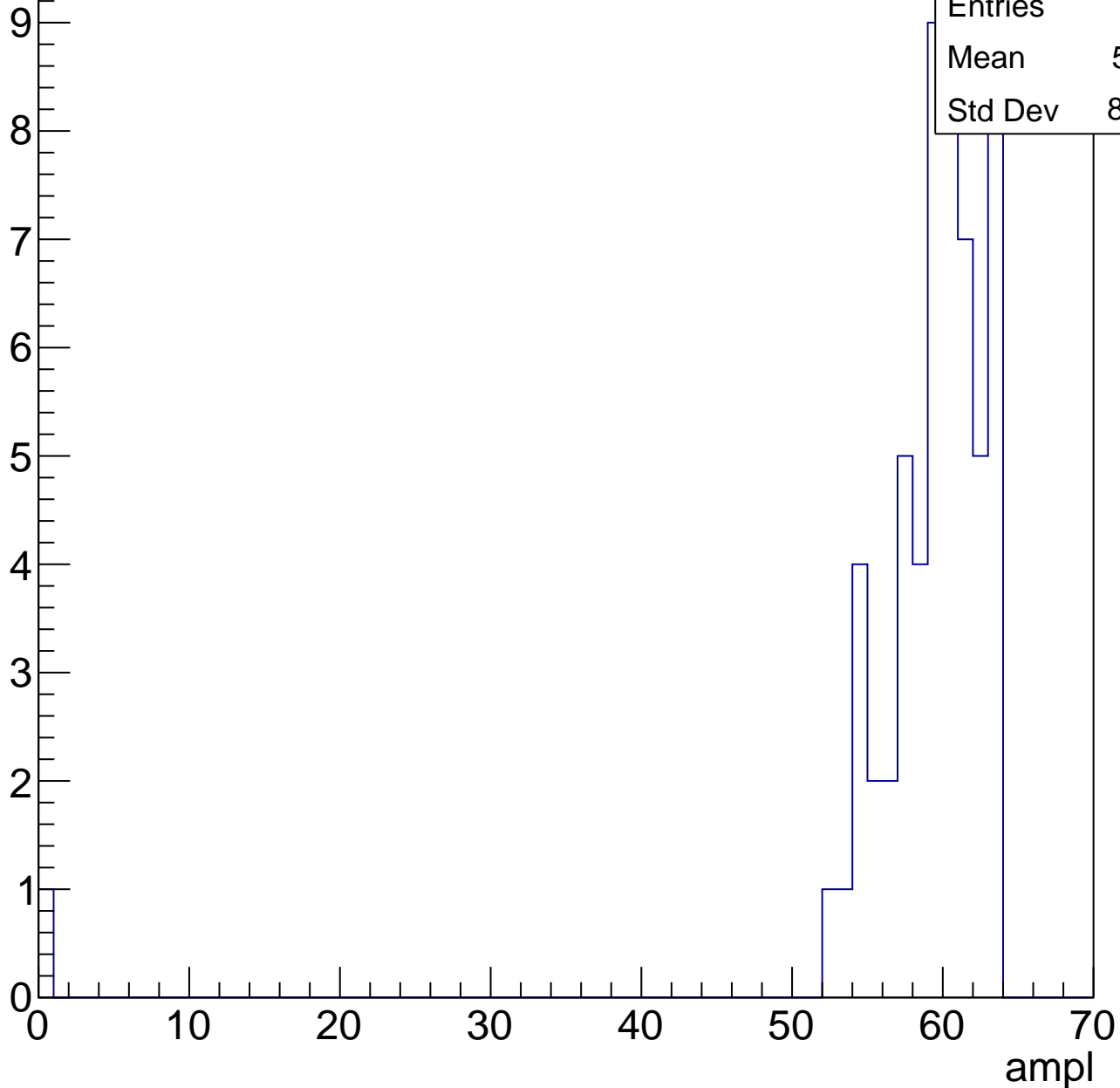
Entry



B0L001S, U14-ch67, adc5

calib_packv5_042523_0143.root, FC#9, port A1

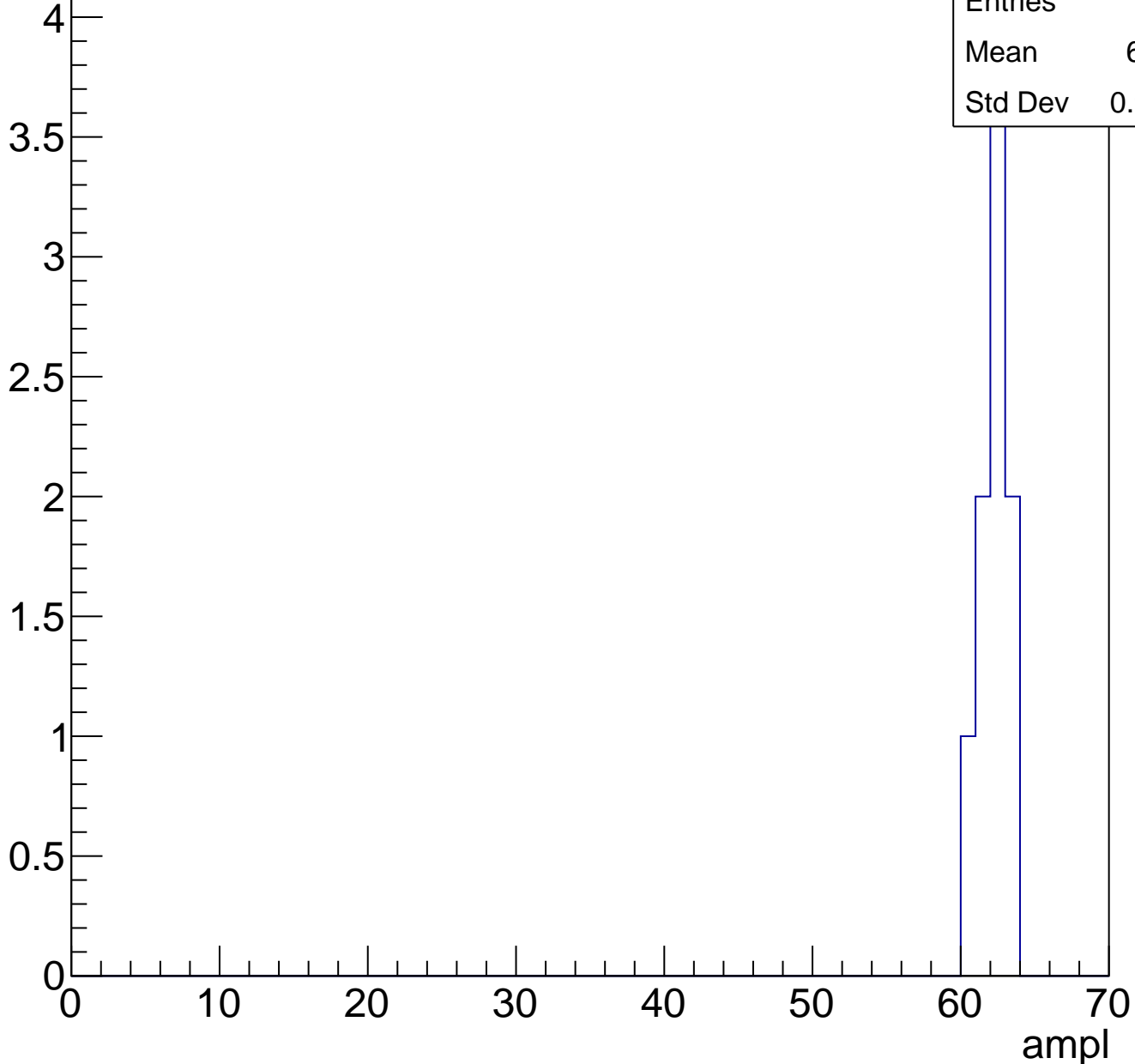
Entry



B0L001S, U14-ch67, adc6

calib_packv5_042523_0143.root, FC#9, port A1

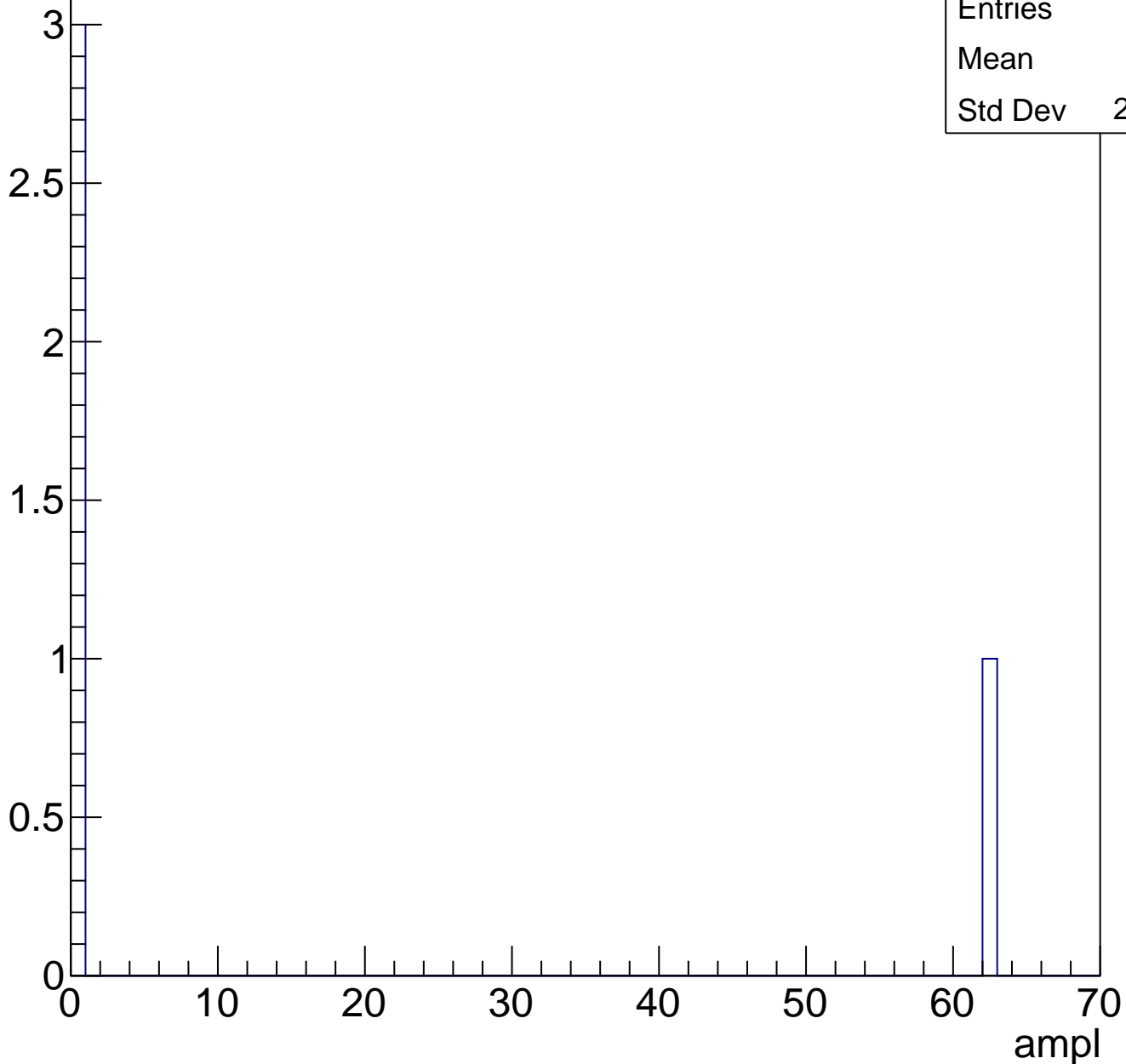
Entry



B0L001S, U14-ch67, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch68, adc0

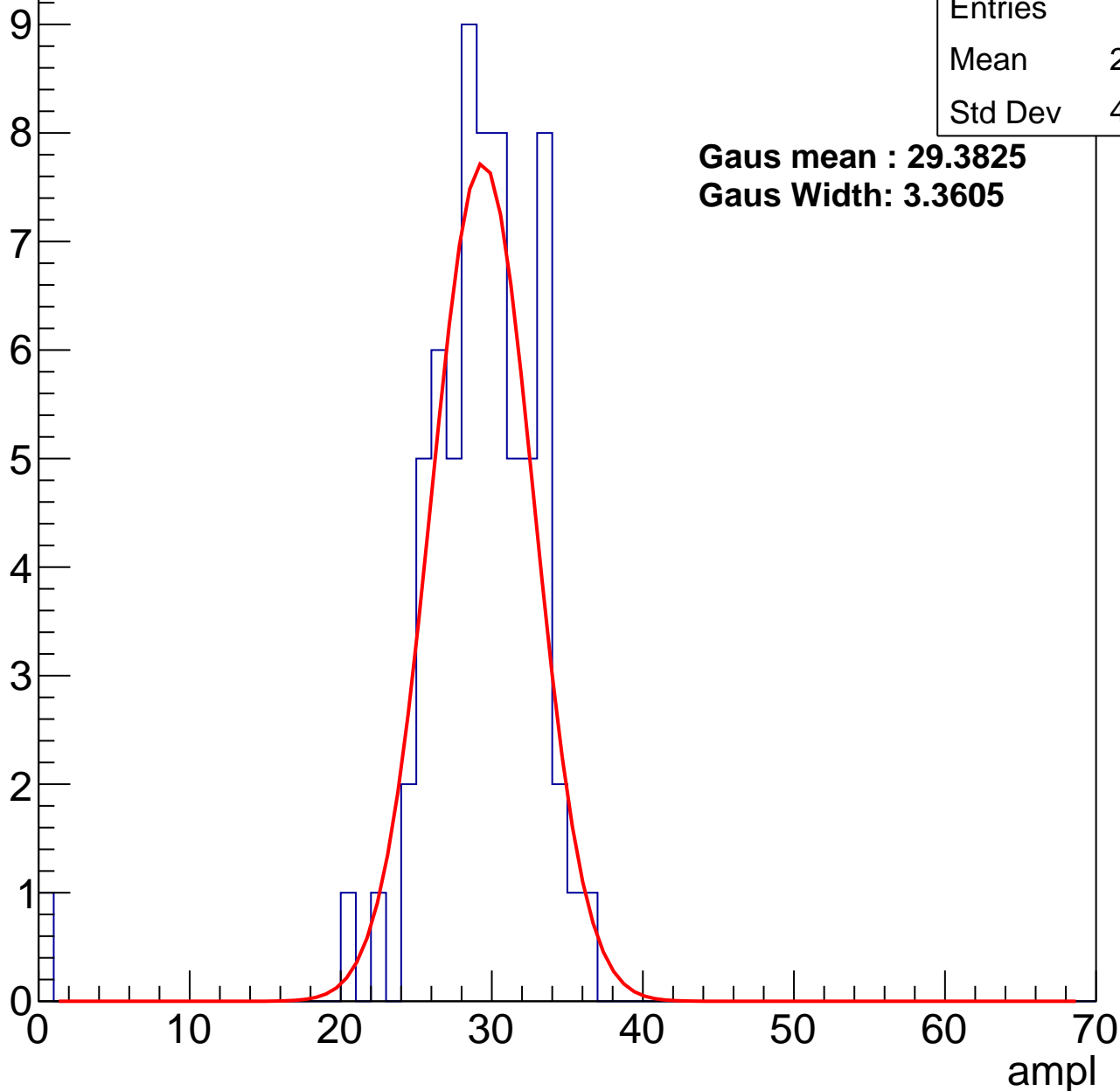
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	28.65
Std Dev	4.715

Gaus mean : 29.3825

Gaus Width: 3.3605



B0L001S, U14-ch68, adc1

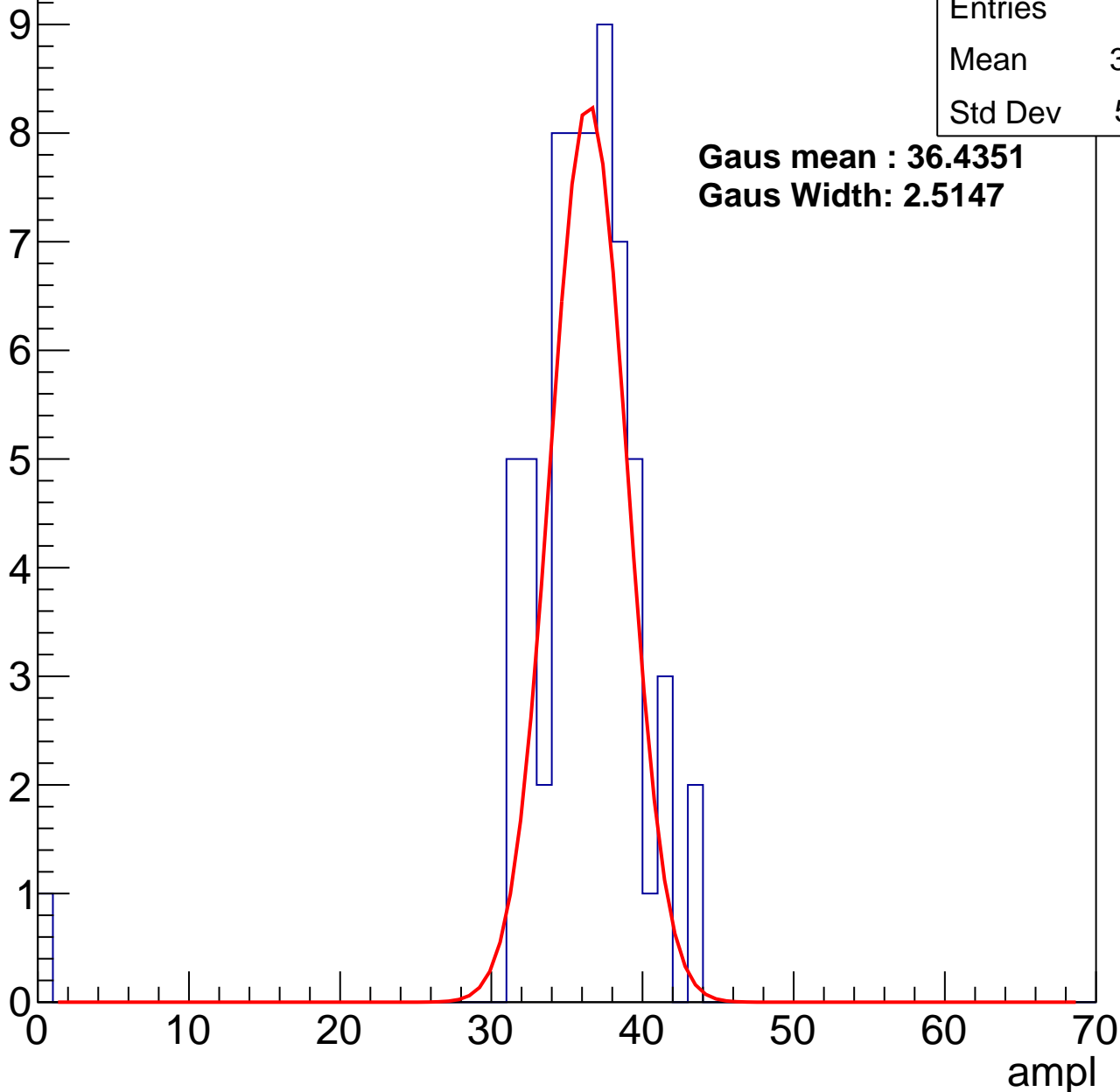
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	35.38
Std Dev	5.311

Gaus mean : 36.4351

Gaus Width: 2.5147



B0L001S, U14-ch68, adc2

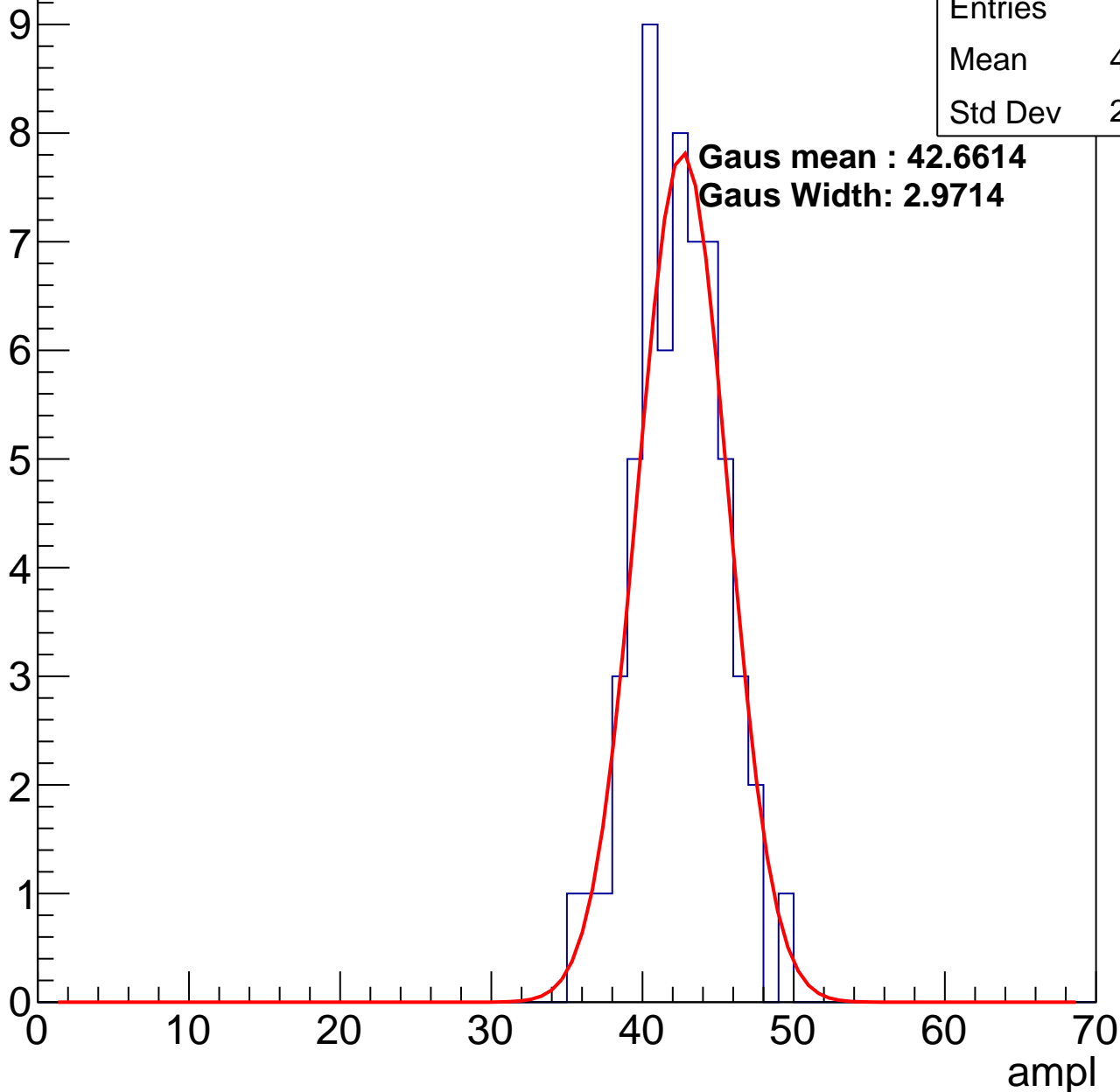
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	41.93
Std Dev	2.828

Gaus mean : 42.6614

Gaus Width: 2.9714

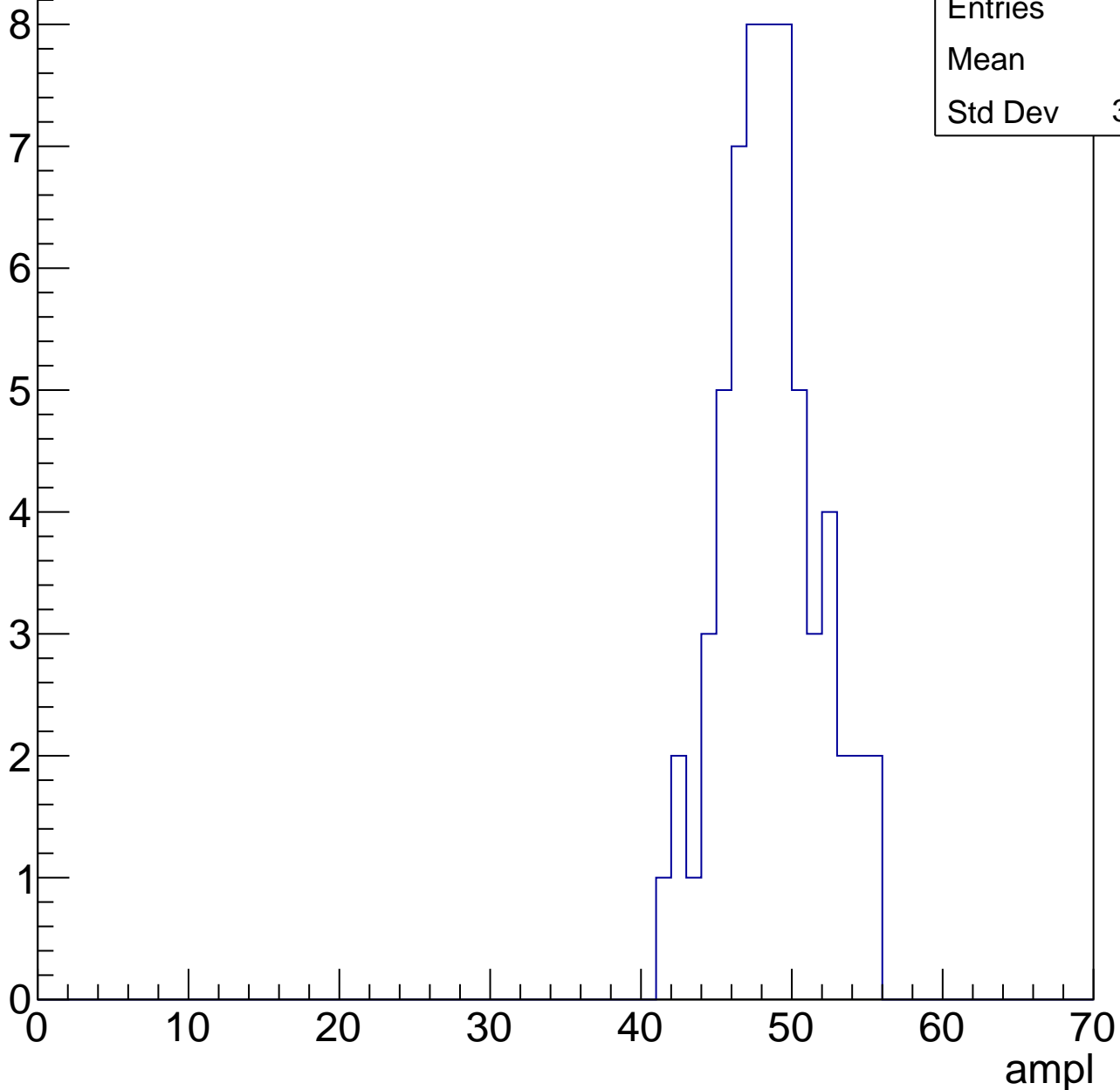


B0L001S, U14-ch68, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

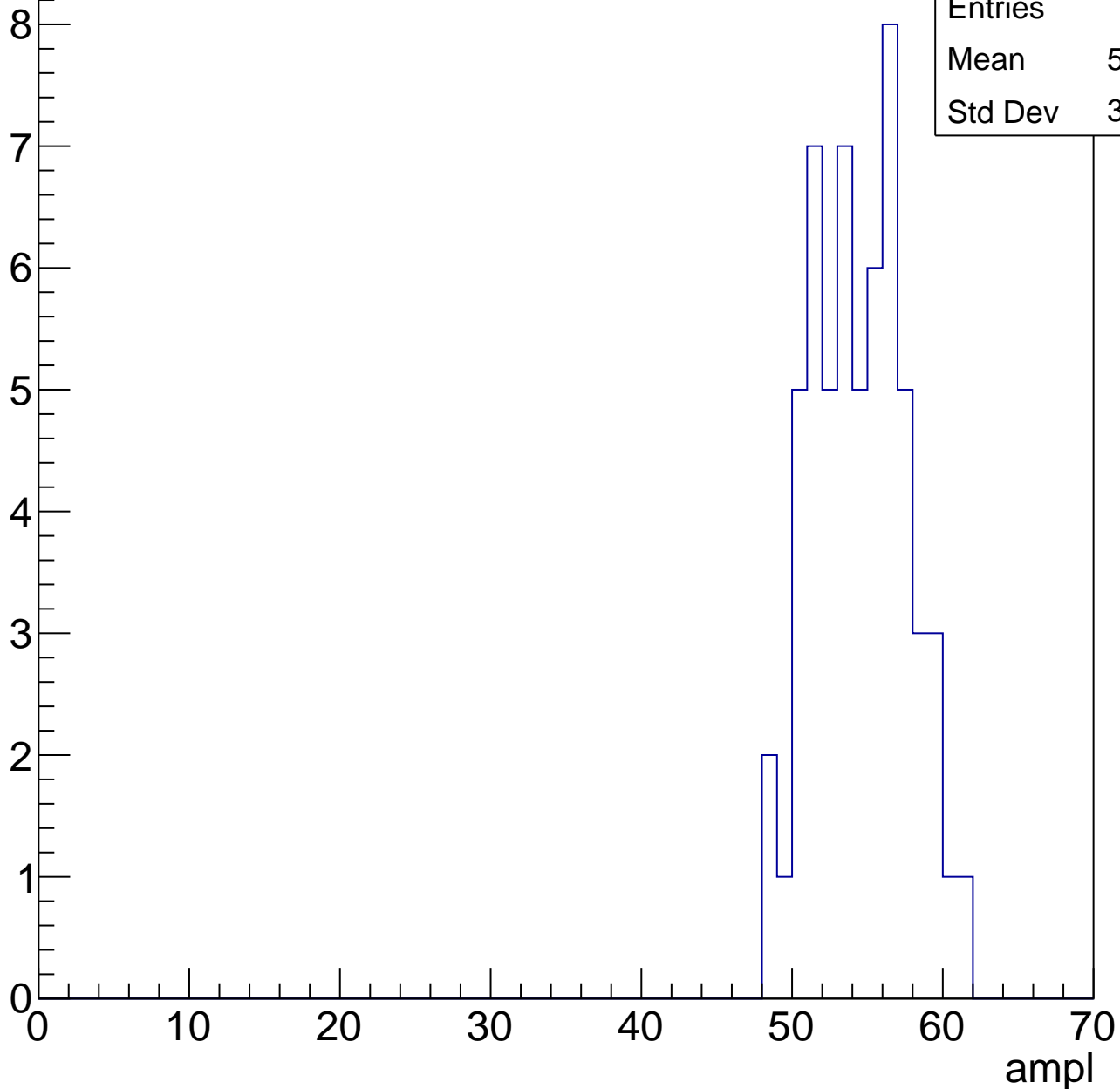
Entries	61
Mean	48.1
Std Dev	3.171



B0L001S, U14-ch68, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

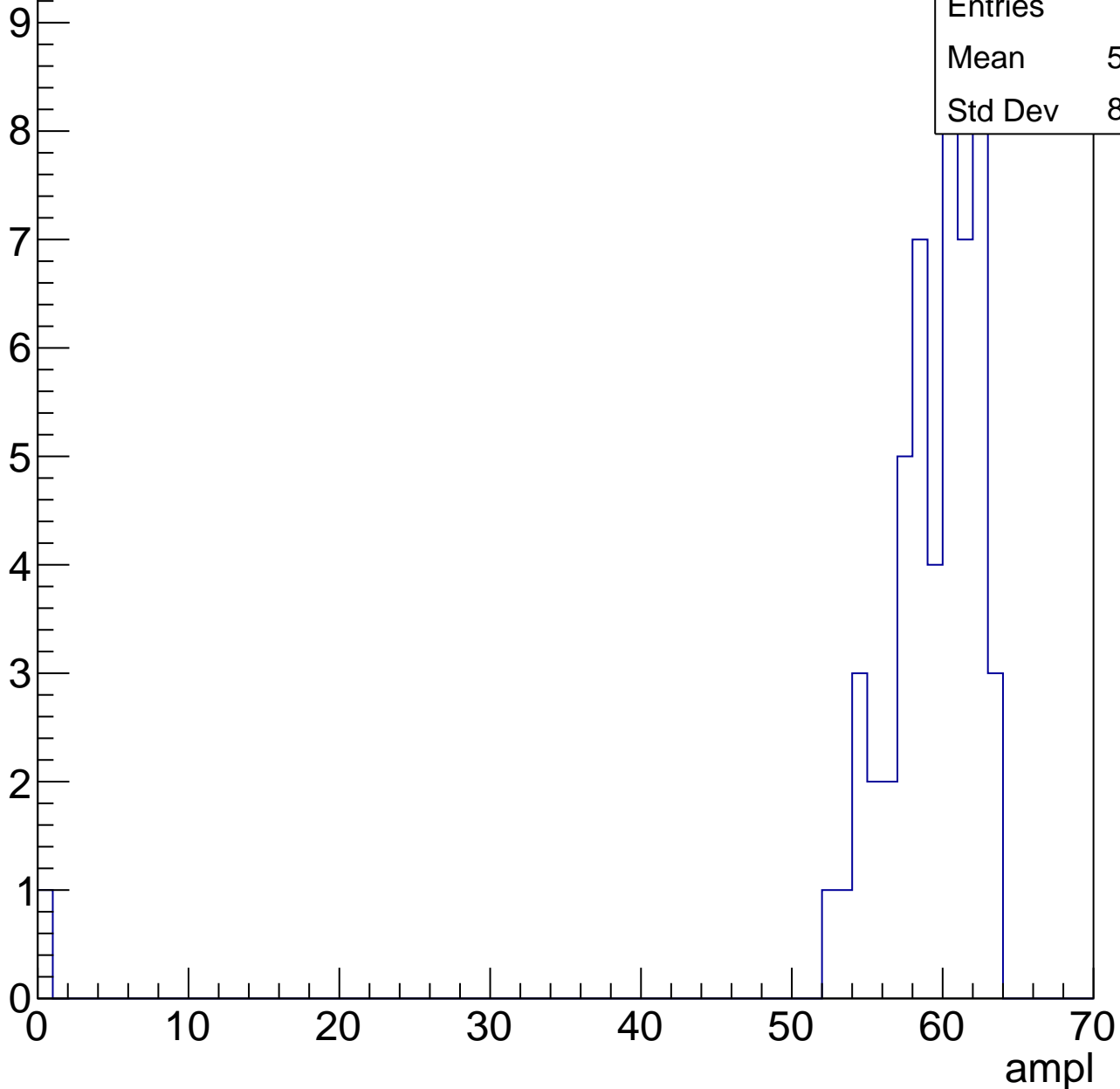


B0L001S, U14-ch68, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	57.92
Std Dev	8.492

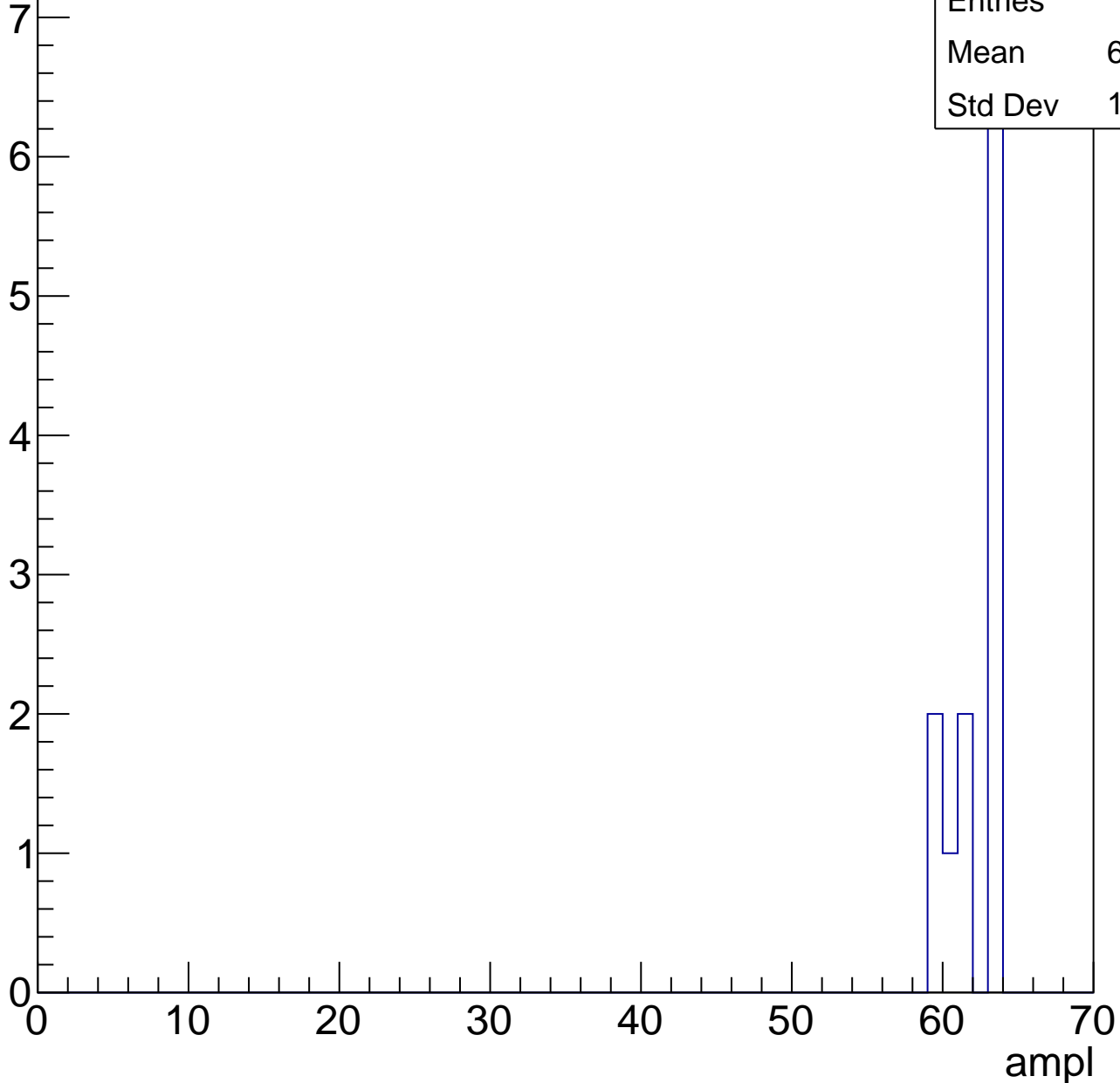


B0L001S, U14-ch68, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

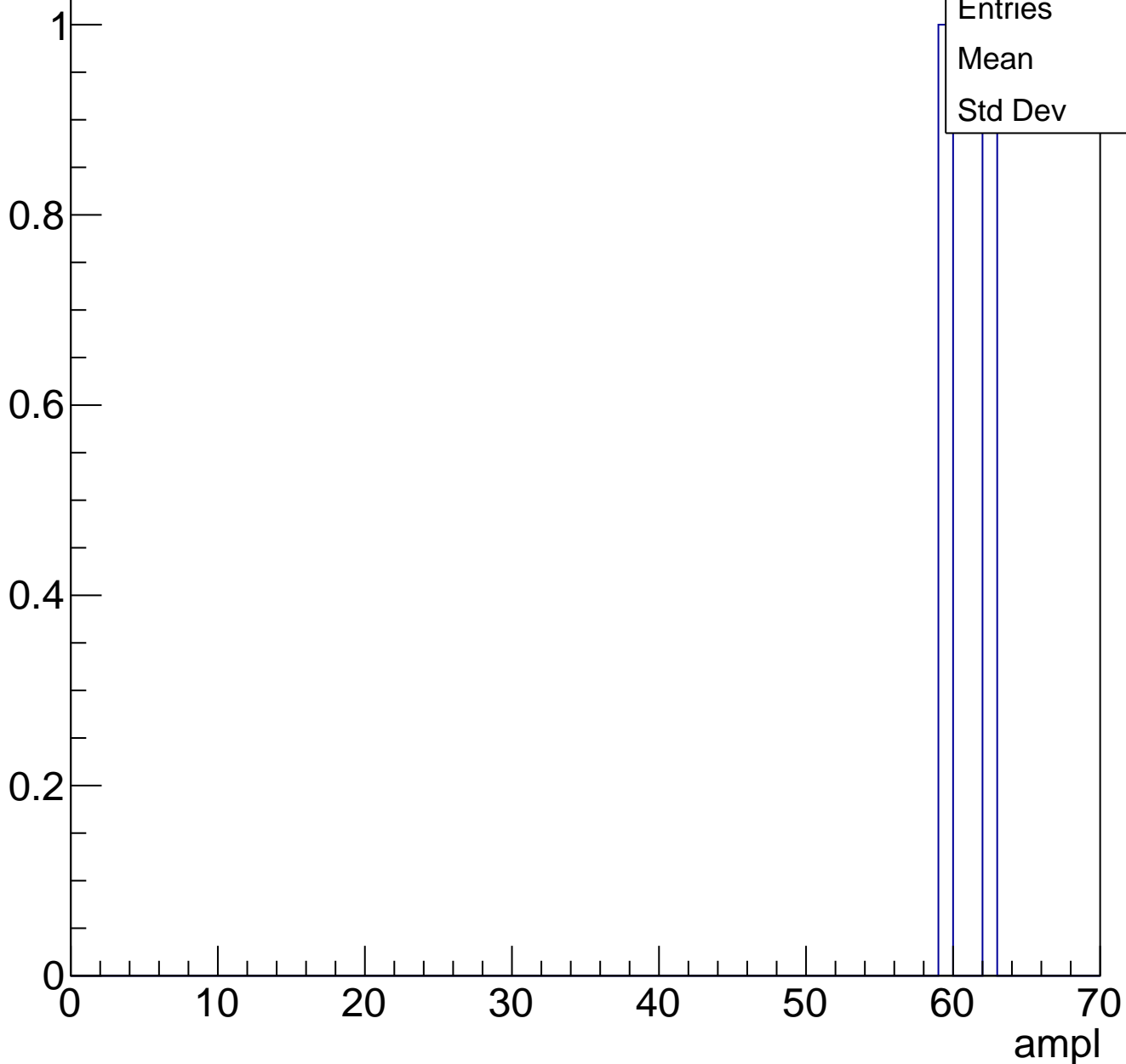
Entries	12
Mean	61.75
Std Dev	1.588



B0L001S, U14-ch68, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch69, adc0

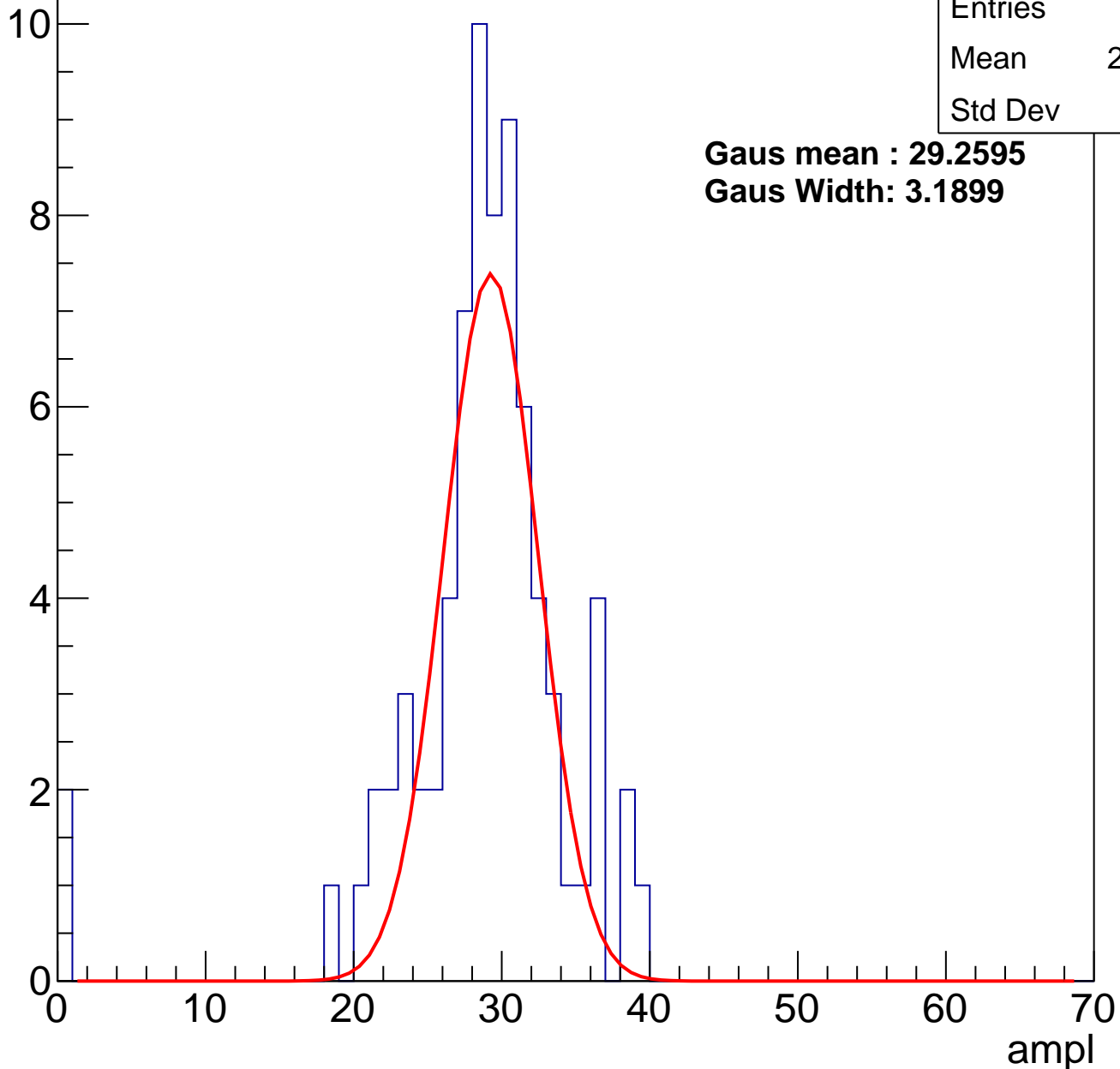
calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	28.09
Std Dev	6.27

Gaus mean : 29.2595

Gaus Width: 3.1899

Entry



B0L001S, U14-ch69, adc1

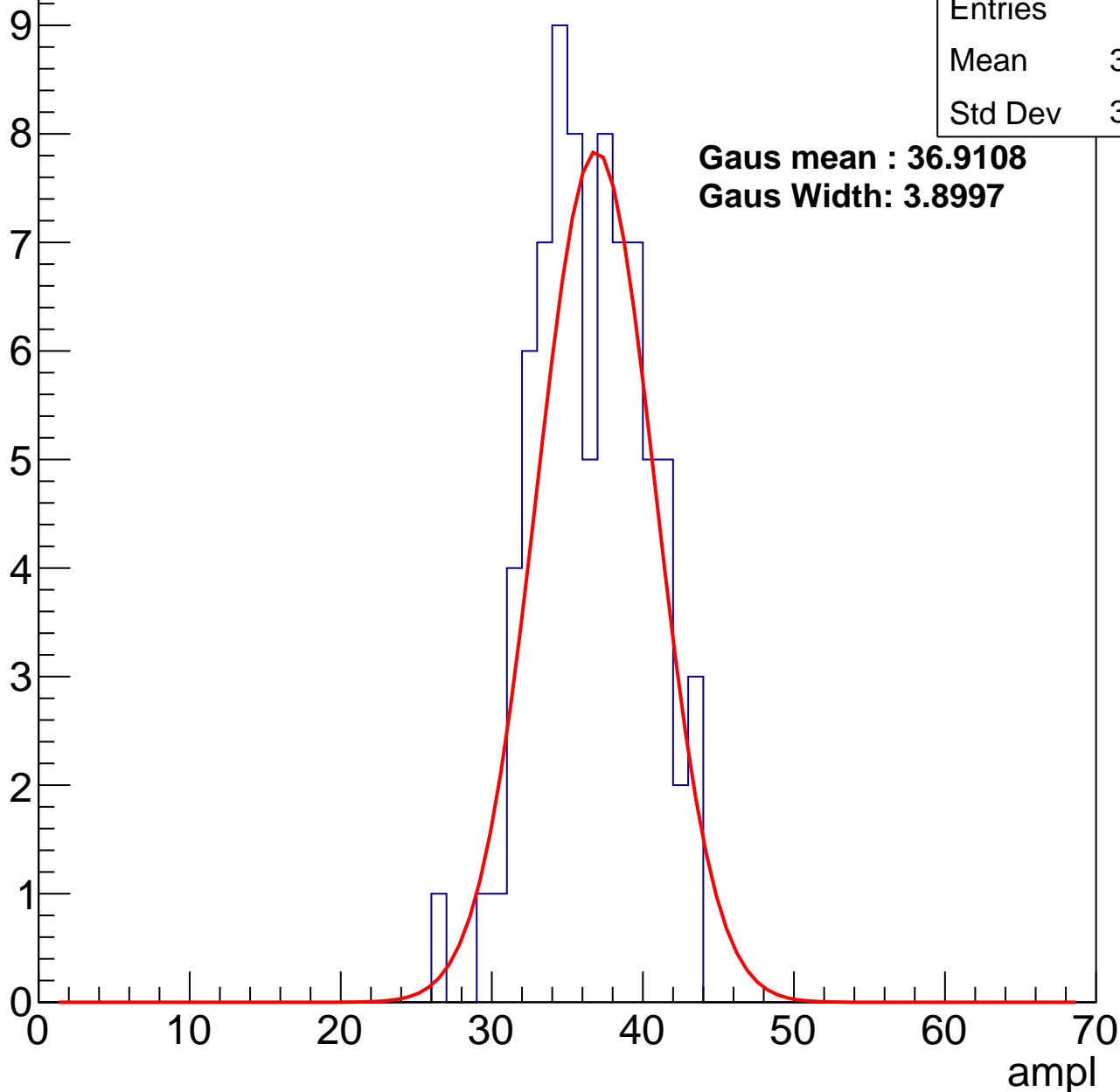
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	36.09
Std Dev	3.576

Gaus mean : 36.9108

Gaus Width: 3.8997



B0L001S, U14-ch69, adc2

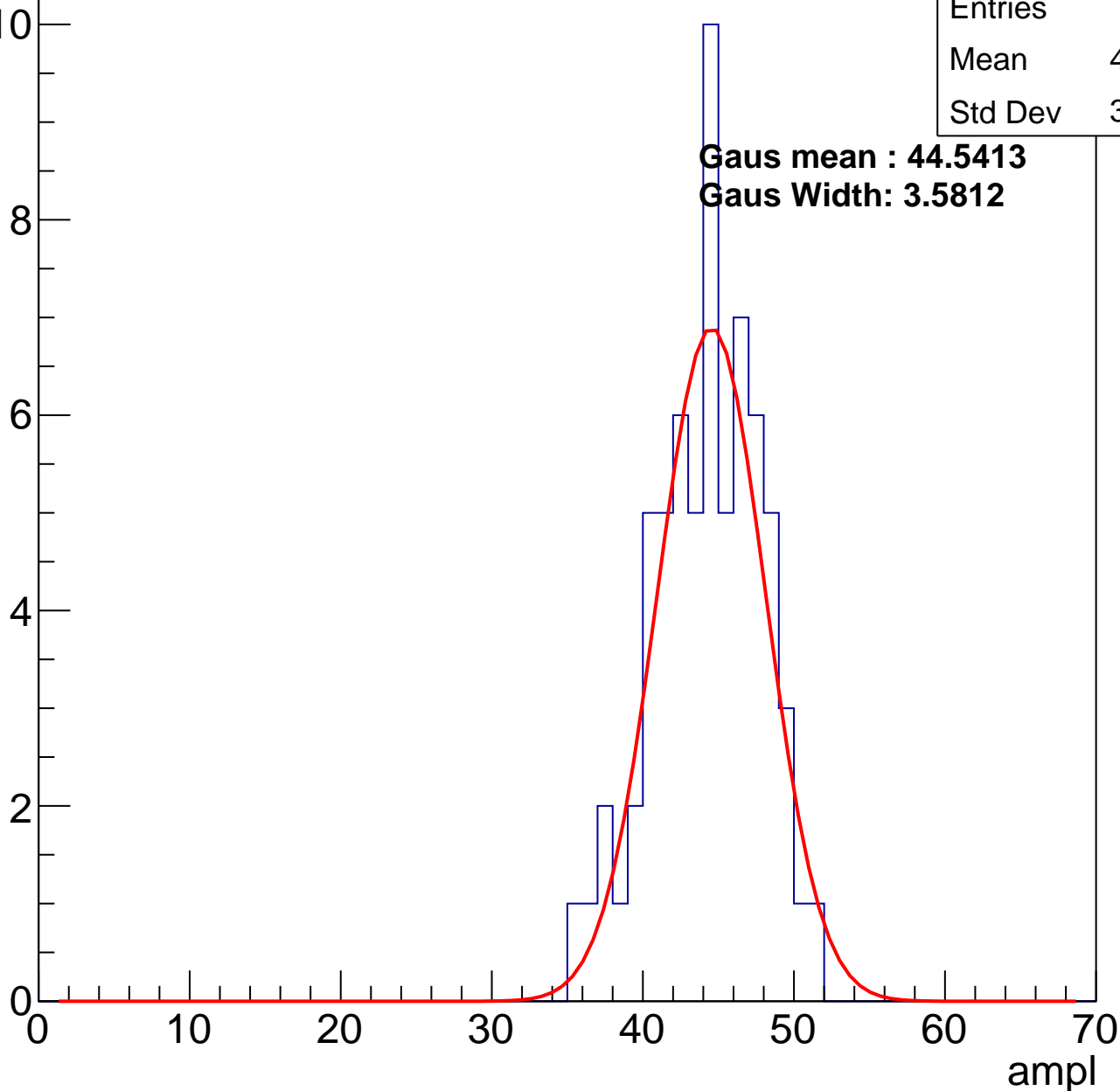
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	43.79
Std Dev	3.497

Gaus mean : 44.5413

Gaus Width: 3.5812

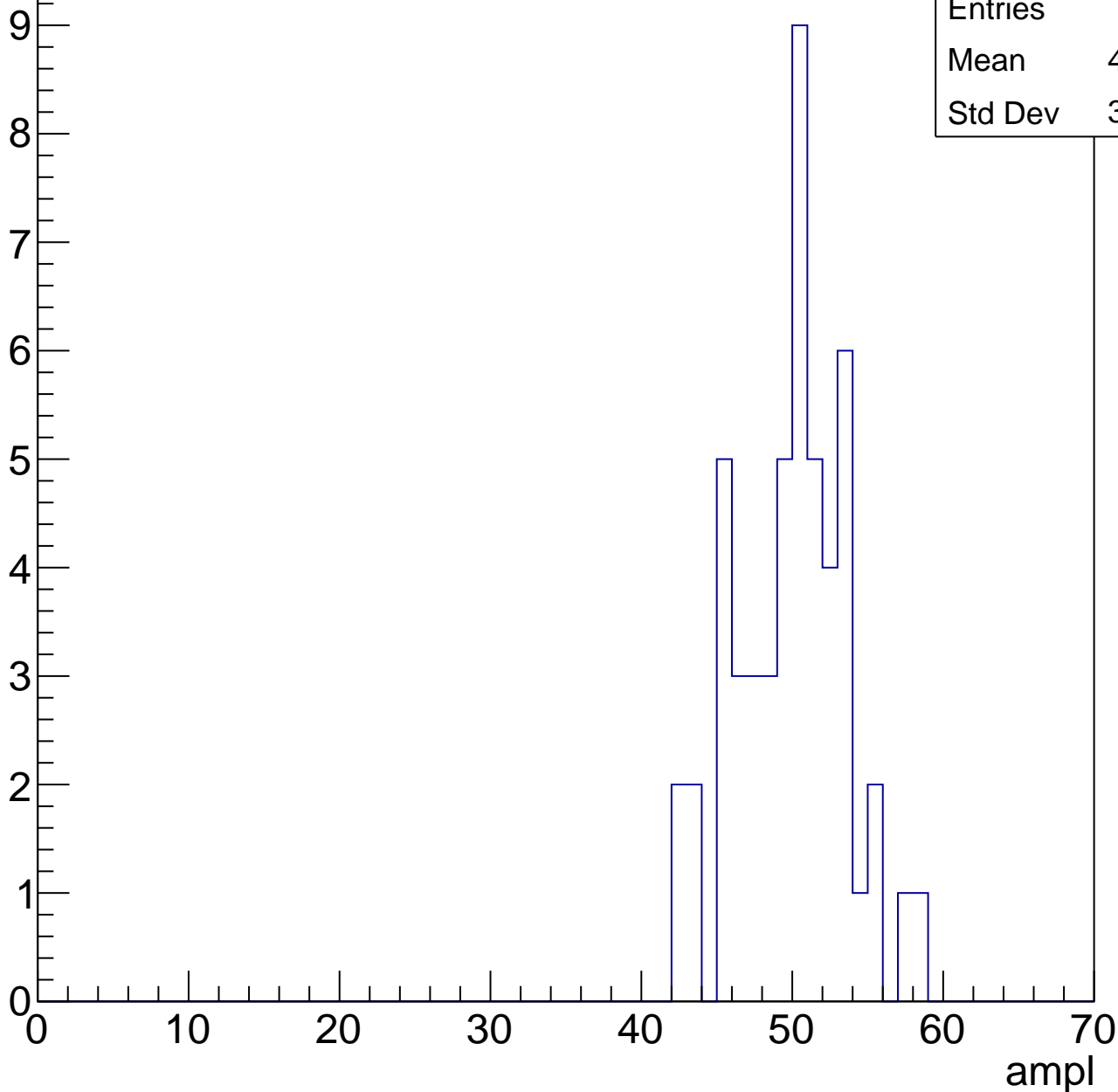


B0L001S, U14-ch69, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	49.48
Std Dev	3.624

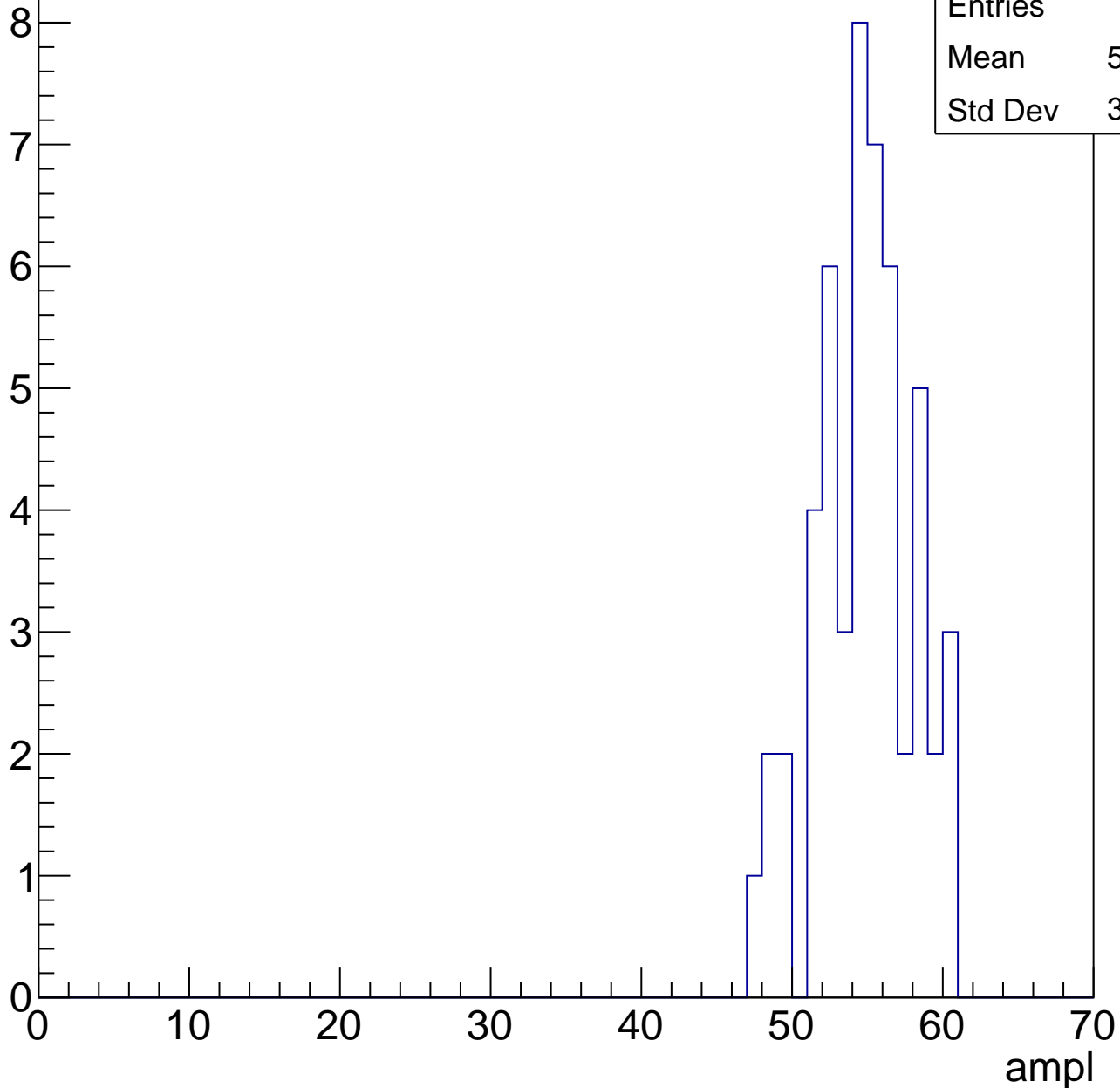


B0L001S, U14-ch69, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	54.33
Std Dev	3.179

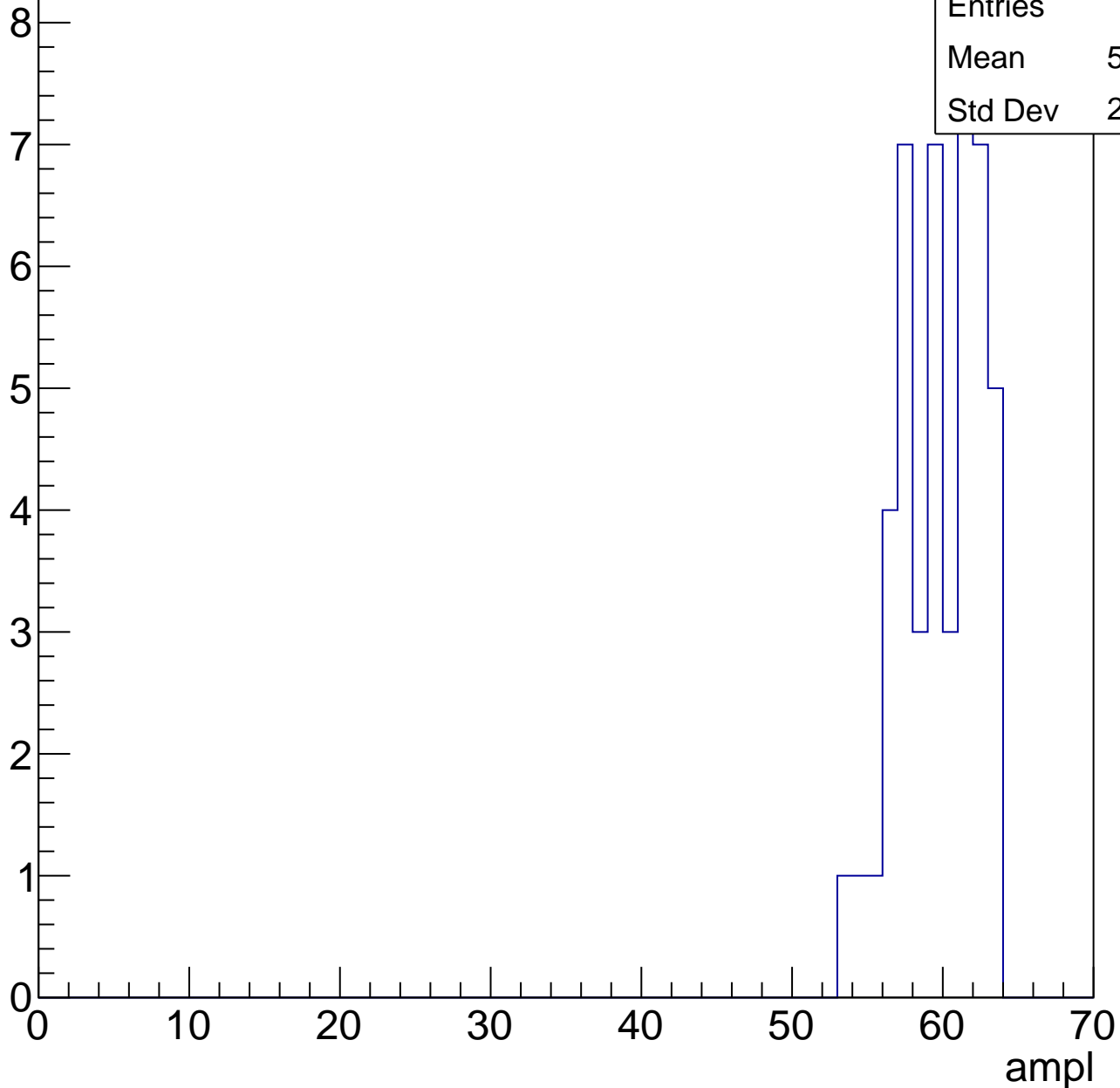


B0L001S, U14-ch69, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

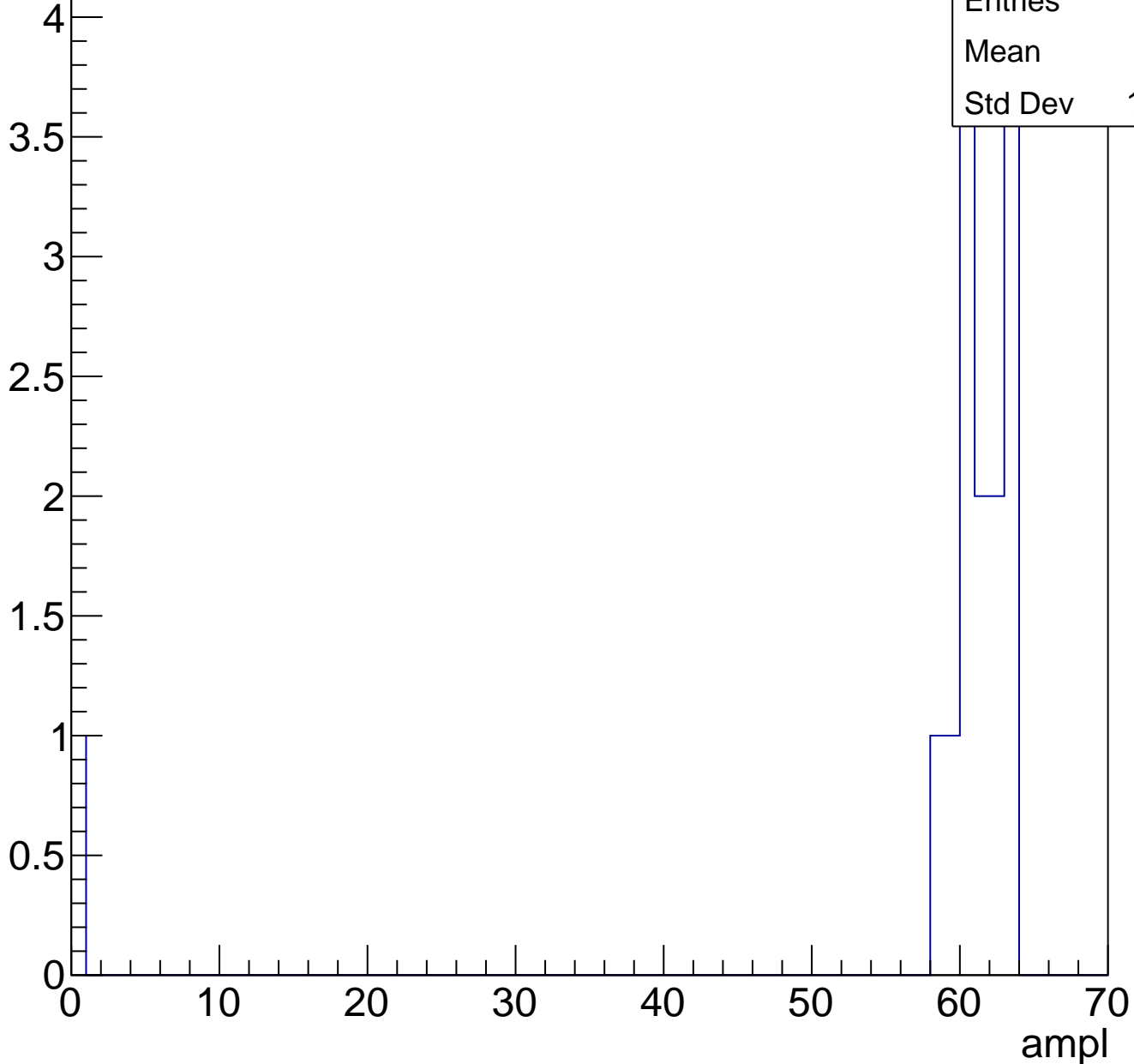
Entries	47
Mean	59.34
Std Dev	2.595



B0L001S, U14-ch69, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch69, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch70, adc0

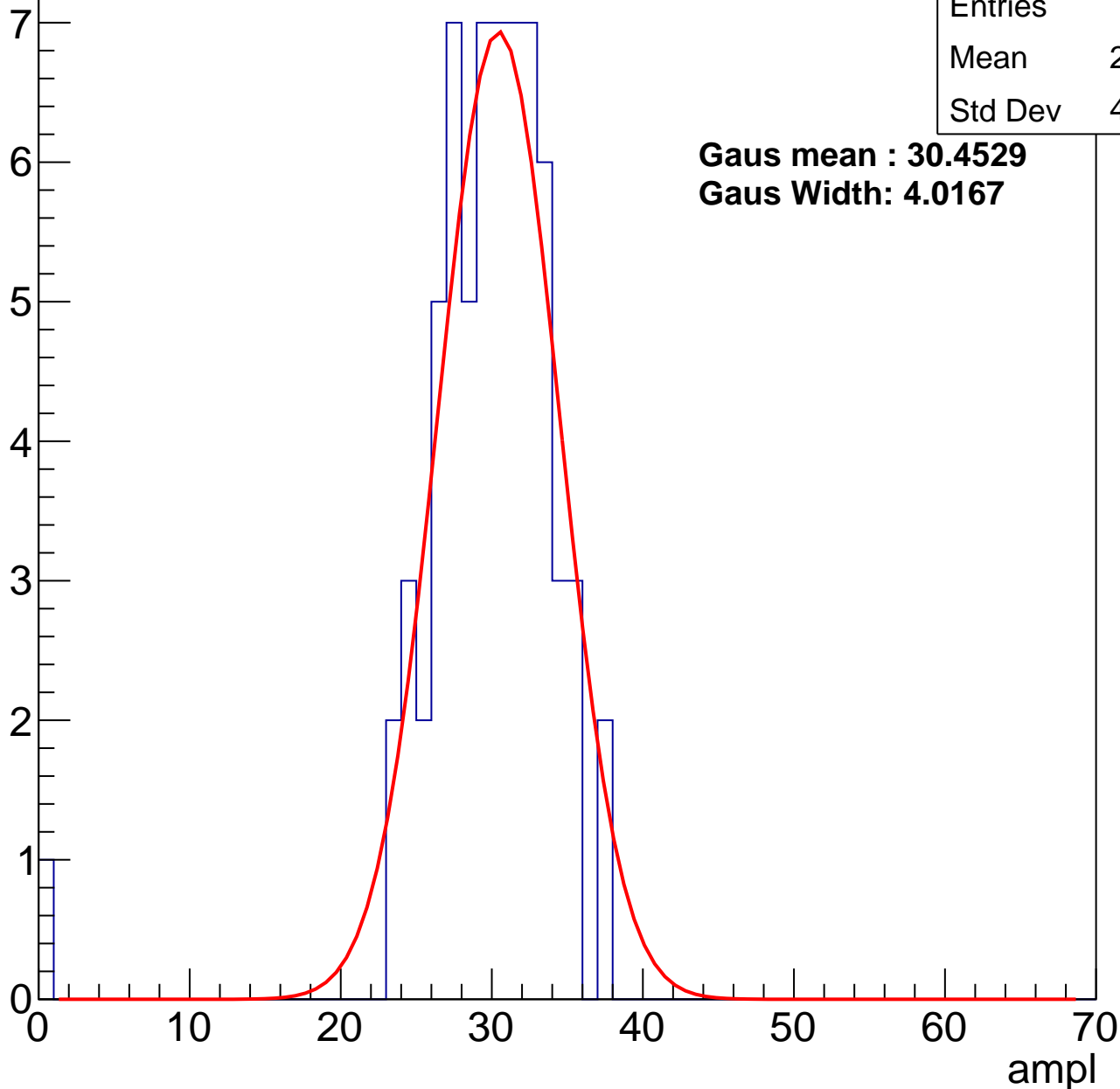
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	29.25
Std Dev	4.885

Gaus mean : 30.4529

Gaus Width: 4.0167



B0L001S, U14-ch70, adc1

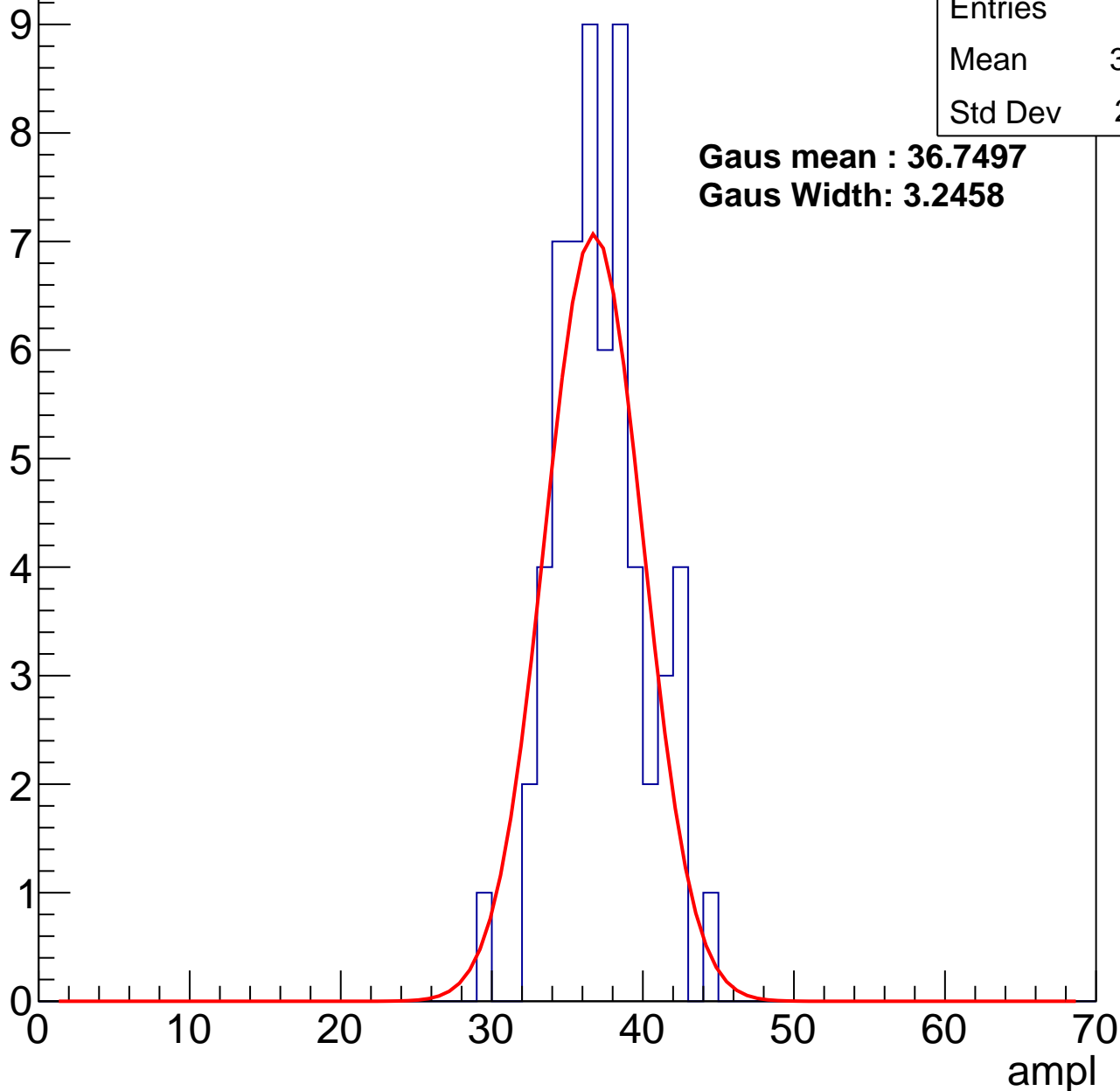
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	36.73
Std Dev	2.951

Gaus mean : 36.7497

Gaus Width: 3.2458



B0L001S, U14-ch70, adc2

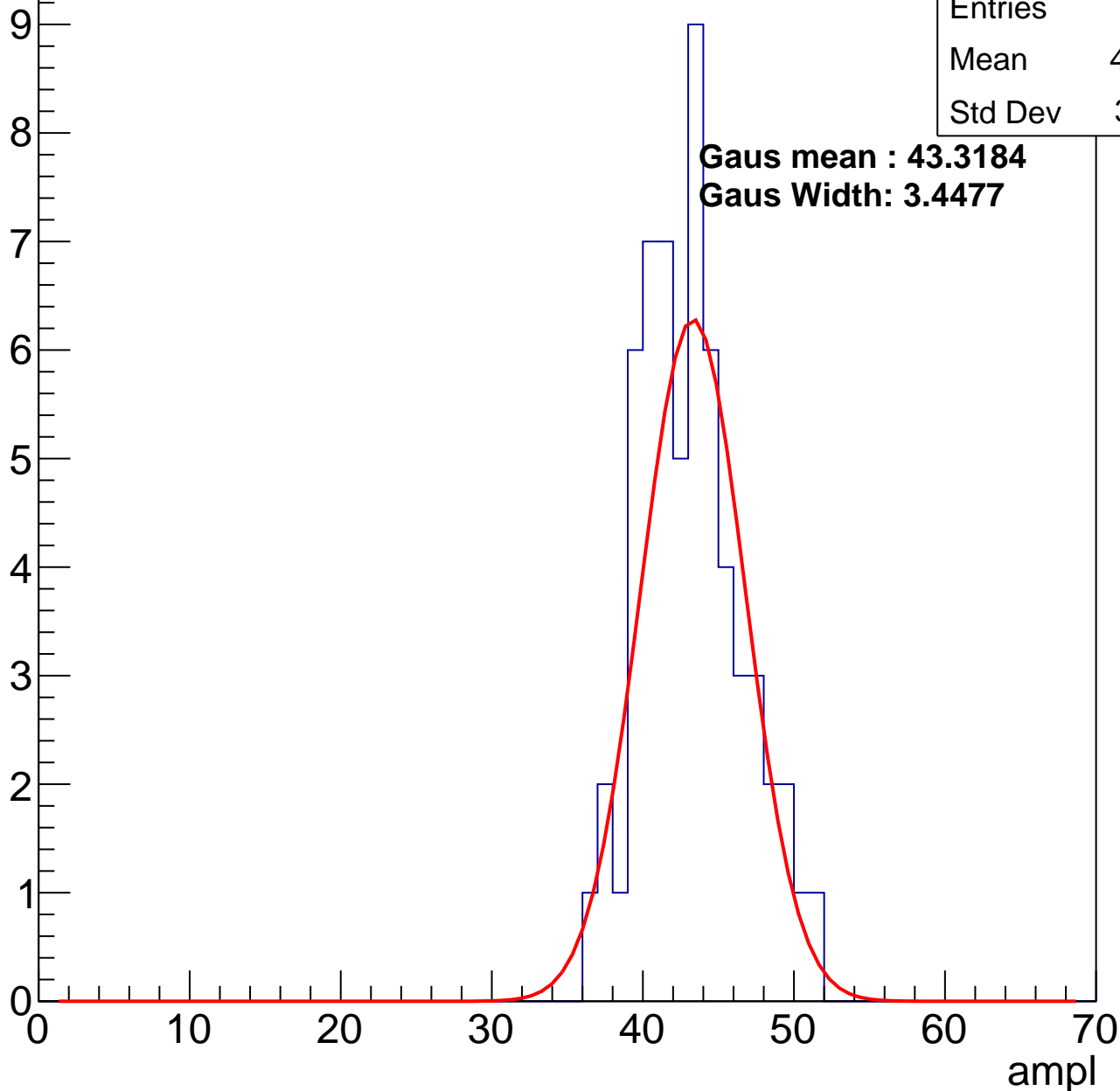
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	42.73
Std Dev	3.331

Gaus mean : 43.3184

Gaus Width: 3.4477

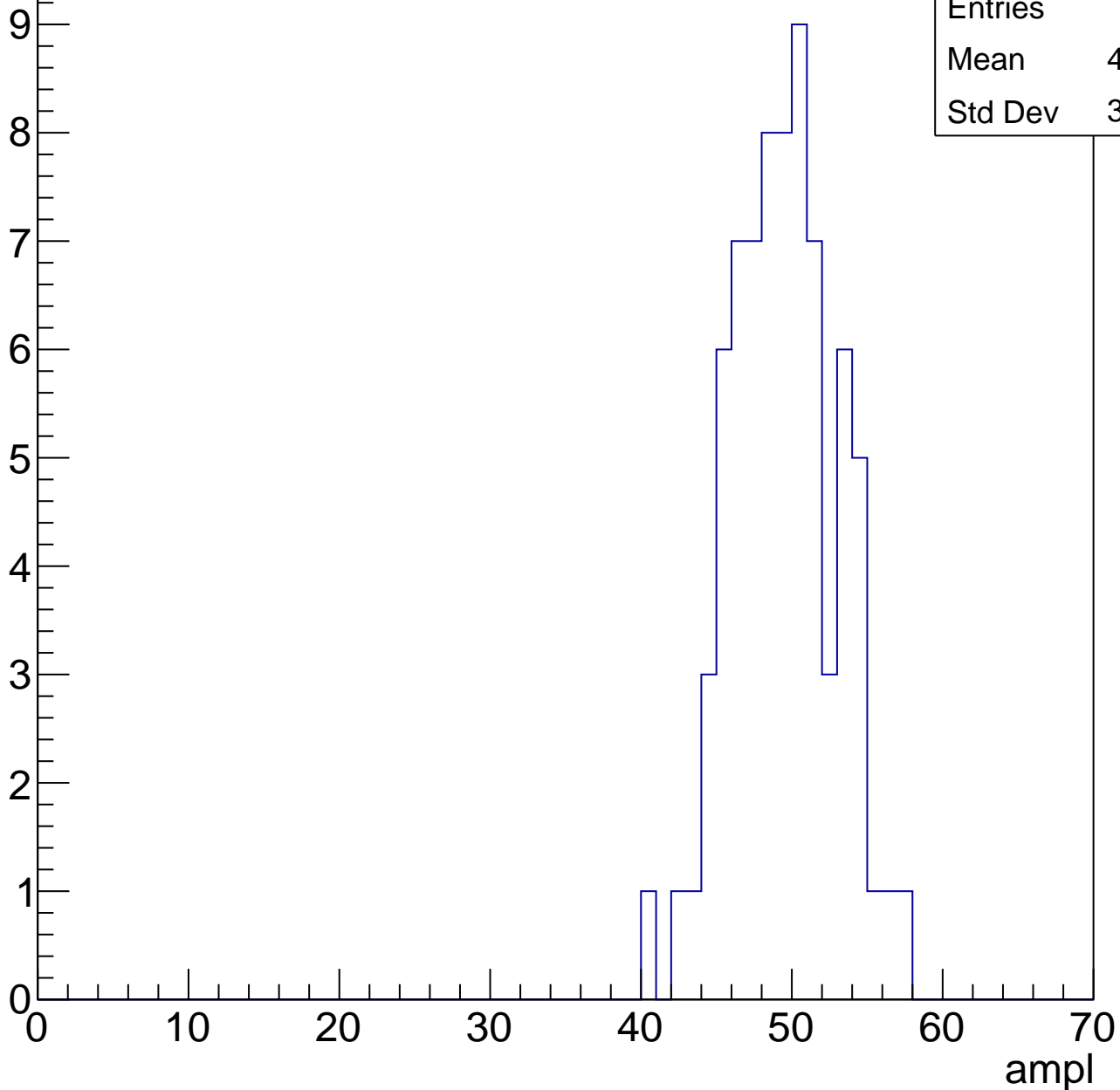


B0L001S, U14-ch70, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	48.97
Std Dev	3.406

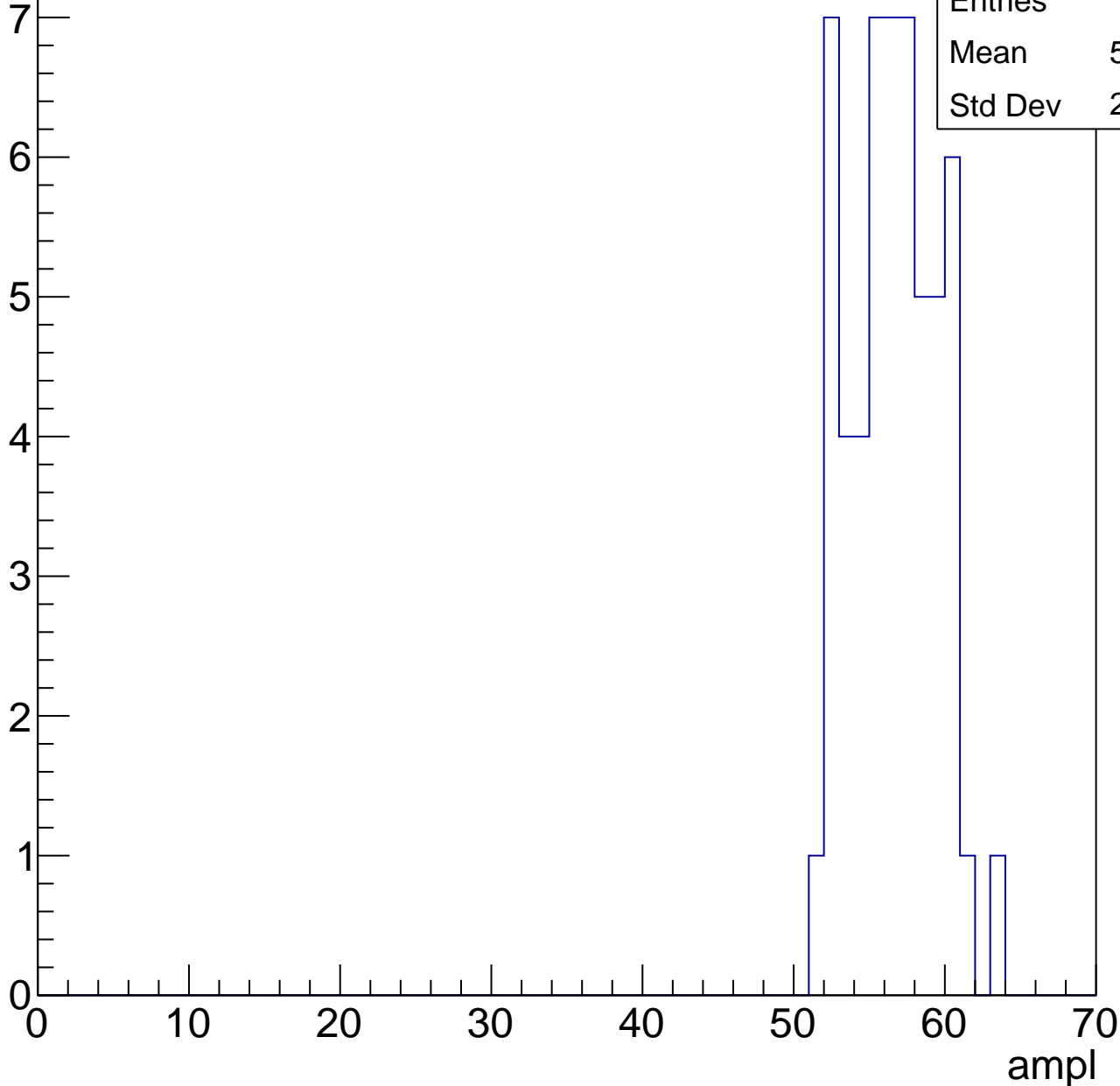


B0L001S, U14-ch70, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.15
Std Dev	2.818

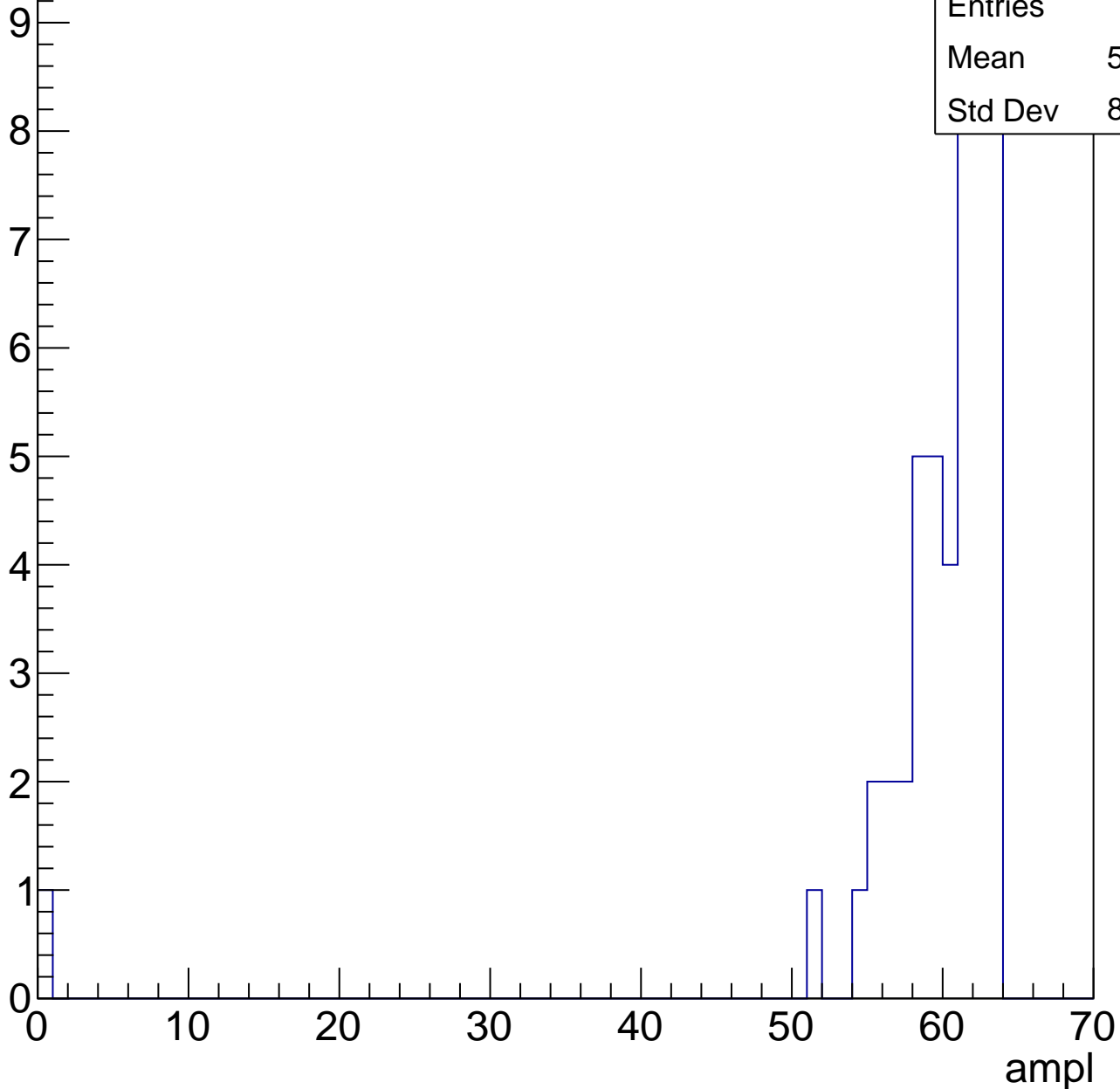


B0L001S, U14-ch70, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.65
Std Dev	8.976



B0L001S, U14-ch70, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

B0L001S, U14-ch70, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch71, adc0

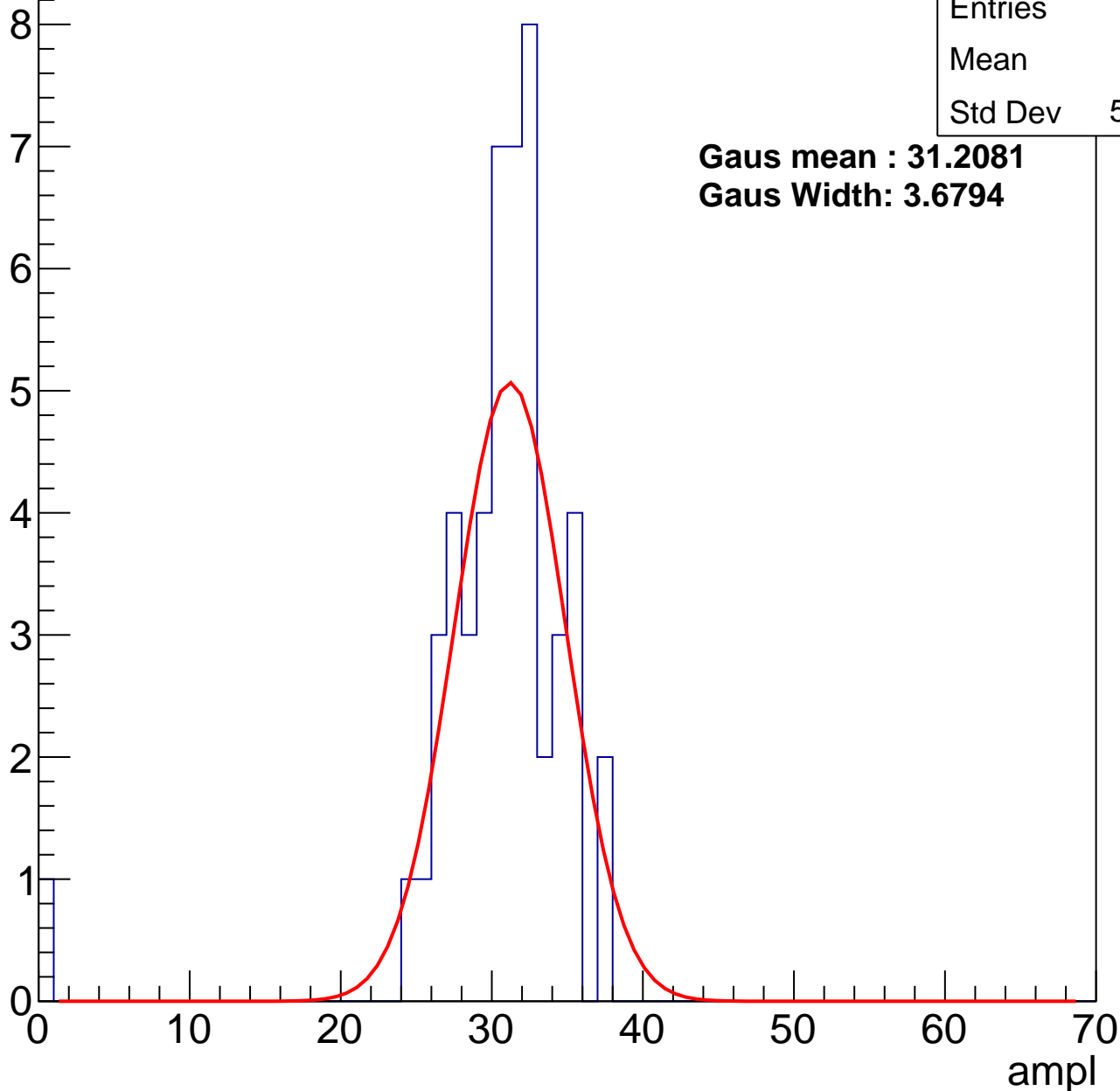
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	30
Std Dev	5.223

Gaus mean : 31.2081

Gaus Width: 3.6794



B0L001S, U14-ch71, adc1

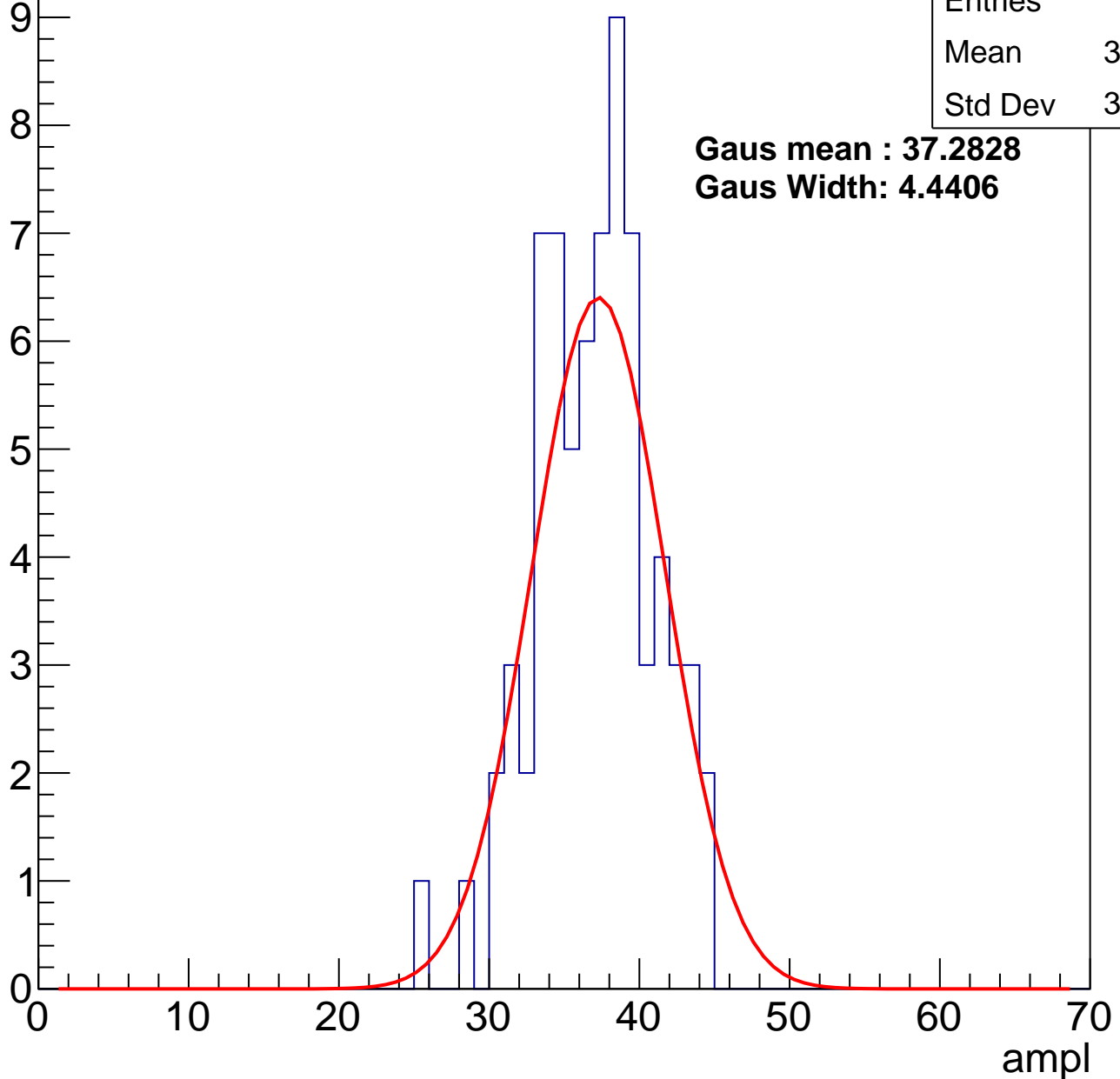
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.54
Std Dev	3.866

Gaus mean : 37.2828

Gaus Width: 4.4406



B0L001S, U14-ch71, adc2

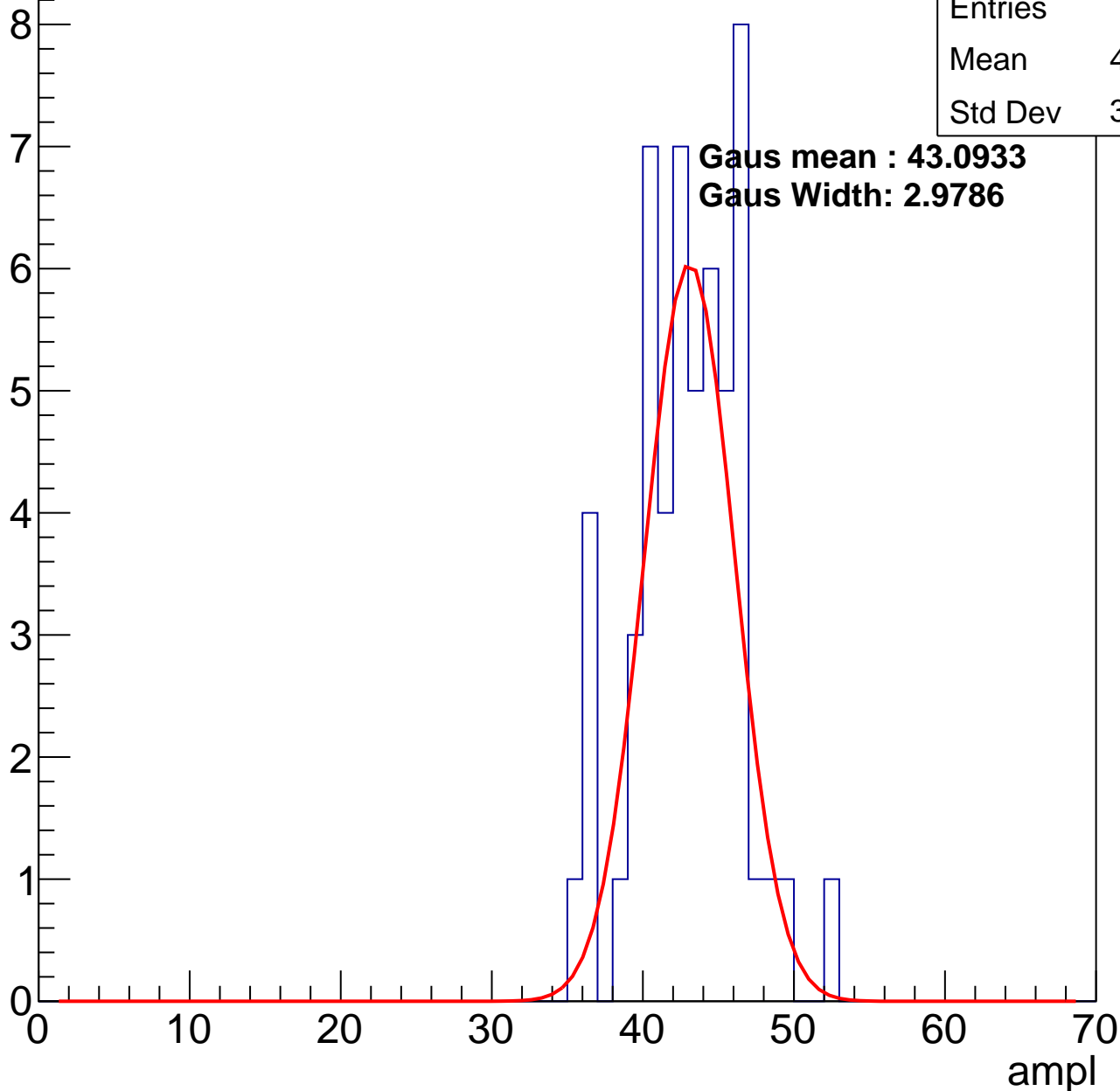
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	42.55
Std Dev	3.474

Gaus mean : 43.0933

Gaus Width: 2.9786

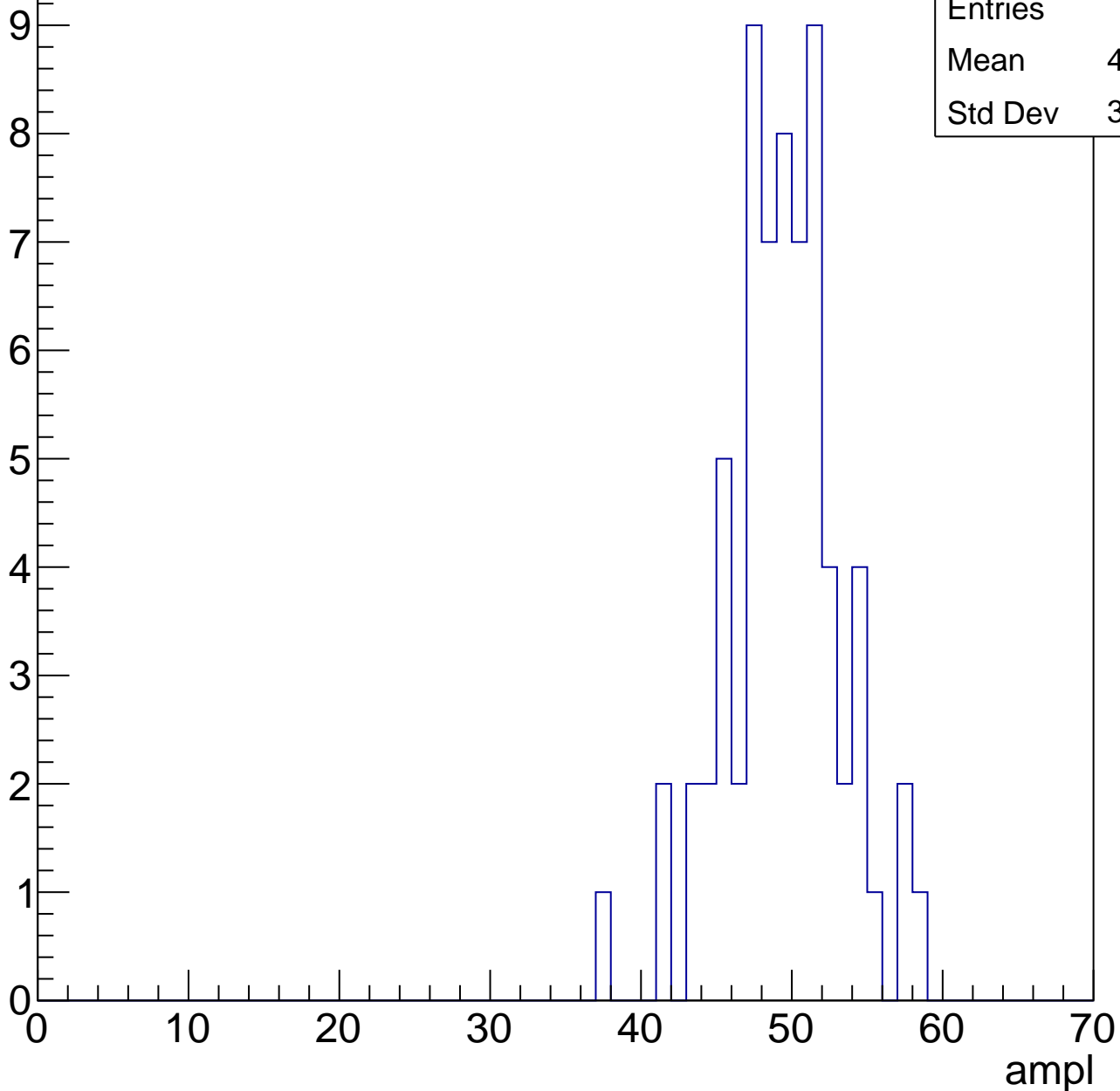


B0L001S, U14-ch71, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

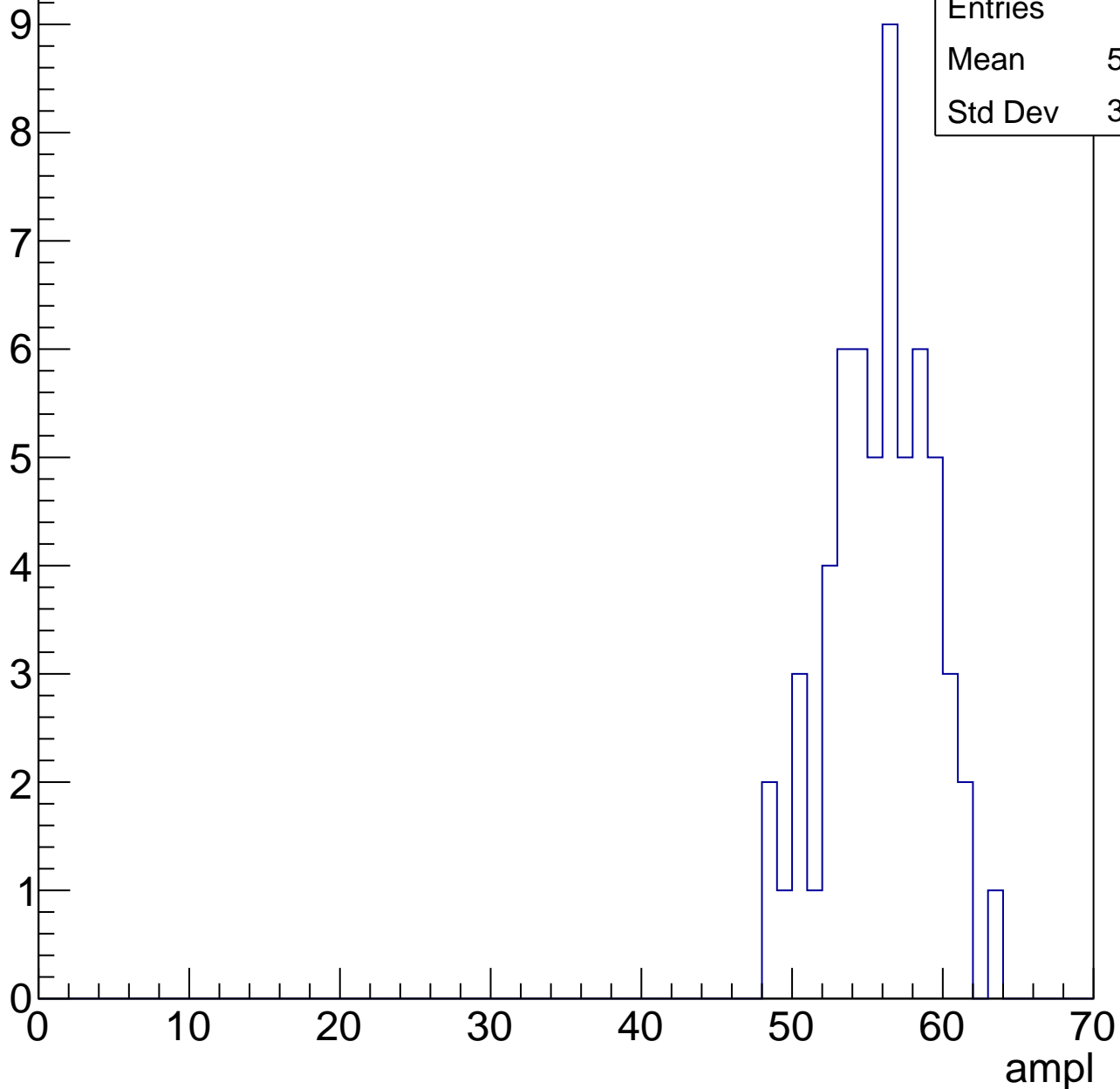
Entries	68
Mean	48.93
Std Dev	3.817



B0L001S, U14-ch71, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

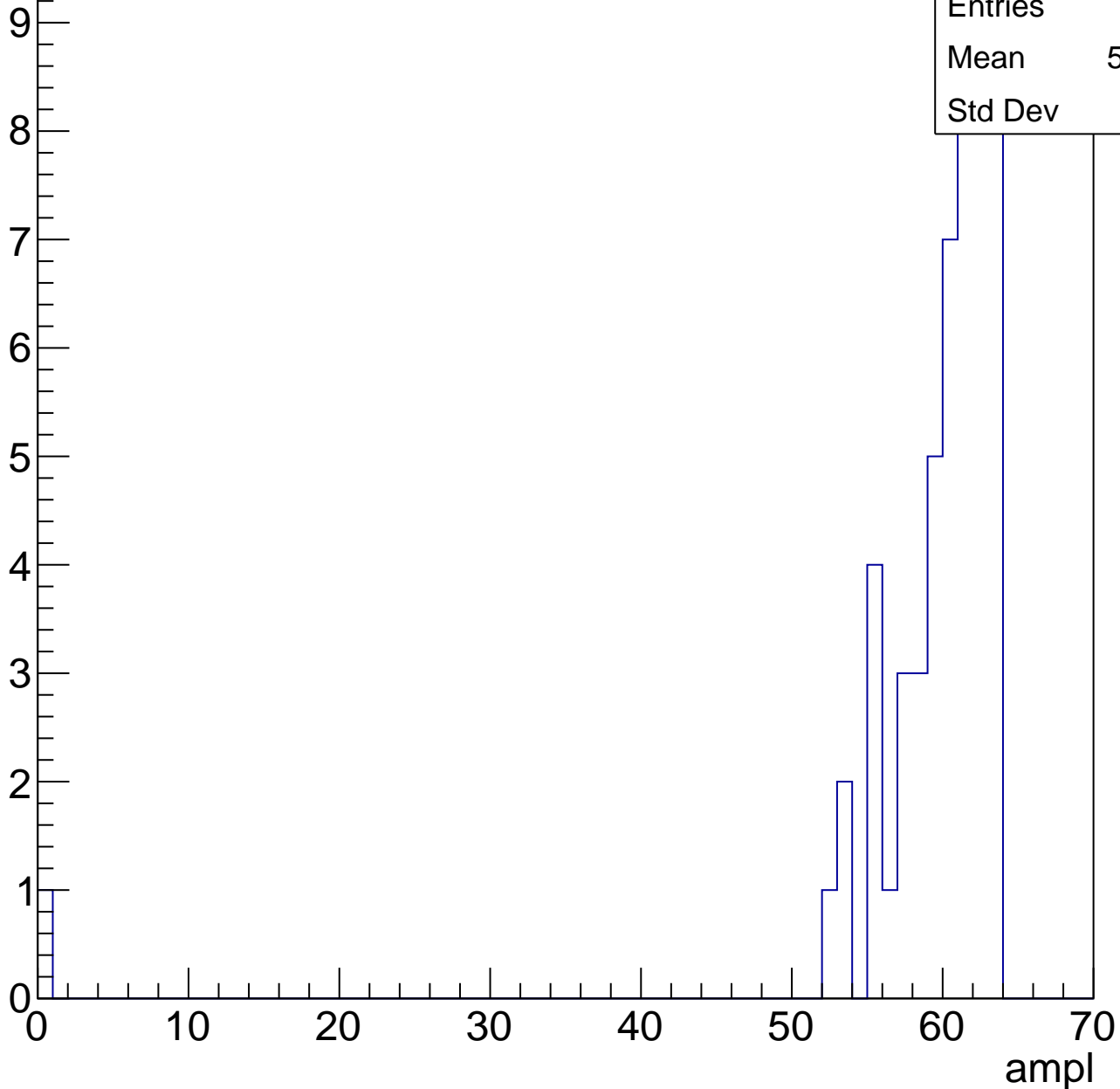


B0L001S, U14-ch71, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	58.54
Std Dev	8.69



B0L001S, U14-ch71, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch71, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch72, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	75
Mean	29.15
Std Dev	3.58

Gaus mean : 29.8618

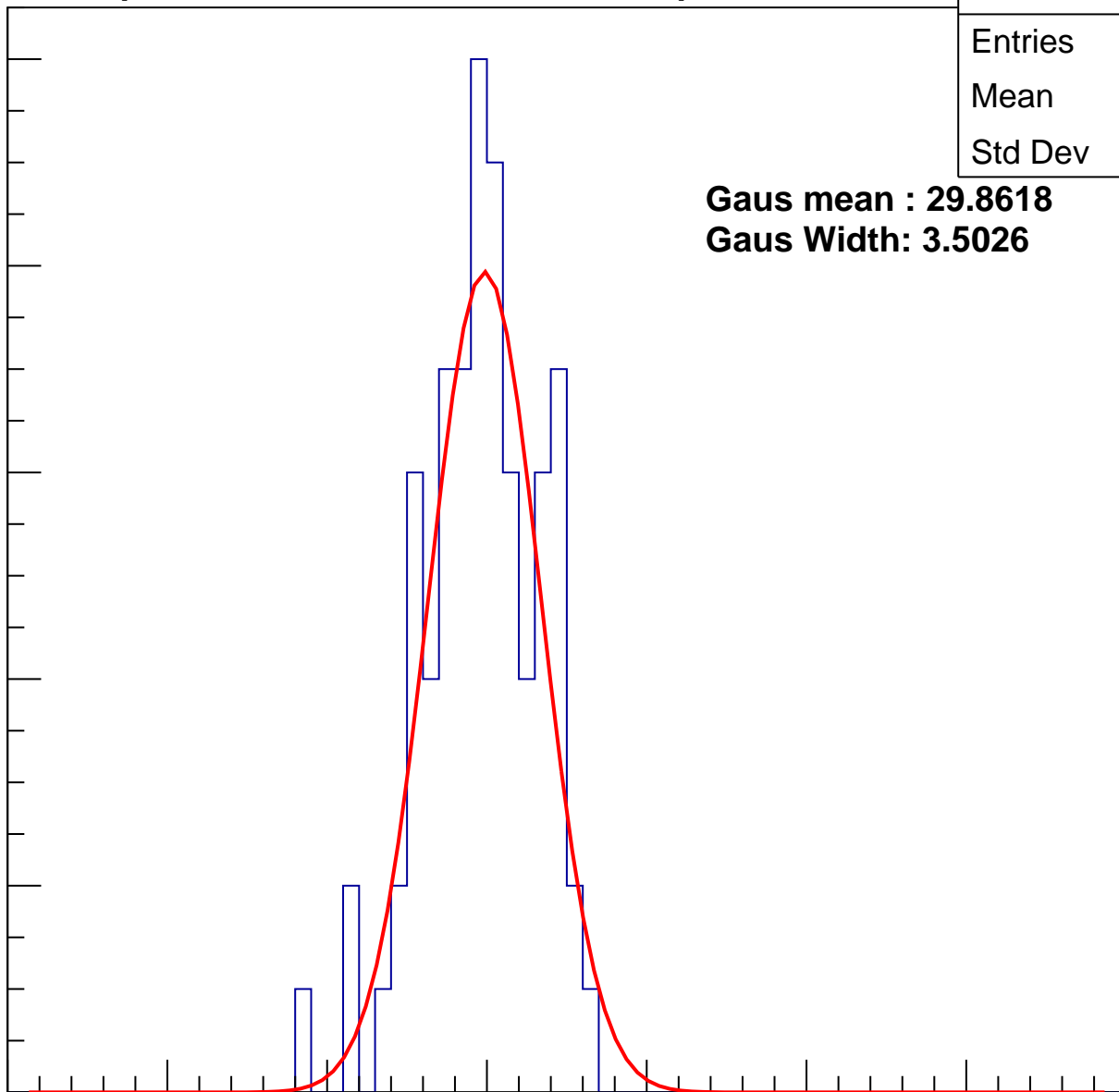
Gaus Width: 3.5026

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch72, adc1

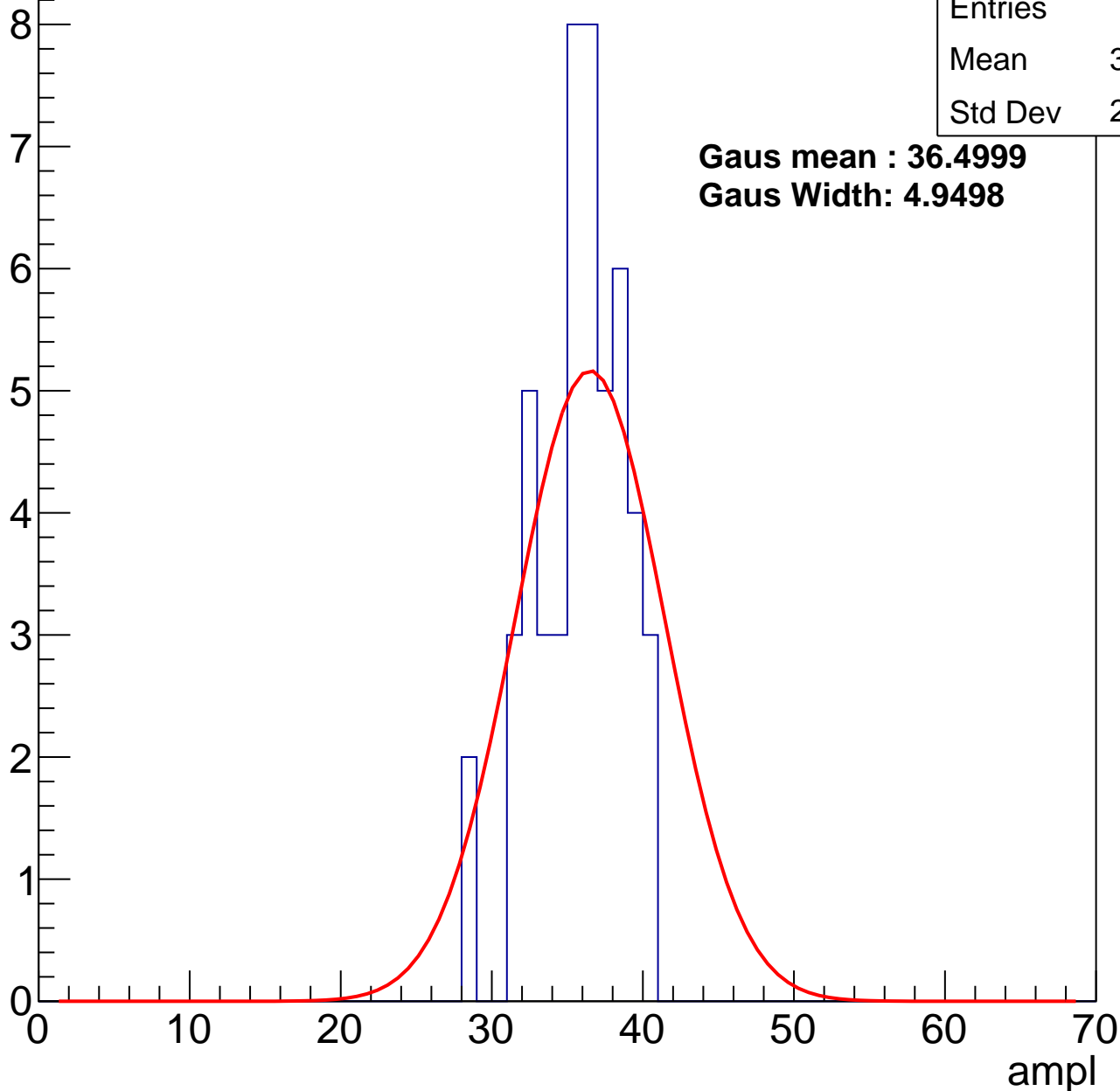
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	35.34
Std Dev	2.902

Gaus mean : 36.4999

Gaus Width: 4.9498



B0L001S, U14-ch72, adc2

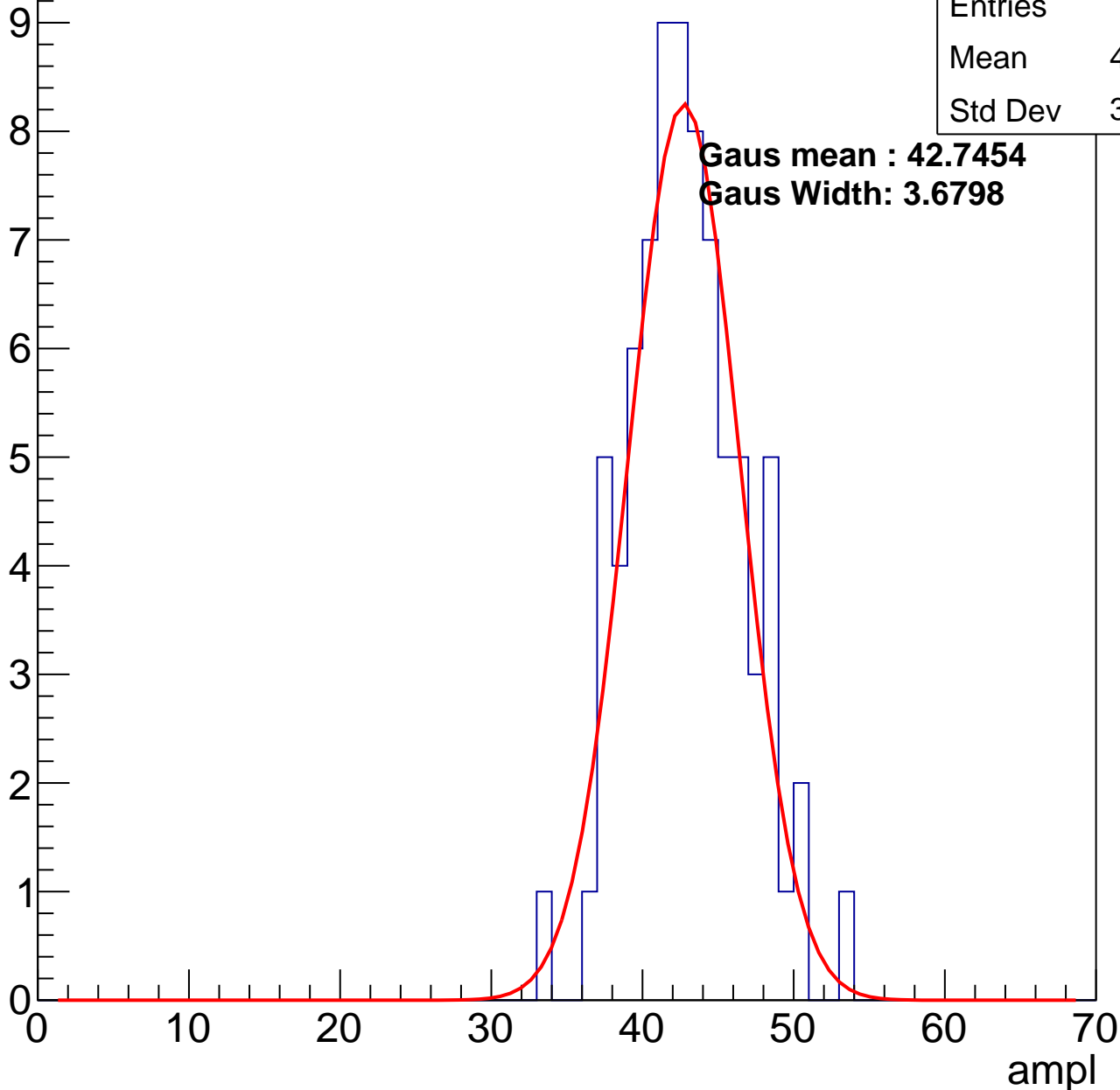
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	42.49
Std Dev	3.717

Gaus mean : 42.7454

Gaus Width: 3.6798

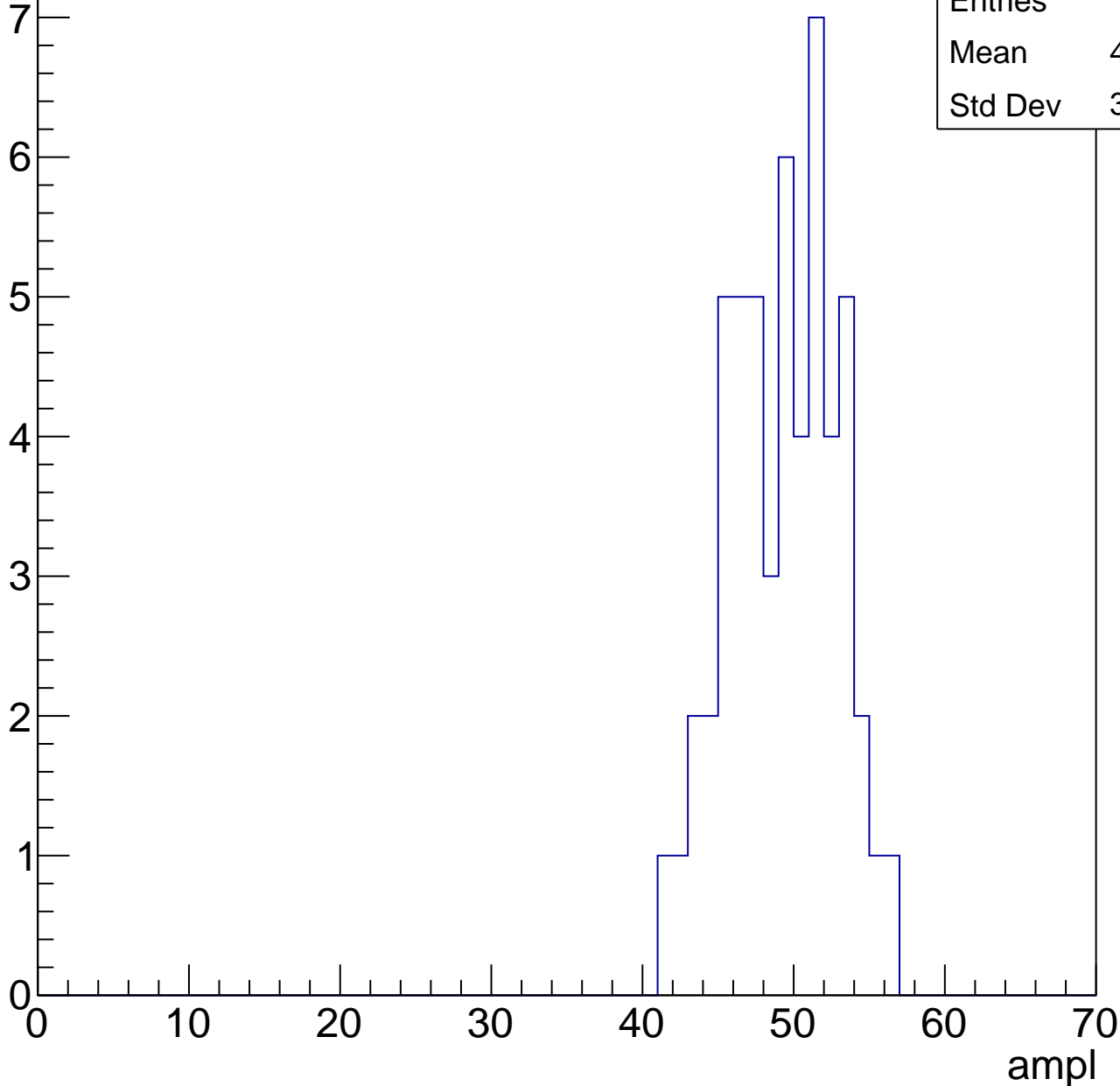


B0L001S, U14-ch72, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	48.78
Std Dev	3.505

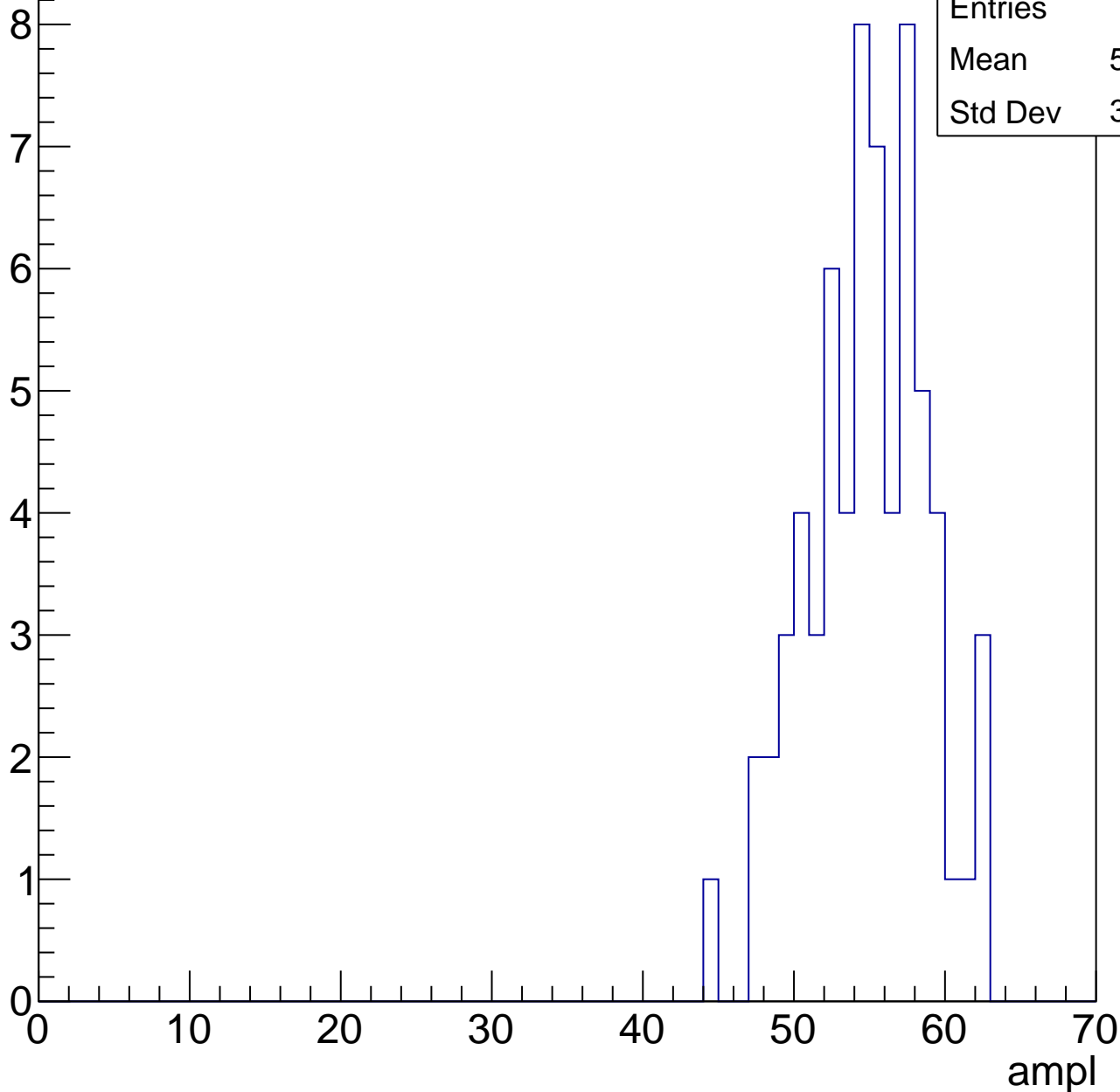


B0L001S, U14-ch72, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	54.36
Std Dev	3.907

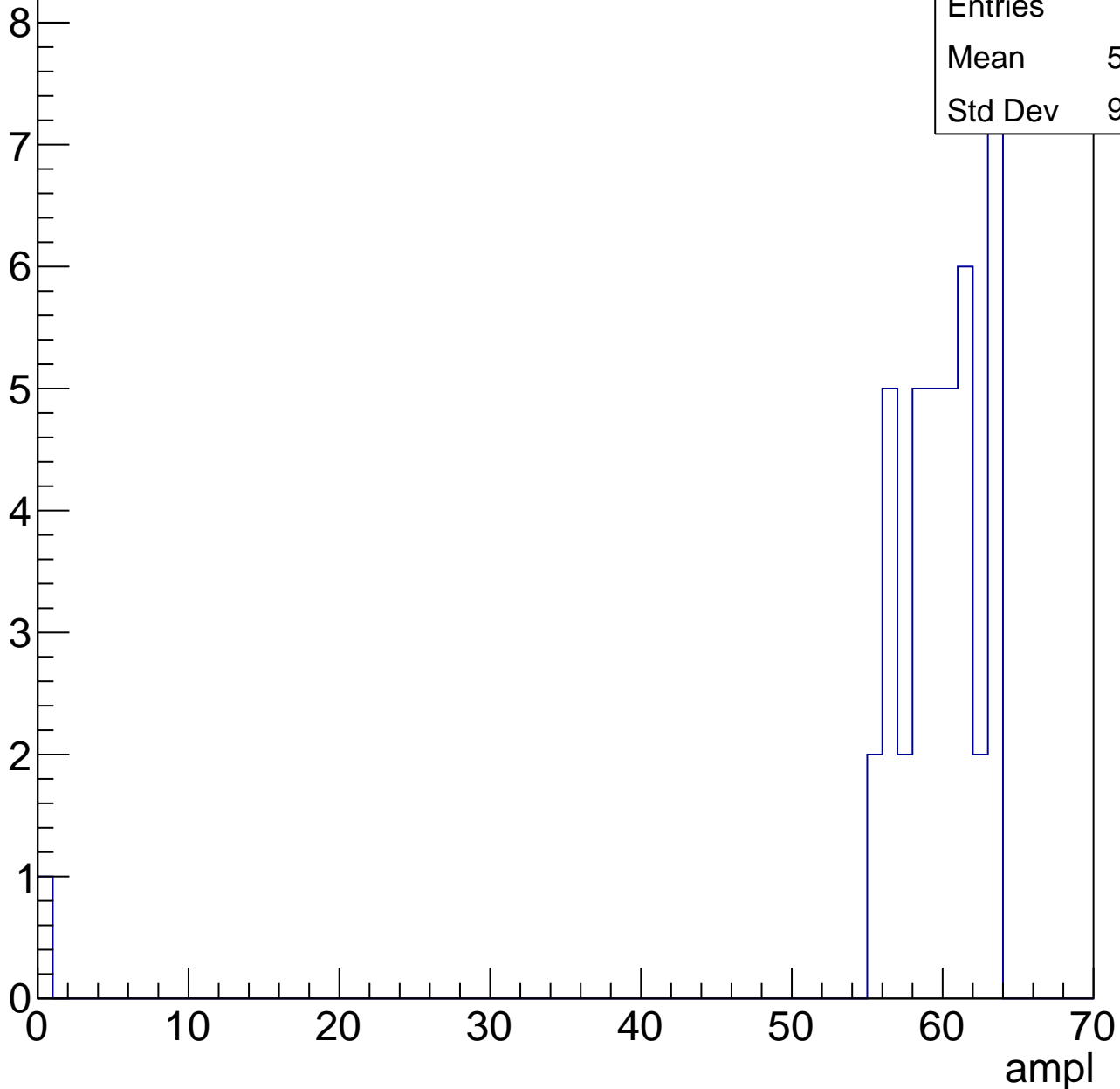


B0L001S, U14-ch72, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.12
Std Dev	9.518

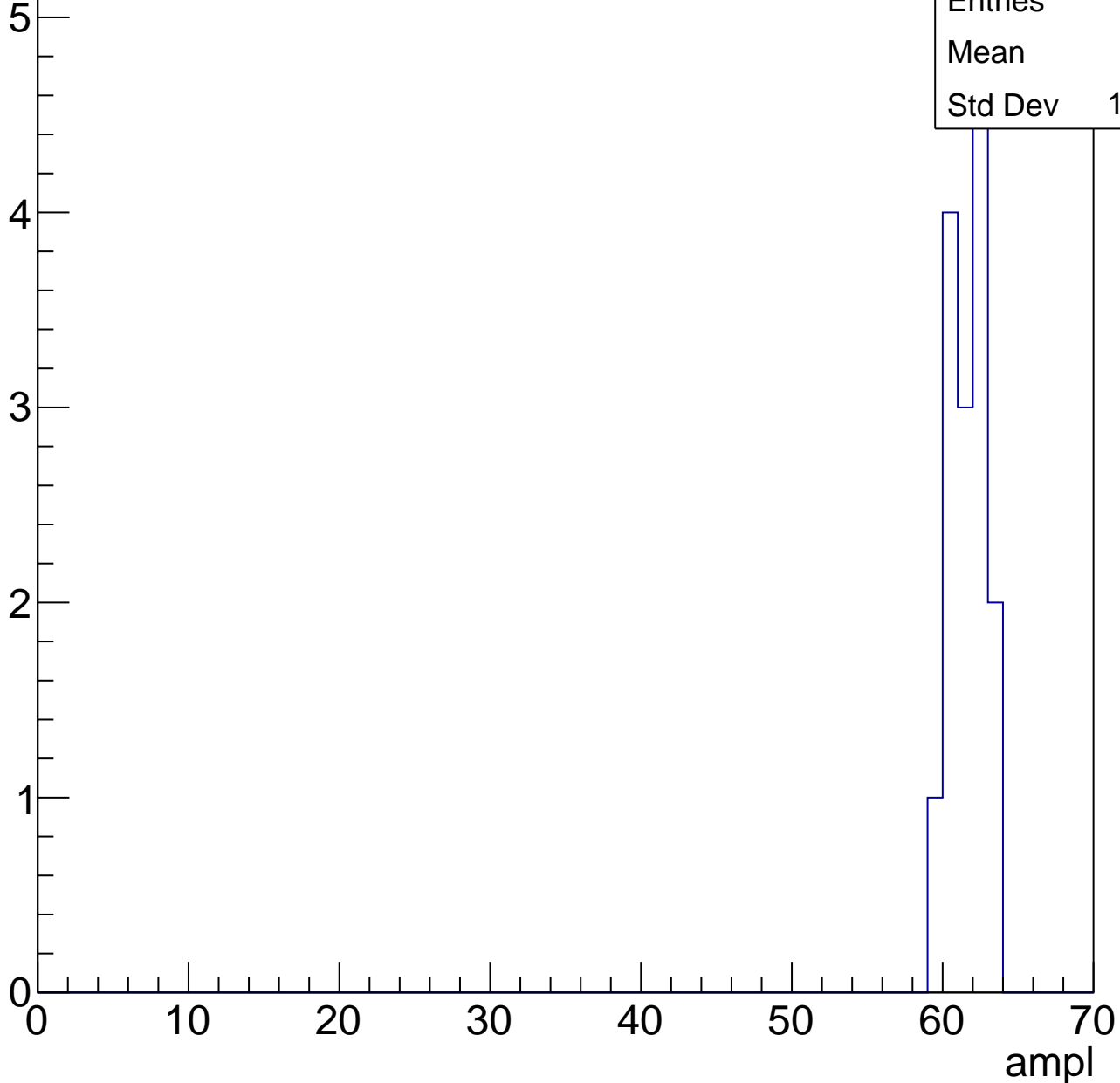


B0L001S, U14-ch72, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	15
Mean	61.2
Std Dev	1.166



B0L001S, U14-ch72, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch73, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	66
Mean	30.53
Std Dev	3.225

Gaus mean : 31.0293

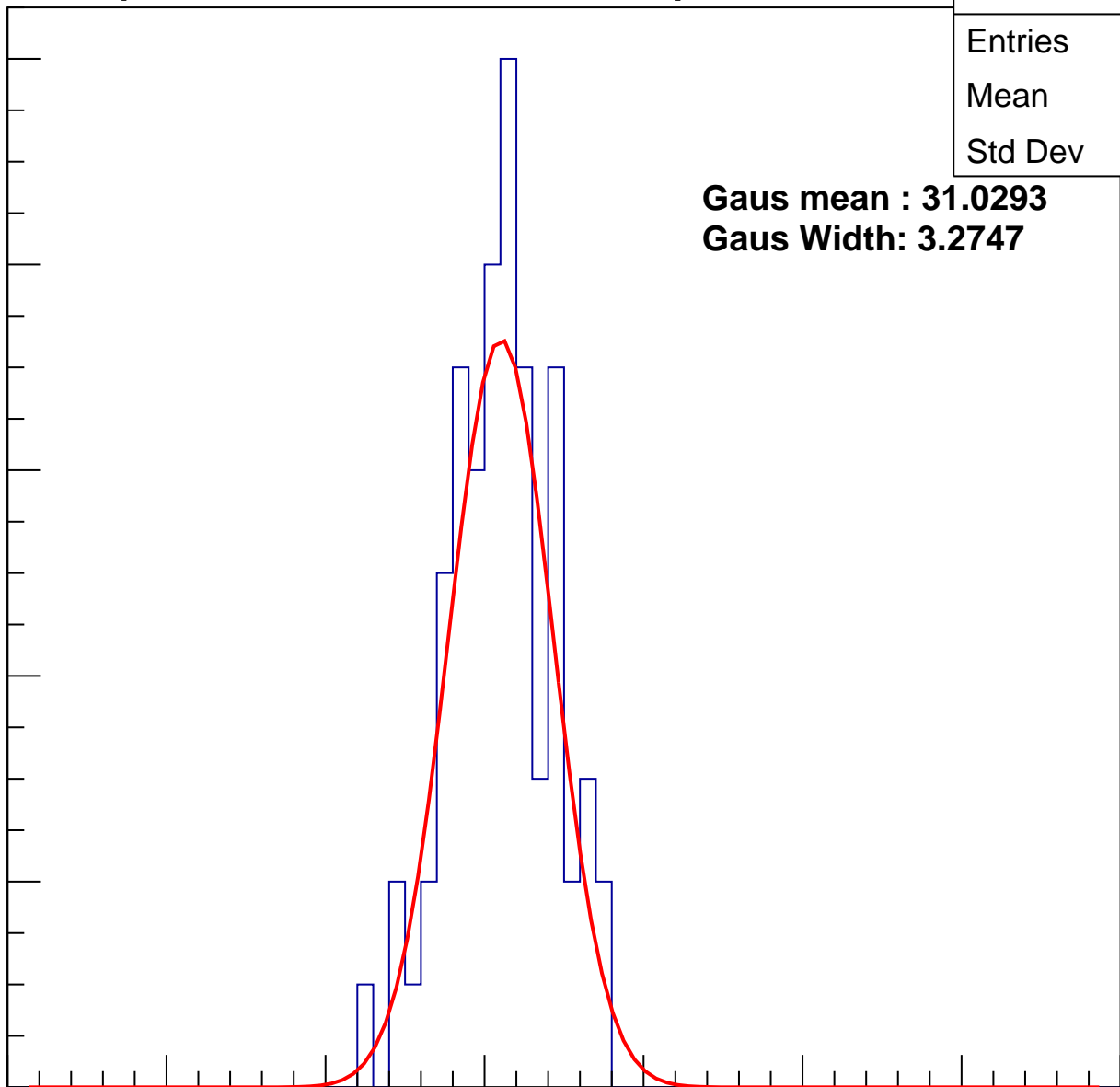
Gaus Width: 3.2747

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch73, adc1

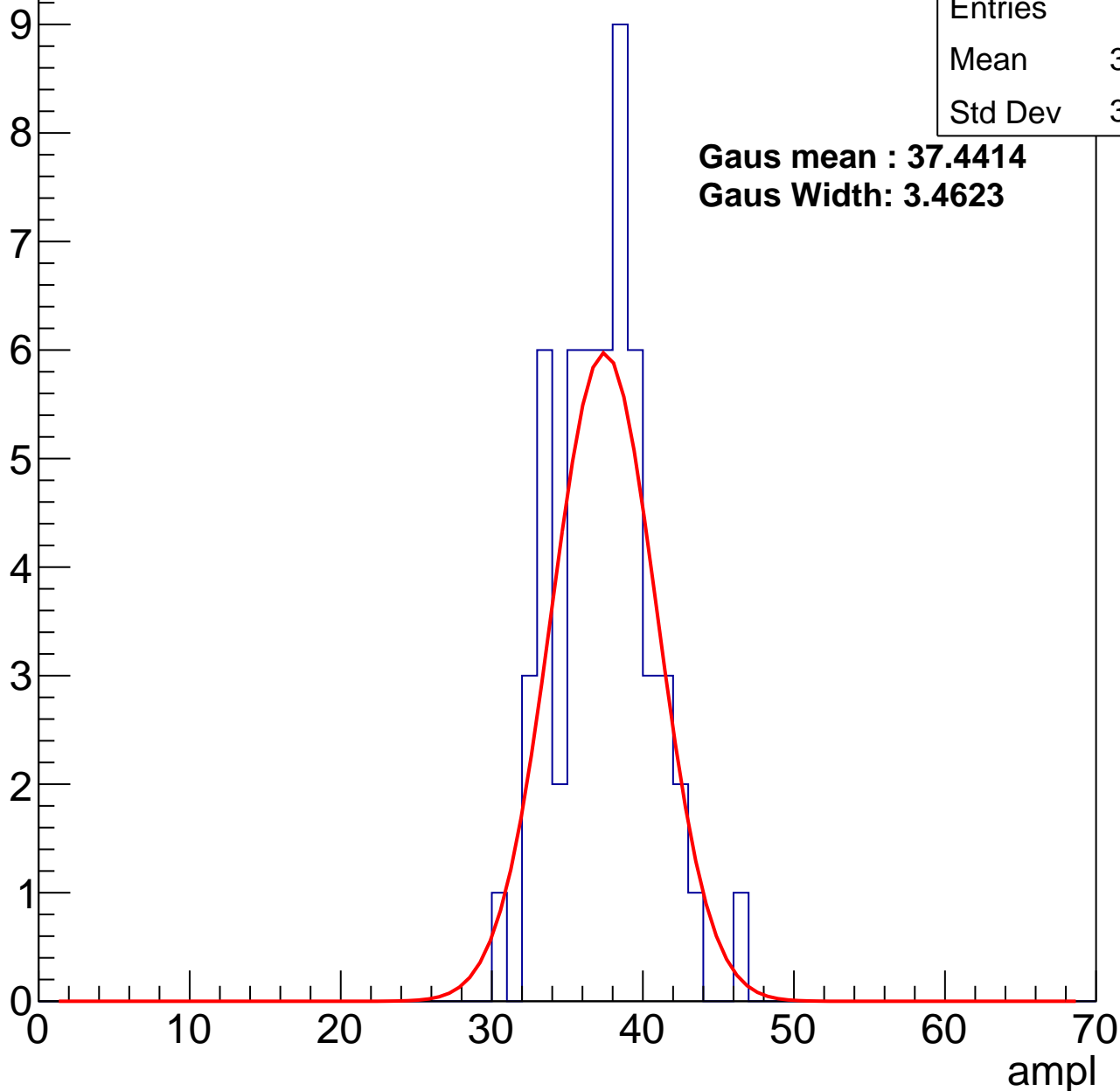
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	36.95
Std Dev	3.142

Gaus mean : 37.4414

Gaus Width: 3.4623



B0L001S, U14-ch73, adc2

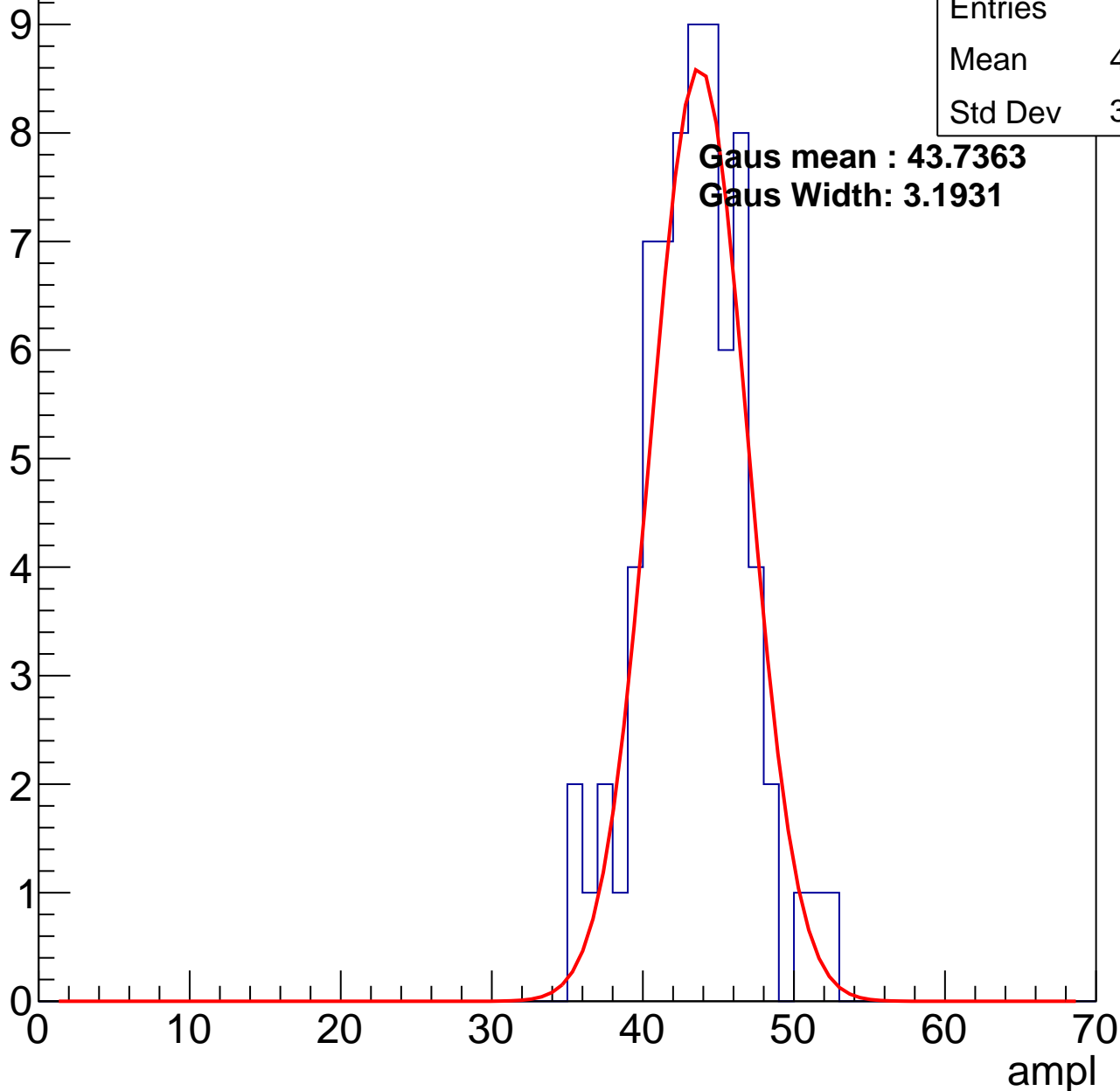
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	42.95
Std Dev	3.412

Gaus mean : 43.7363

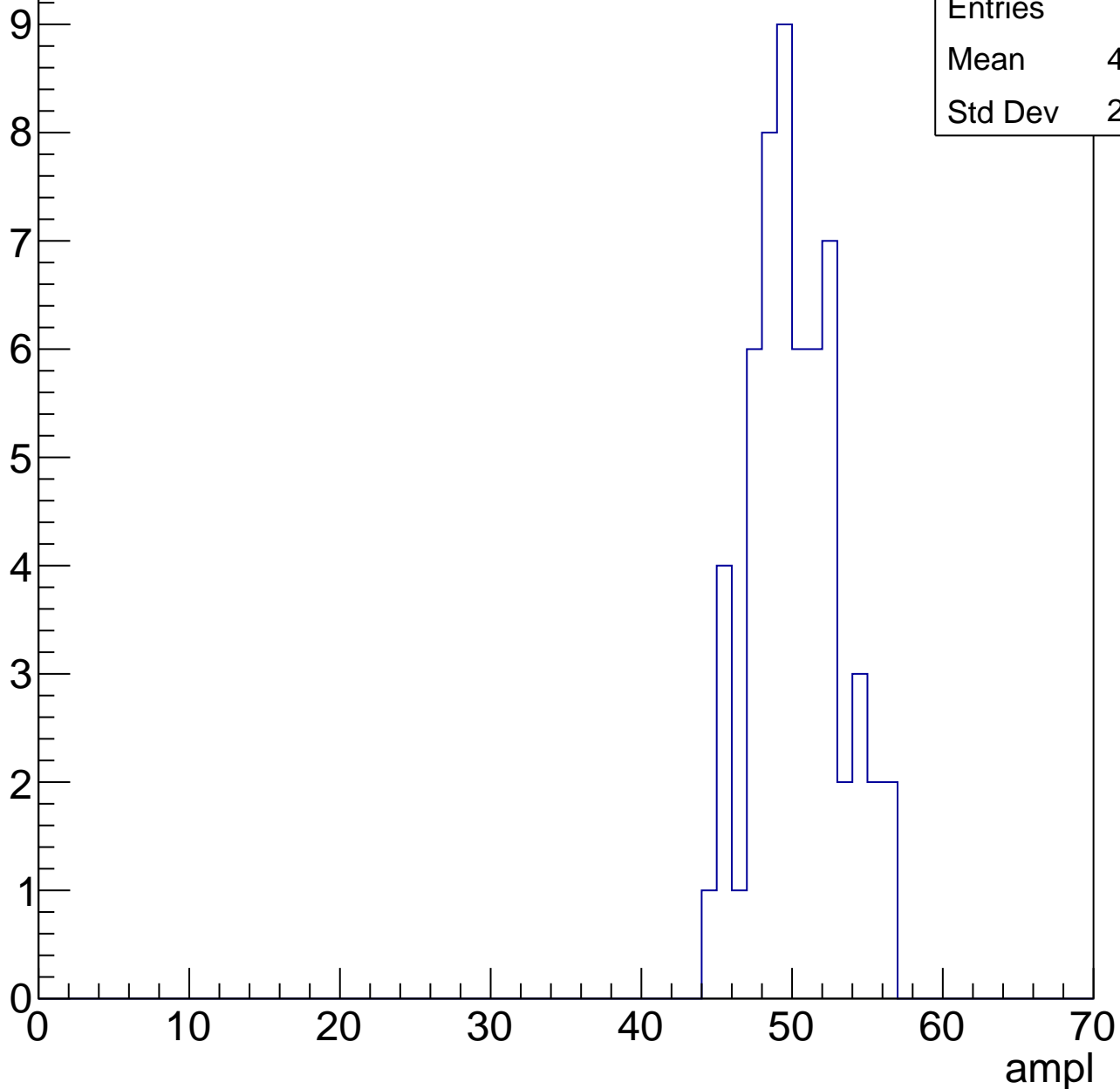
Gaus Width: 3.1931



B0L001S, U14-ch73, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



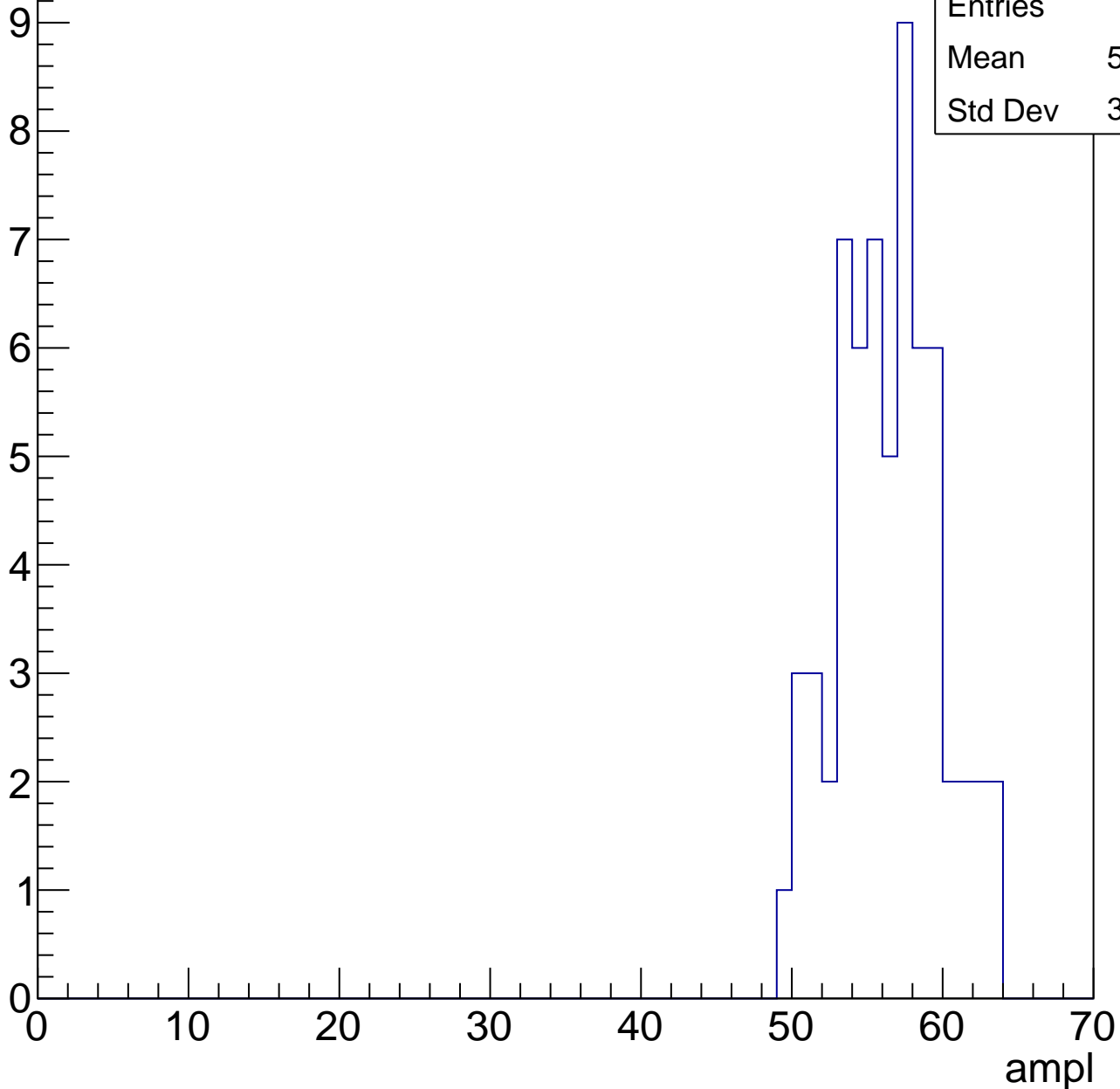
Entries	57
Mean	49.77
Std Dev	2.859

B0L001S, U14-ch73, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	55.92
Std Dev	3.325



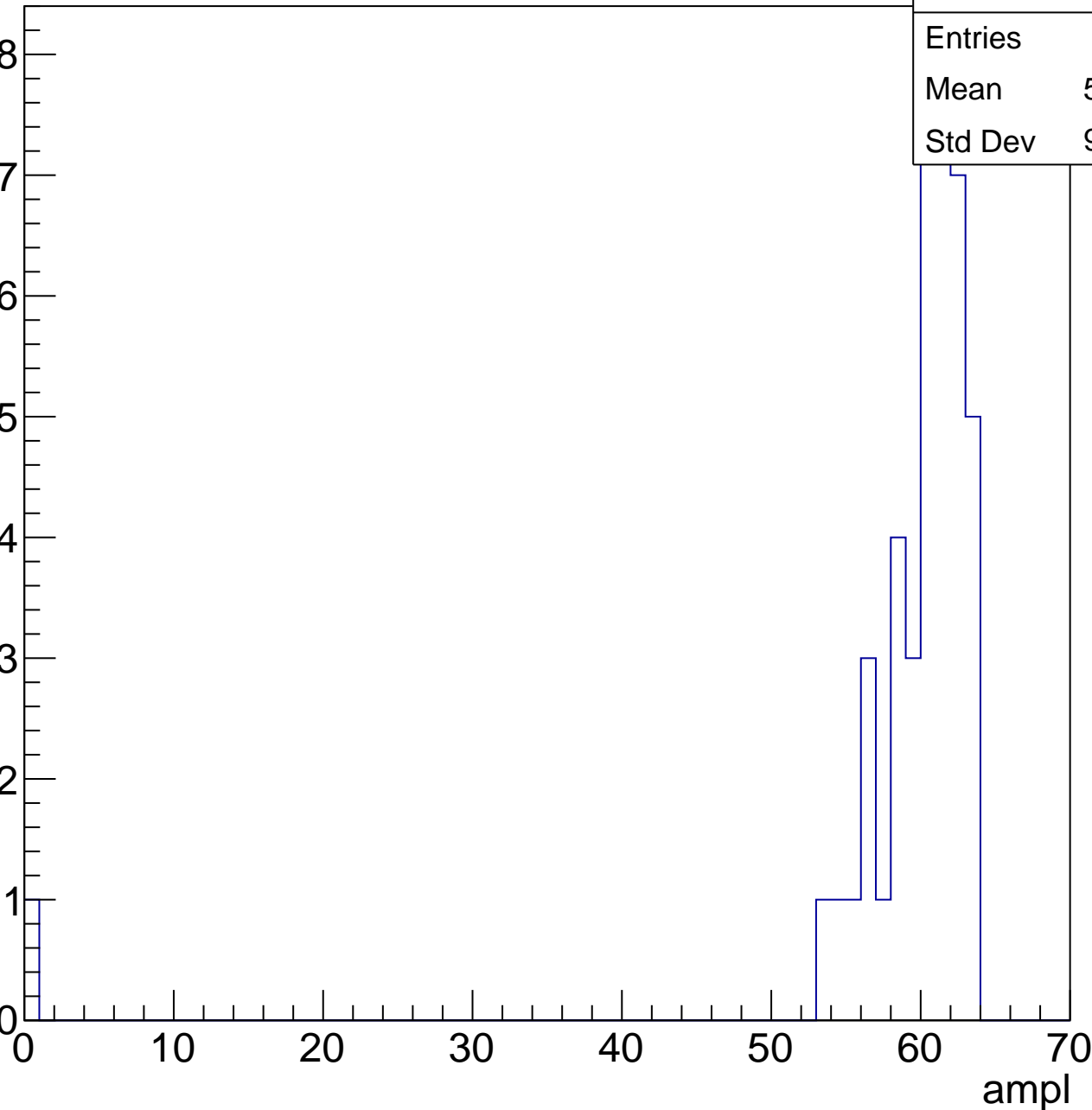
B0L001S, U14-ch73, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	58.44
Std Dev	9.354

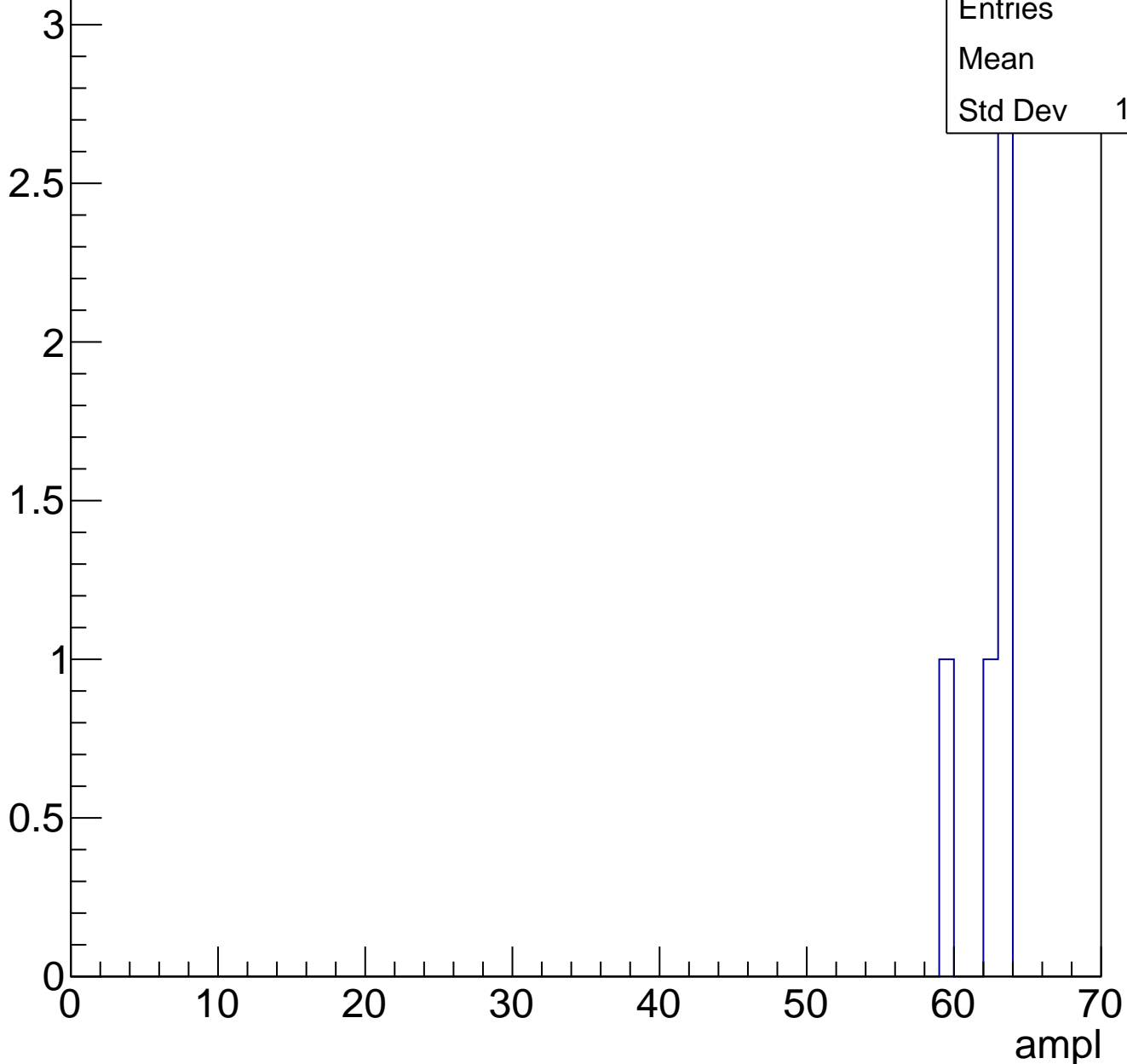
8
7
6
5
4
3
2
1
0



B0L001S, U14-ch73, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch73, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch74, adc0

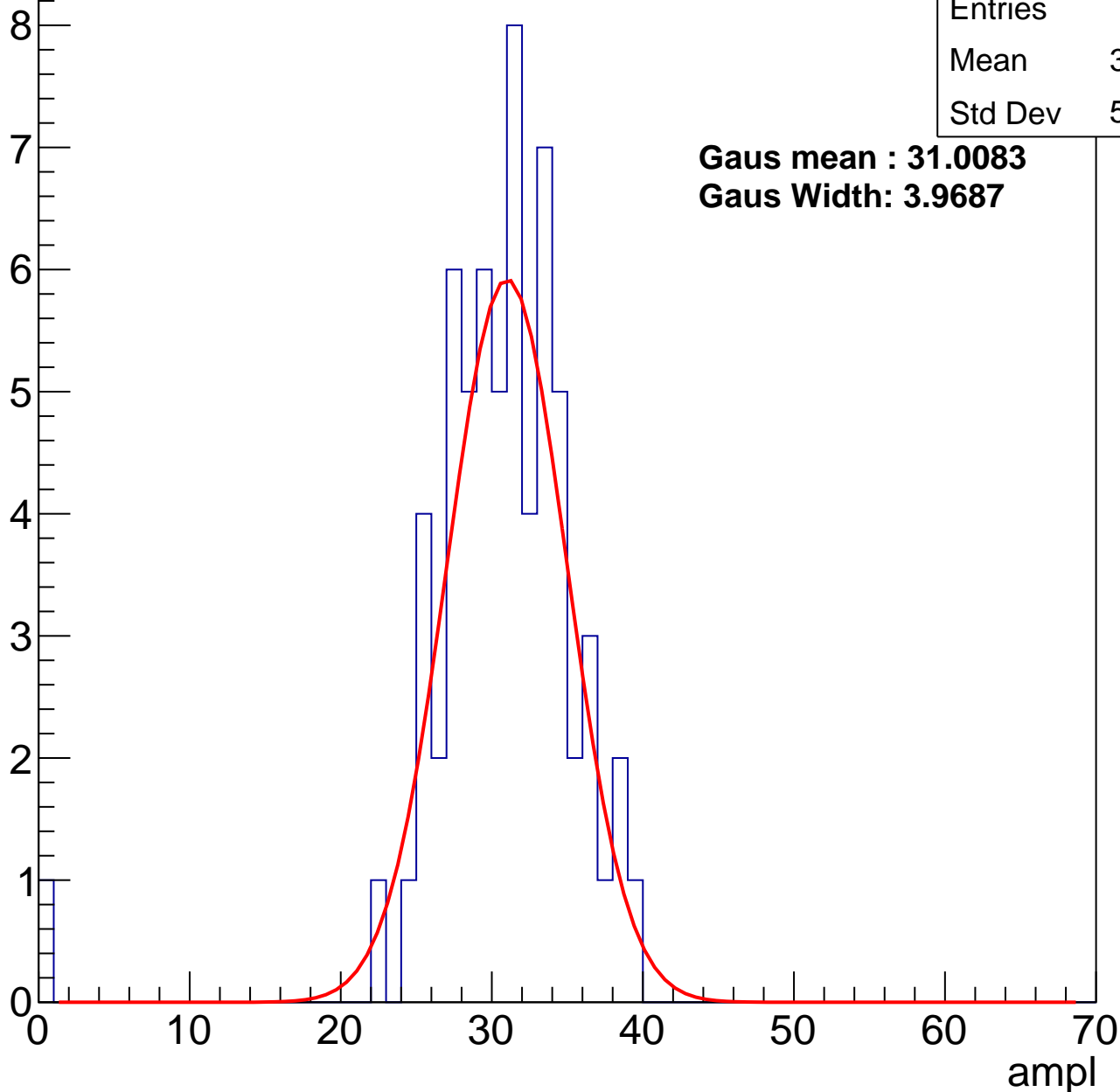
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.17
Std Dev	5.293

Gaus mean : 31.0083

Gaus Width: 3.9687



B0L001S, U14-ch74, adc1

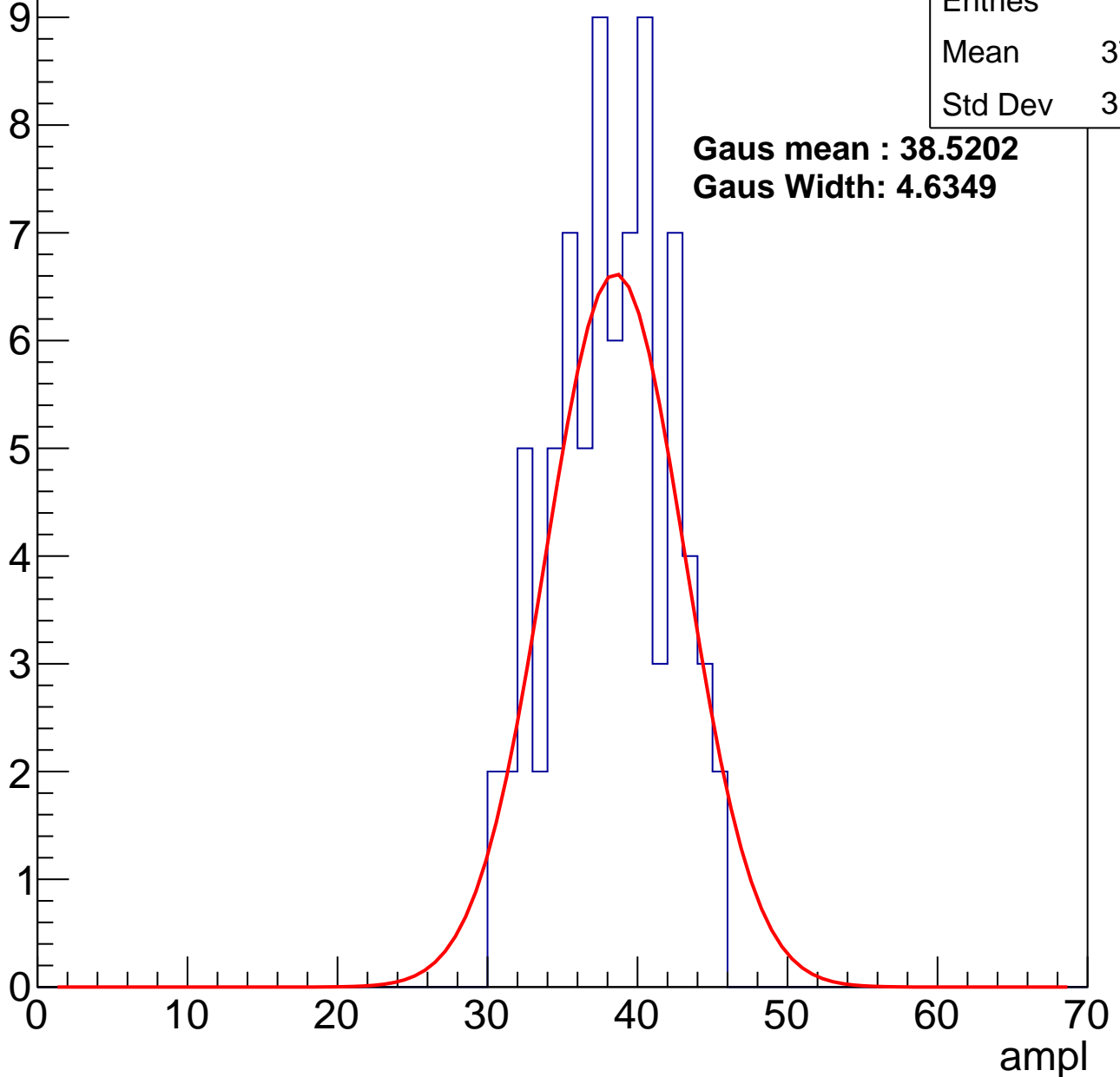
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.79
Std Dev	3.777

Gaus mean : 38.5202

Gaus Width: 4.6349



B0L001S, U14-ch74, adc2

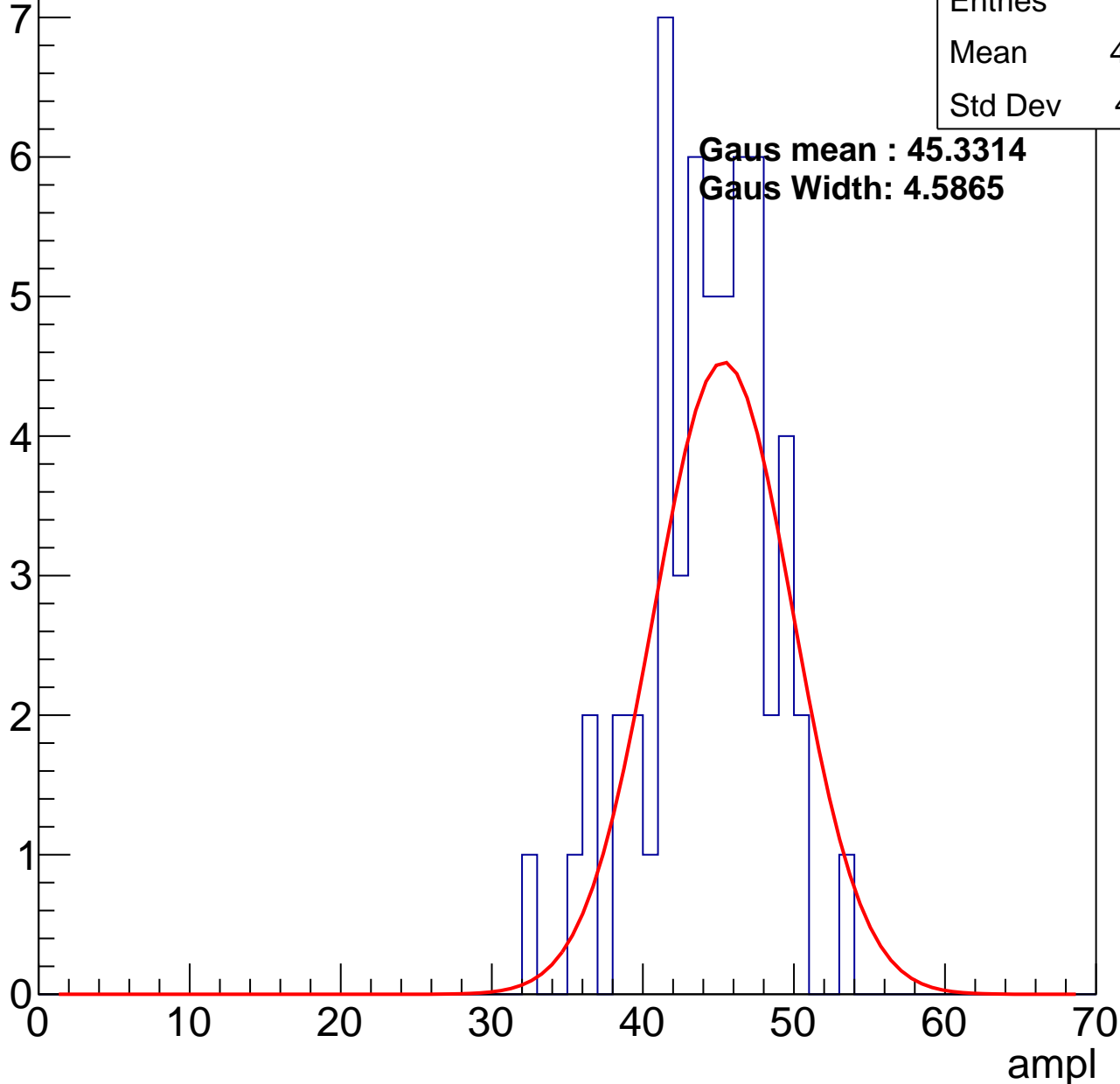
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	43.79
Std Dev	4.091

Gaus mean : 45.3314

Gaus Width: 4.5865

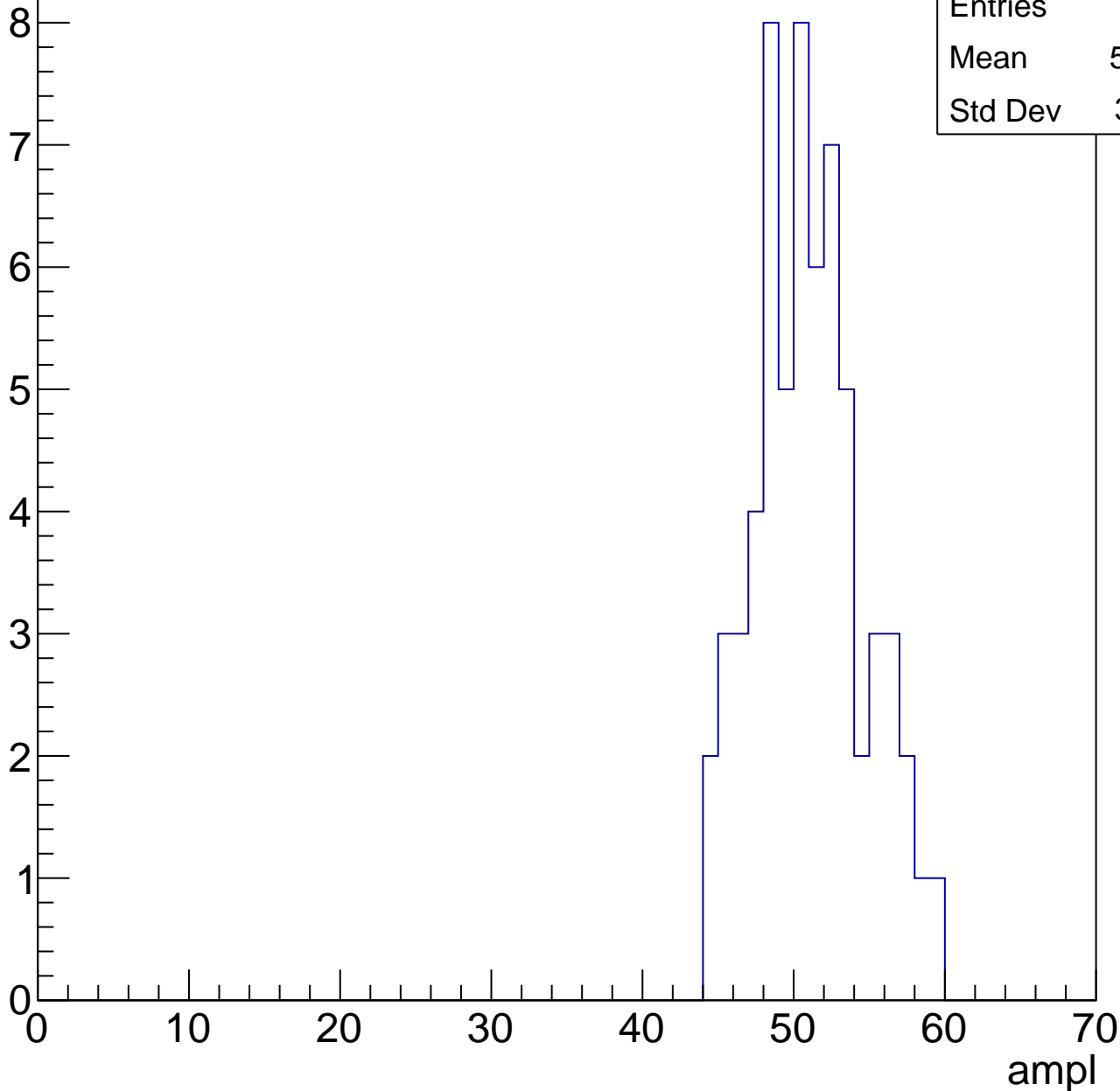


B0L001S, U14-ch74, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	50.56
Std Dev	3.531

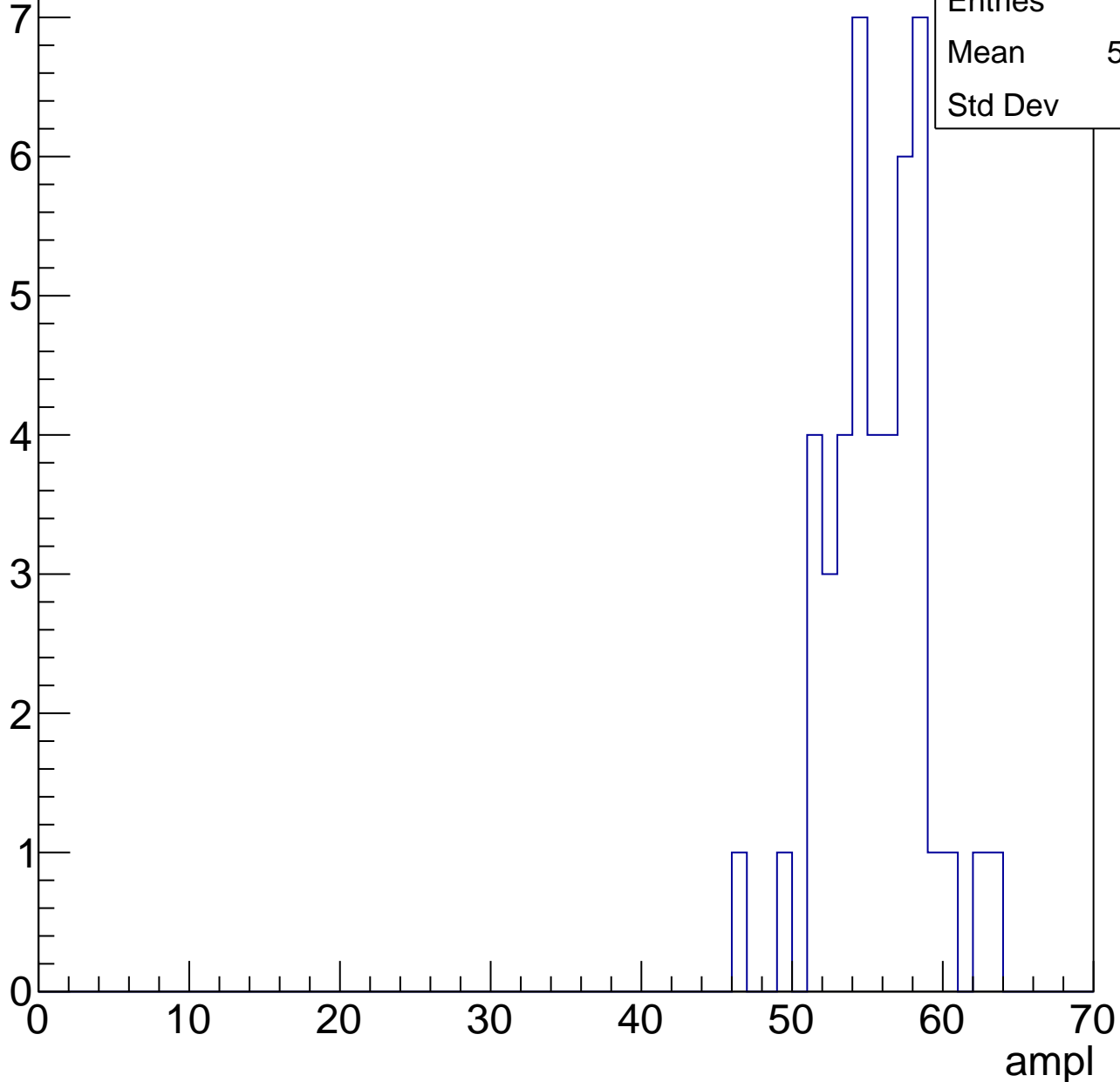


B0L001S, U14-ch74, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	55.13
Std Dev	3.25



B0L001S, U14-ch74, adc5

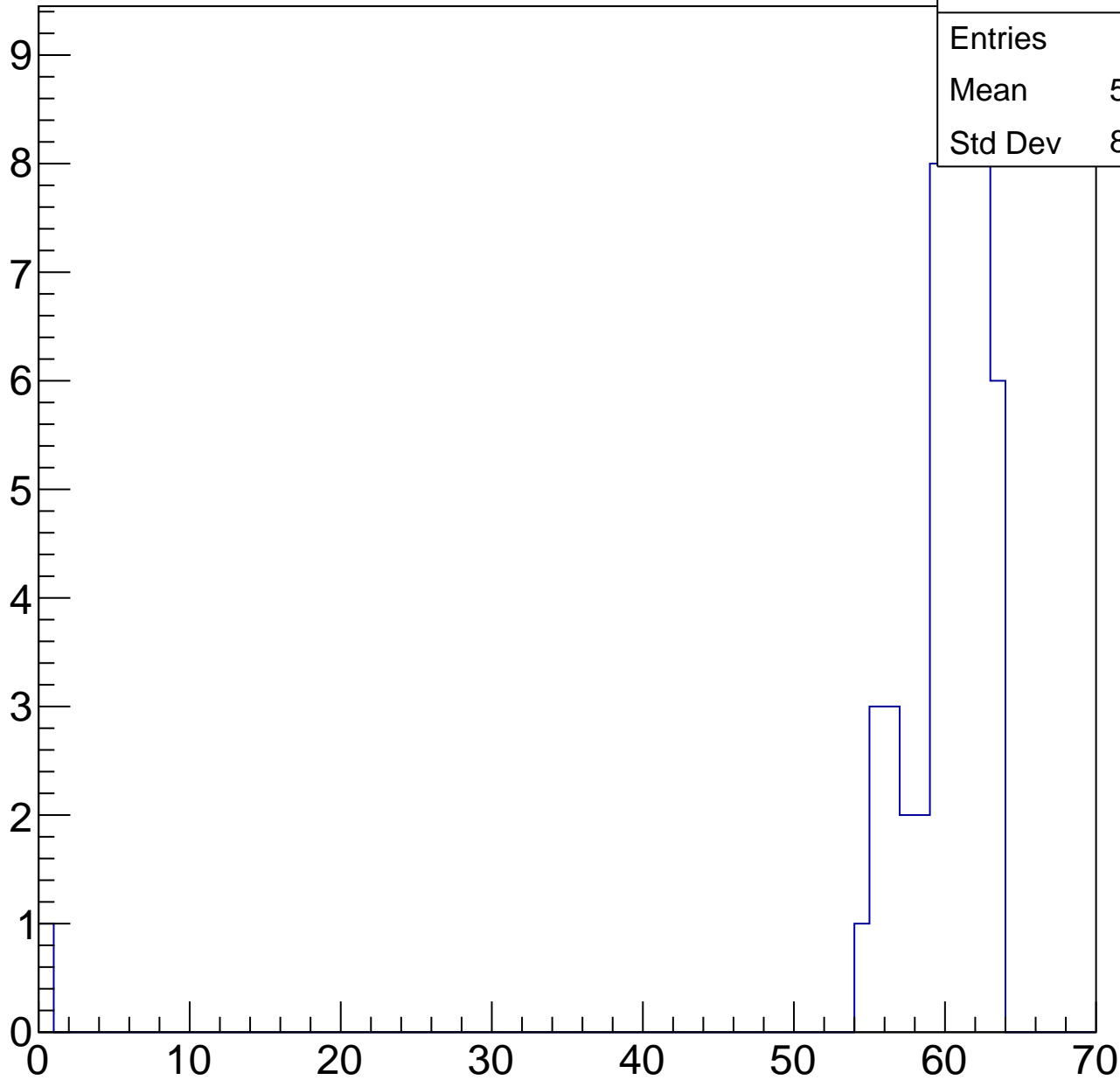
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.65
Std Dev	8.625

ampl



B0L001S, U14-ch74, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch74, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch75, adc0

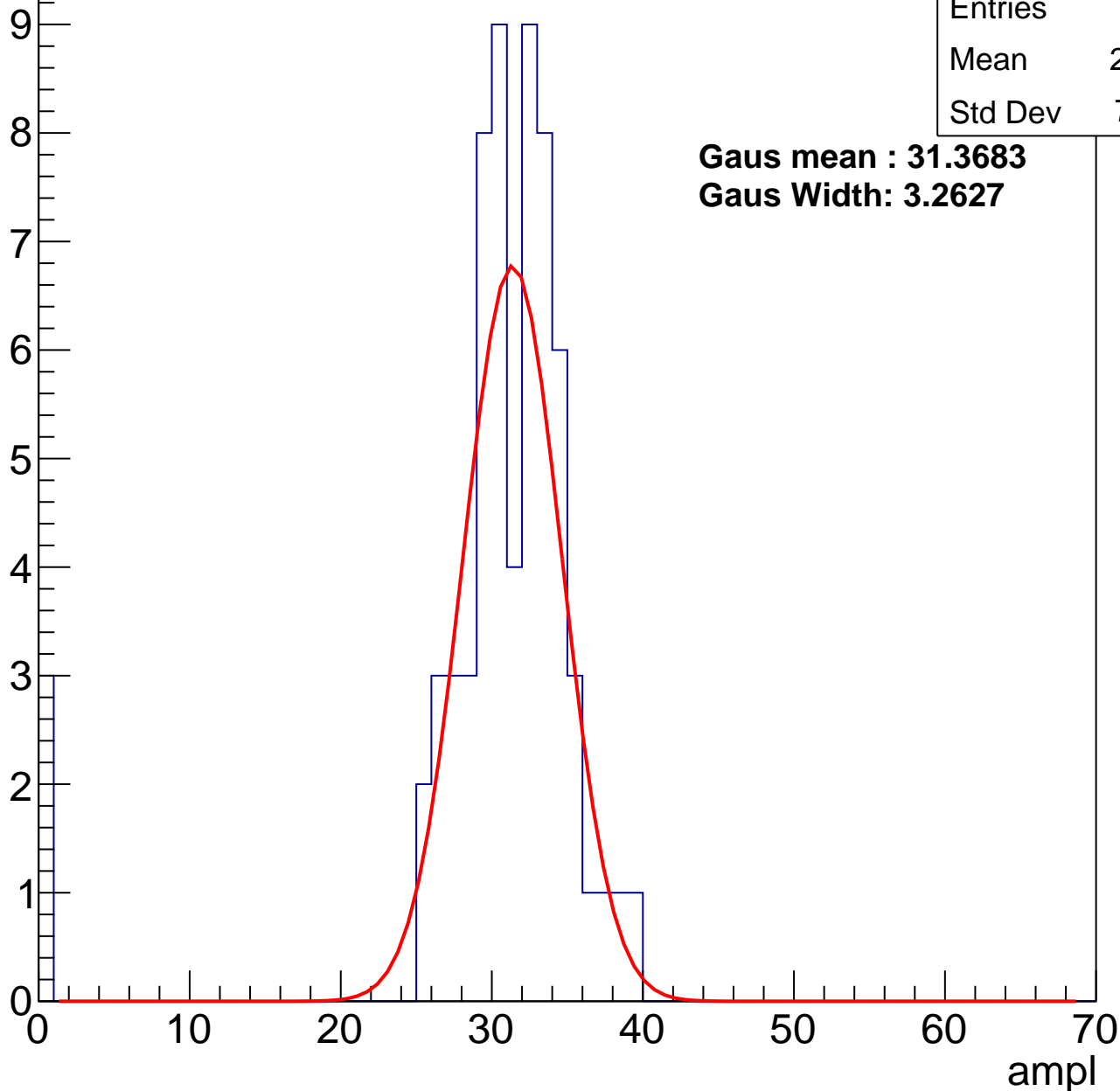
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	29.69
Std Dev	7.181

Gaus mean : 31.3683

Gaus Width: 3.2627



B0L001S, U14-ch75, adc1

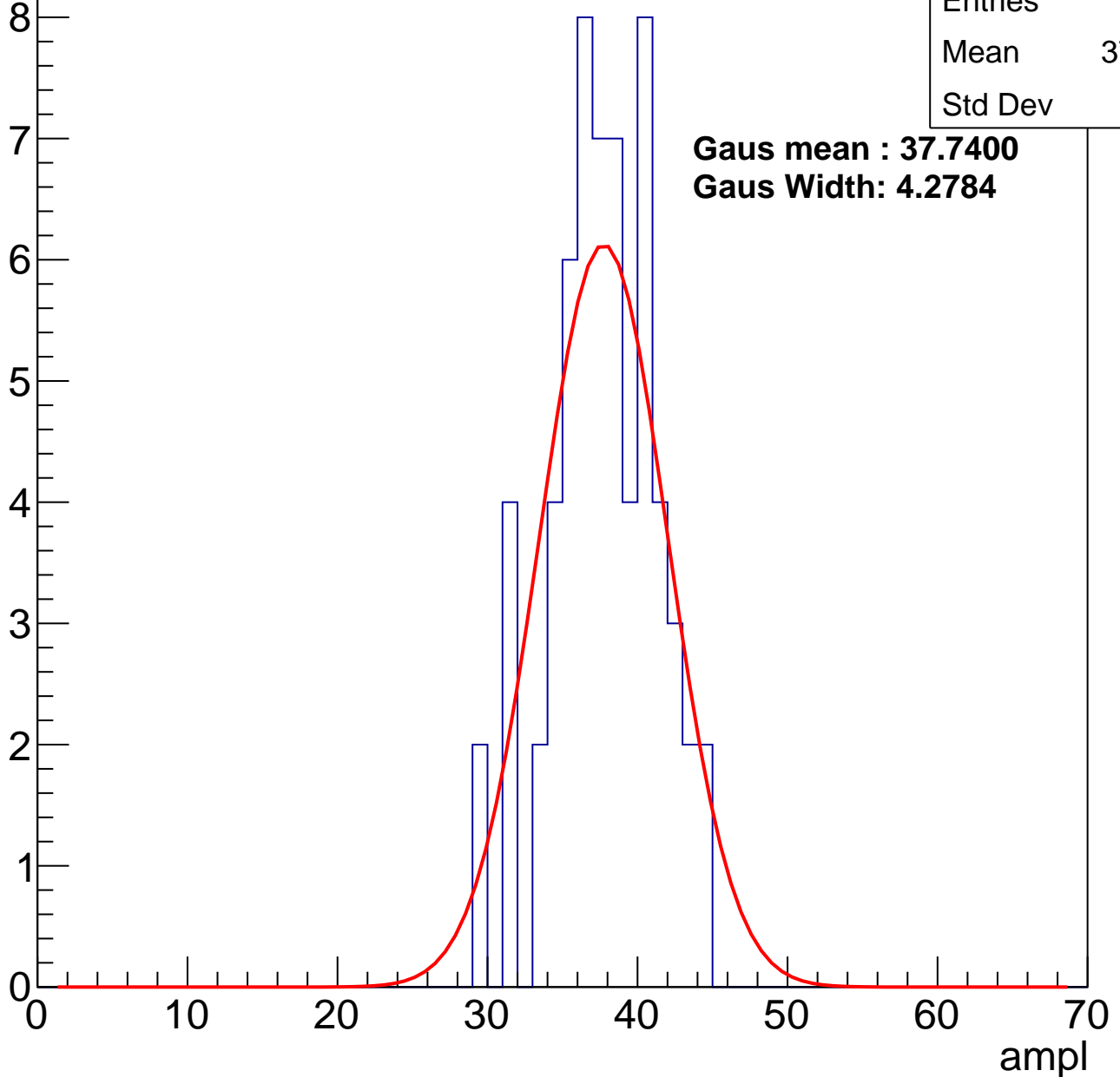
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.25
Std Dev	3.5

Gaus mean : 37.7400

Gaus Width: 4.2784



B0L001S, U14-ch75, adc2

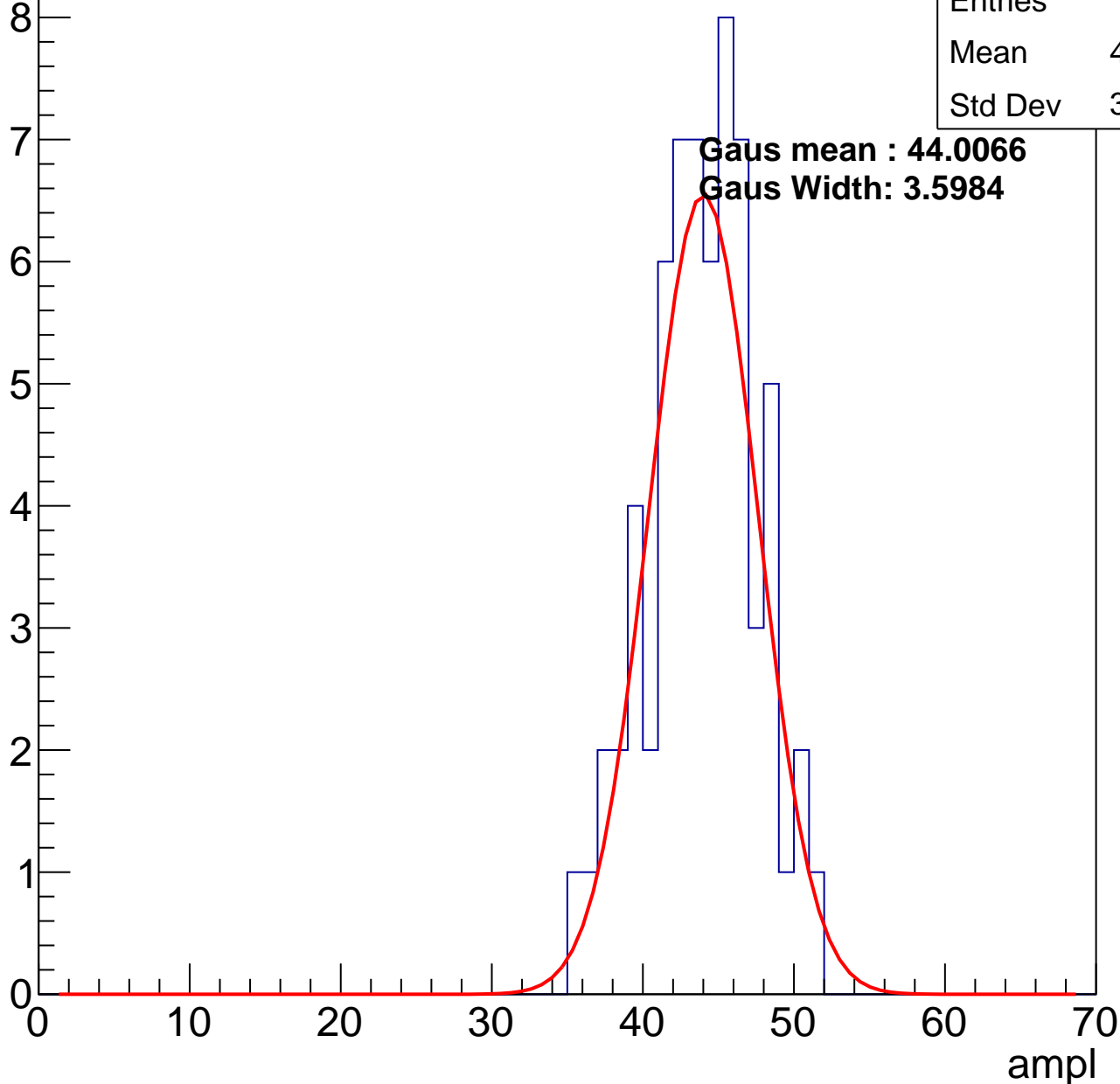
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	43.46
Std Dev	3.522

Gaus mean : 44.0066

Gaus Width: 3.5984

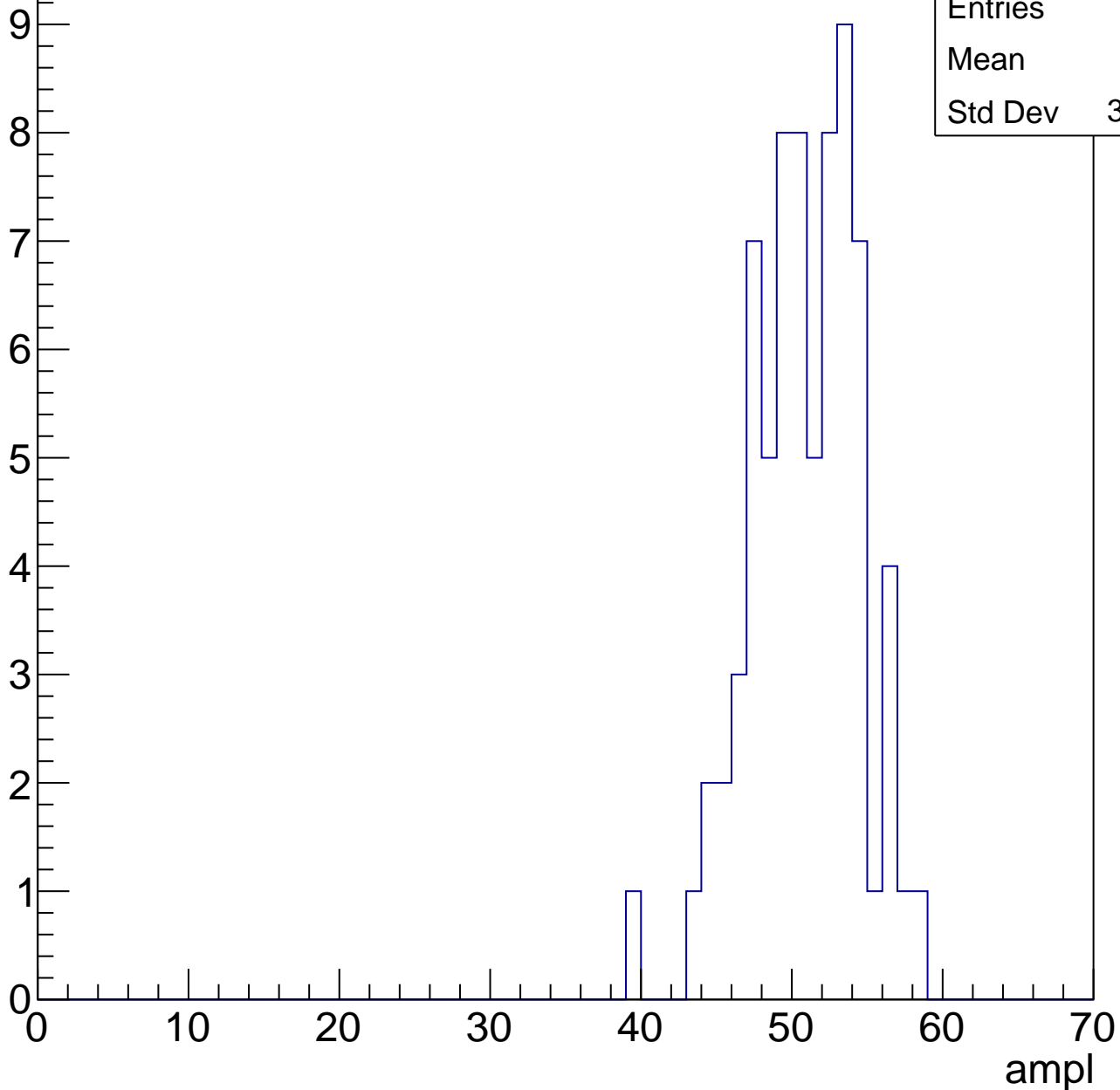


B0L001S, U14-ch75, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	50.4
Std Dev	3.572

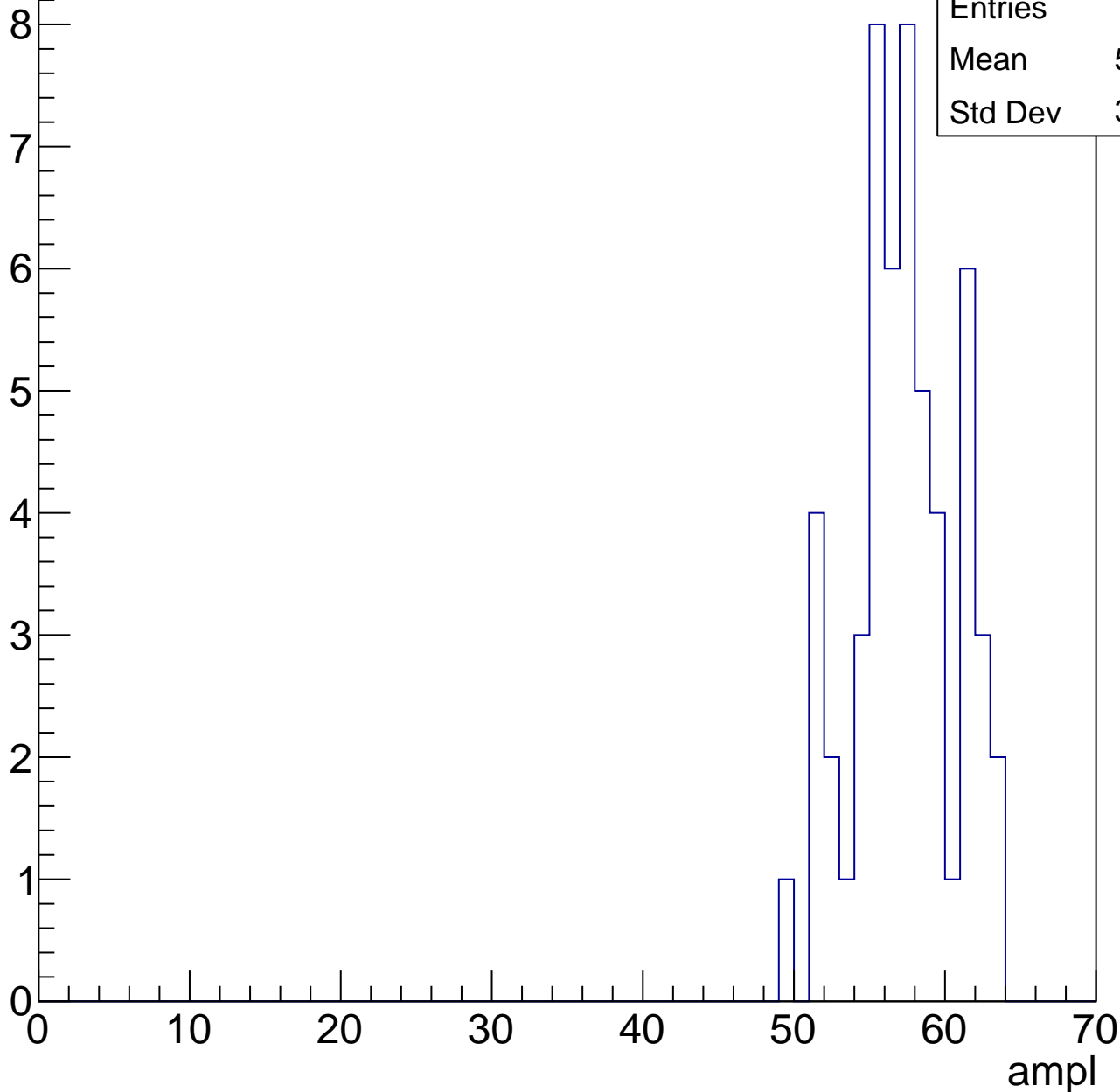


B0L001S, U14-ch75, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	56.81
Std Dev	3.361

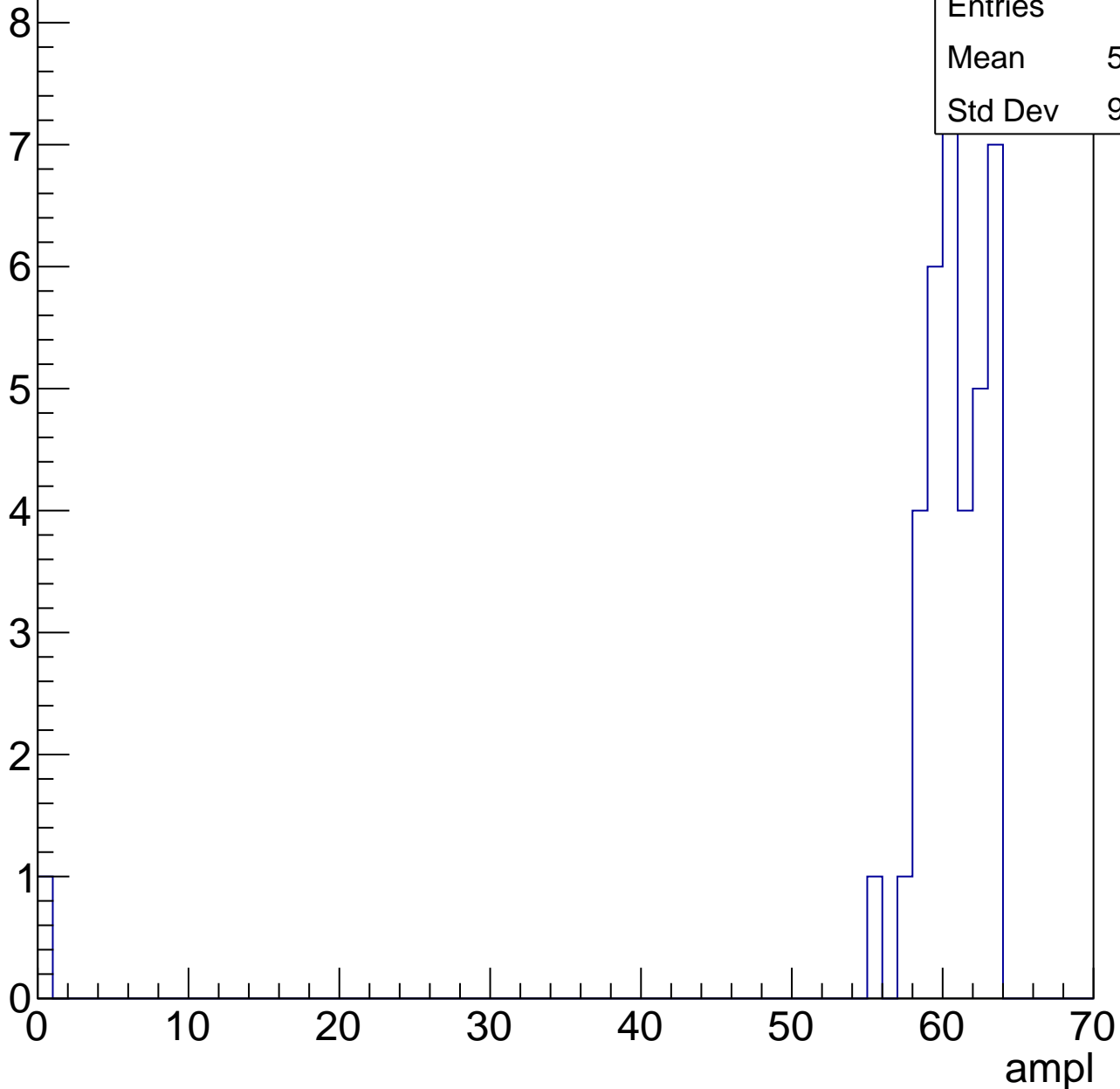


B0L001S, U14-ch75, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

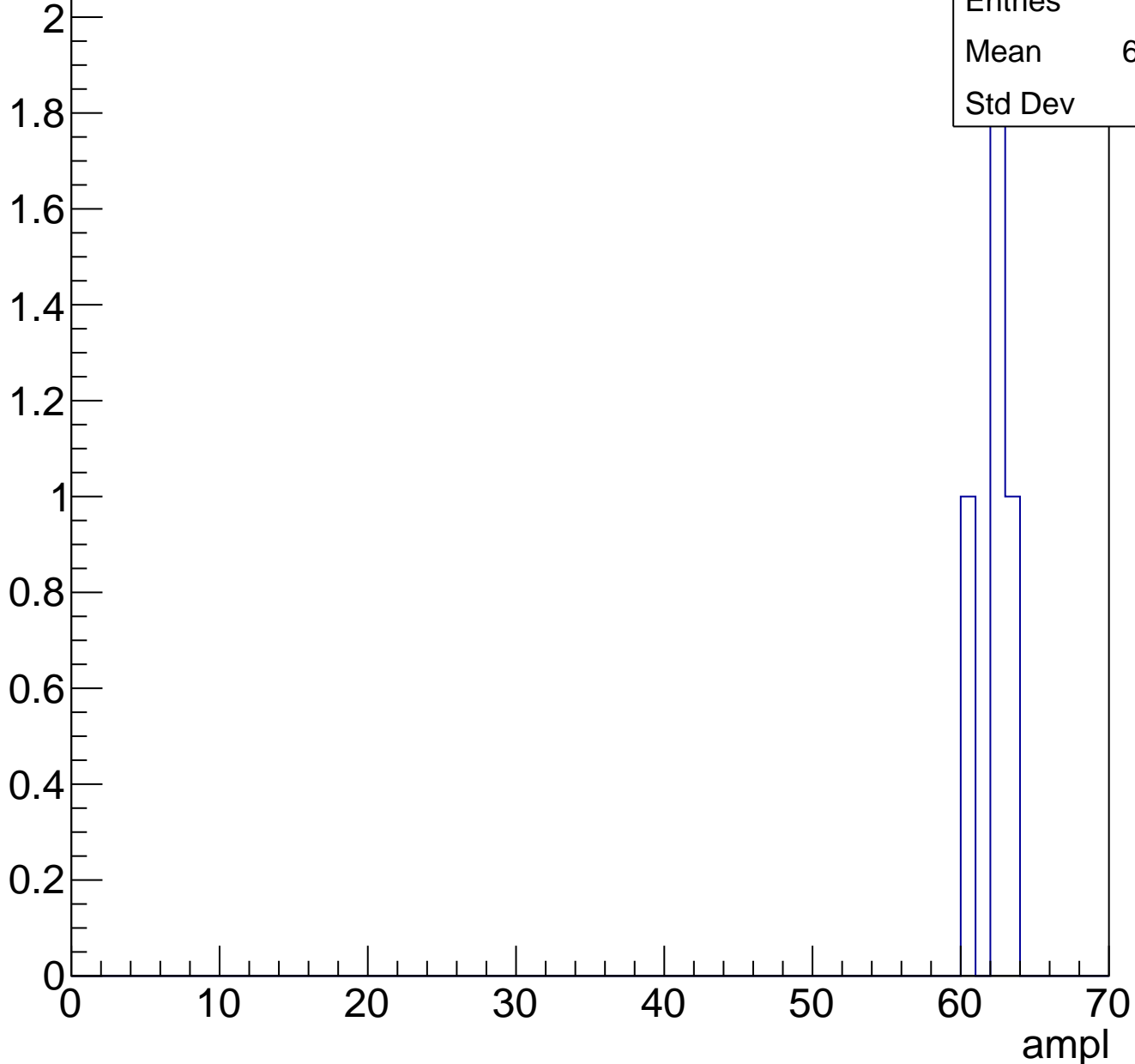
Entries	37
Mean	58.73
Std Dev	9.977



B0L001S, U14-ch75, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch75, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch76, adc0

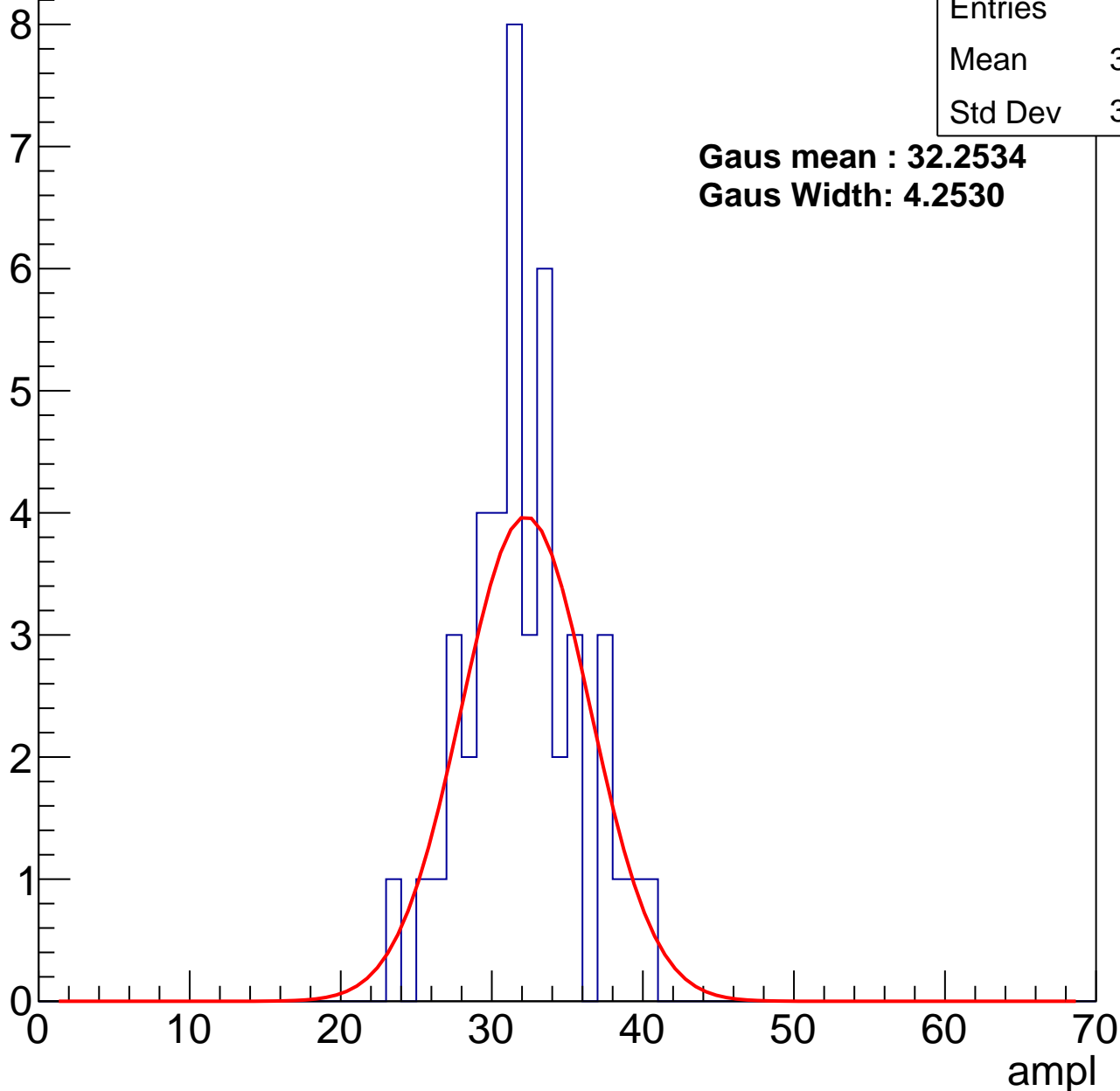
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	31.59
Std Dev	3.664

Gaus mean : 32.2534

Gaus Width: 4.2530



B0L001S, U14-ch76, adc1

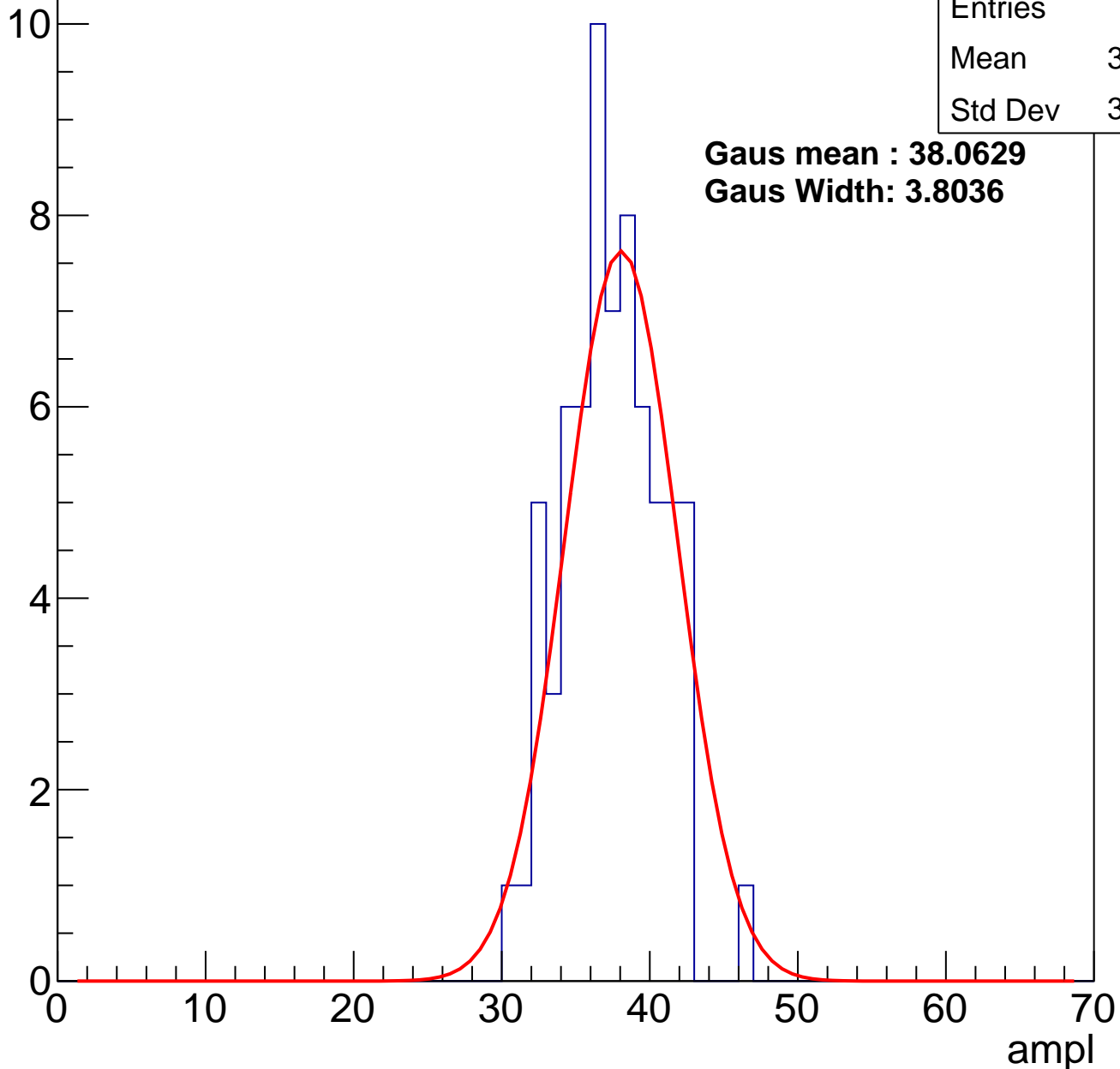
calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	36.99
Std Dev	3.205

Gaus mean : 38.0629

Gaus Width: 3.8036

Entry



B0L001S, U14-ch76, adc2

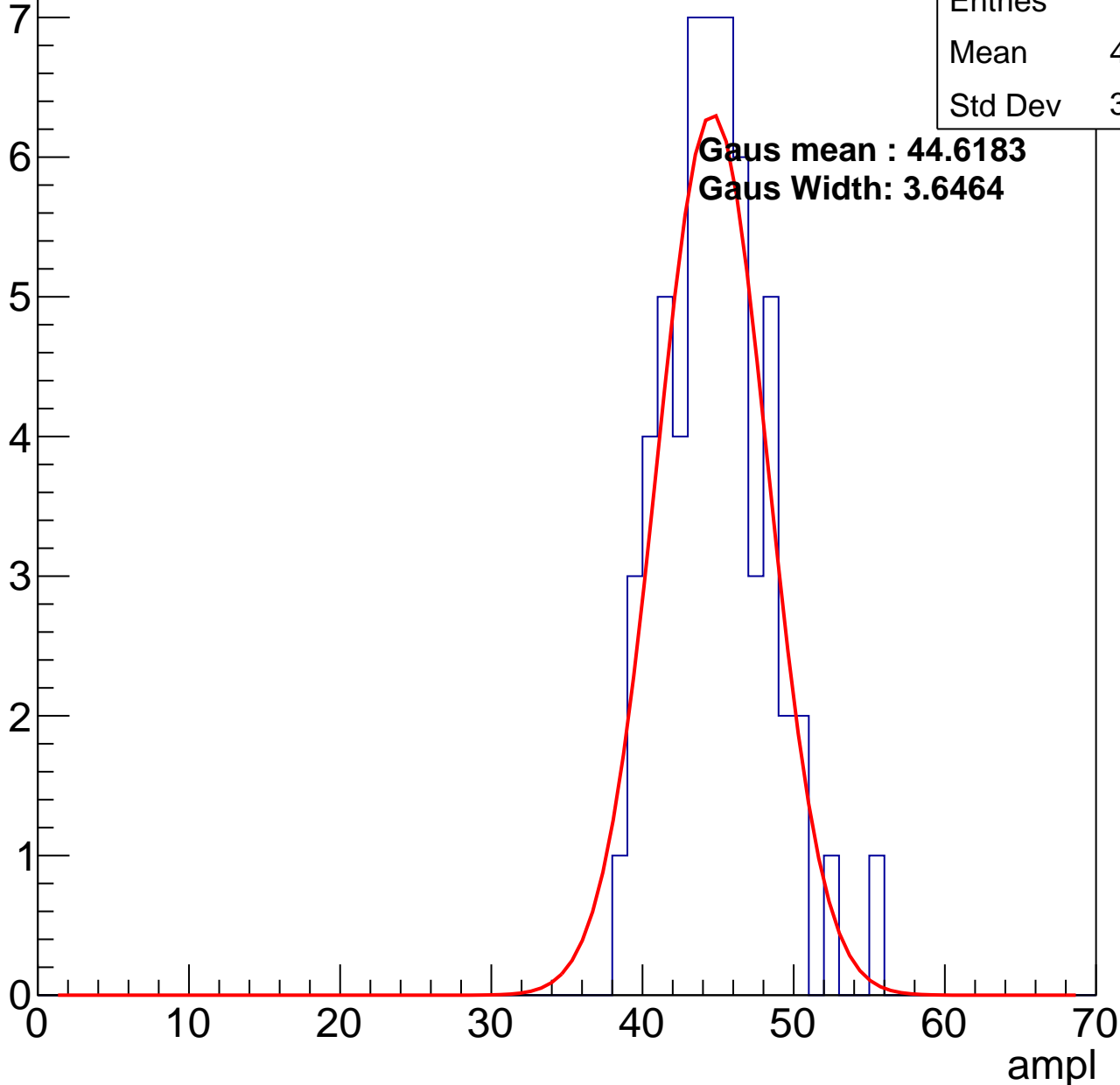
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.38
Std Dev	3.423

Gaus mean : 44.6183

Gaus Width: 3.6464

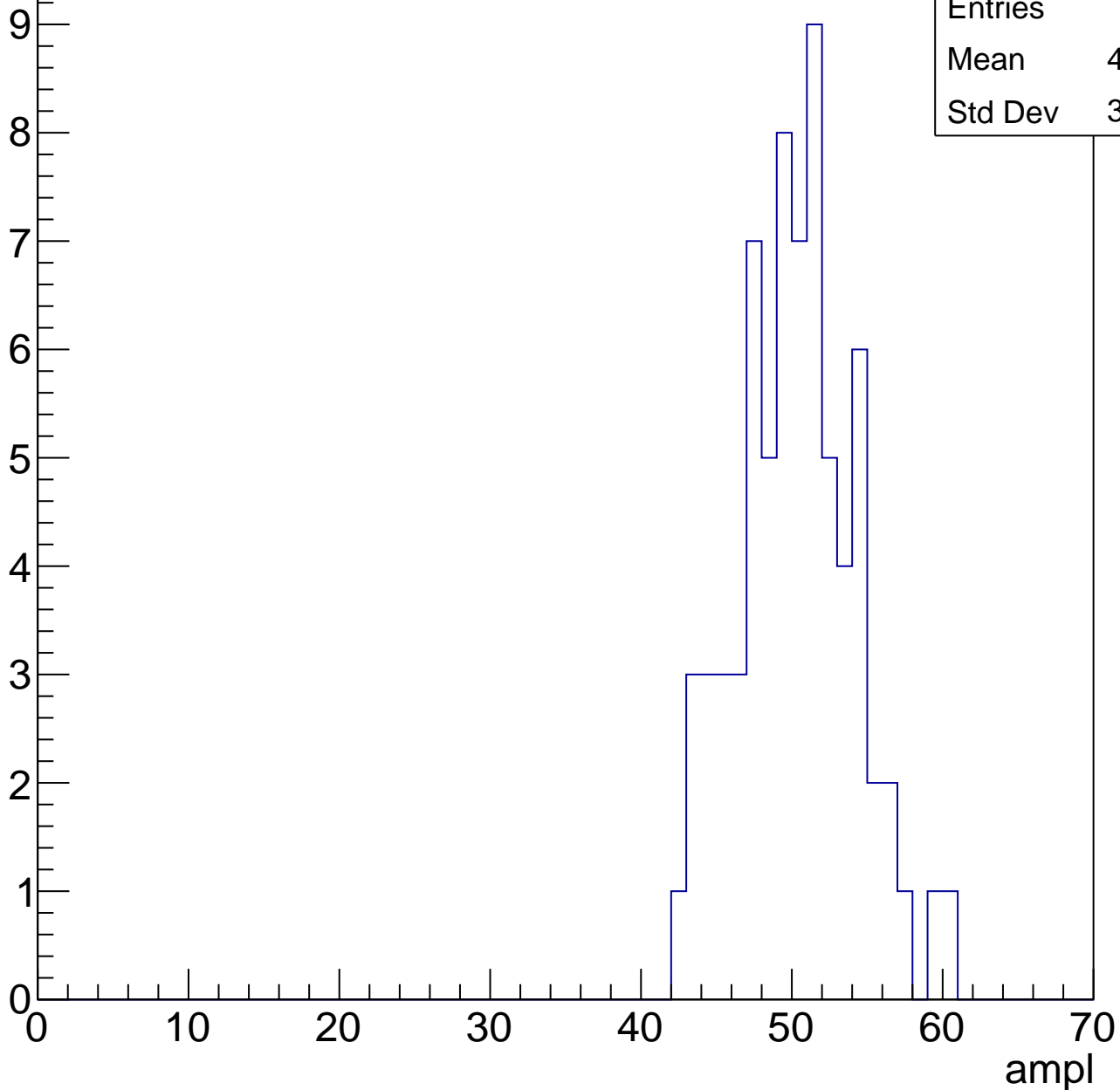


B0L001S, U14-ch76, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	49.86
Std Dev	3.836

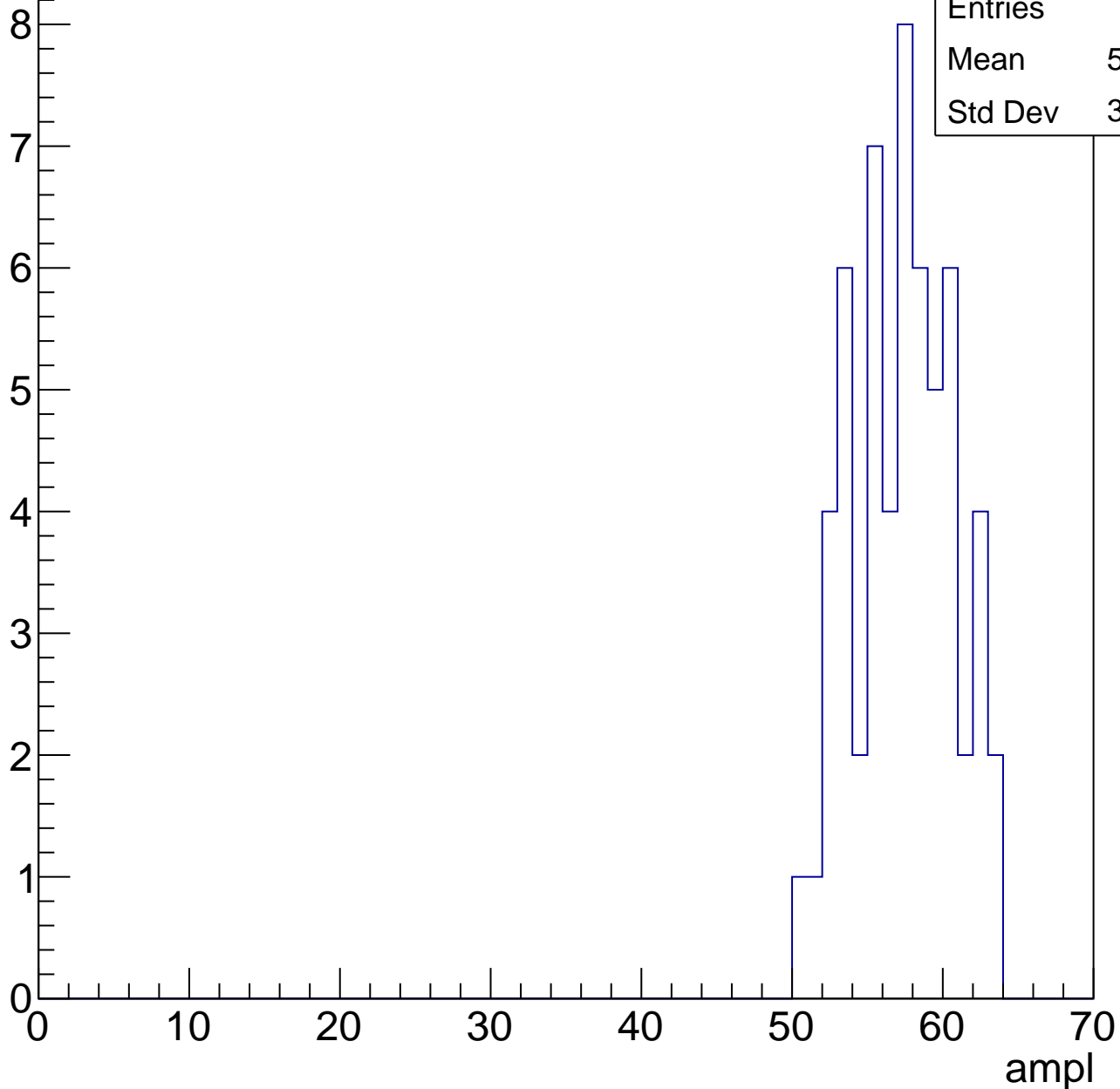


B0L001S, U14-ch76, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	56.88
Std Dev	3.254

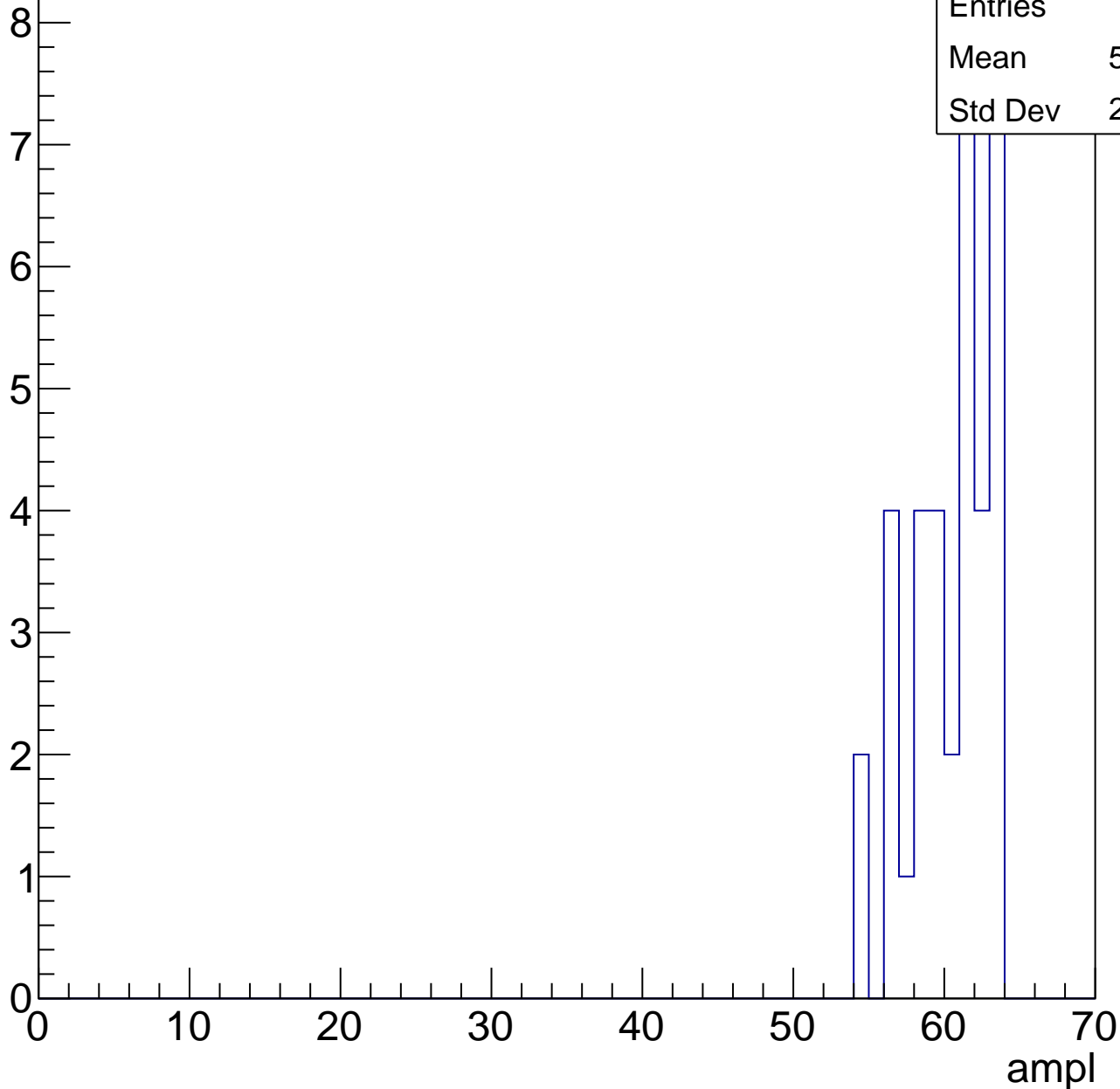


B0L001S, U14-ch76, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

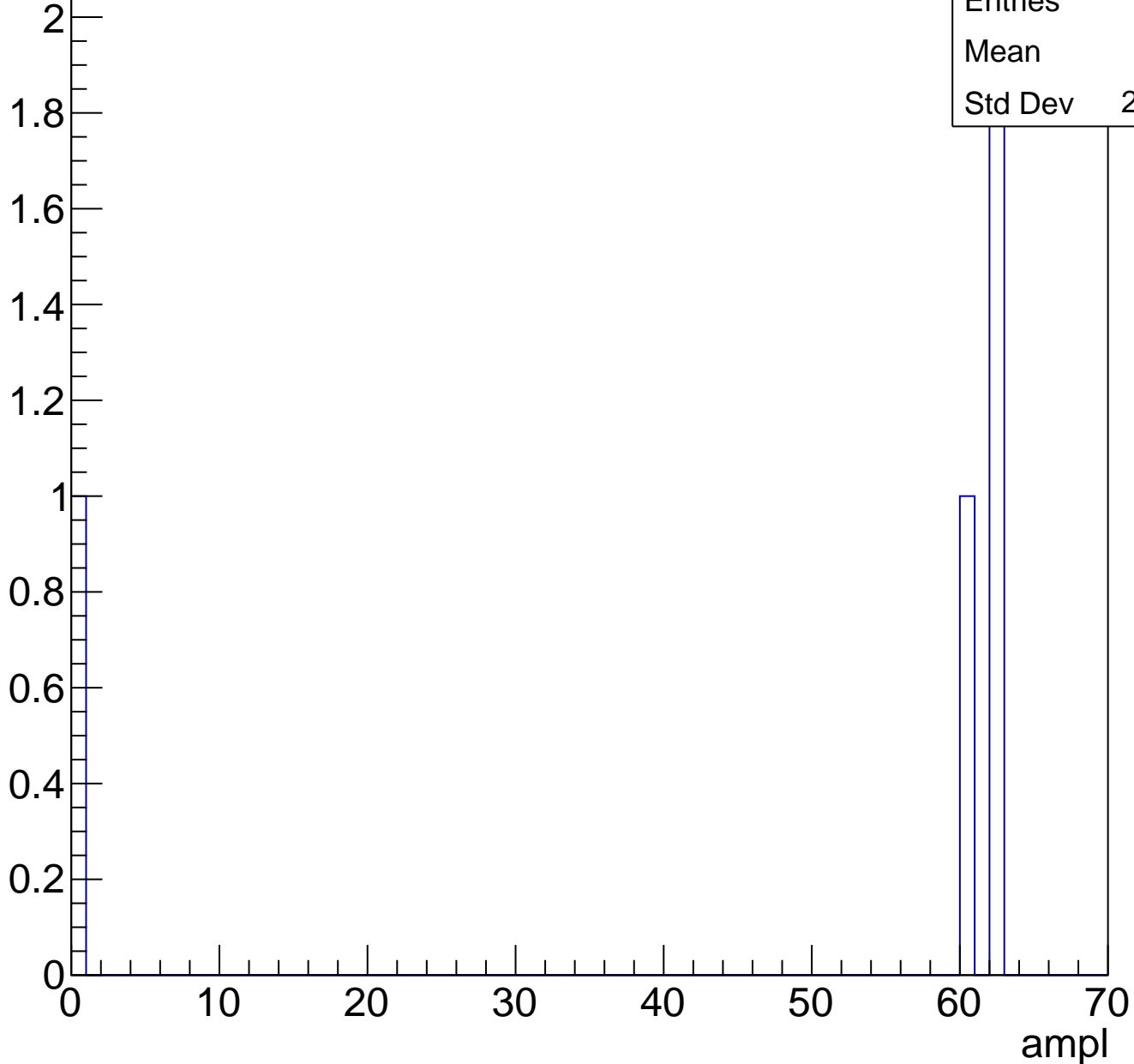
Entries	37
Mean	59.92
Std Dev	2.655



B0L001S, U14-ch76, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch76, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch77, adc0

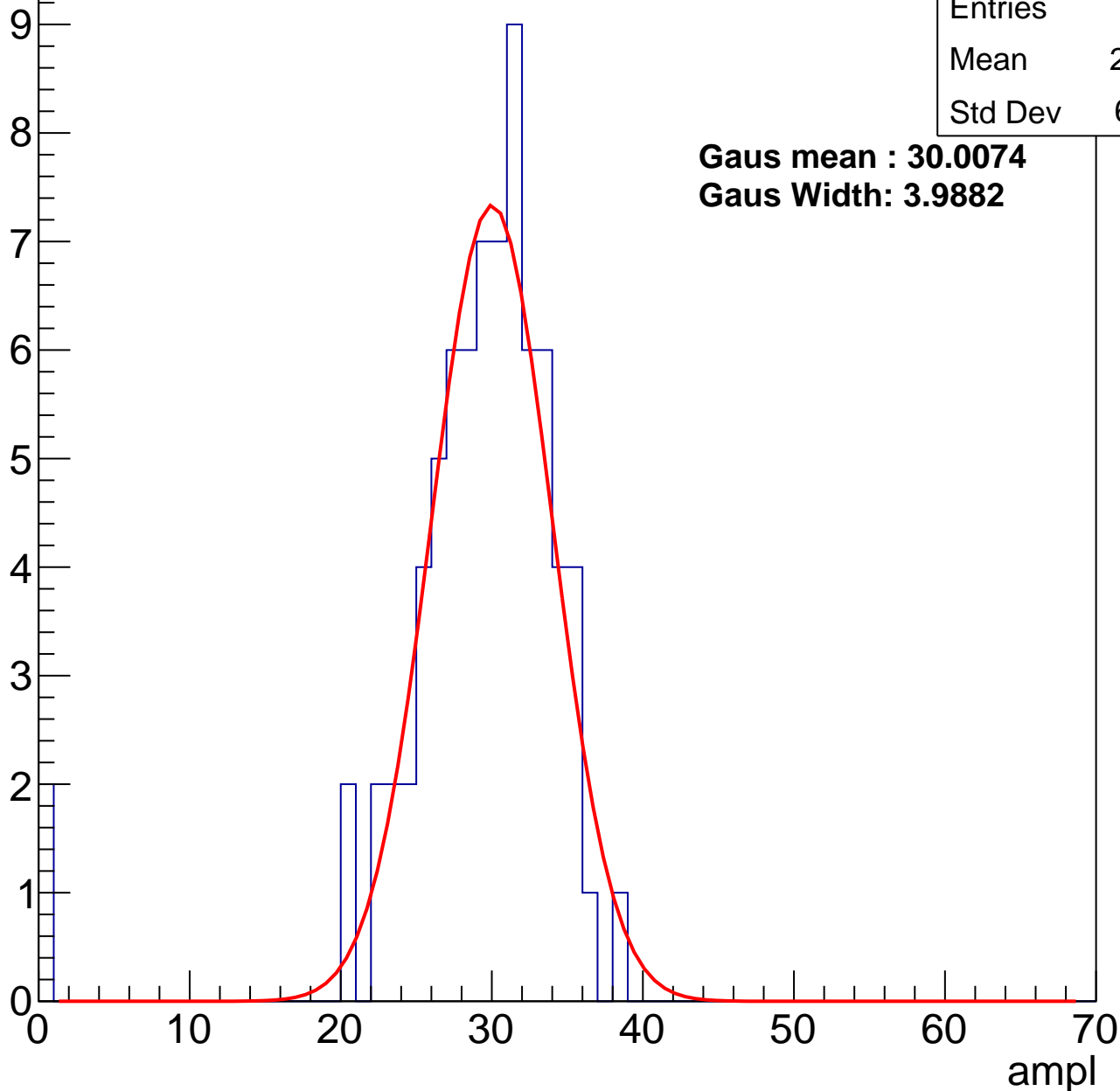
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	28.55
Std Dev	6.021

Gaus mean : 30.0074

Gaus Width: 3.9882



B0L001S, U14-ch77, adc1

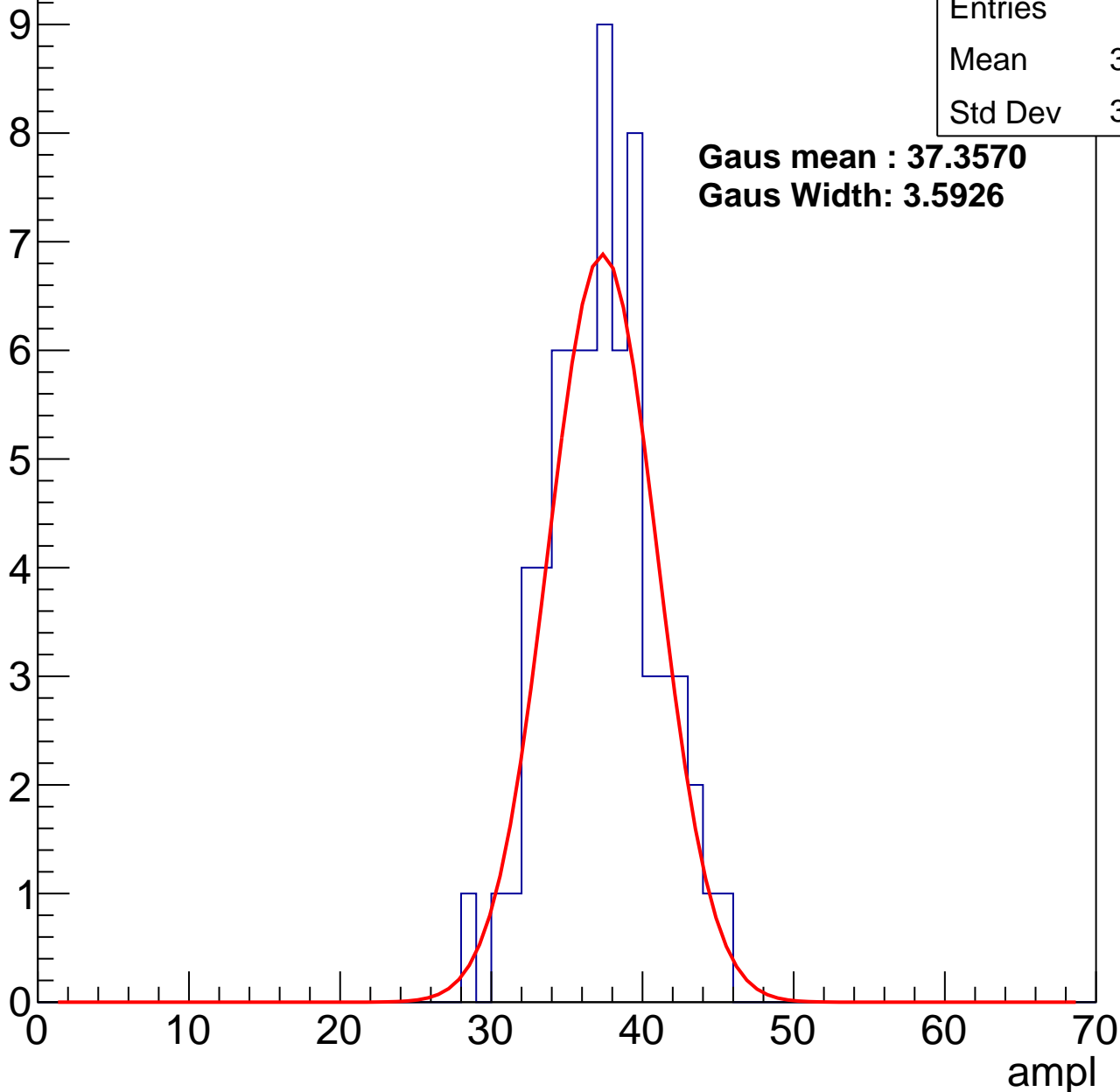
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.86
Std Dev	3.477

Gaus mean : 37.3570

Gaus Width: 3.5926



B0L001S, U14-ch77, adc2

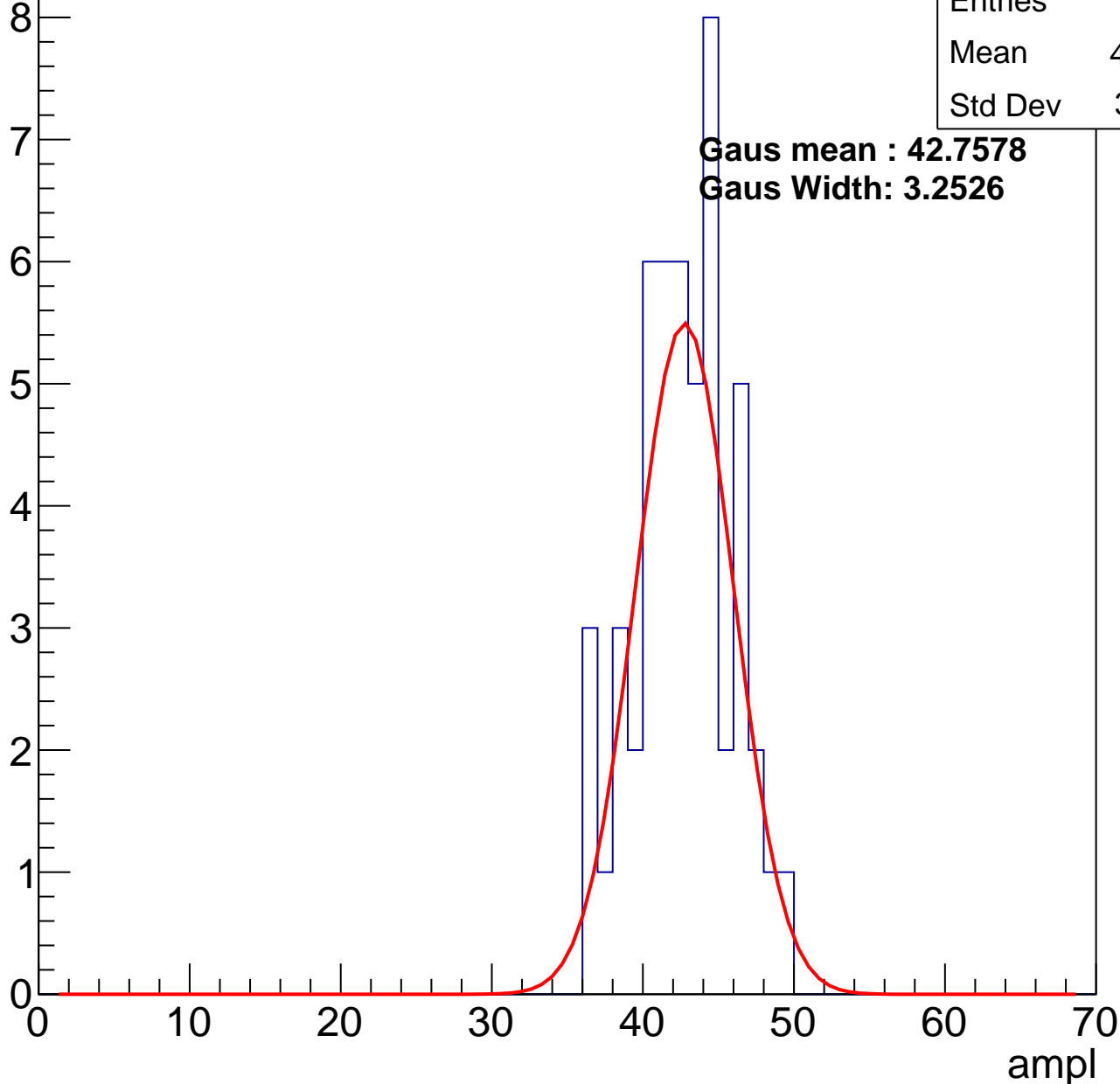
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	42.22
Std Dev	3.121

Gaus mean : 42.7578

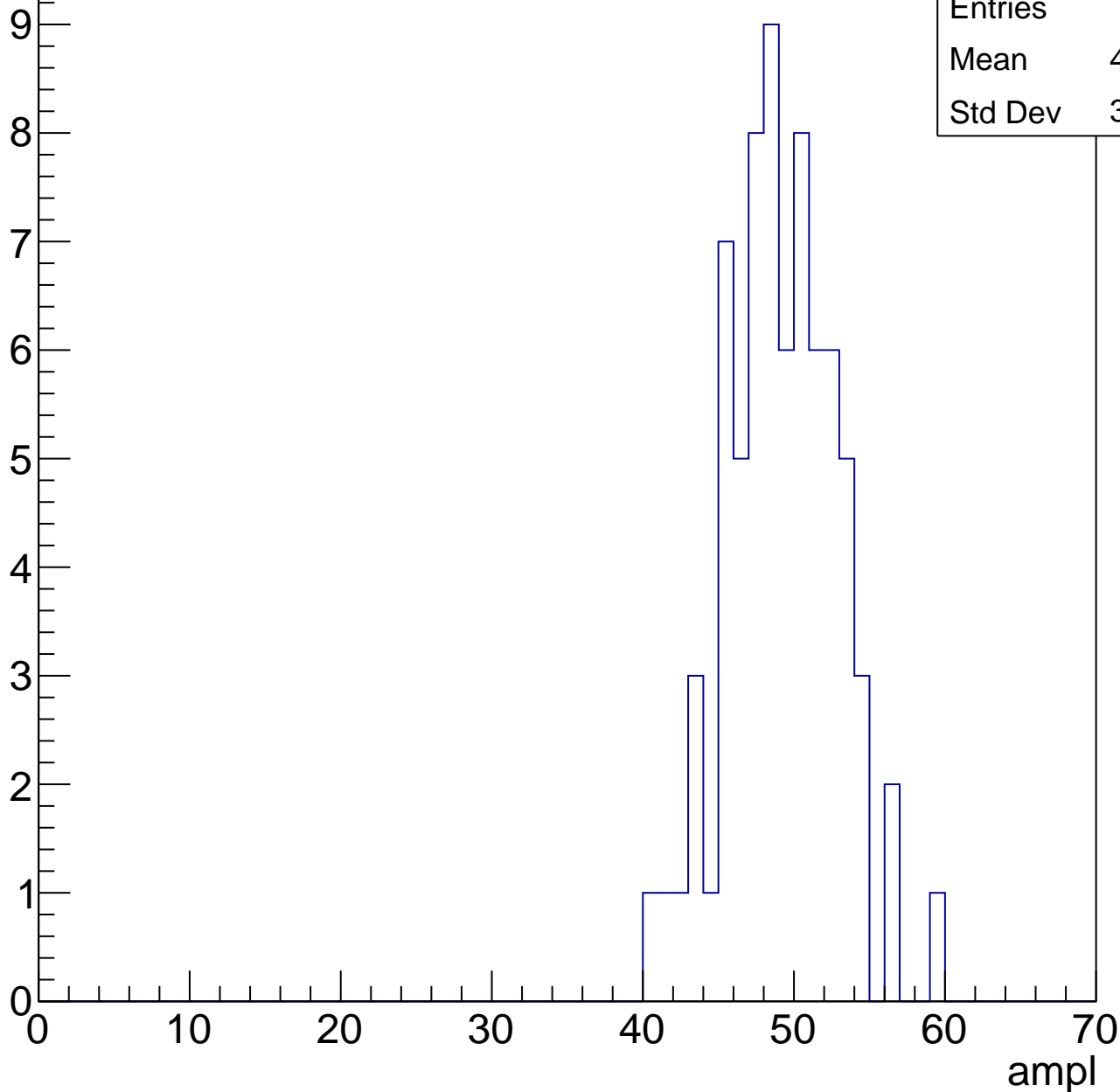
Gaus Width: 3.2526



B0L001S, U14-ch77, adc3

calib_packv5_042523_0143.root, FC#9, port A1

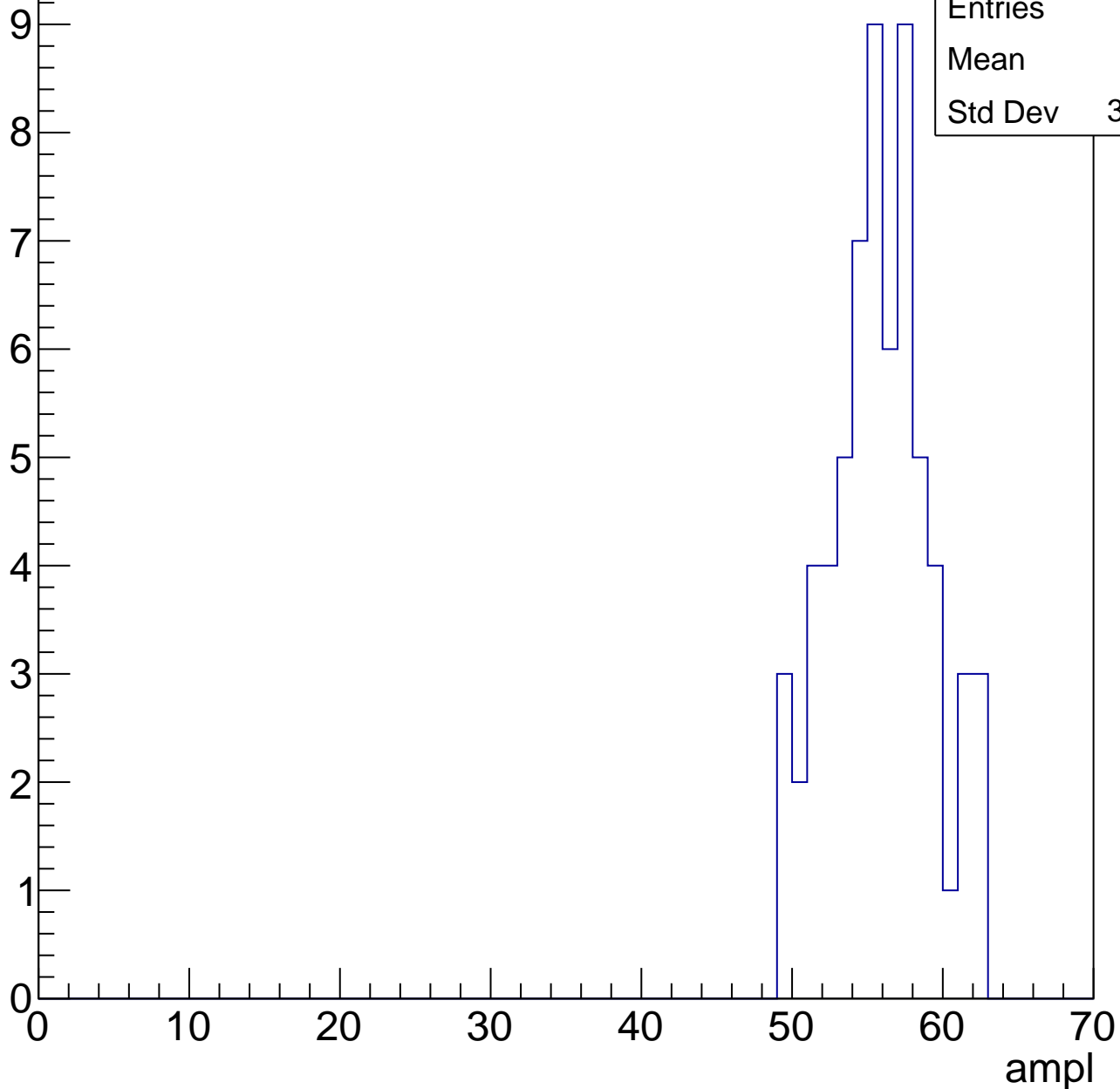
Entry



B0L001S, U14-ch77, adc4

calib_packv5_042523_0143.root, FC#9, port A1

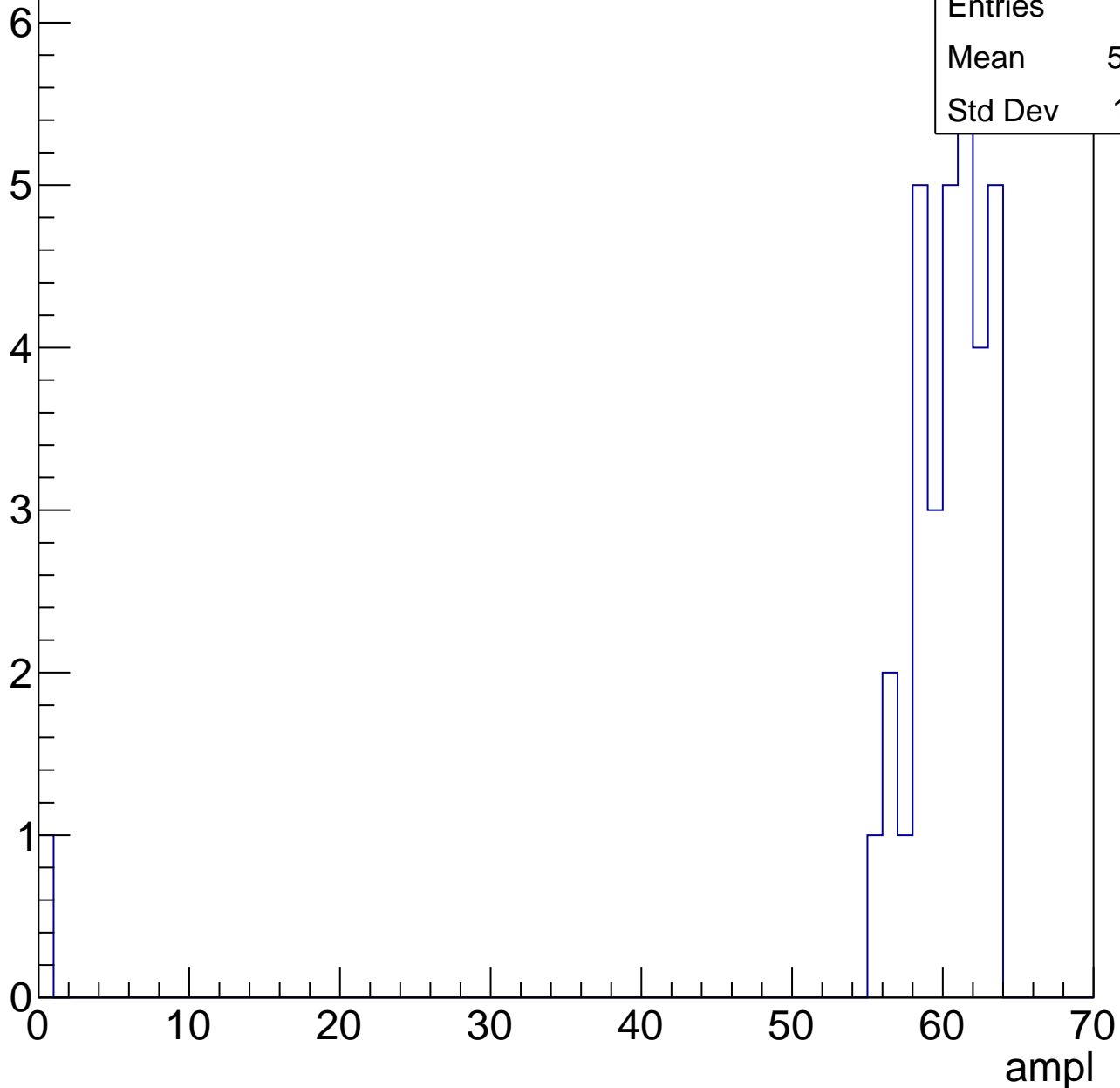
Entry



B0L001S, U14-ch77, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch77, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	13
Mean	61.15
Std Dev	1.511

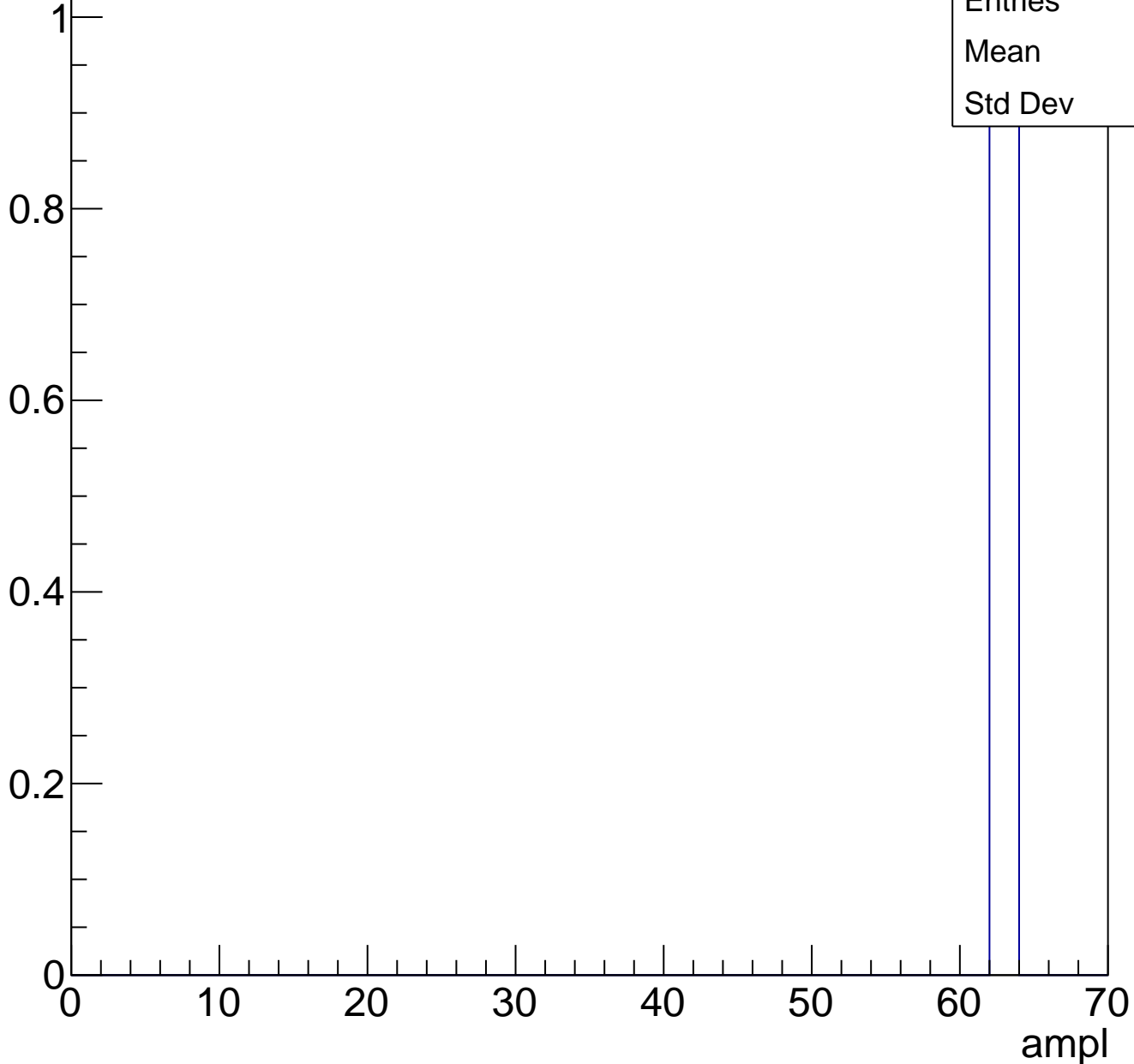
ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch77, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch78, adc0

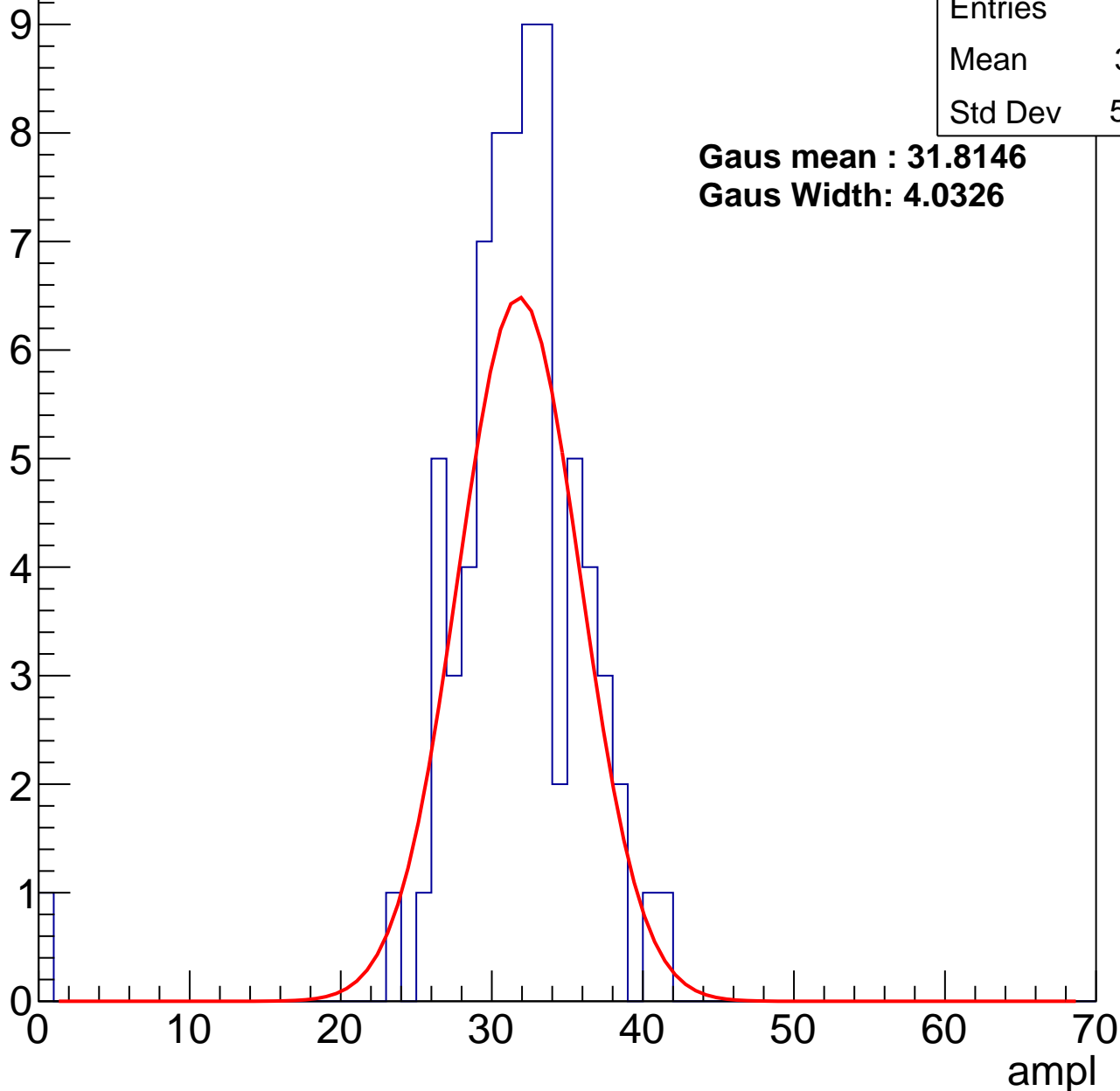
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	31.11
Std Dev	5.114

Gaus mean : 31.8146

Gaus Width: 4.0326



B0L001S, U14-ch78, adc1

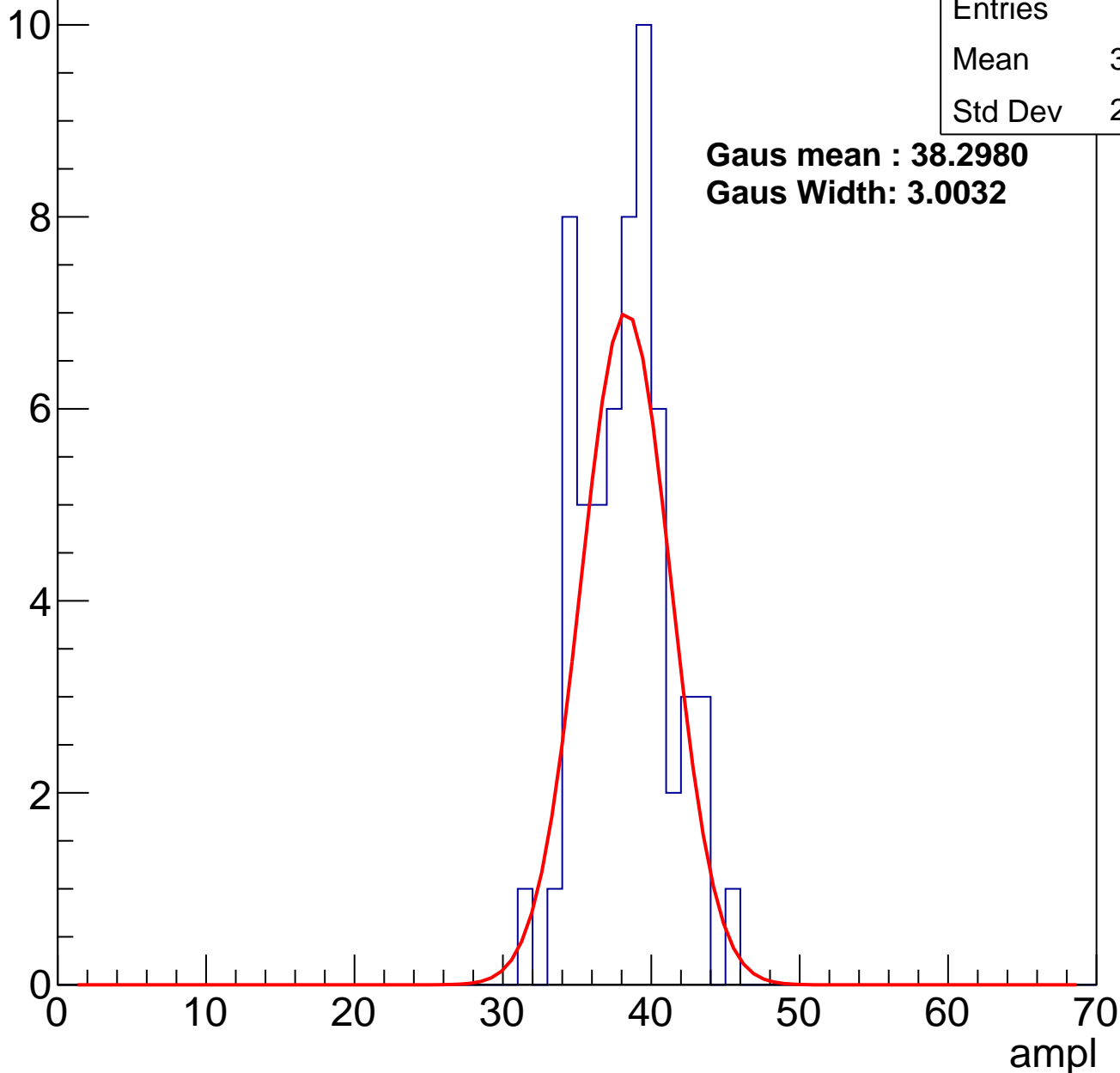
calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	37.78
Std Dev	2.894

Gaus mean : 38.2980

Gaus Width: 3.0032

Entry



B0L001S, U14-ch78, adc2

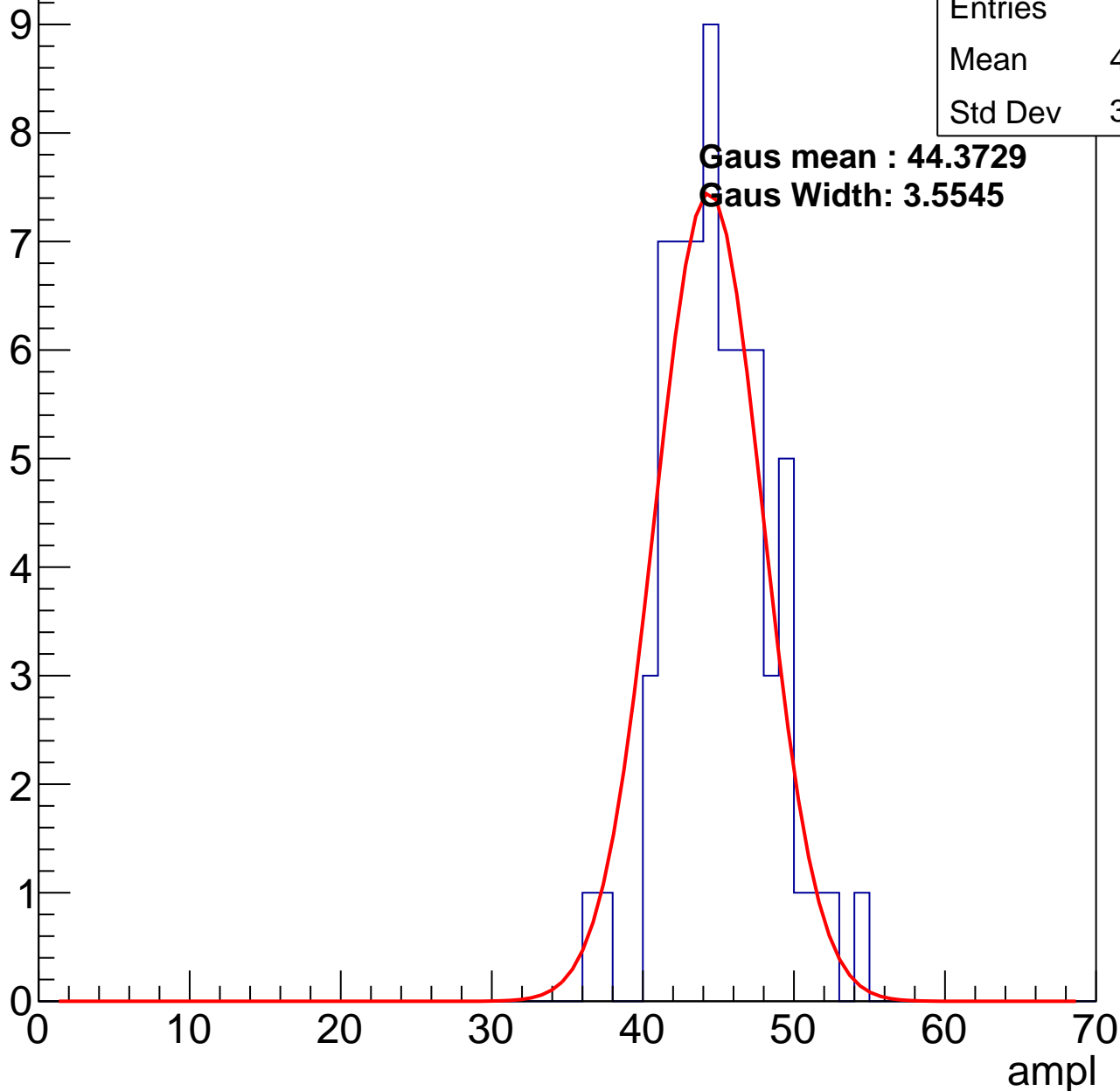
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	44.54
Std Dev	3.388

Gaus mean : 44.3729

Gaus Width: 3.5545

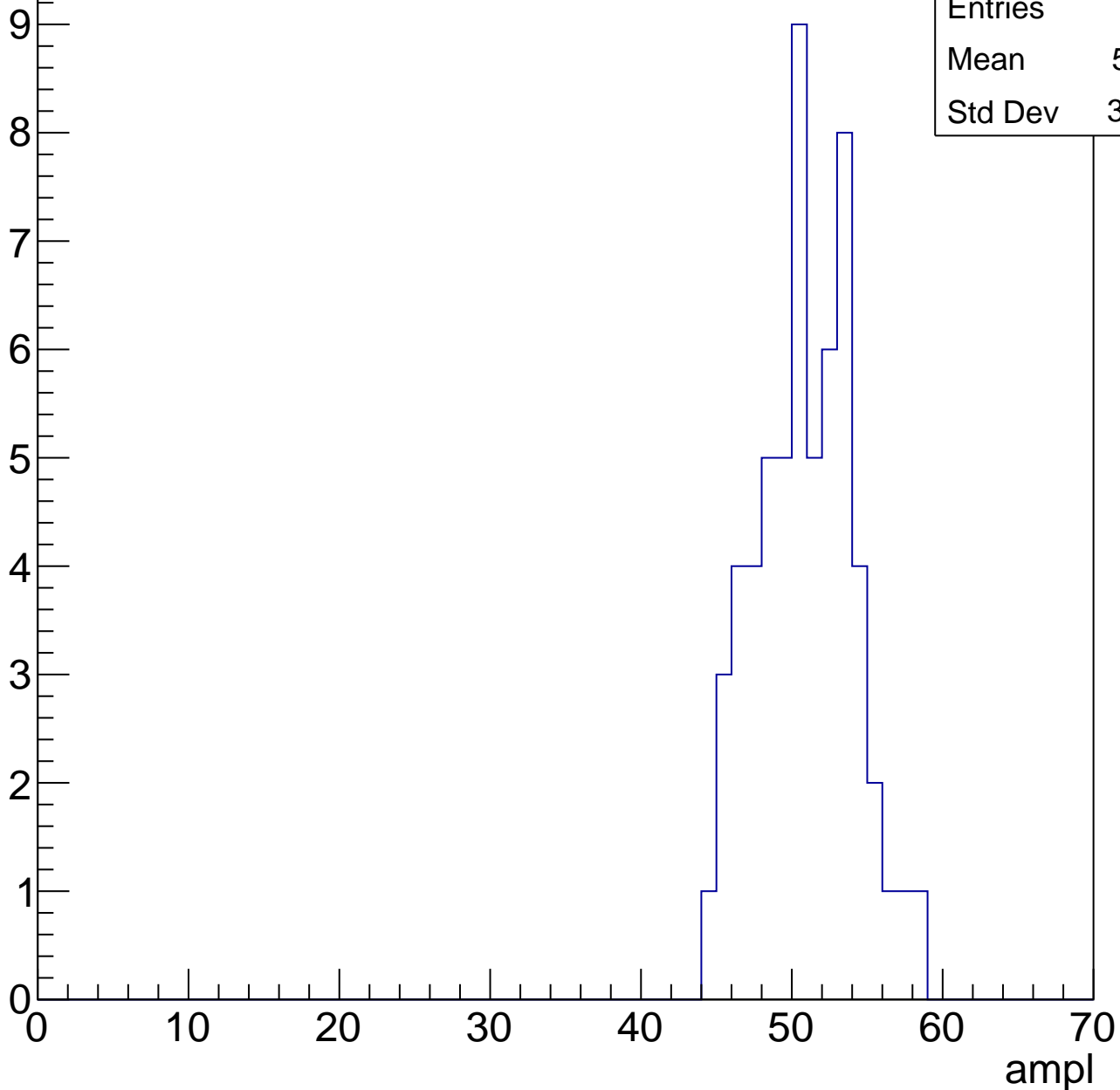


B0L001S, U14-ch78, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.41
Std Dev	3.163

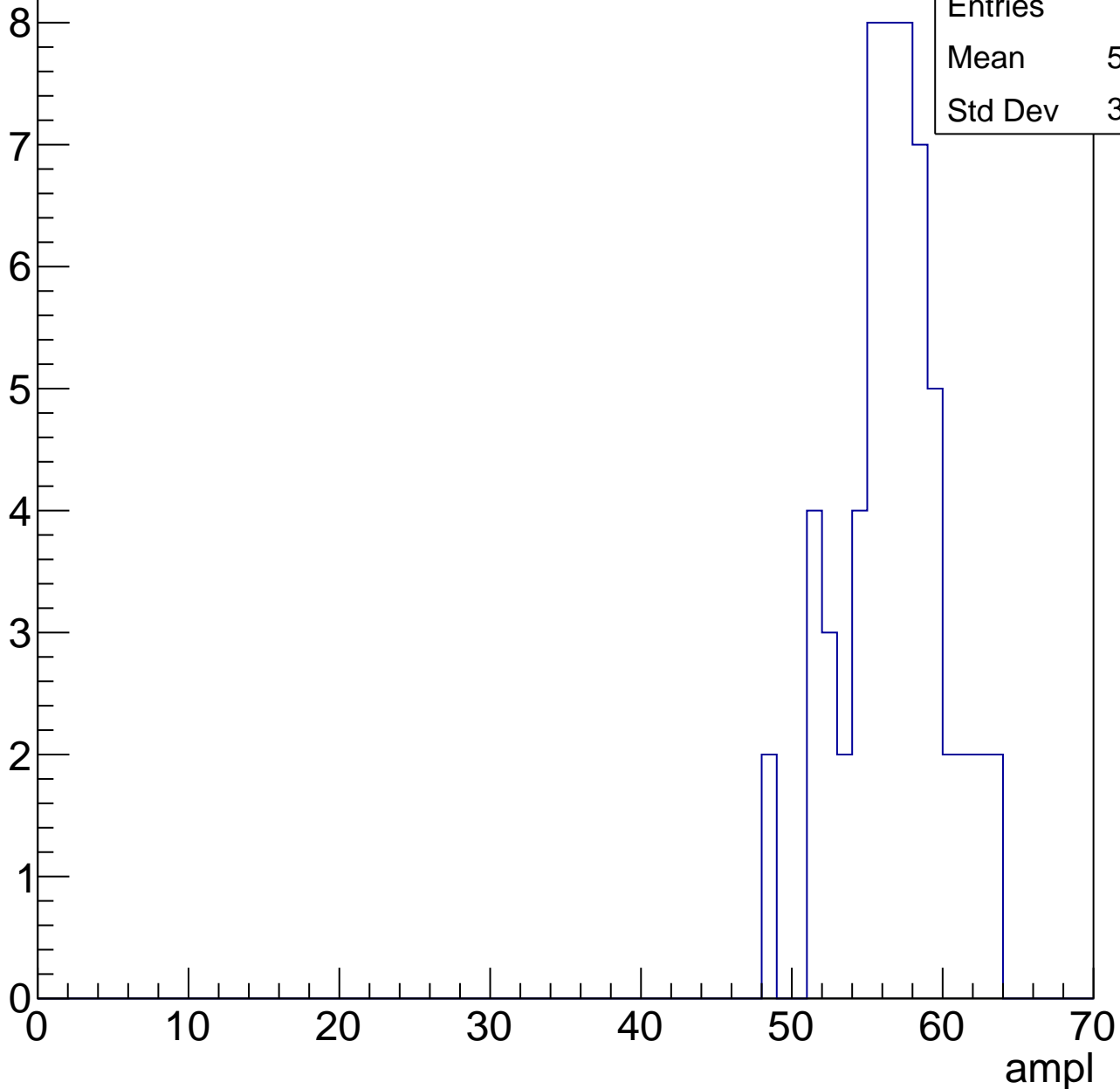


B0L001S, U14-ch78, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	56.19
Std Dev	3.316



B0L001S, U14-ch78, adc5

calib_packv5_042523_0143.root, FC#9, port A1

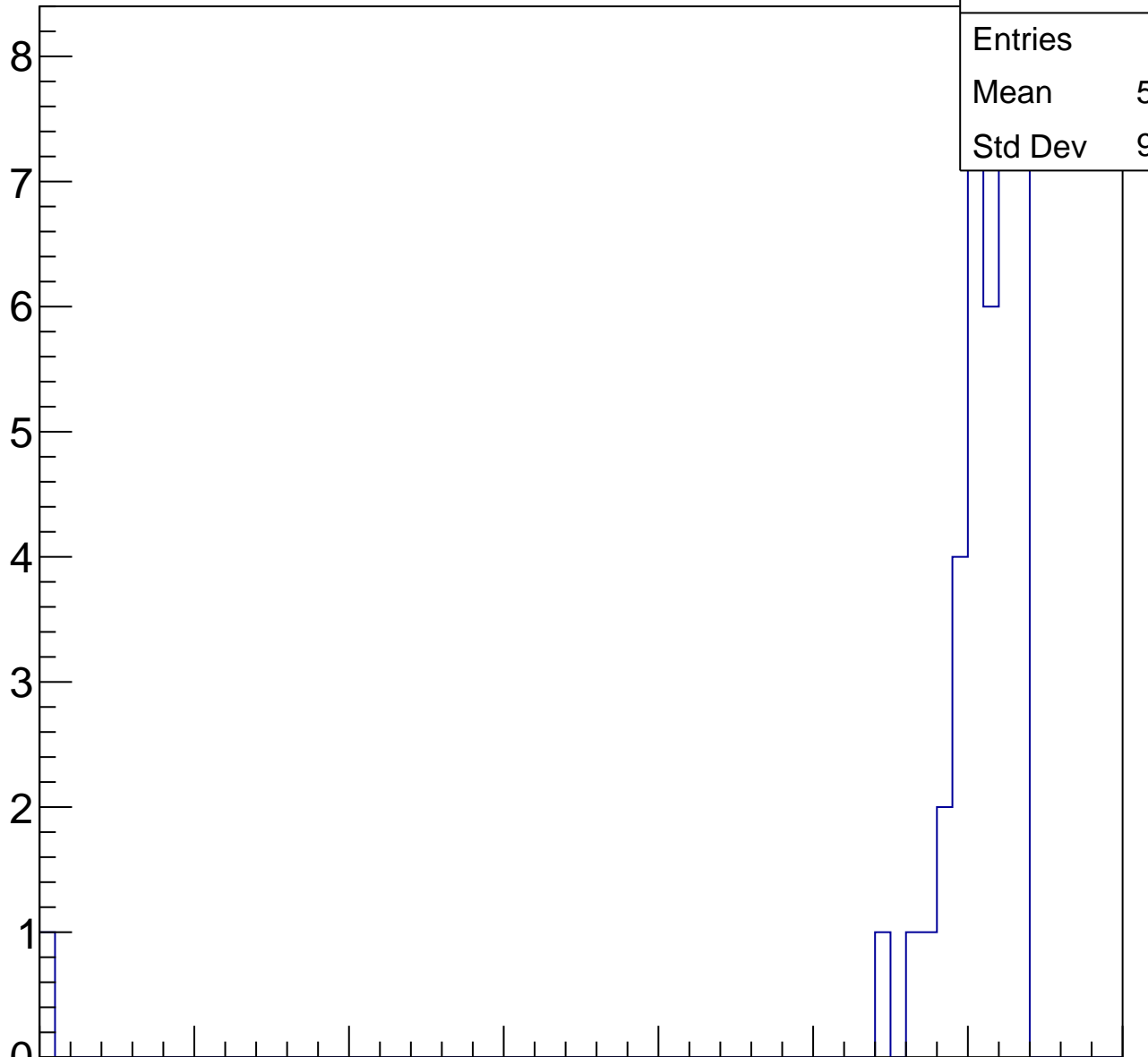
Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	59.12
Std Dev	9.686

ampl

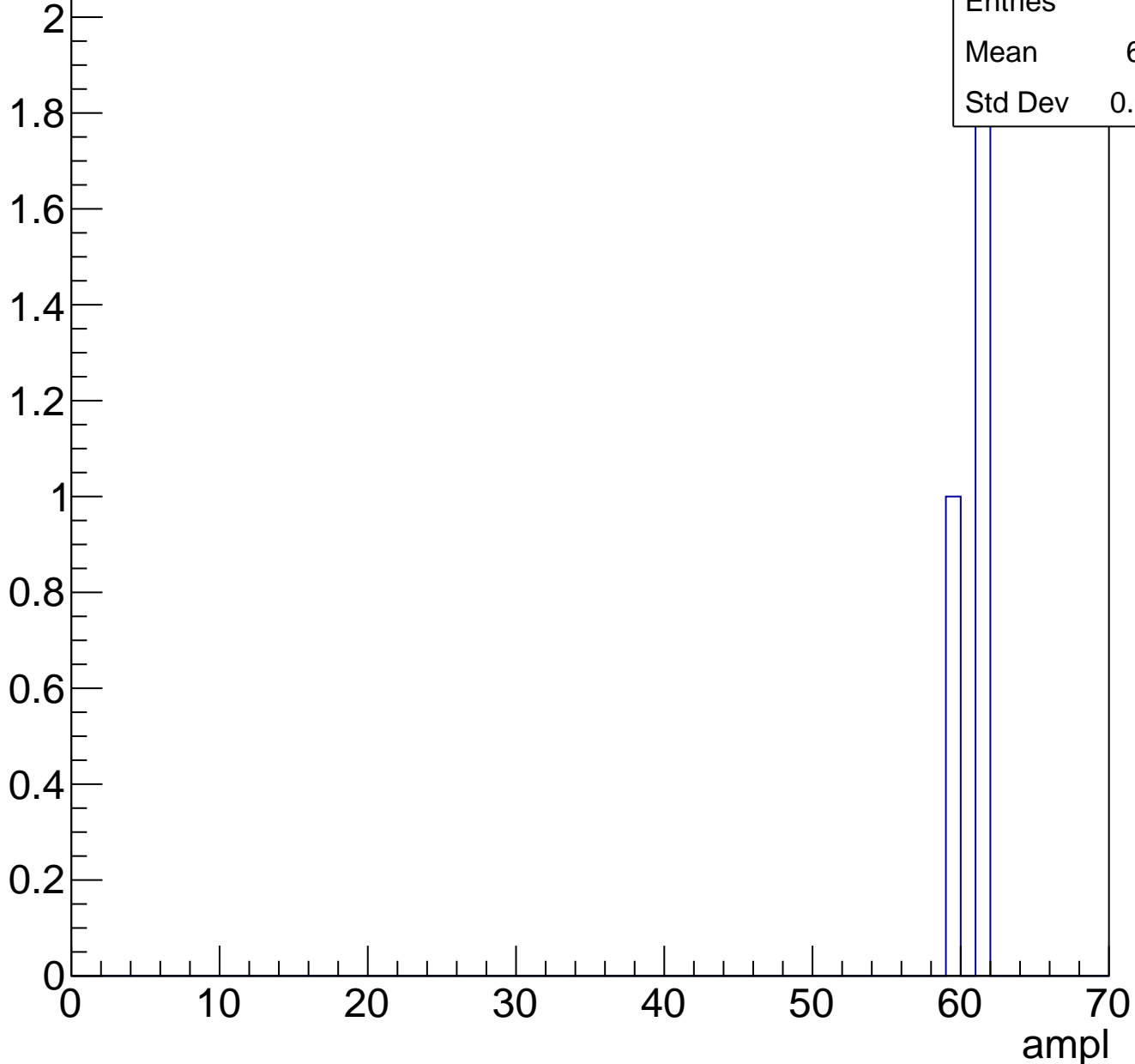
0 10 20 30 40 50 60 70



B0L001S, U14-ch78, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch78, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch79, adc0

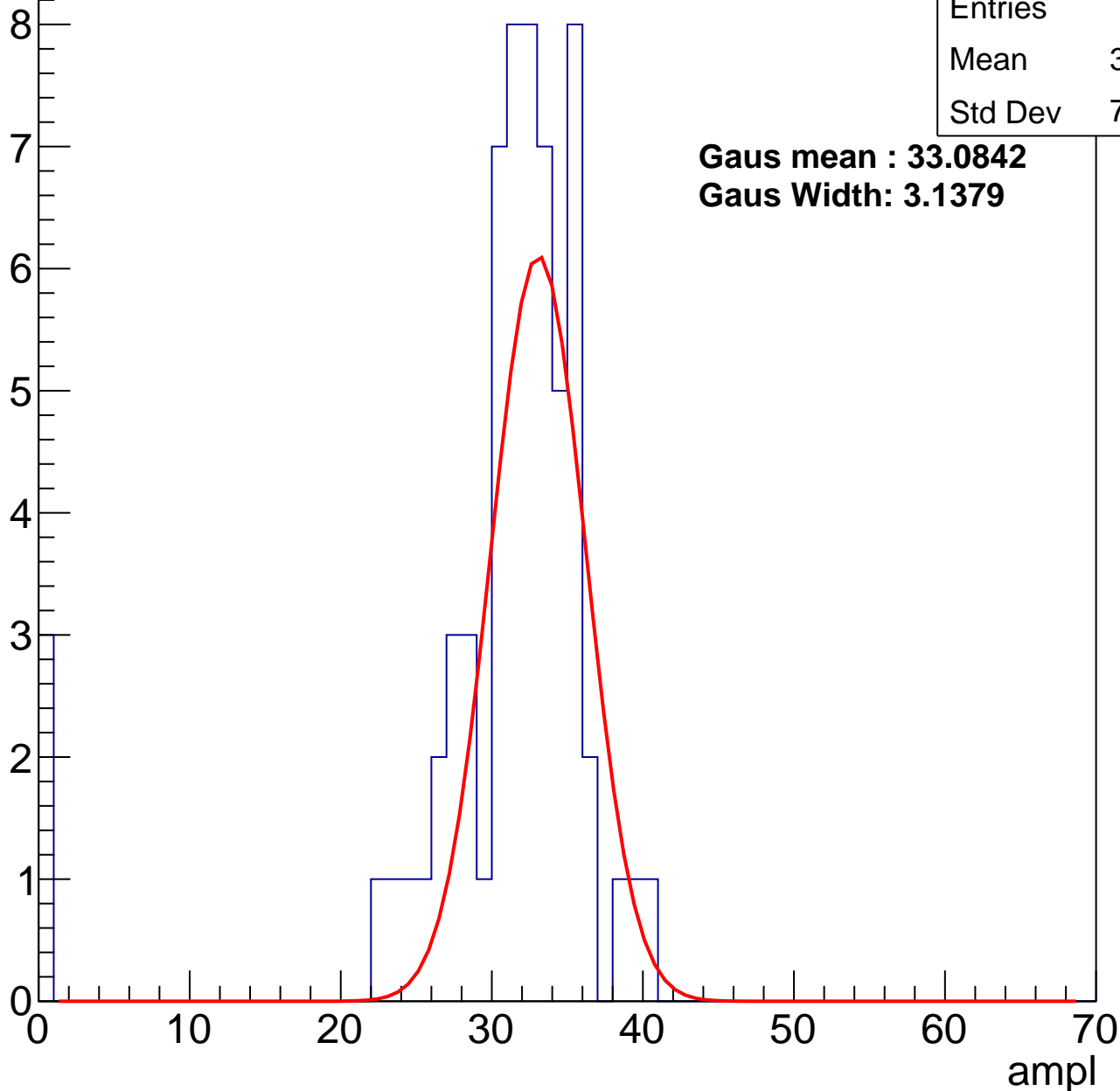
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	30.06
Std Dev	7.548

Gaus mean : 33.0842

Gaus Width: 3.1379



B0L001S, U14-ch79, adc1

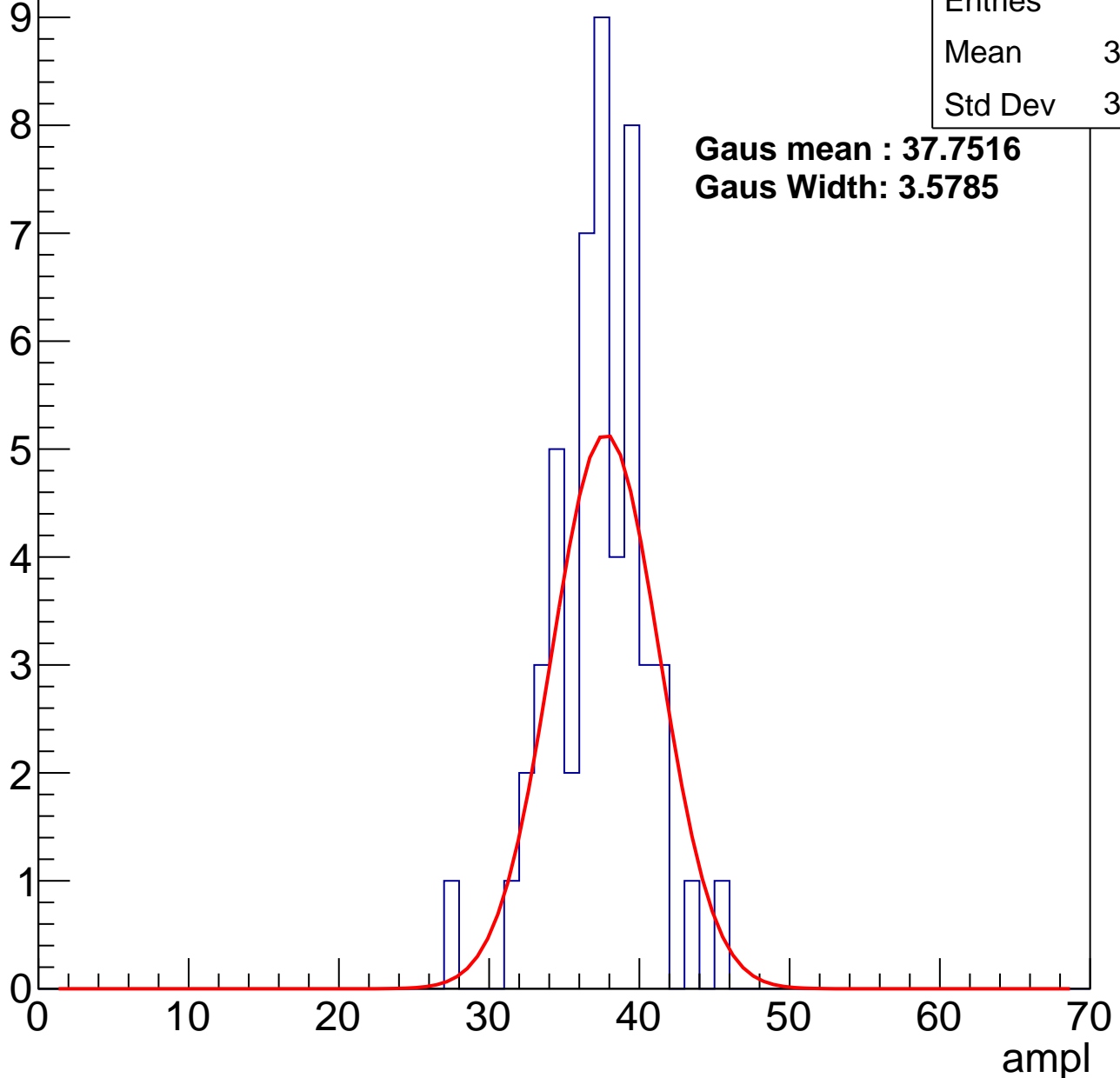
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	36.82
Std Dev	3.173

Gaus mean : 37.7516

Gaus Width: 3.5785



B0L001S, U14-ch79, adc2

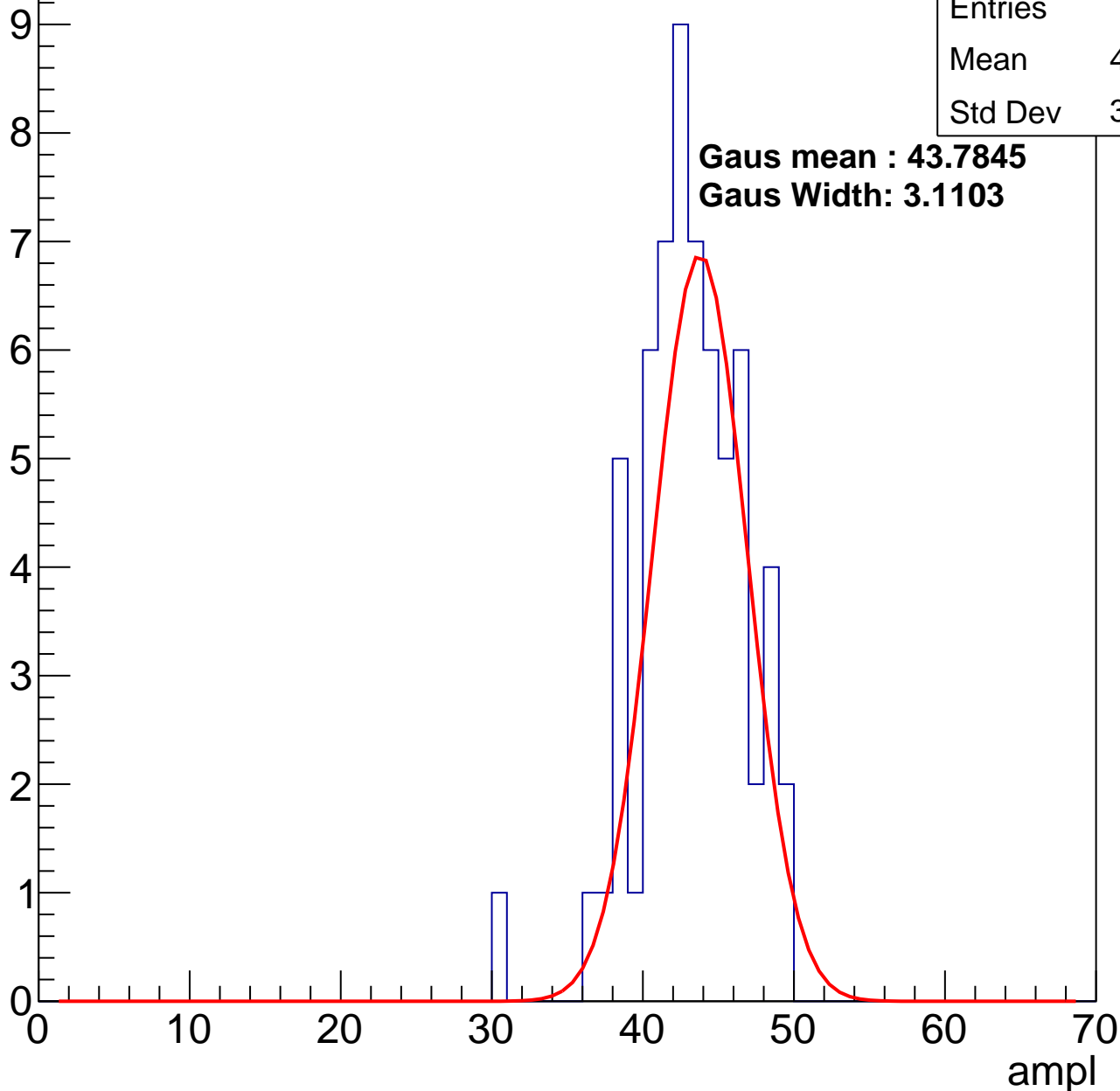
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.65
Std Dev	3.479

Gaus mean : 43.7845

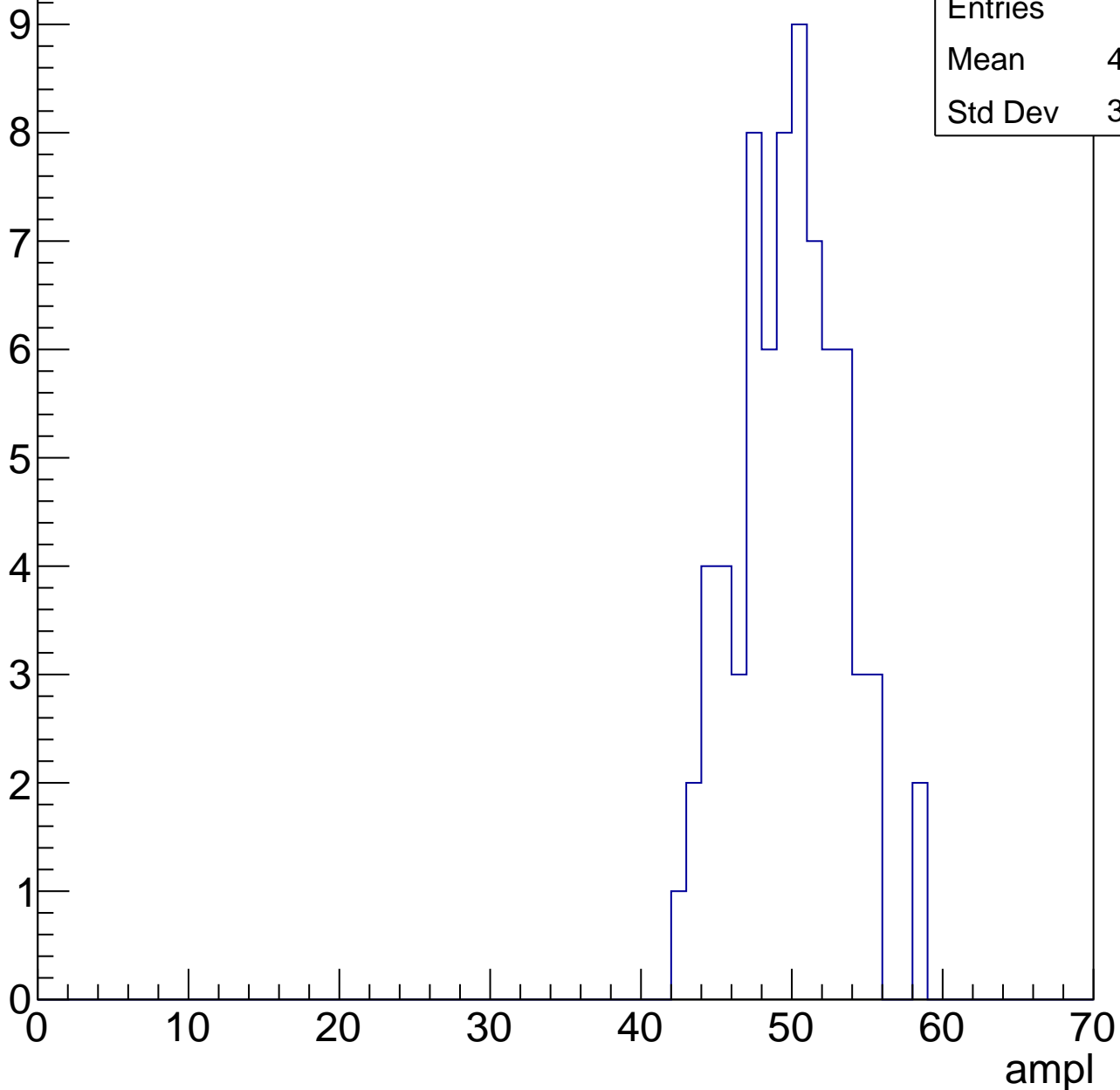
Gaus Width: 3.1103



B0L001S, U14-ch79, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

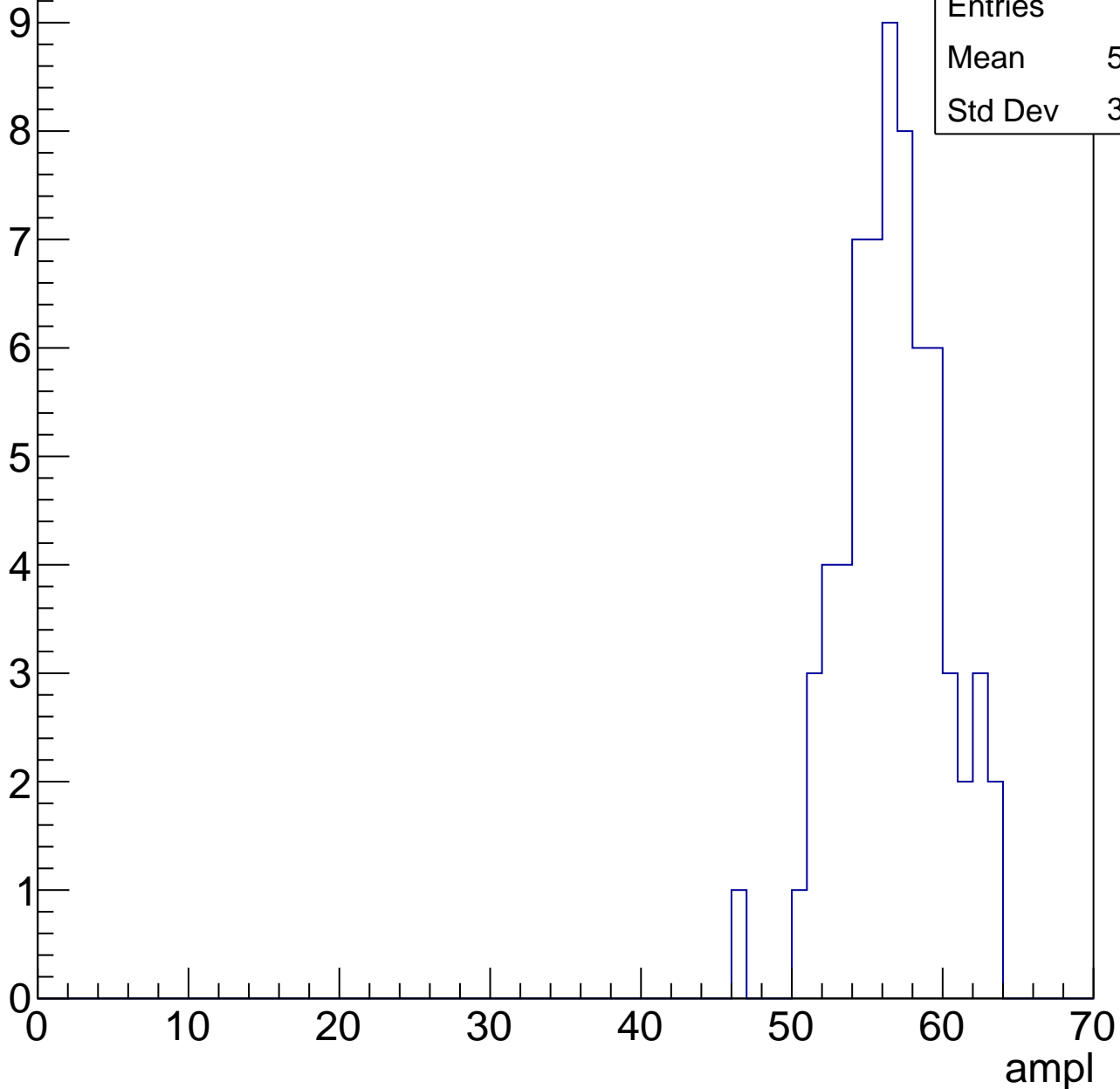


B0L001S, U14-ch79, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.18
Std Dev	3.334

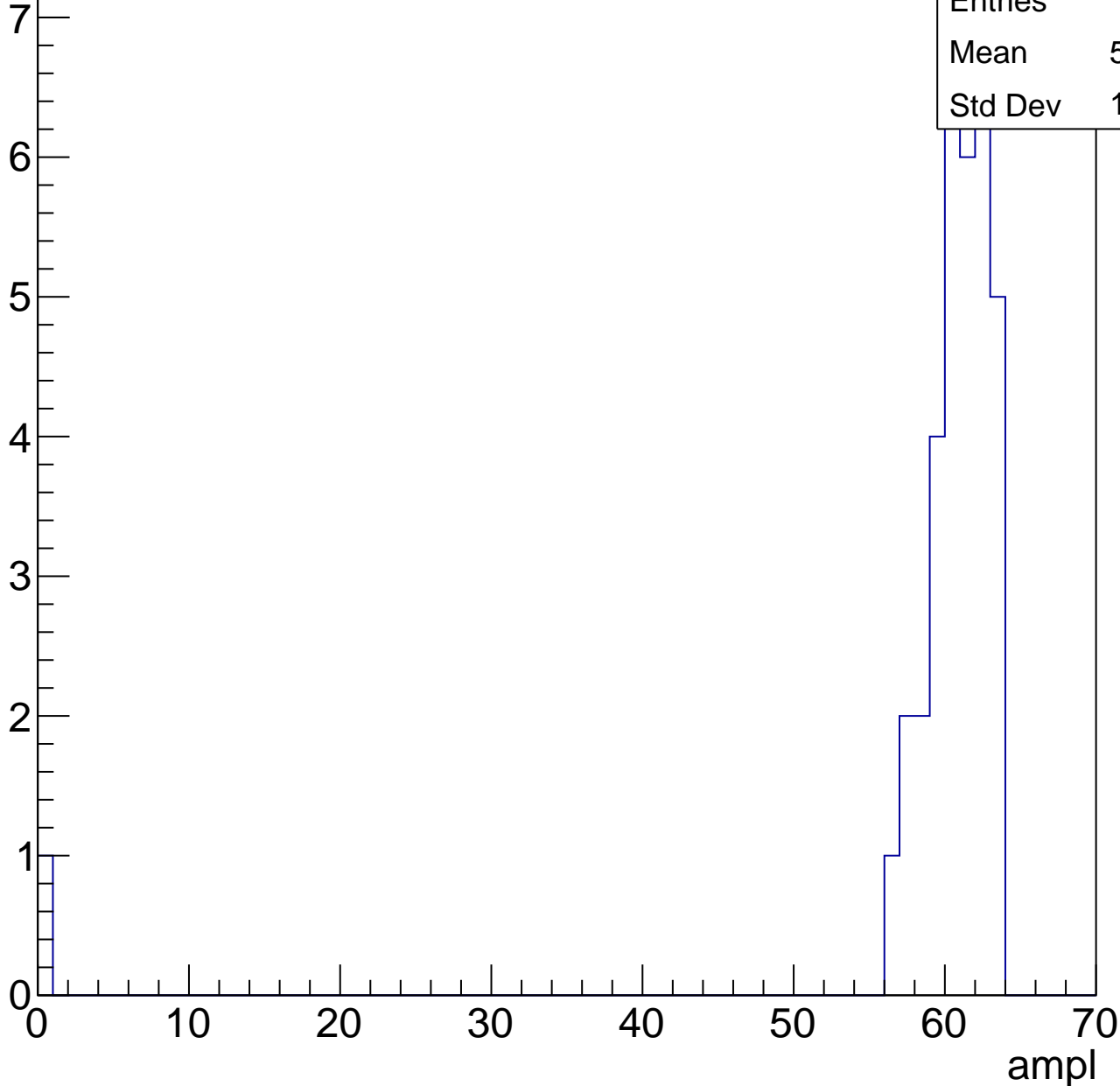


B0L001S, U14-ch79, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	58.77
Std Dev	10.24



B0L001S, U14-ch79, adc6

calib_packv5_042523_0143.root, FC#9, port A1

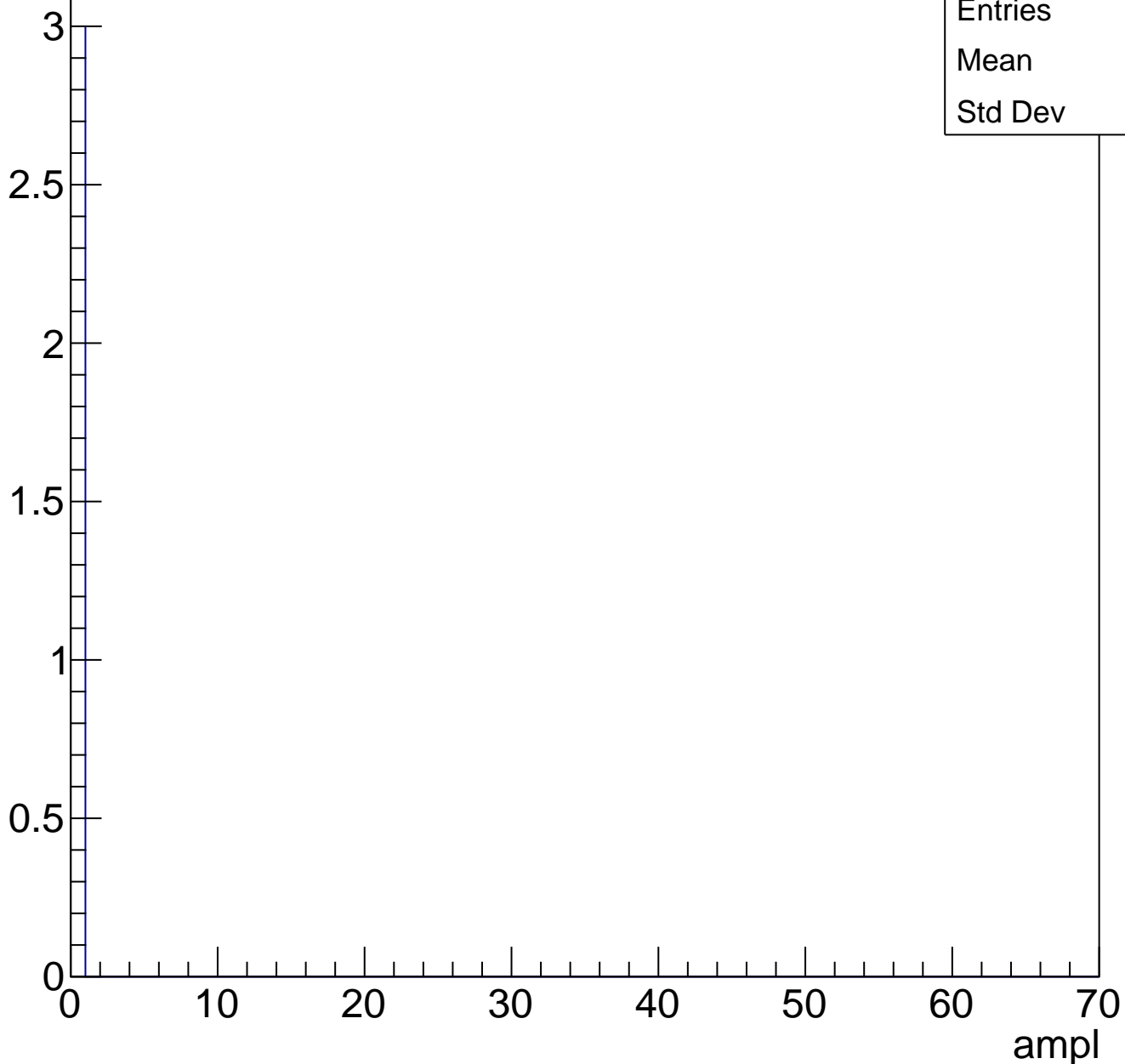
Entry



B0L001S, U14-ch79, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch80, adc0

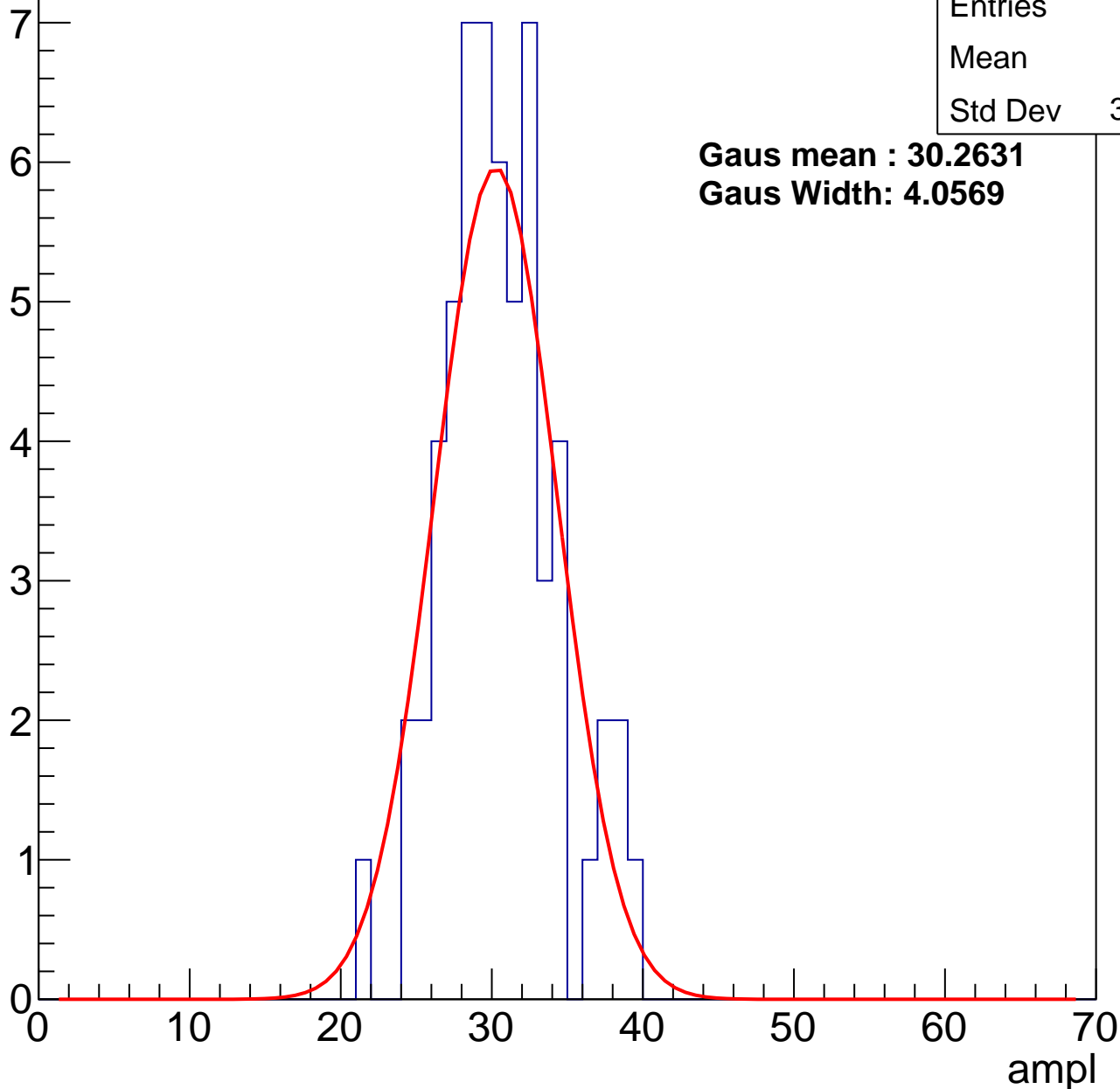
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	30.1
Std Dev	3.713

Gaus mean : 30.2631

Gaus Width: 4.0569



B0L001S, U14-ch80, adc1

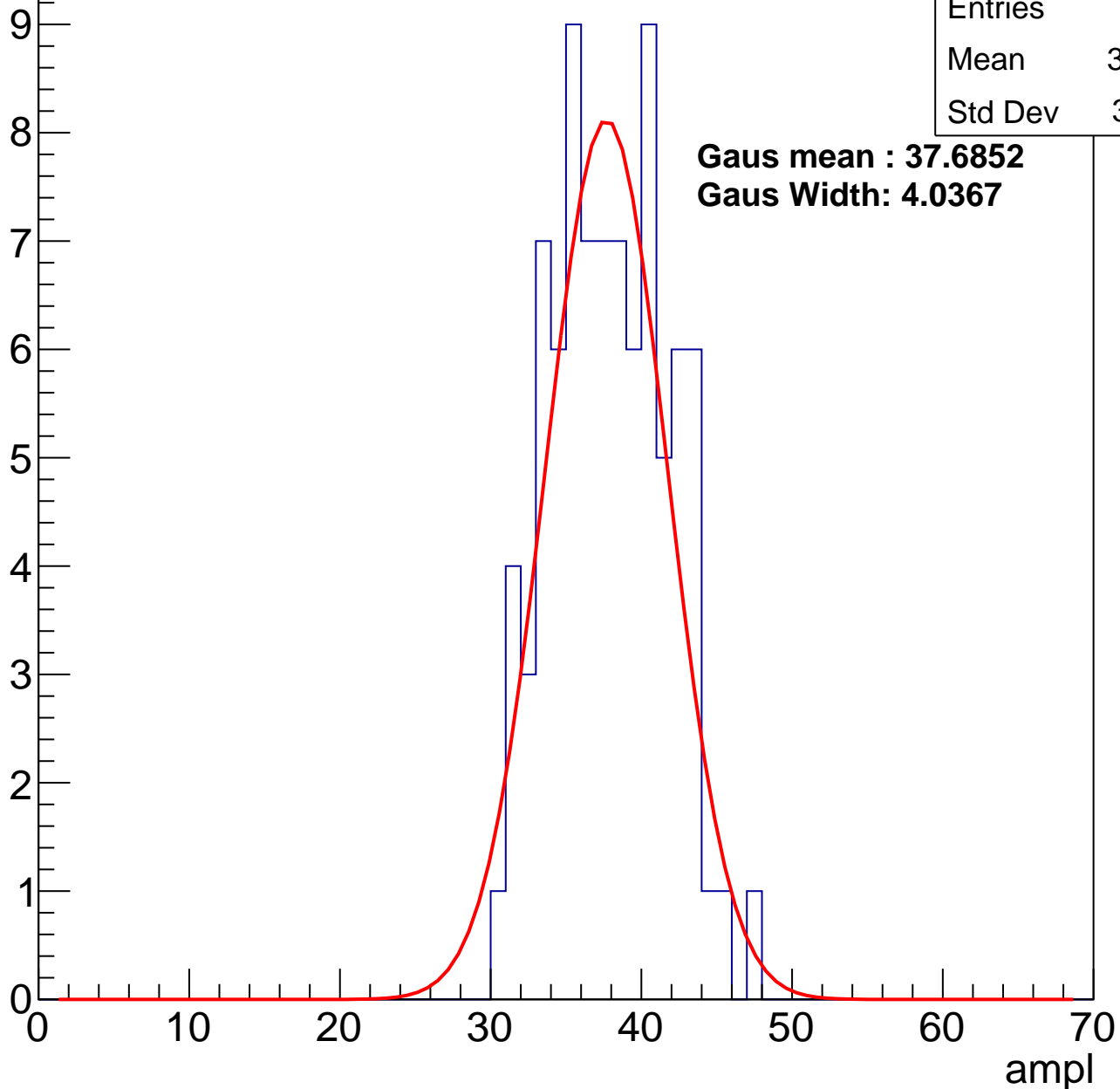
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	37.47
Std Dev	3.781

Gaus mean : 37.6852

Gaus Width: 4.0367



B0L001S, U14-ch80, adc2

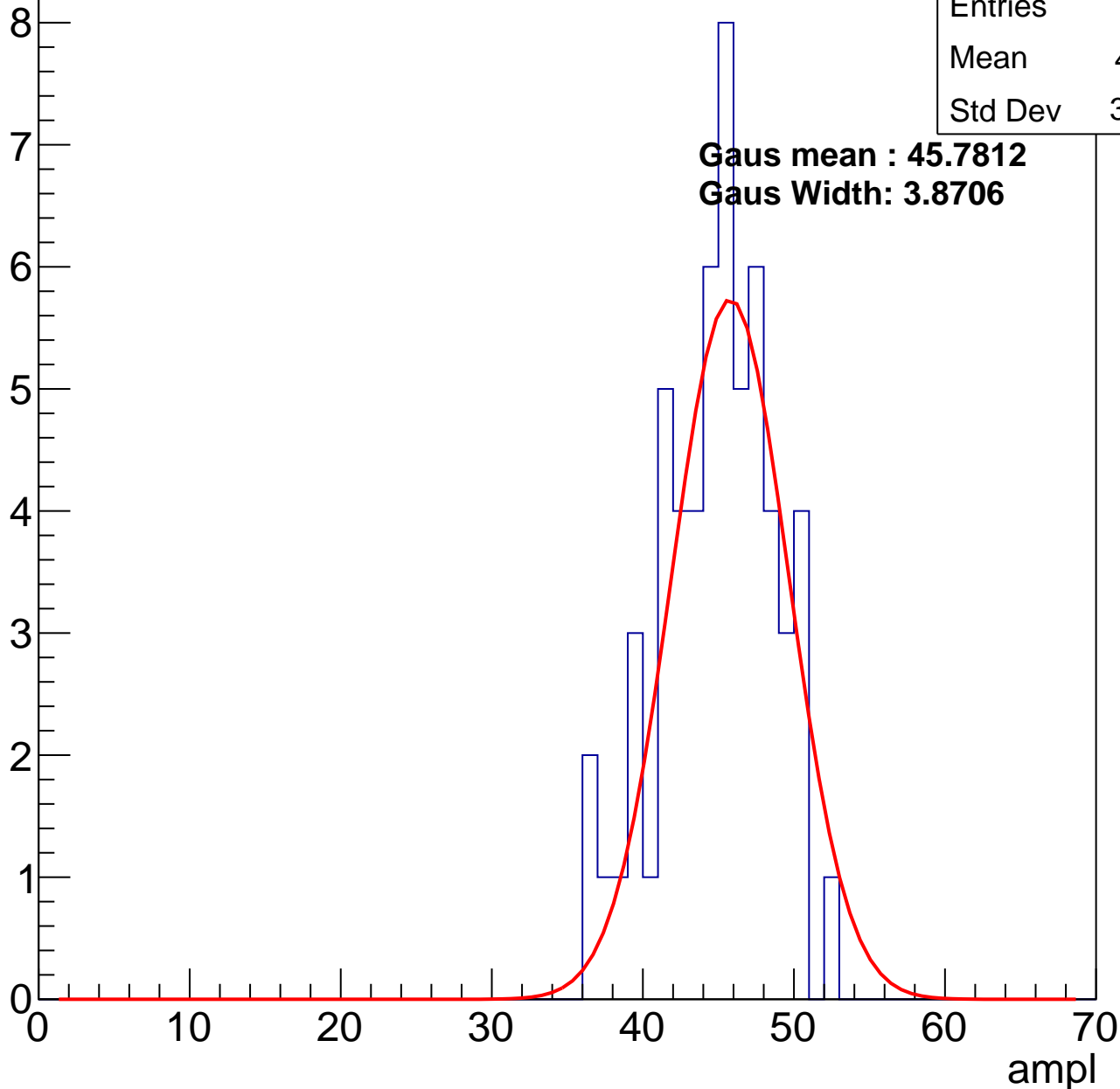
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.41
Std Dev	3.686

Gaus mean : 45.7812

Gaus Width: 3.8706

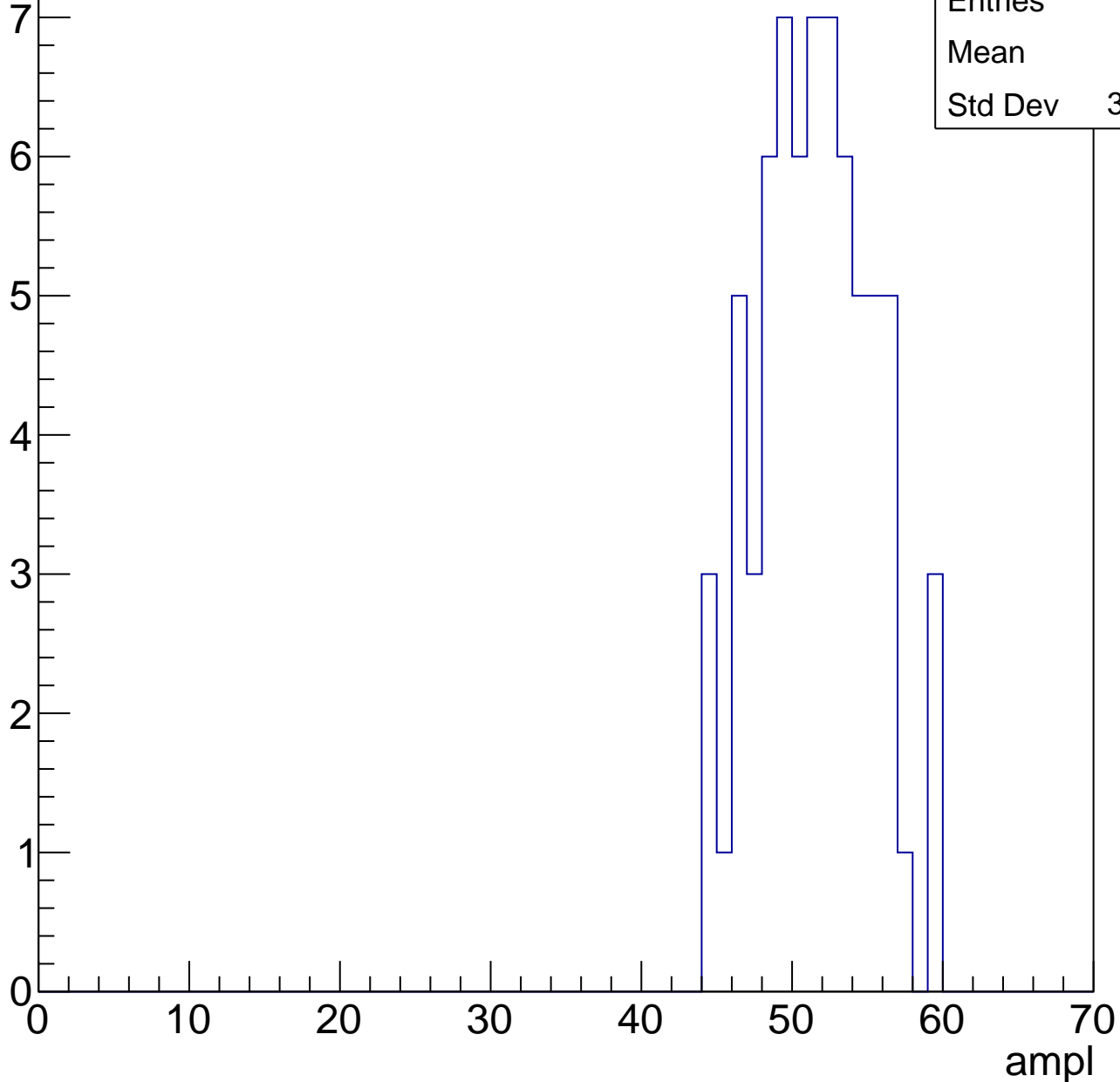


B0L001S, U14-ch80, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	51.1
Std Dev	3.688

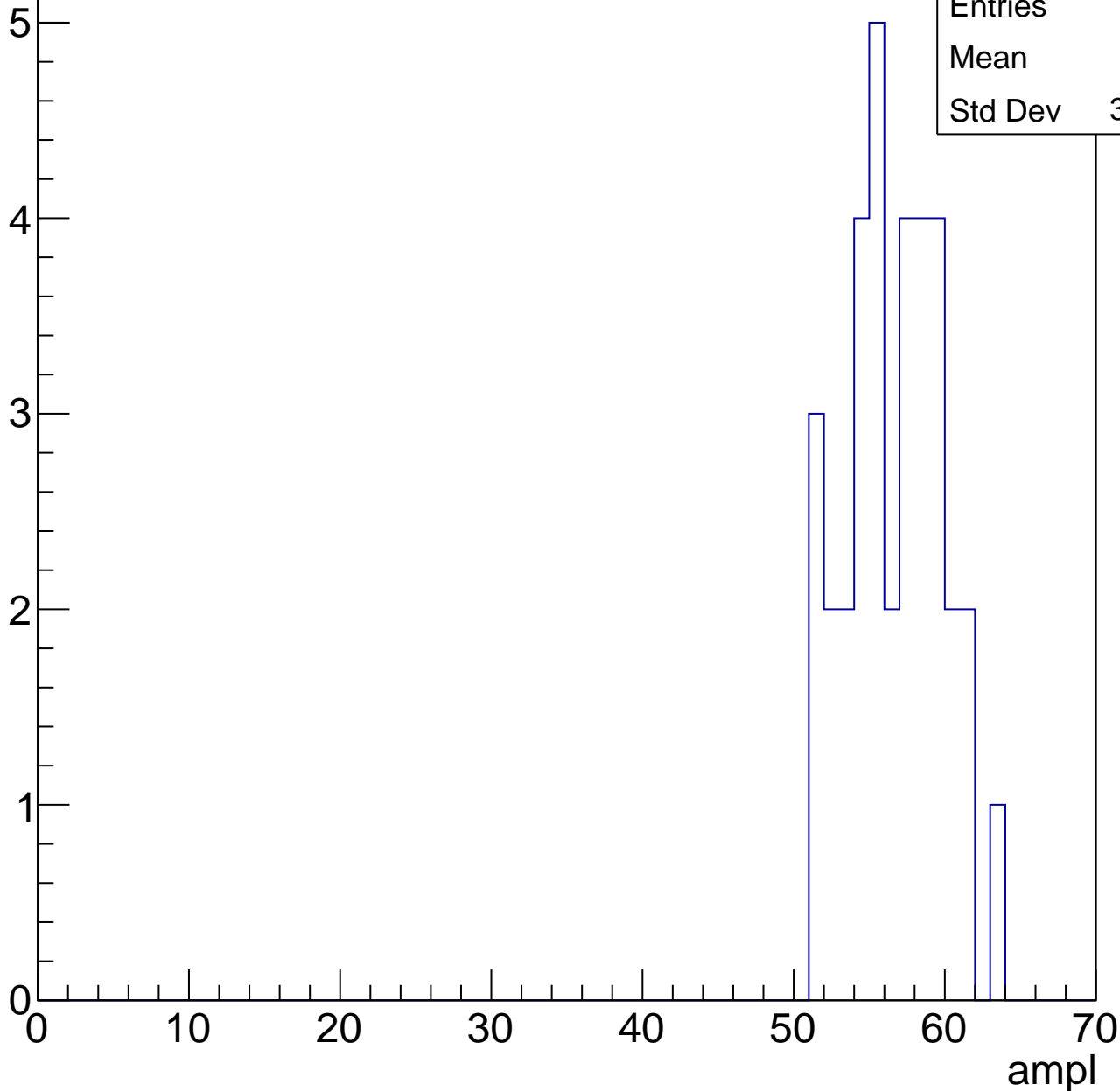


B0L001S, U14-ch80, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	56.2
Std Dev	3.078

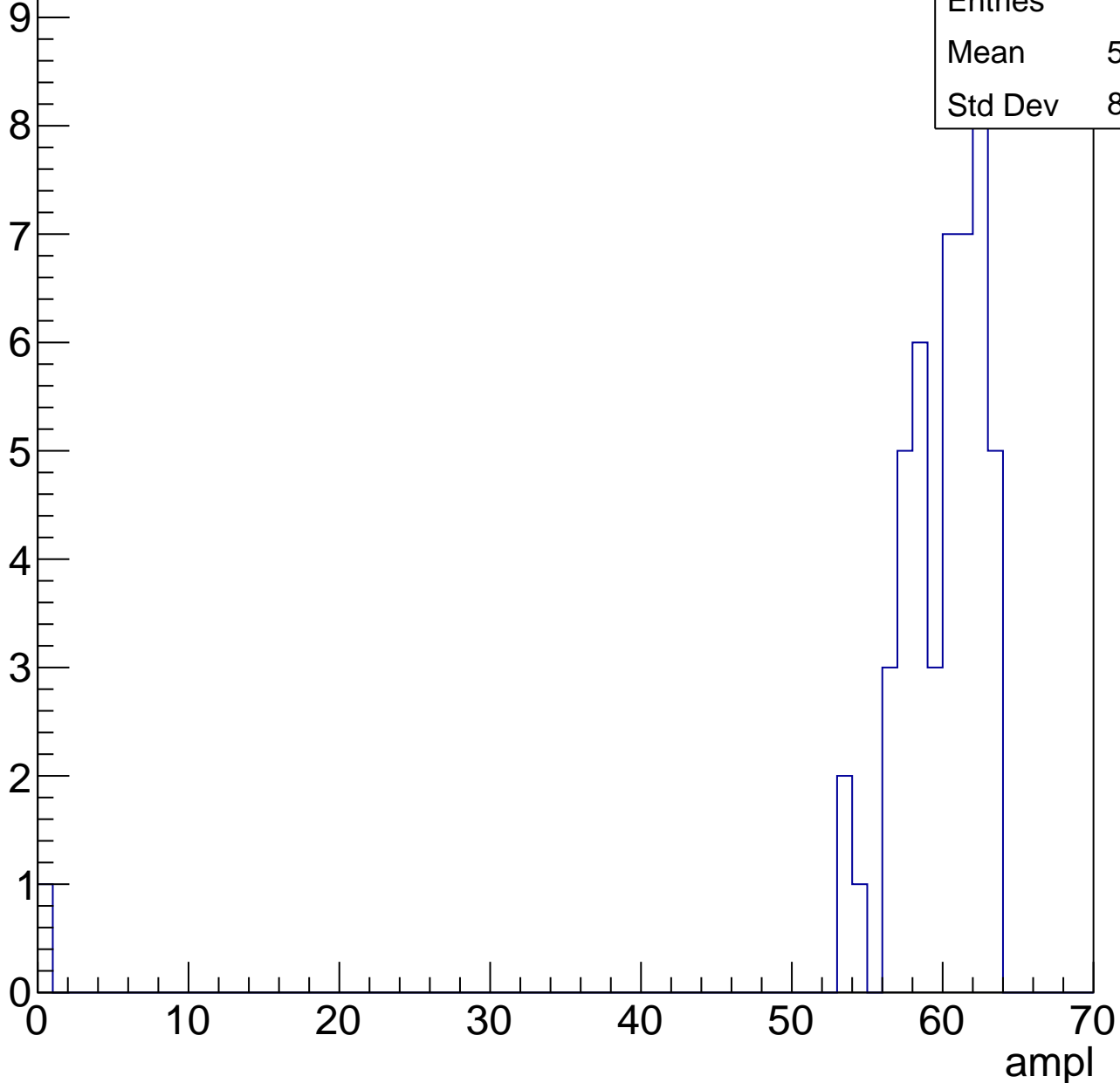


B0L001S, U14-ch80, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	58.33
Std Dev	8.812



B0L001S, U14-ch80, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch80, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch81, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	80
Mean	30.34
Std Dev	3.372

Gaus mean : 30.9182

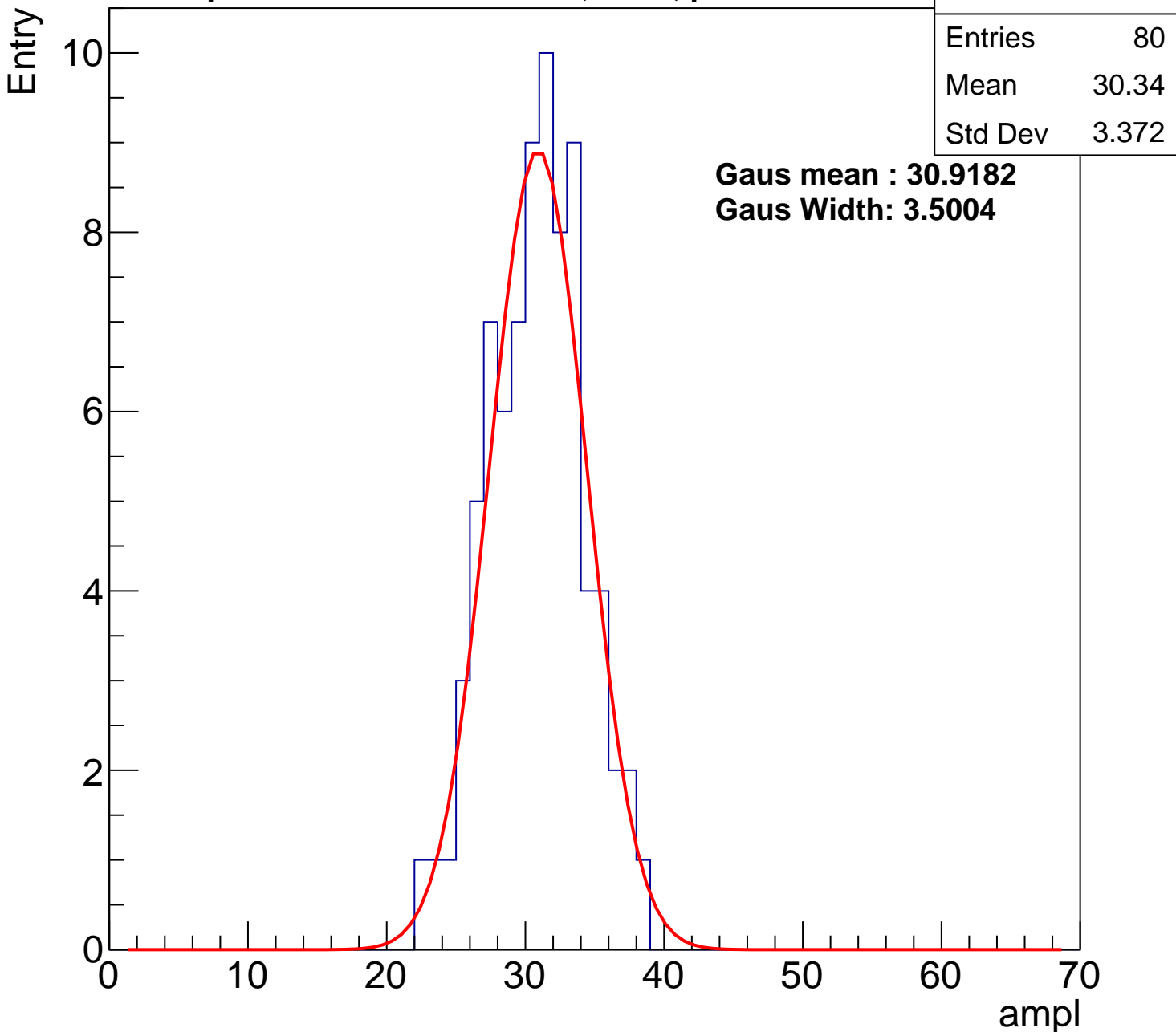
Gaus Width: 3.5004

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



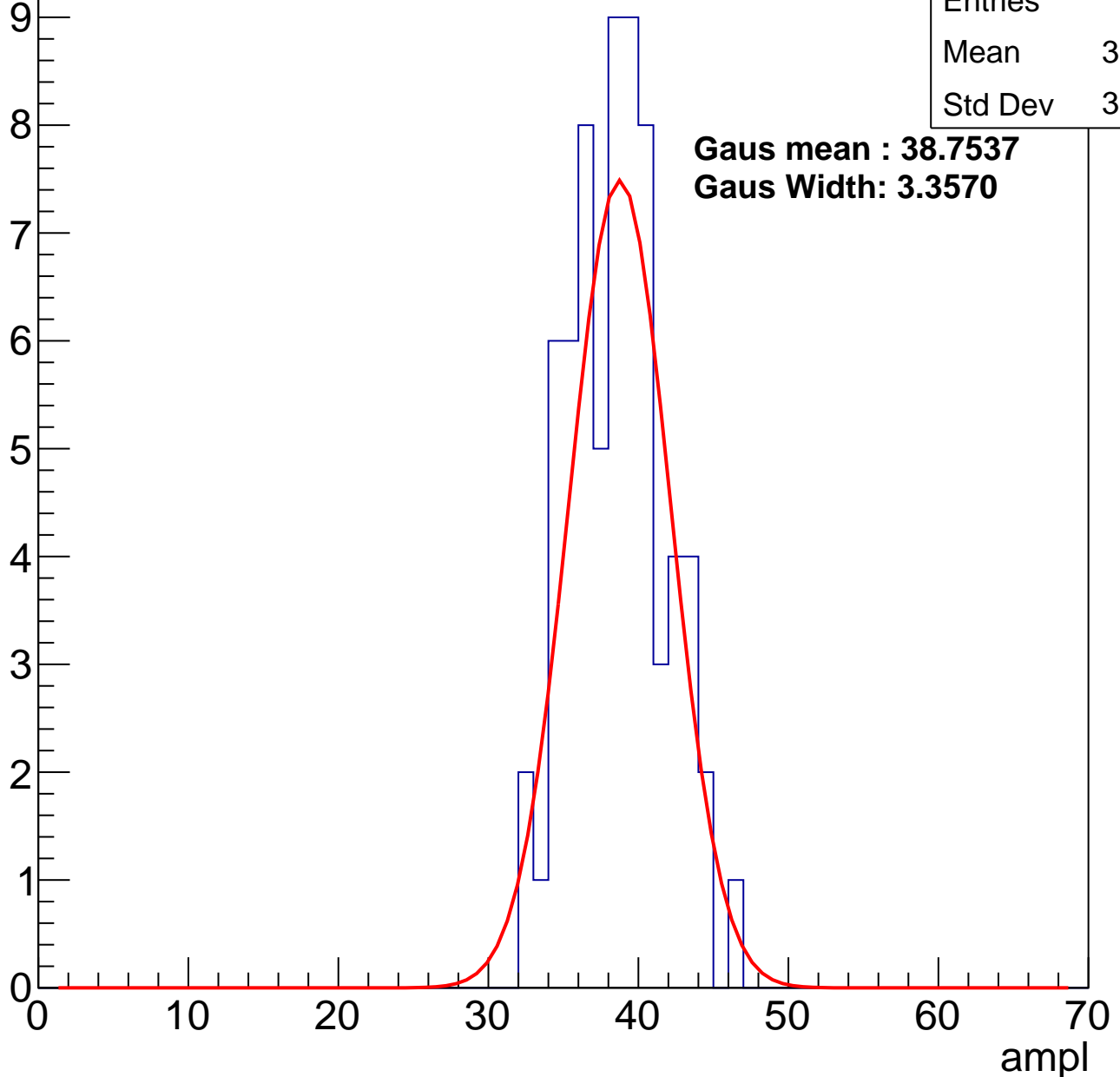
B0L001S, U14-ch81, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	38.15
Std Dev	3.093

Gaus mean : 38.7537
Gaus Width: 3.3570



B0L001S, U14-ch81, adc2

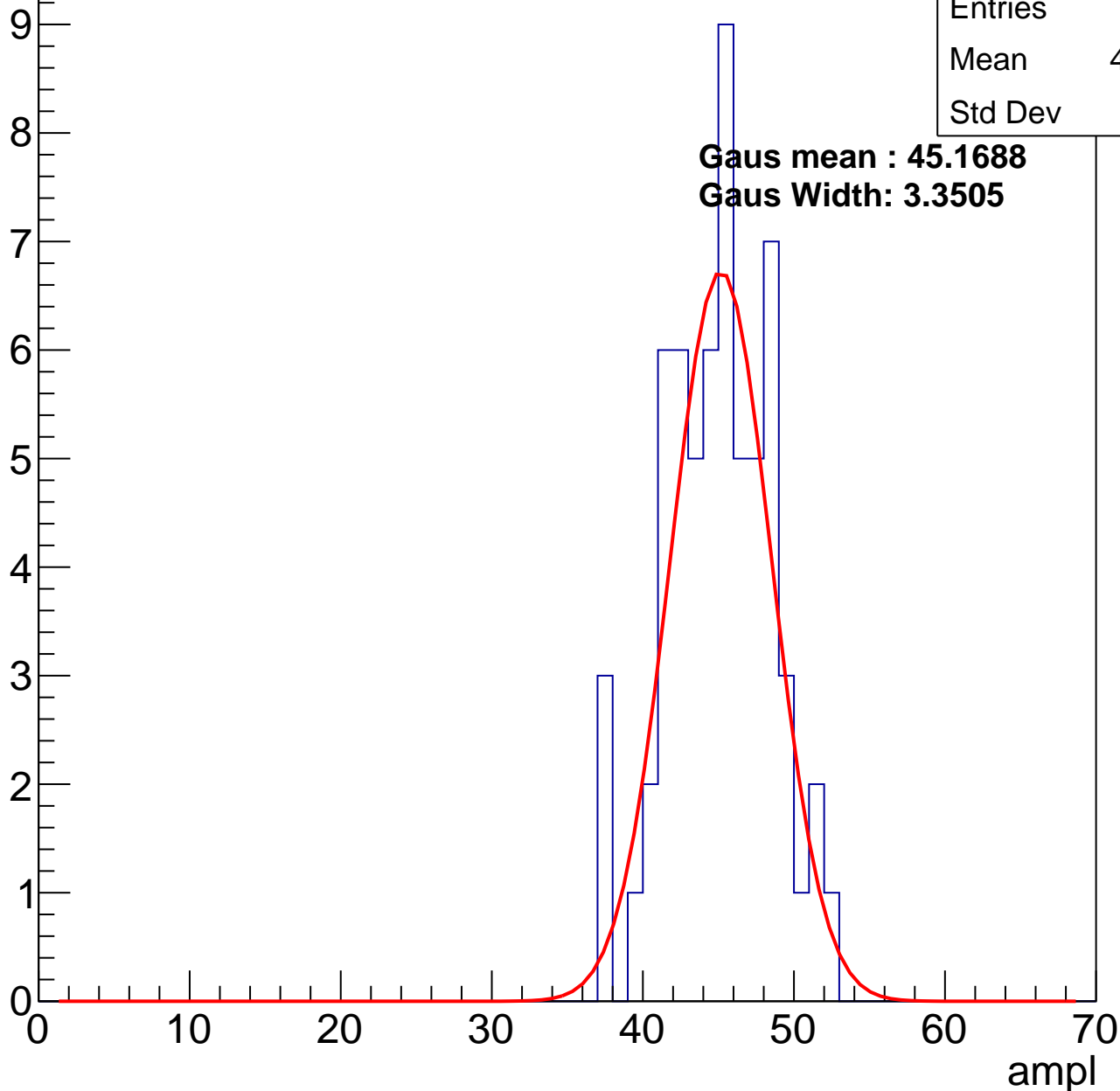
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.58
Std Dev	3.42

Gaus mean : 45.1688

Gaus Width: 3.3505

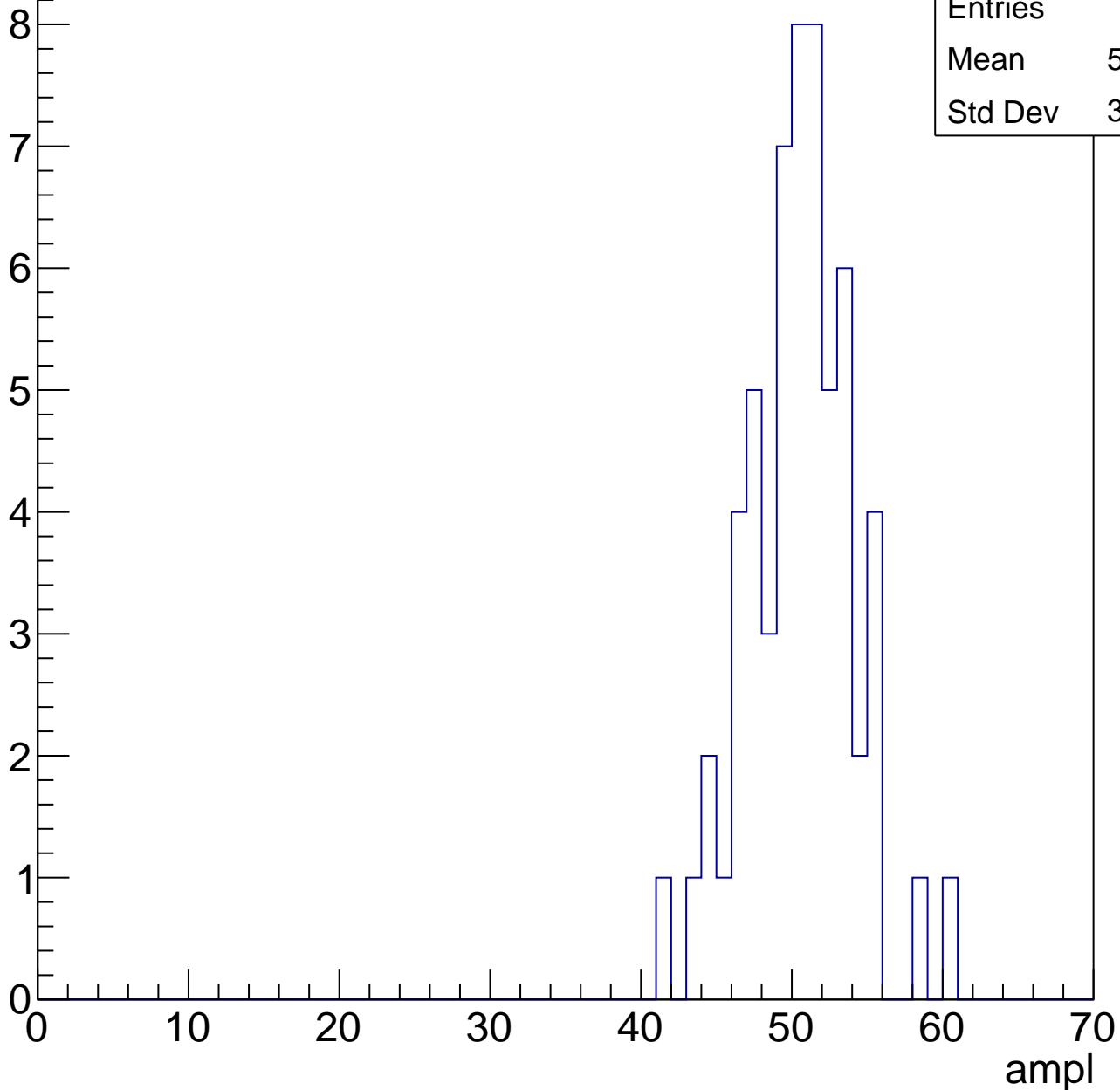


B0L001S, U14-ch81, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	50.08
Std Dev	3.524

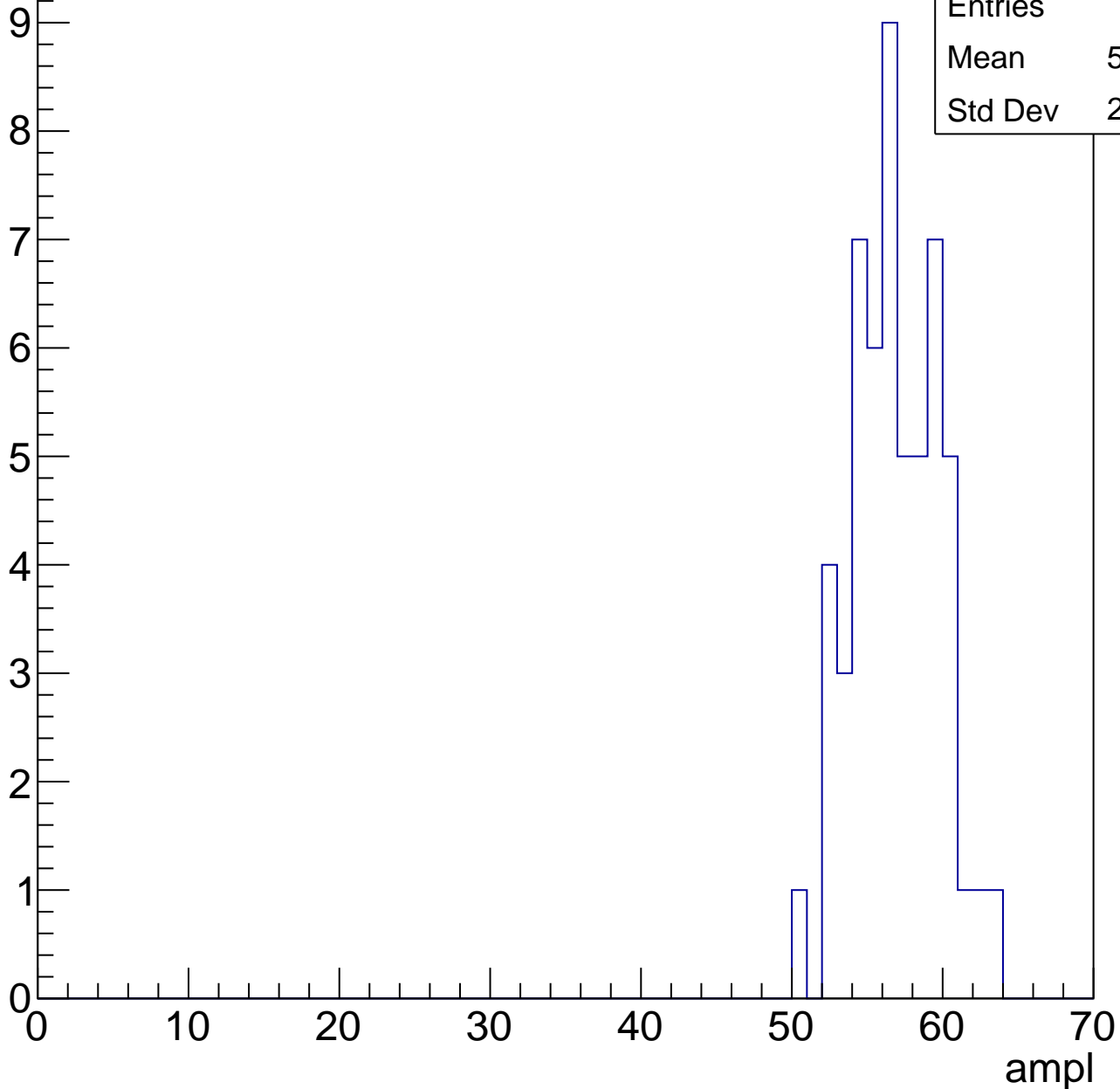


B0L001S, U14-ch81, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	56.42
Std Dev	2.794

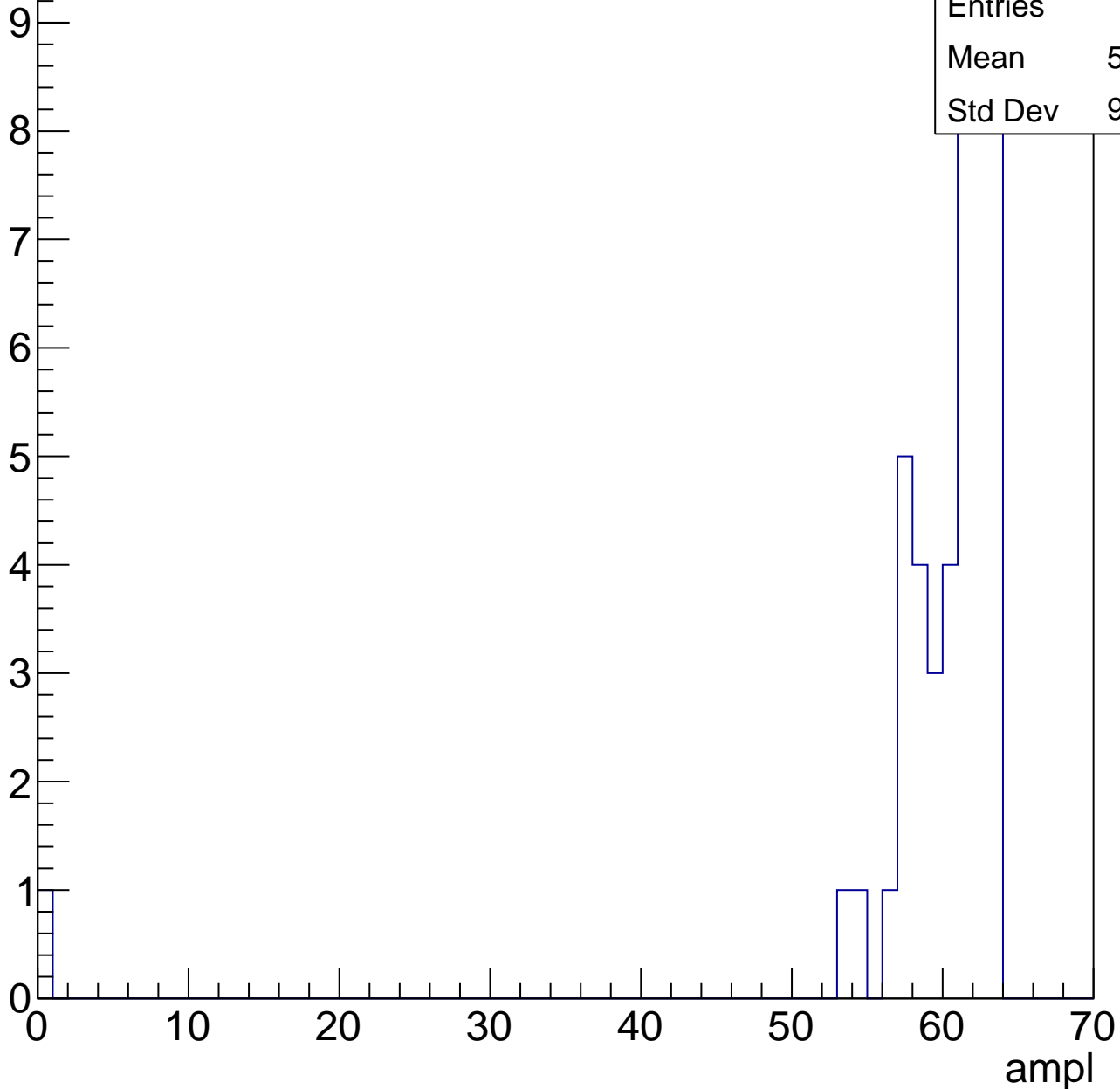


B0L001S, U14-ch81, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	58.82
Std Dev	9.212



B0L001S, U14-ch81, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch81, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch82, adc0

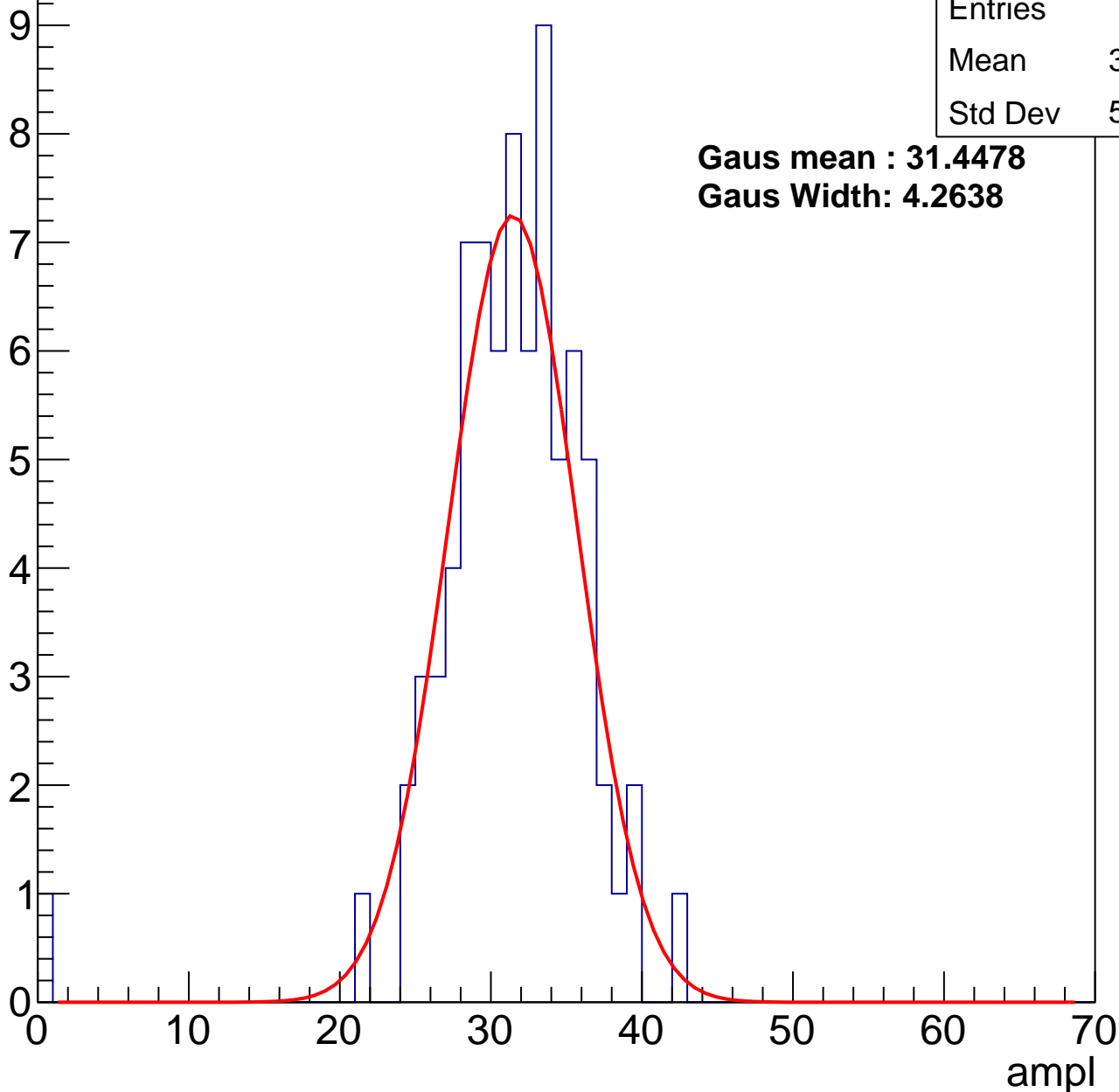
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.86
Std Dev	5.252

Gaus mean : 31.4478

Gaus Width: 4.2638



B0L001S, U14-ch82, adc1

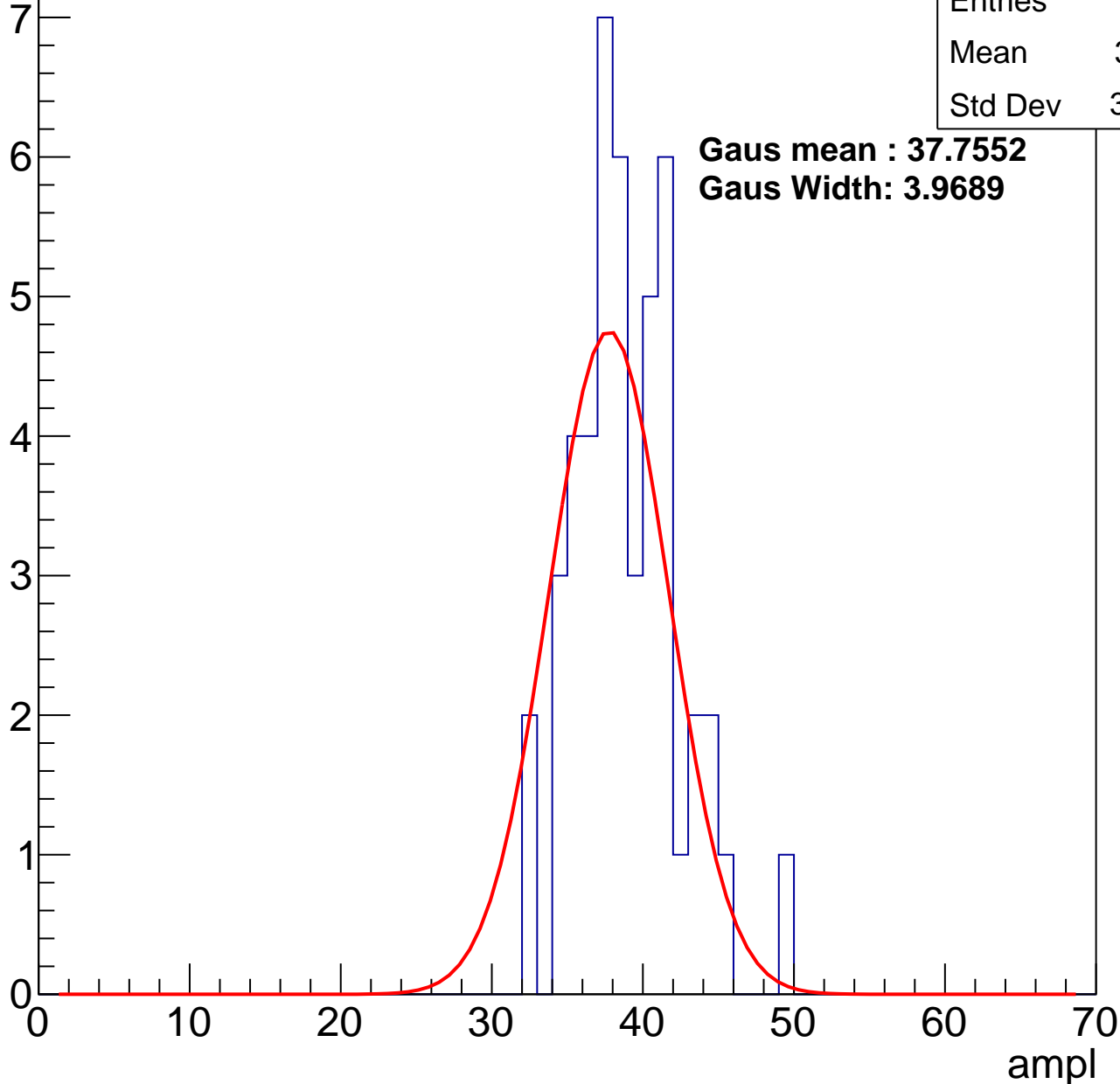
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	47
Mean	38.51
Std Dev	3.426

Gaus mean : 37.7552

Gaus Width: 3.9689



B0L001S, U14-ch82, adc2

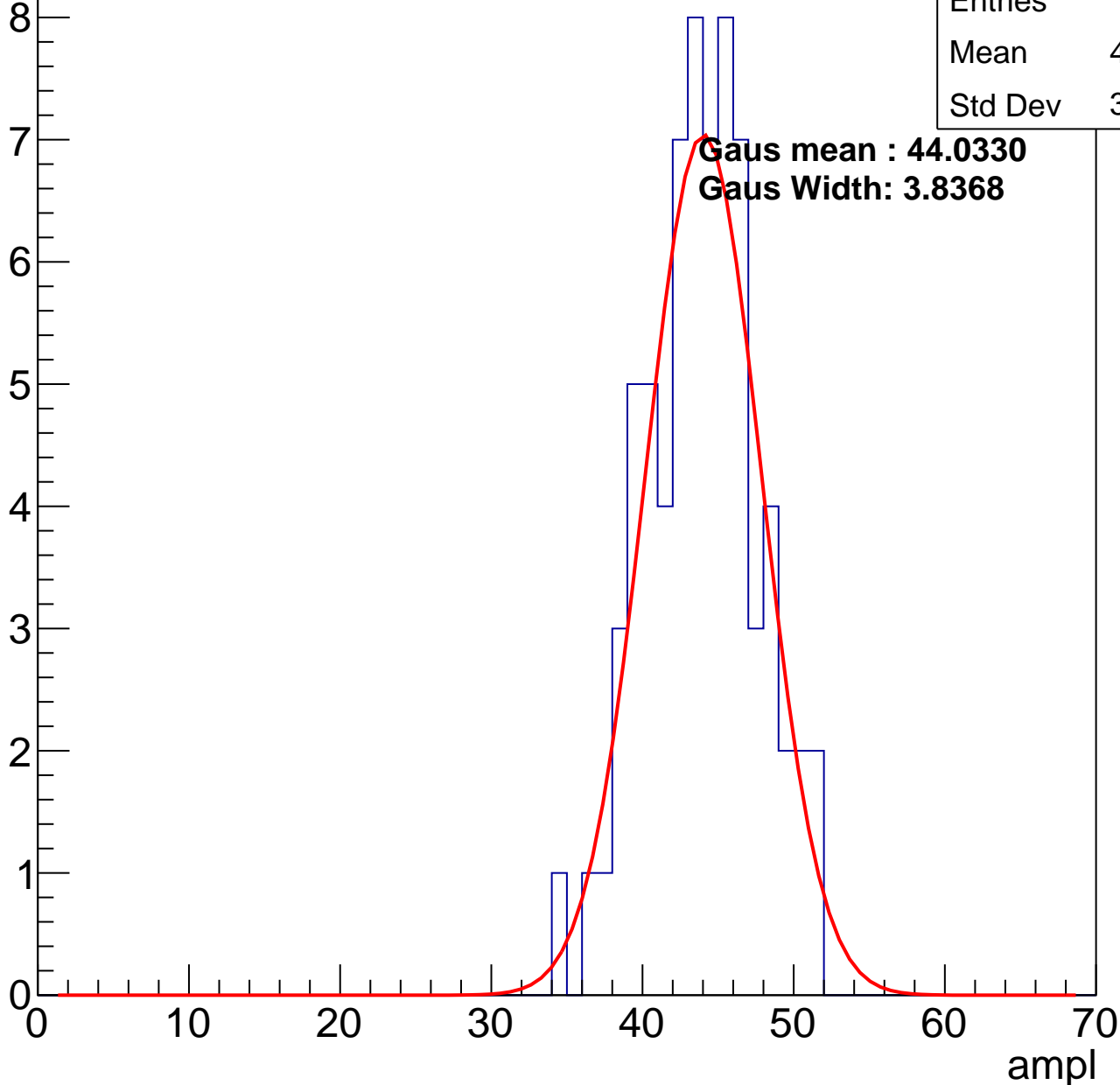
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	43.44
Std Dev	3.632

Gaus mean : 44.0330

Gaus Width: 3.8368

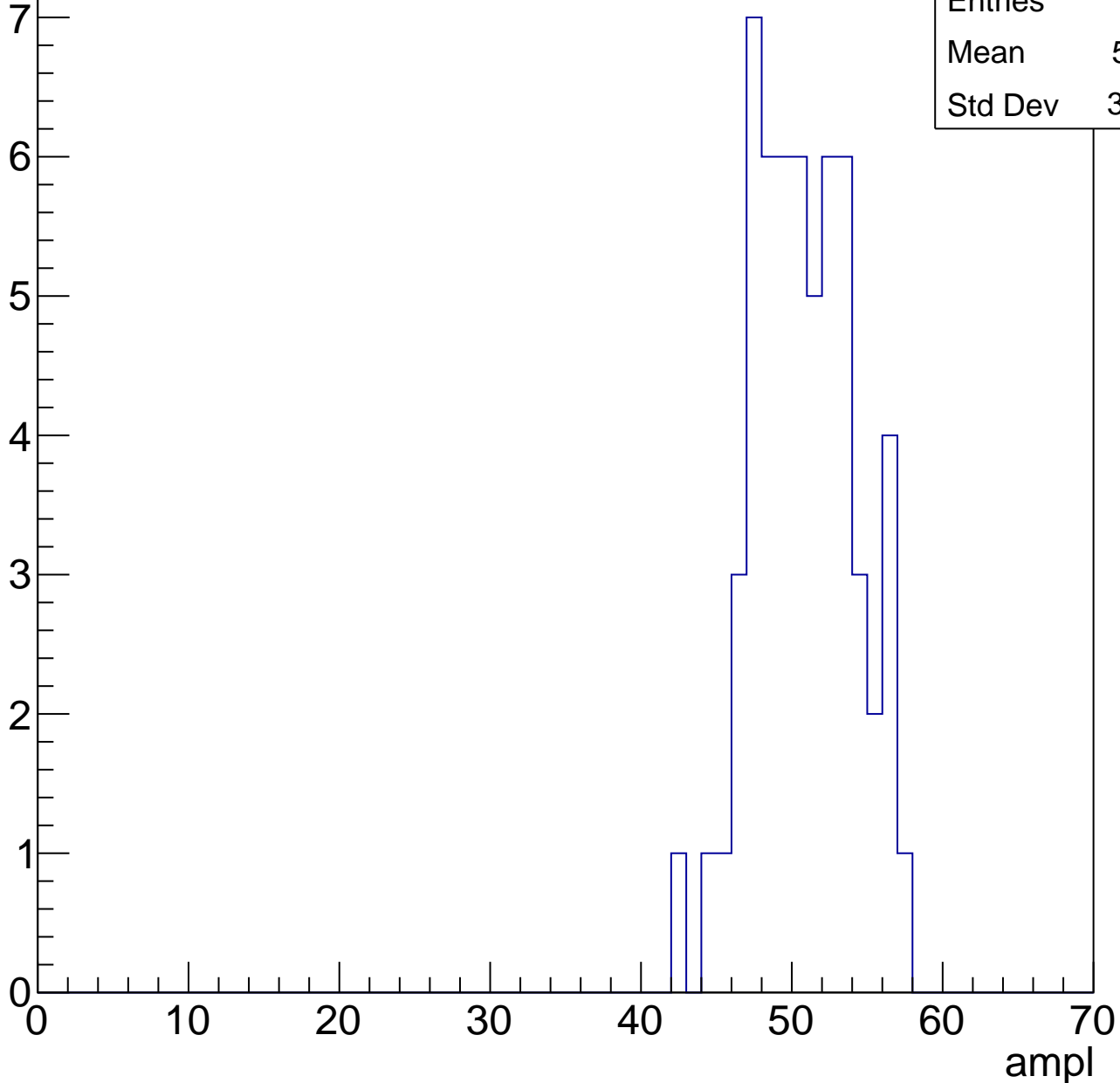


B0L001S, U14-ch82, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

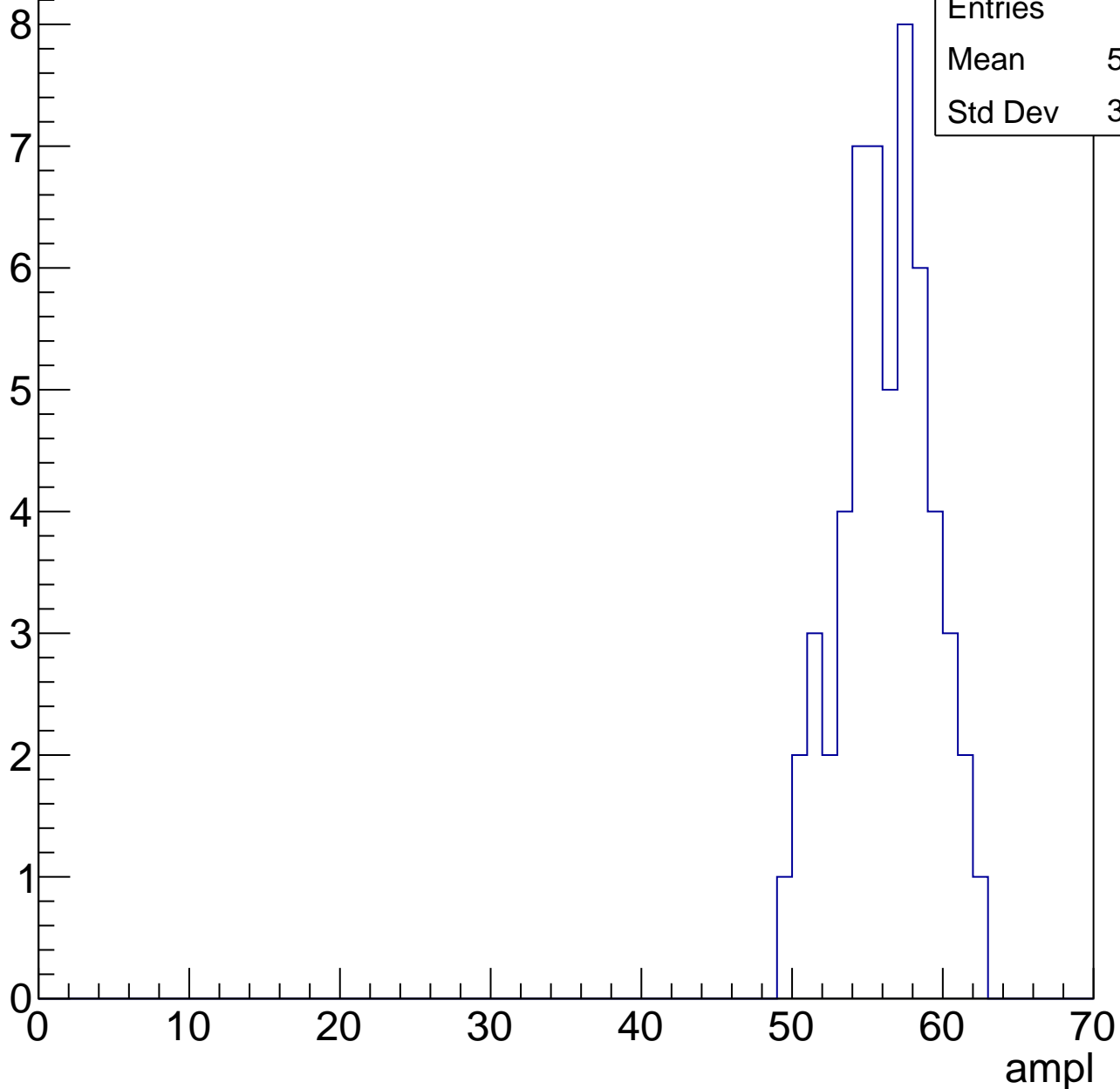
Entries	58
Mean	50.31
Std Dev	3.307



B0L001S, U14-ch82, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

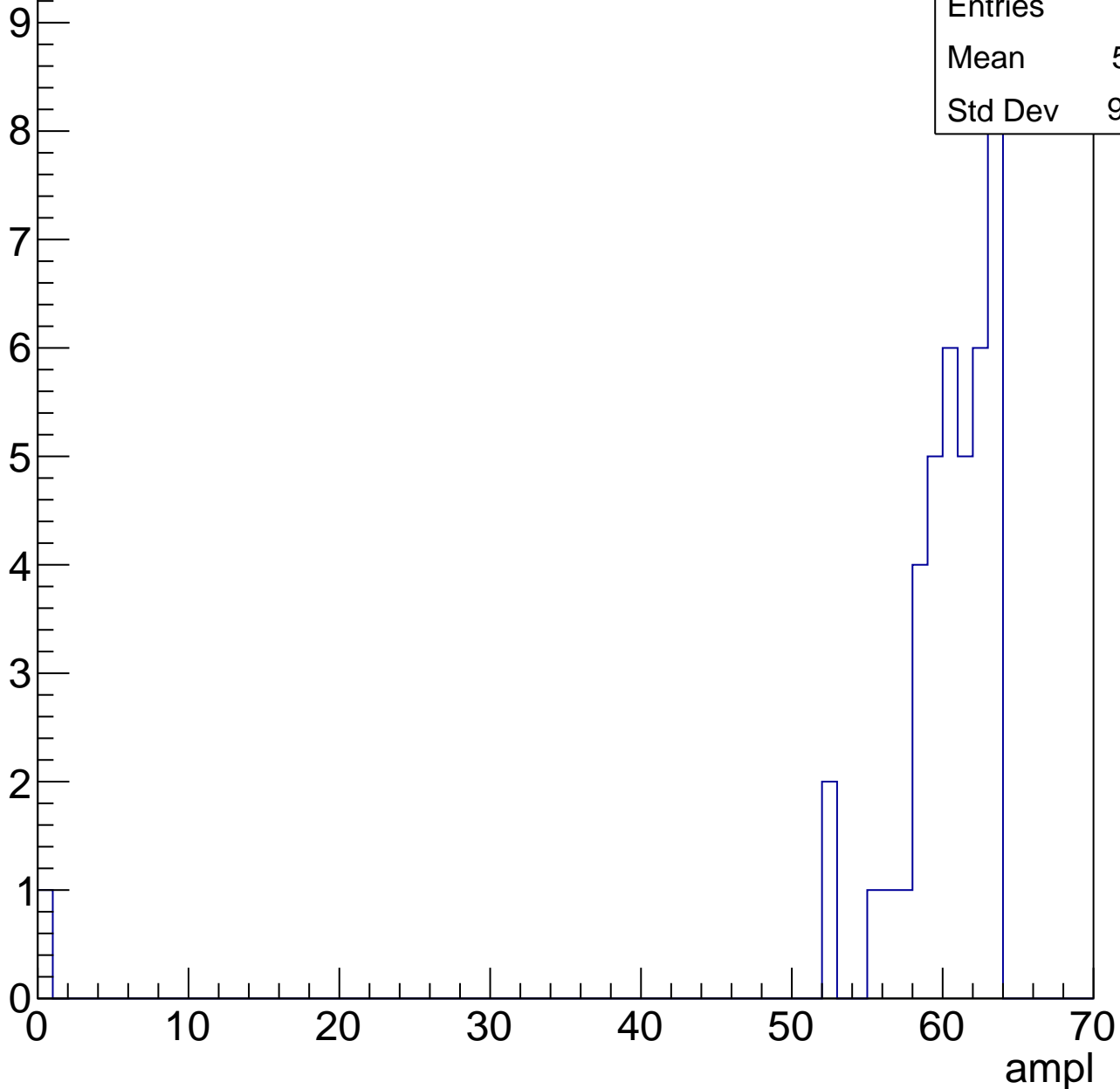


B0L001S, U14-ch82, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

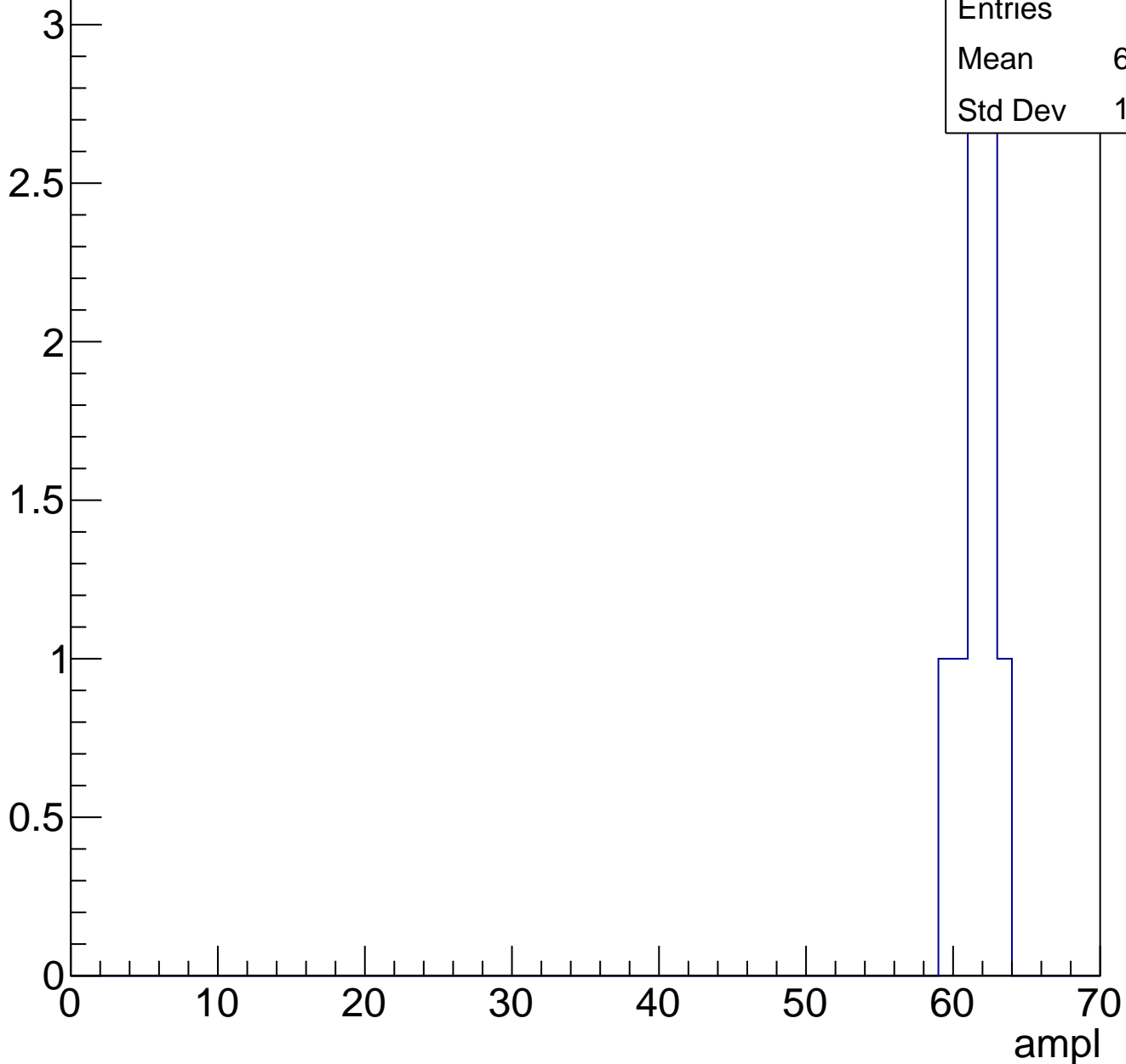
Entries	41
Mean	58.61
Std Dev	9.665



B0L001S, U14-ch82, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	9
Mean	61.22
Std Dev	1.133

B0L001S, U14-ch82, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch83, adc0

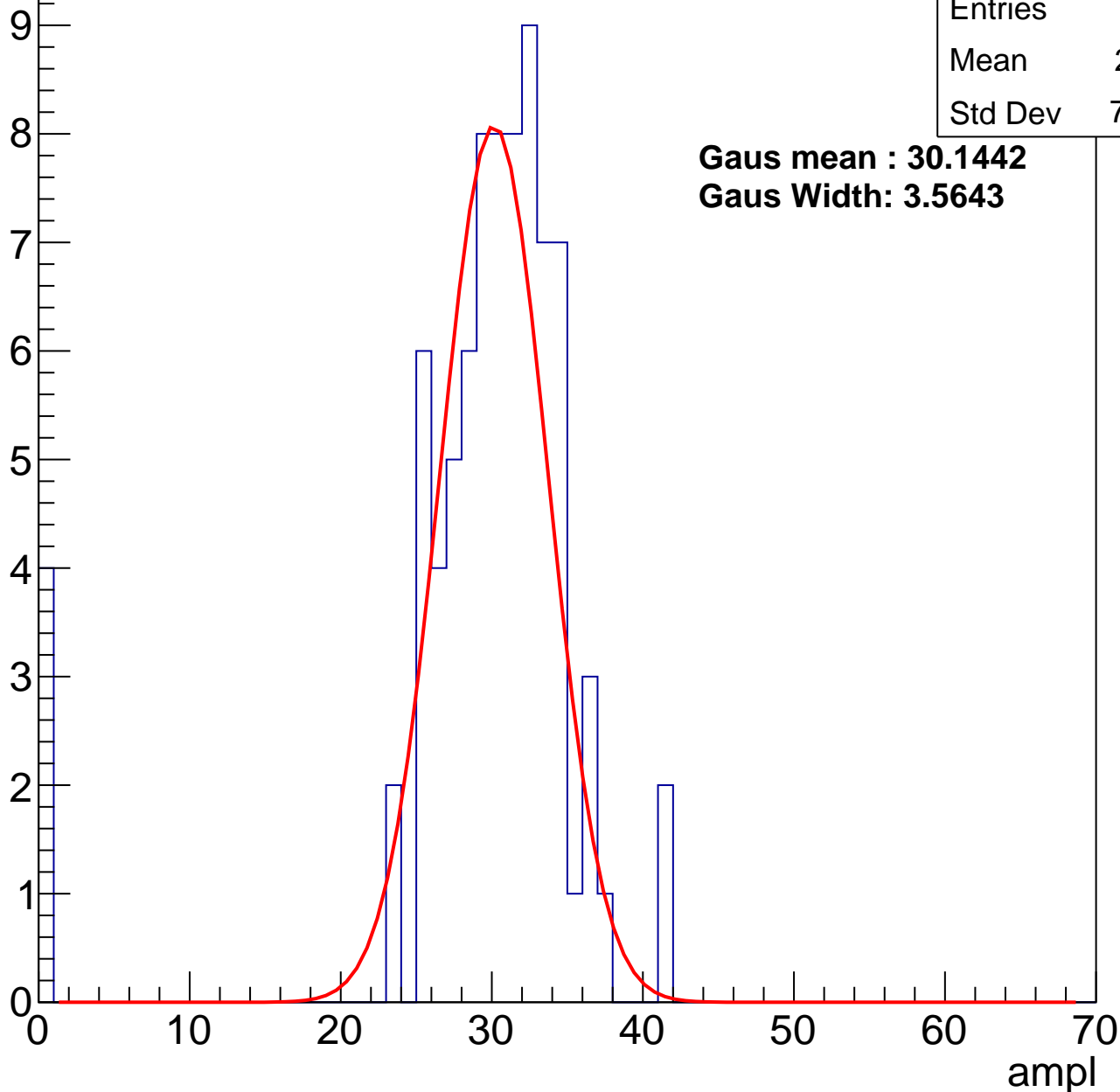
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	28.91
Std Dev	7.484

Gaus mean : 30.1442

Gaus Width: 3.5643



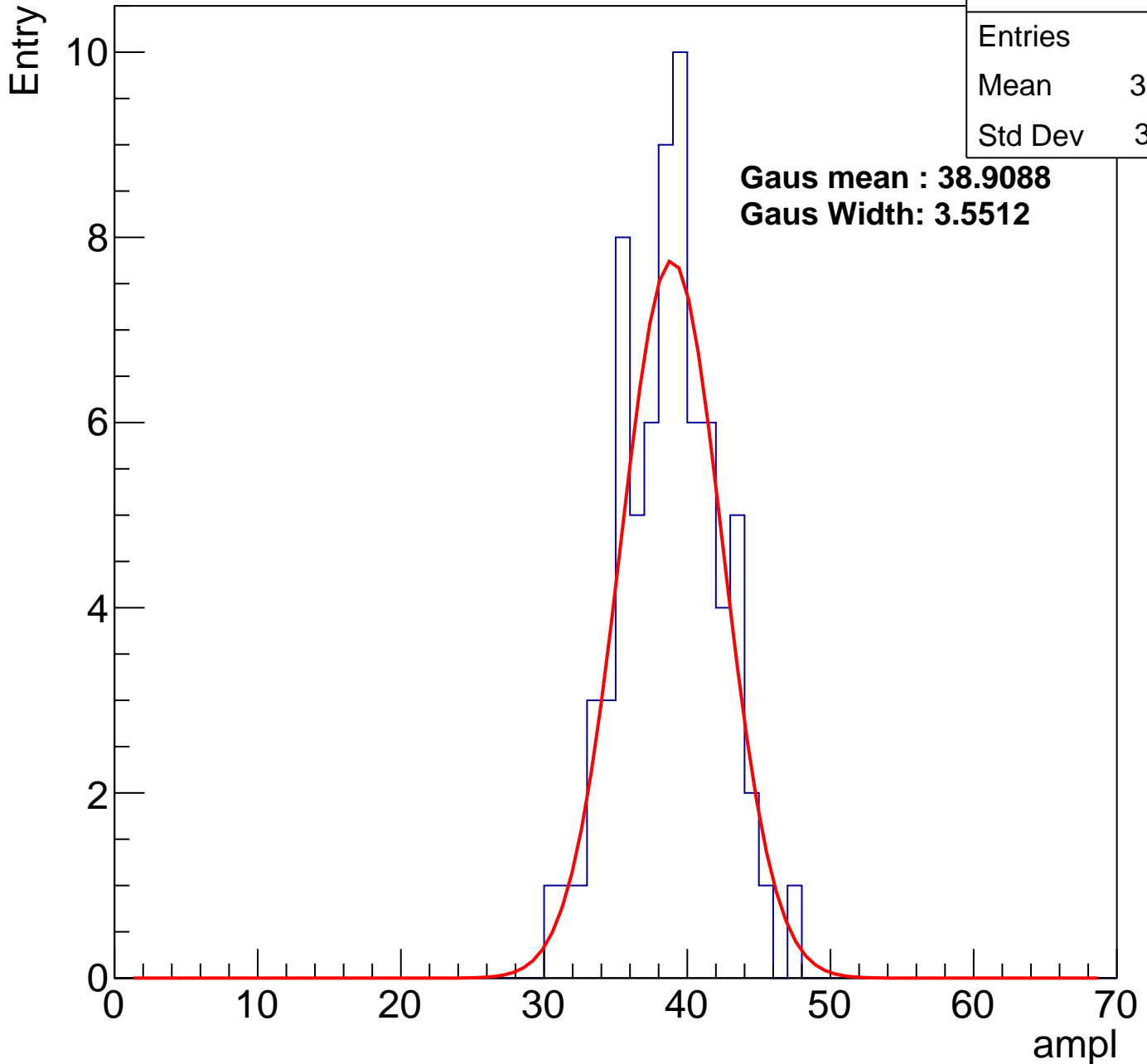
B0L001S, U14-ch83, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	38.29
Std Dev	3.421

Gaus mean : 38.9088

Gaus Width: 3.5512



B0L001S, U14-ch83, adc2

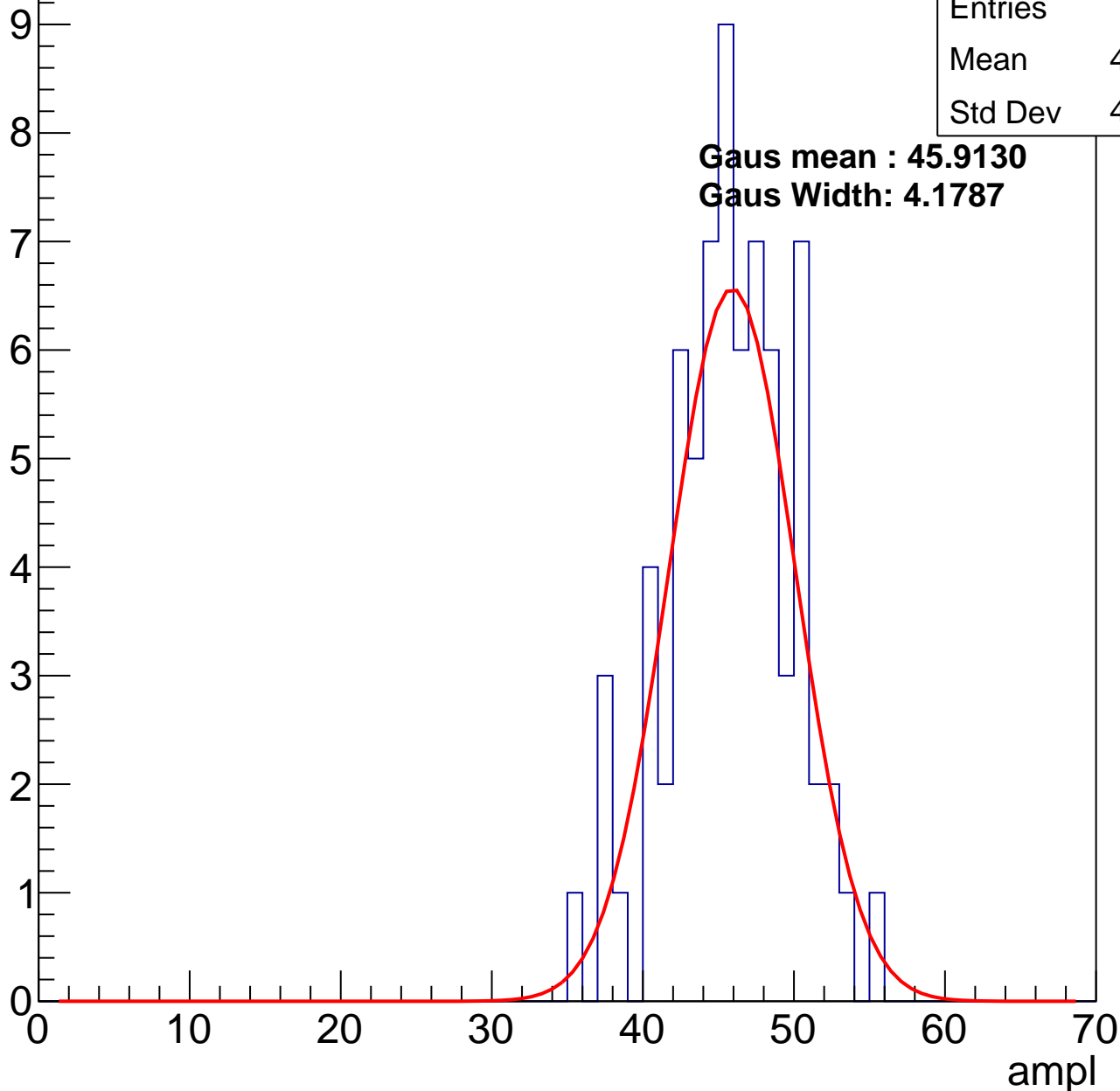
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	45.34
Std Dev	4.038

Gaus mean : 45.9130

Gaus Width: 4.1787

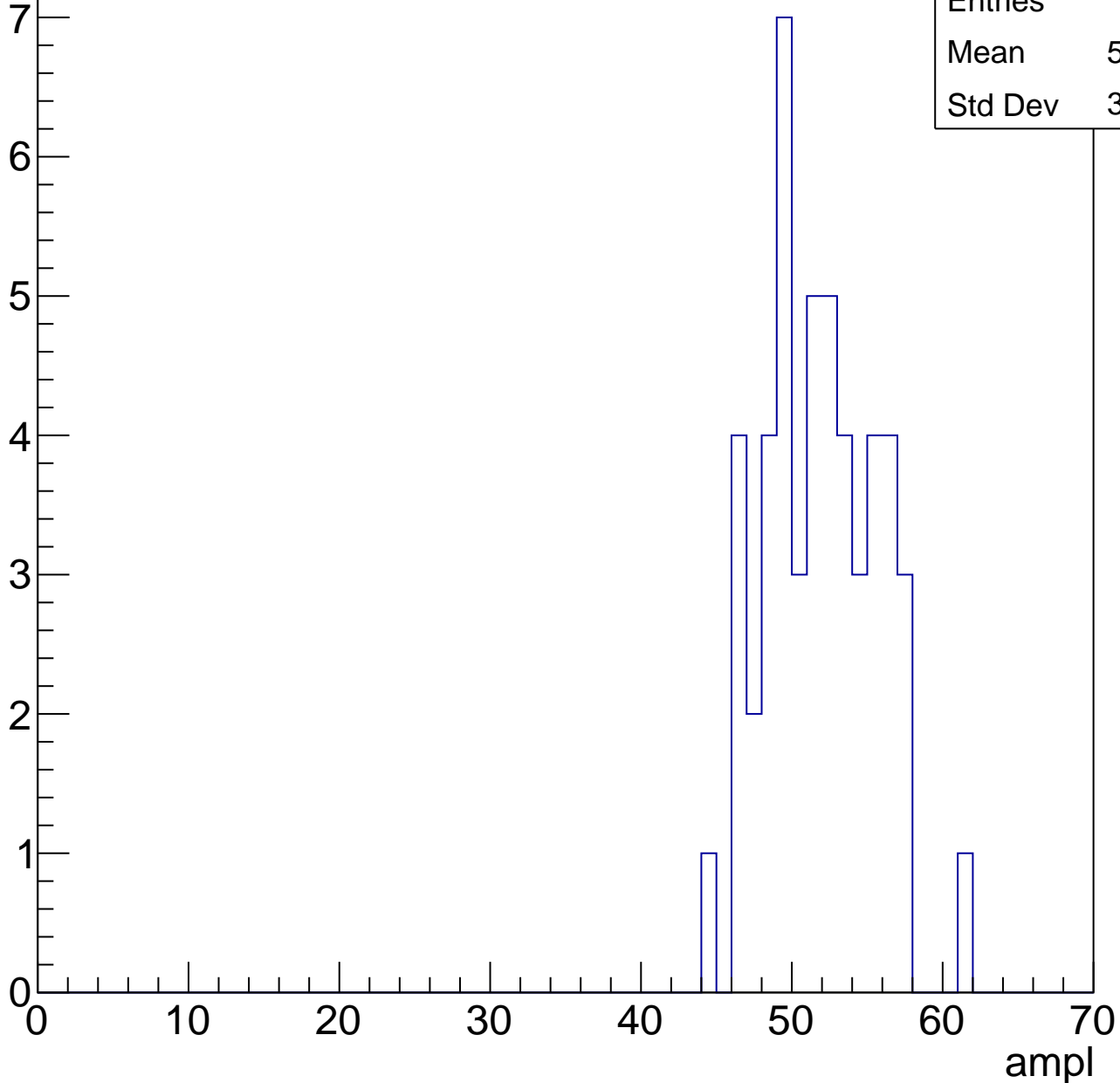


B0L001S, U14-ch83, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	51.44
Std Dev	3.629

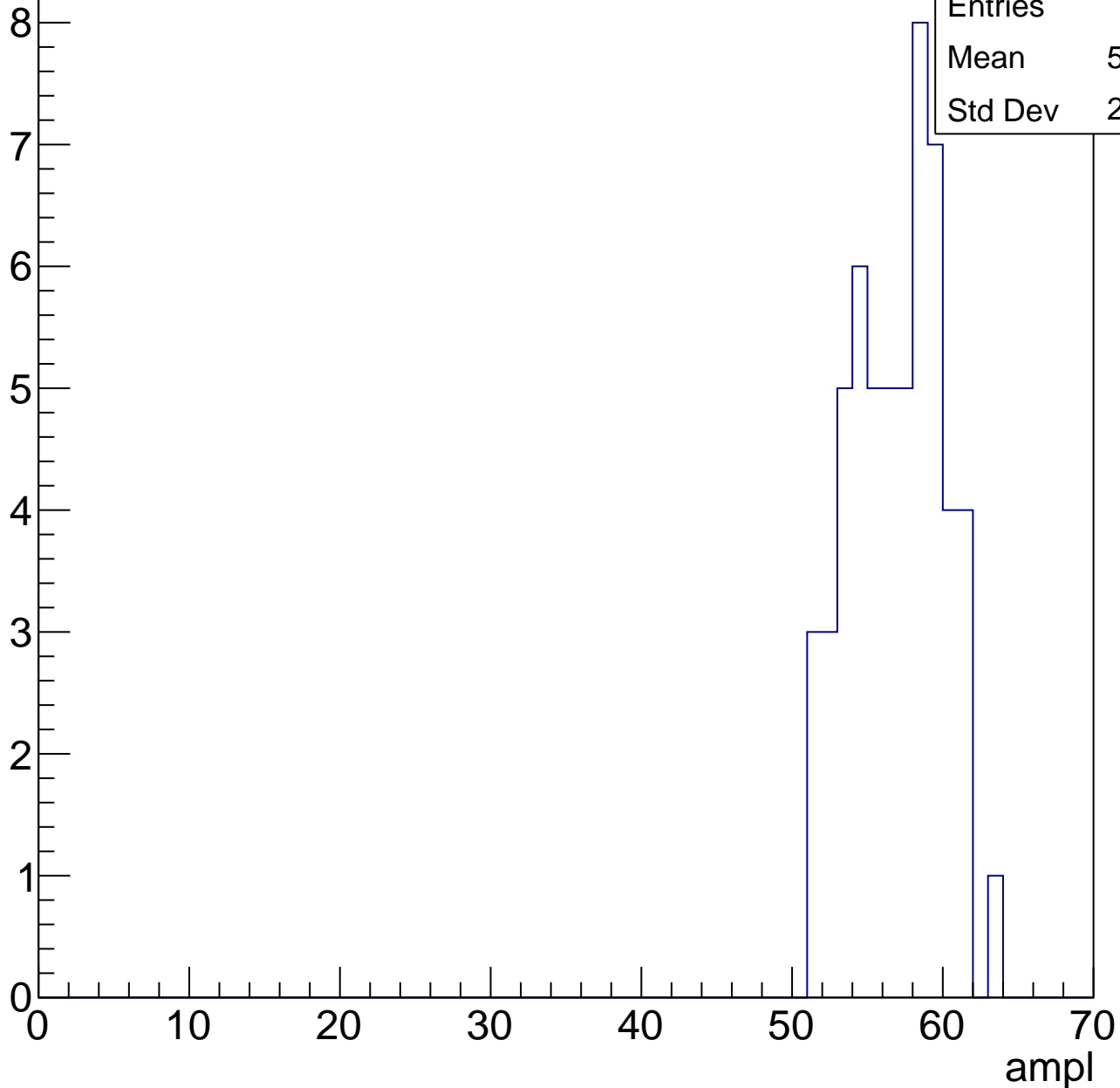


B0L001S, U14-ch83, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	56.46
Std Dev	2.982

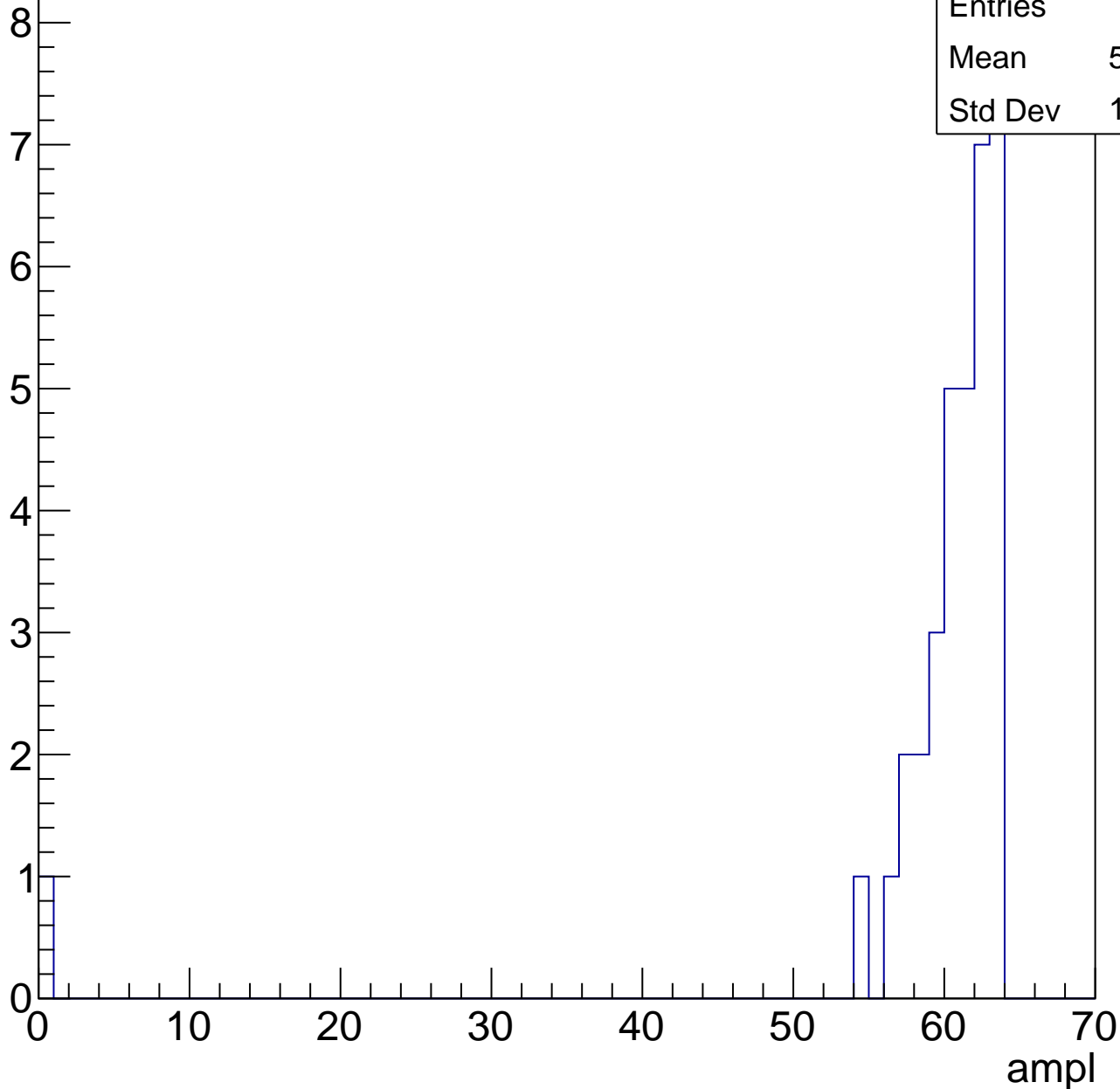


B0L001S, U14-ch83, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

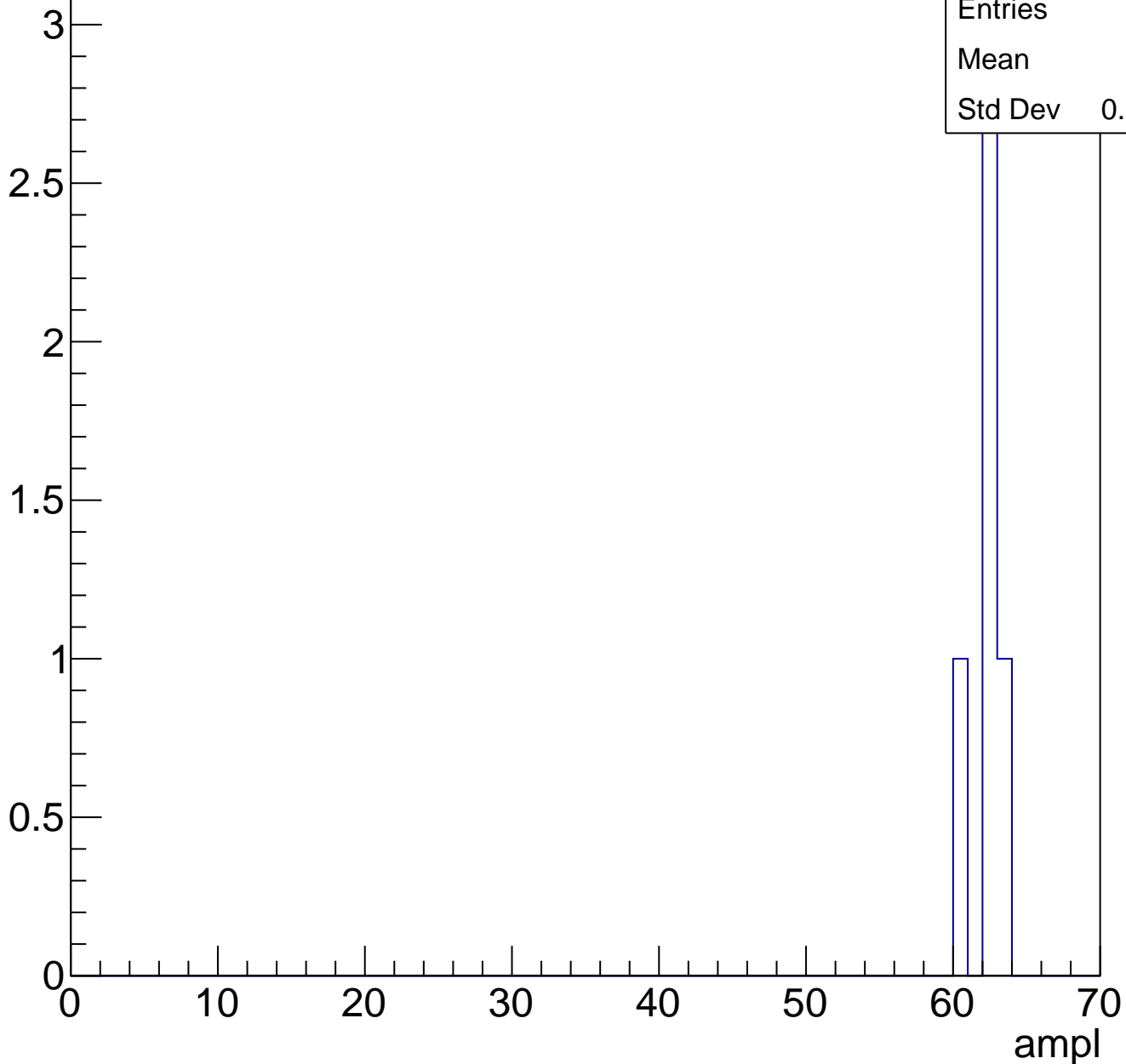
Entries	35
Mean	58.86
Std Dev	10.34



B0L001S, U14-ch83, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch83, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch84, adc0

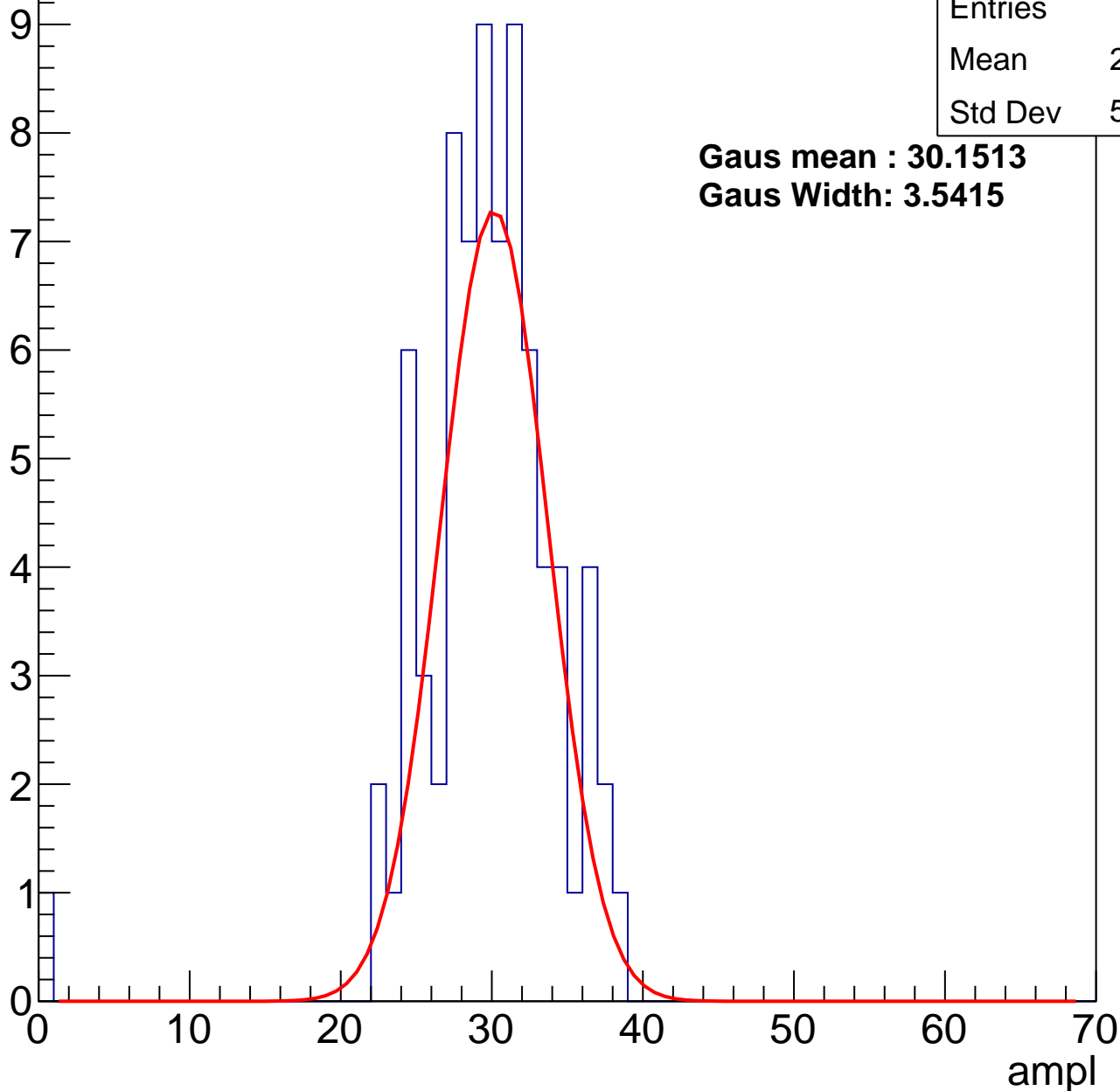
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	29.23
Std Dev	5.004

Gaus mean : 30.1513

Gaus Width: 3.5415



B0L001S, U14-ch84, adc1

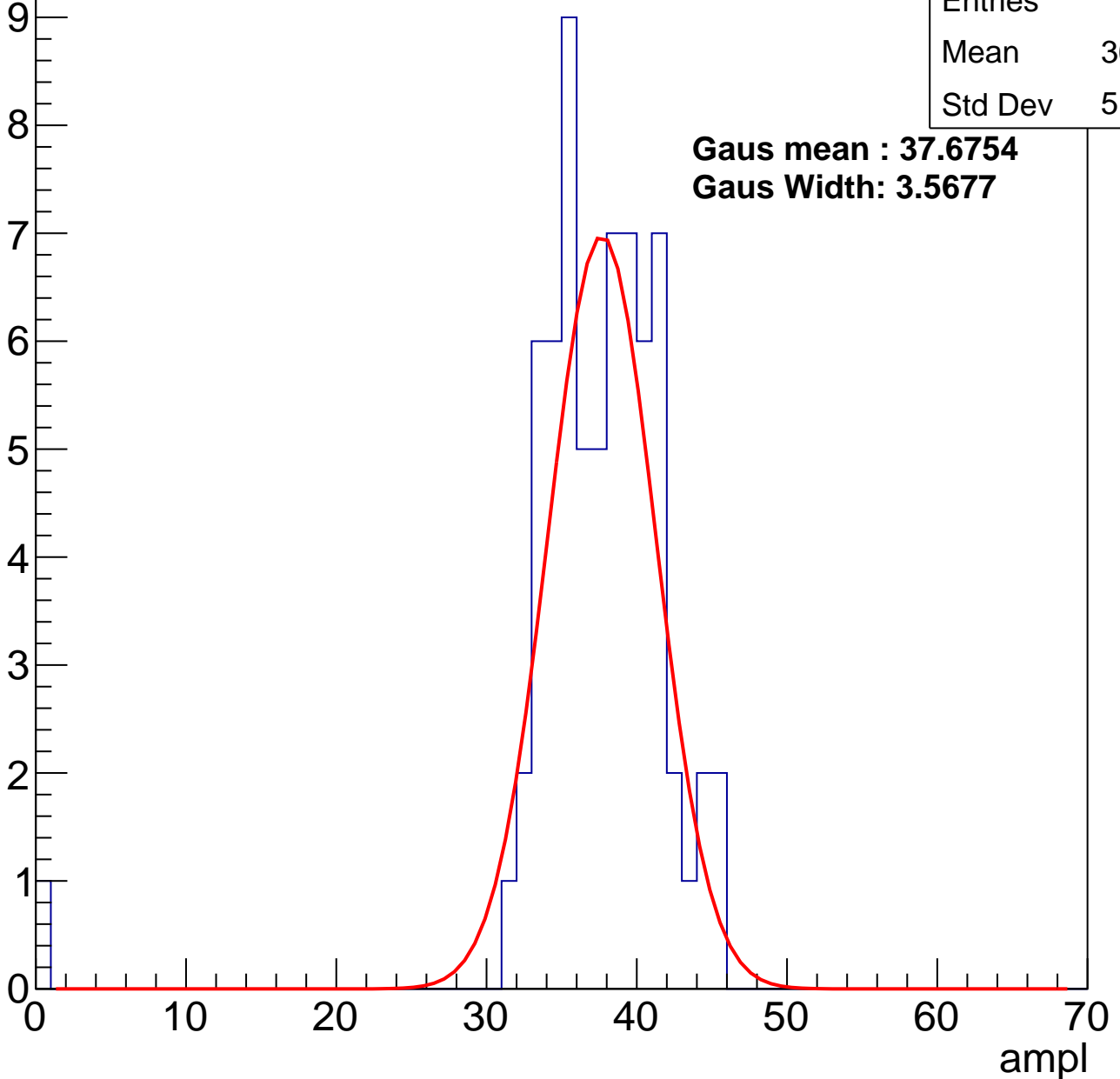
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	36.93
Std Dev	5.593

Gaus mean : 37.6754

Gaus Width: 3.5677



B0L001S, U14-ch84, adc2

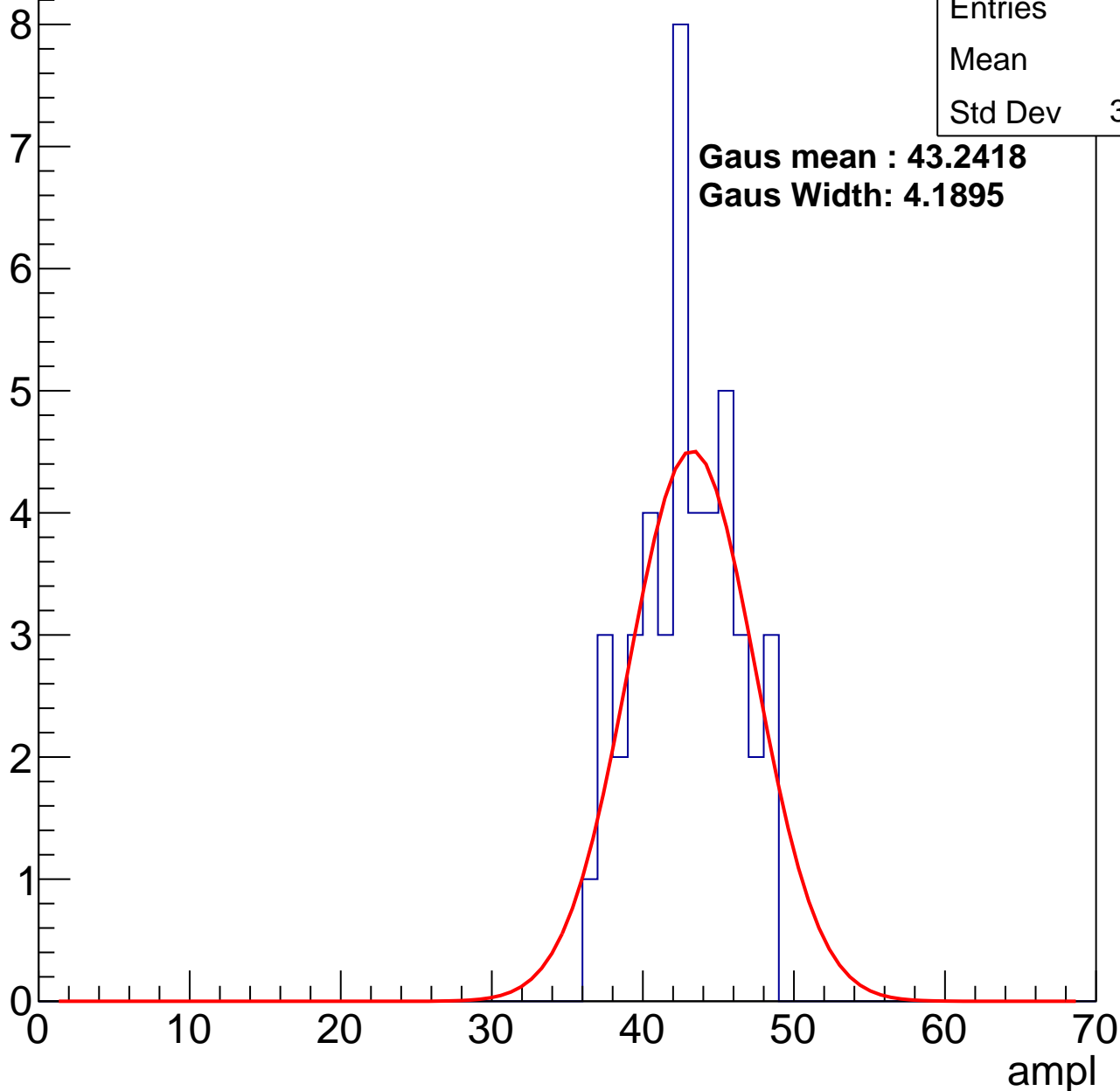
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	45
Mean	42.4
Std Dev	3.172

Gaus mean : 43.2418

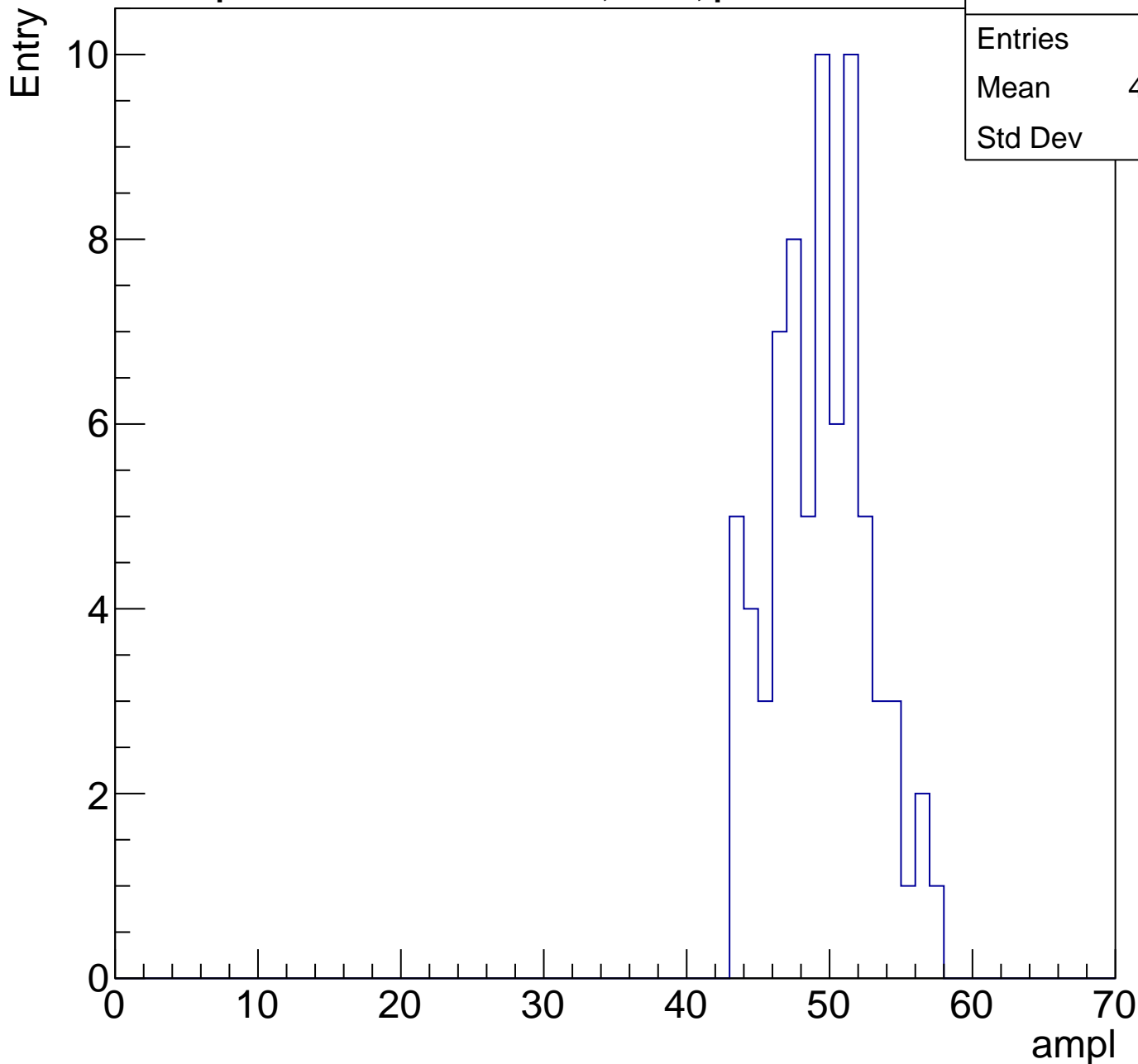
Gaus Width: 4.1895



B0L001S, U14-ch84, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	73
Mean	48.89
Std Dev	3.39

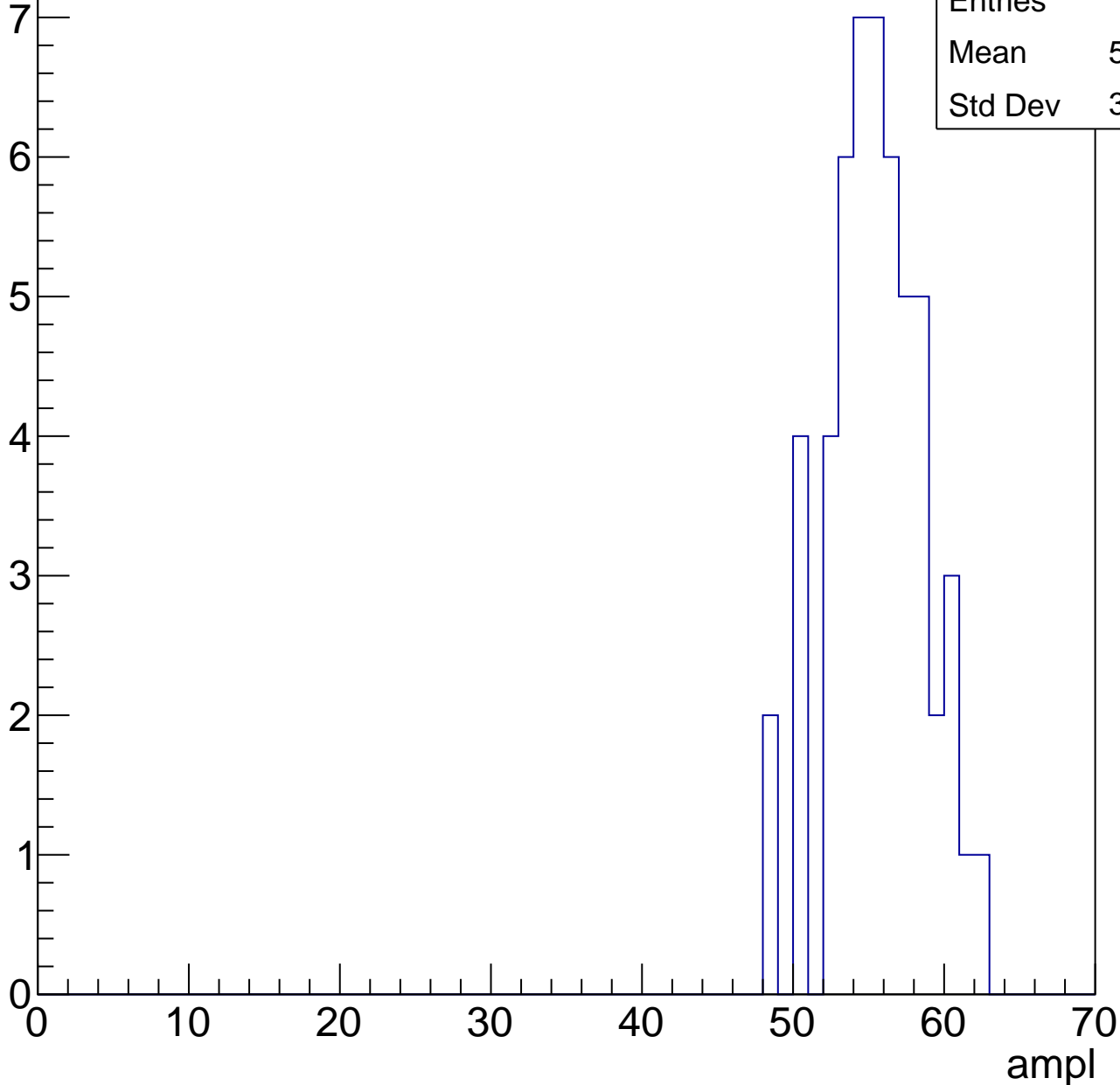


B0L001S, U14-ch84, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.04
Std Dev	3.156

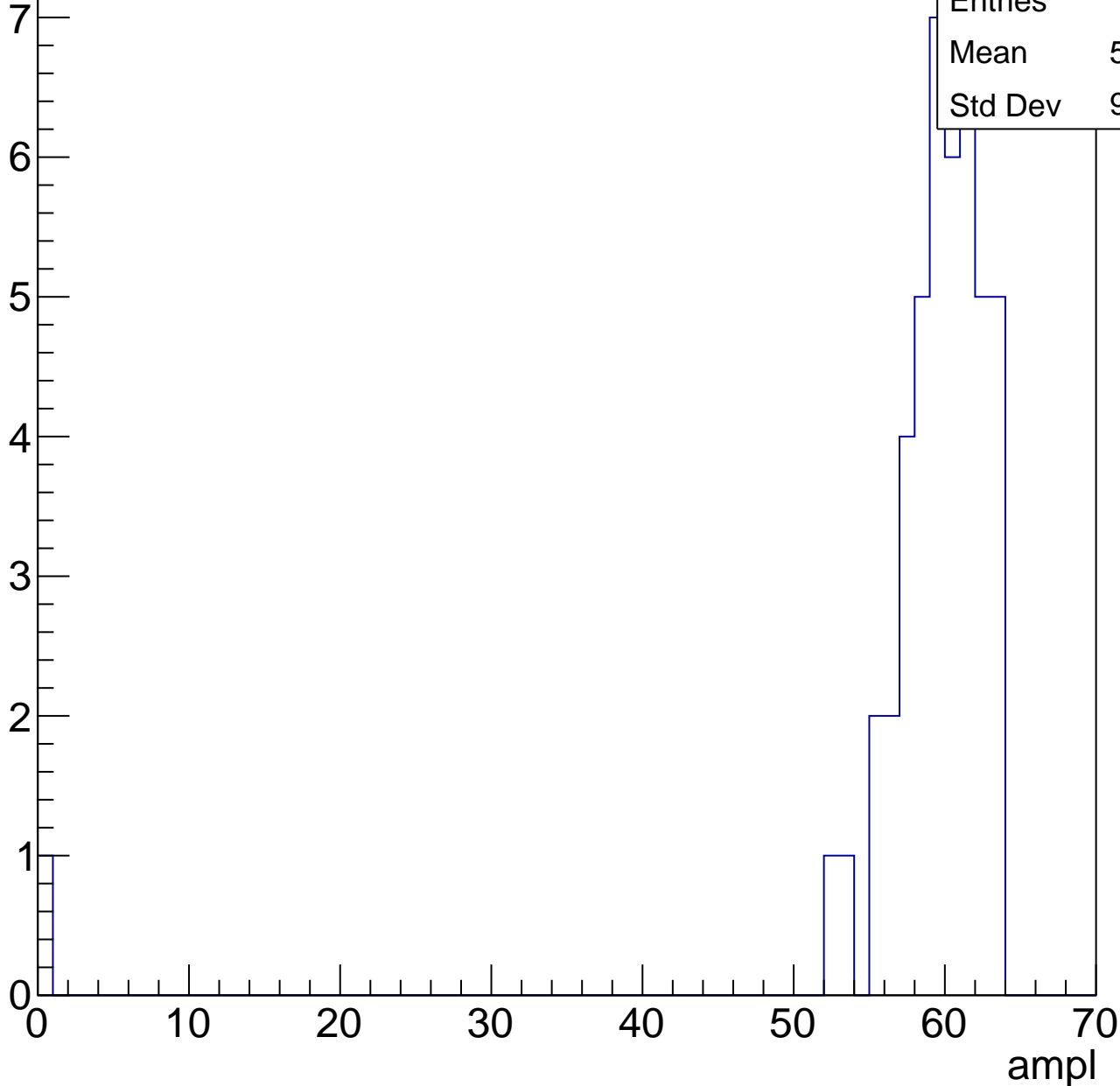


B0L001S, U14-ch84, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	58.04
Std Dev	9.034

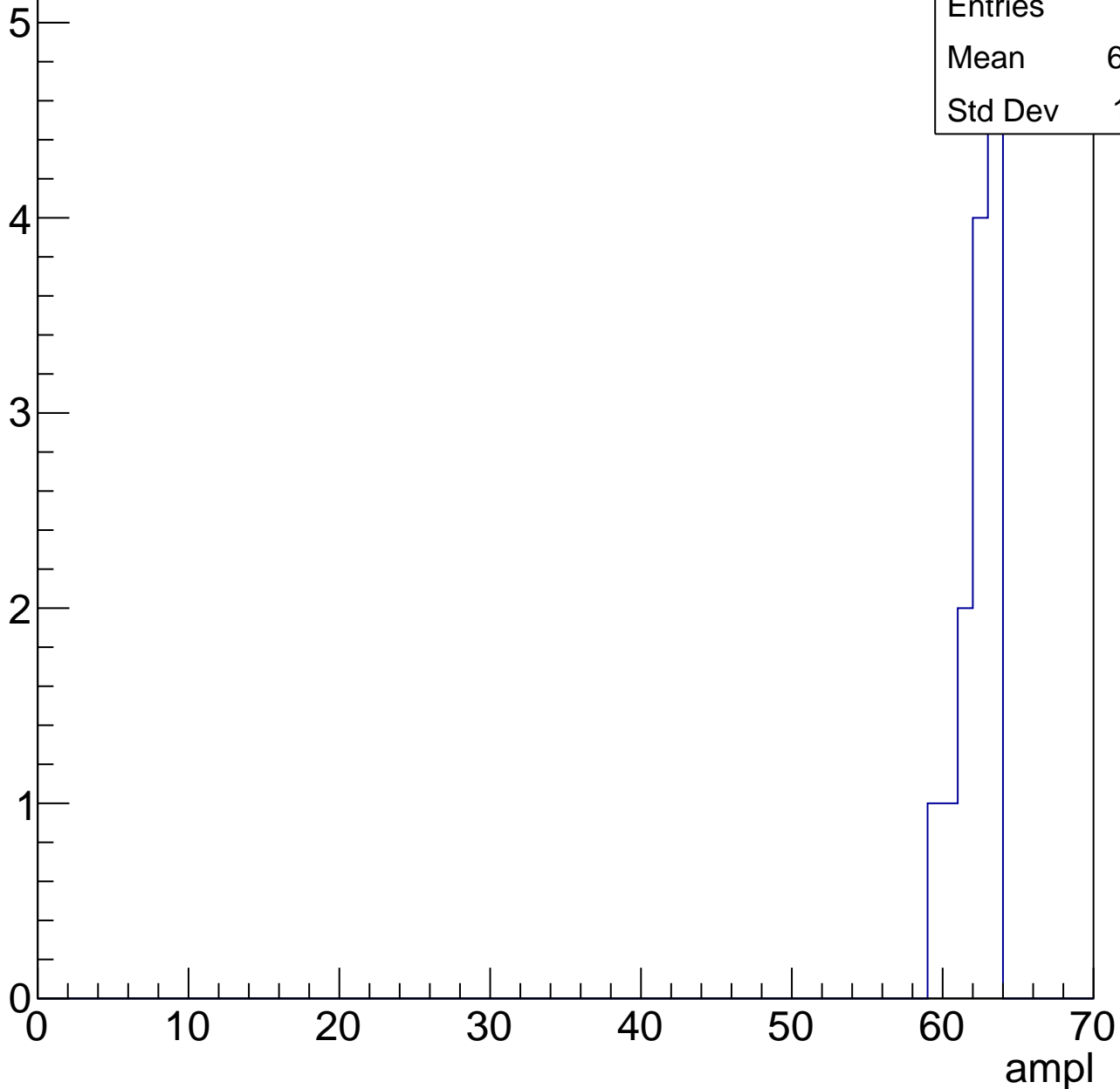


B0L001S, U14-ch84, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	13
Mean	61.85
Std Dev	1.231



B0L001S, U14-ch84, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch85, adc0

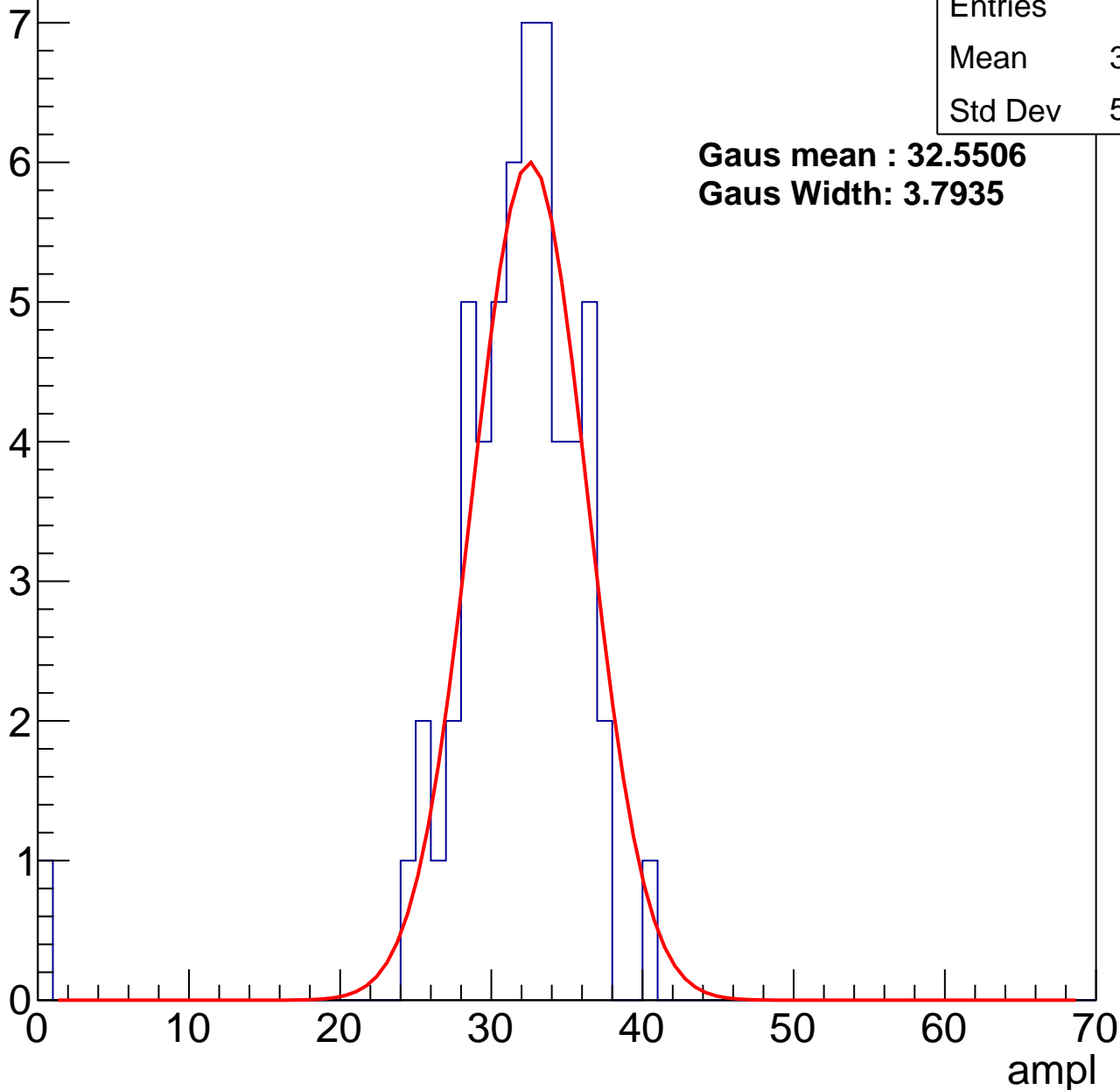
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	31.07
Std Dev	5.327

Gaus mean : 32.5506

Gaus Width: 3.7935



B0L001S, U14-ch85, adc1

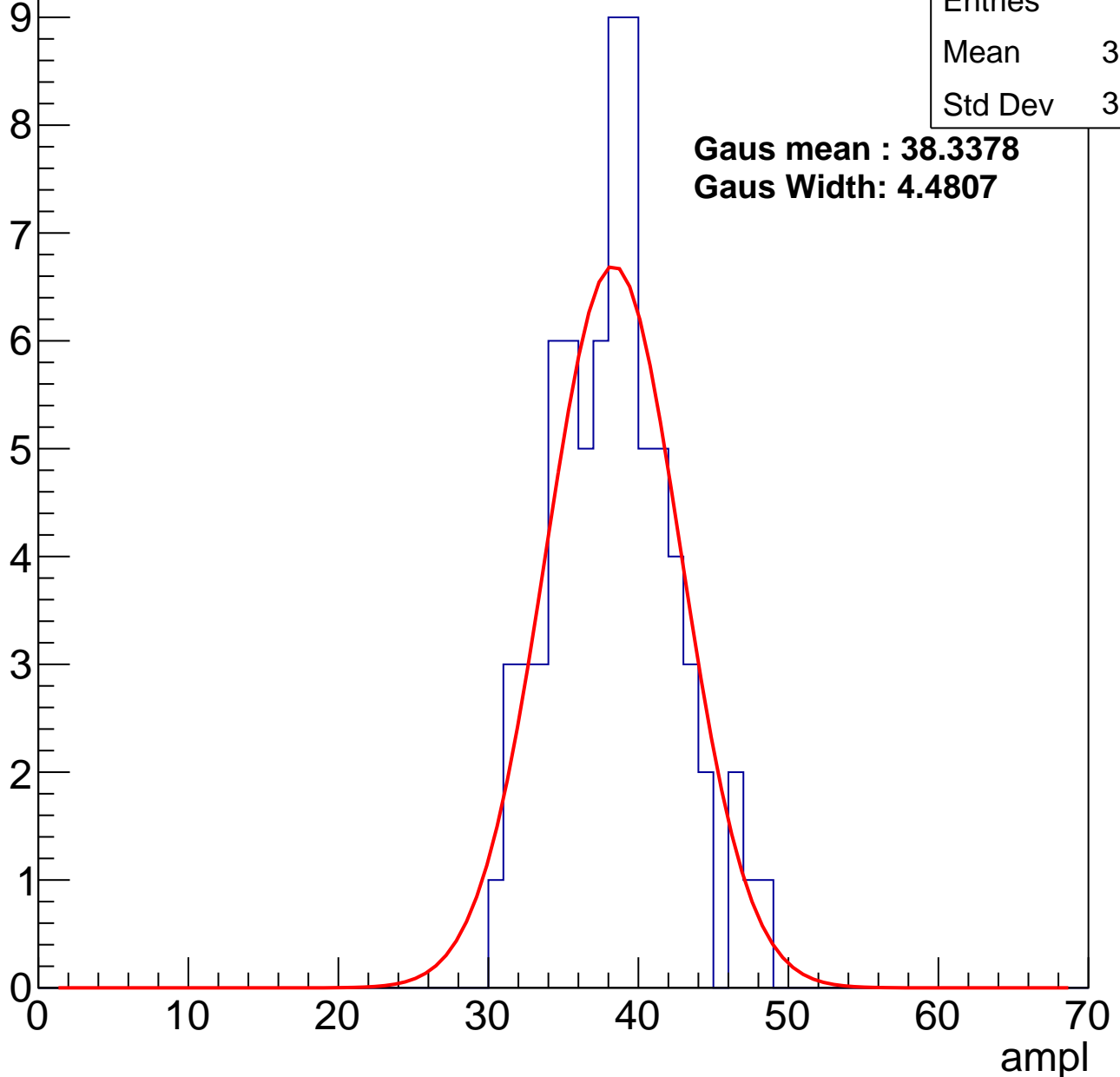
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	37.89
Std Dev	3.965

Gaus mean : 38.3378

Gaus Width: 4.4807



B0L001S, U14-ch85, adc2

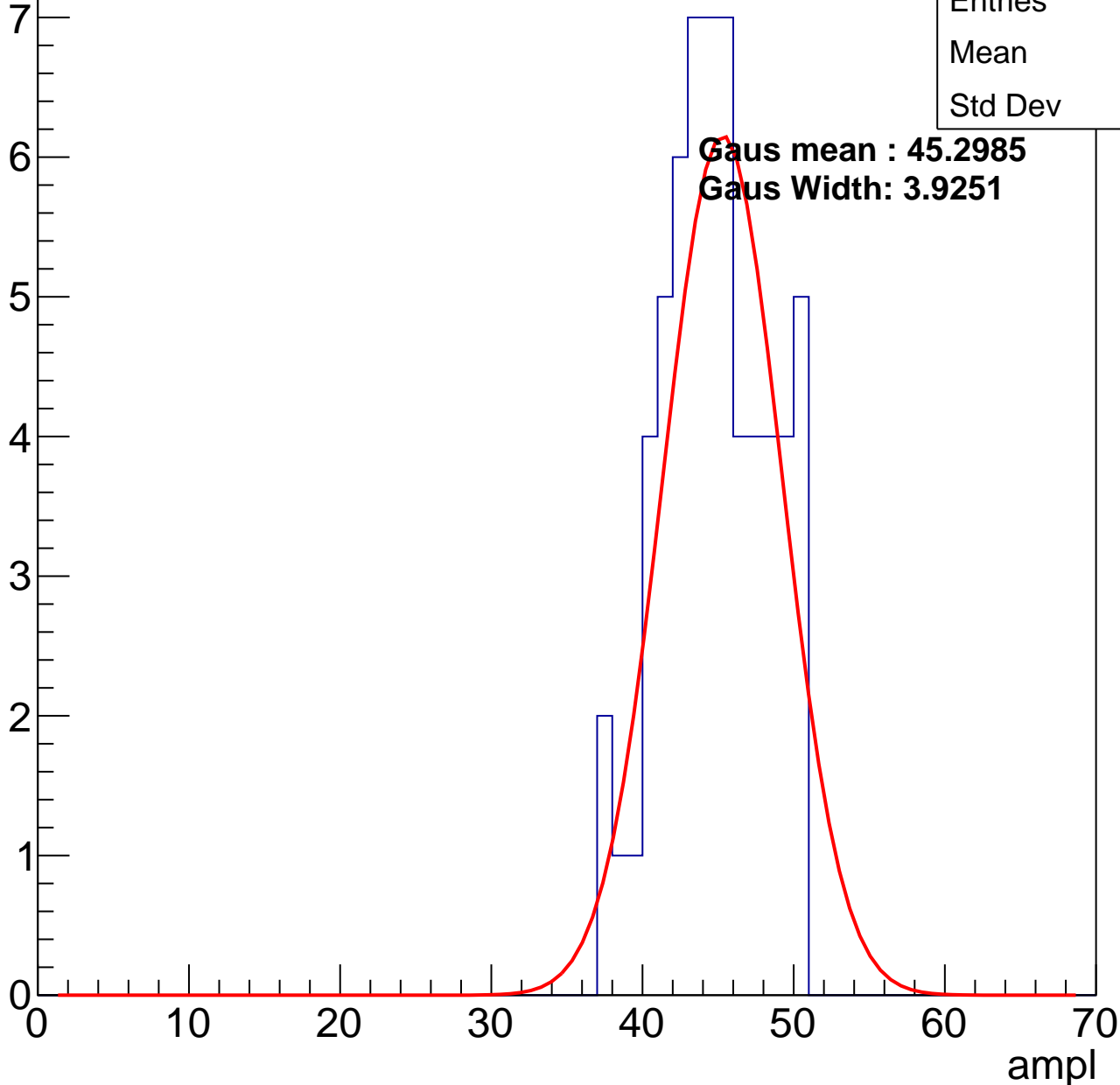
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	44.3
Std Dev	3.38

Gaus mean : 45.2985

Gaus Width: 3.9251

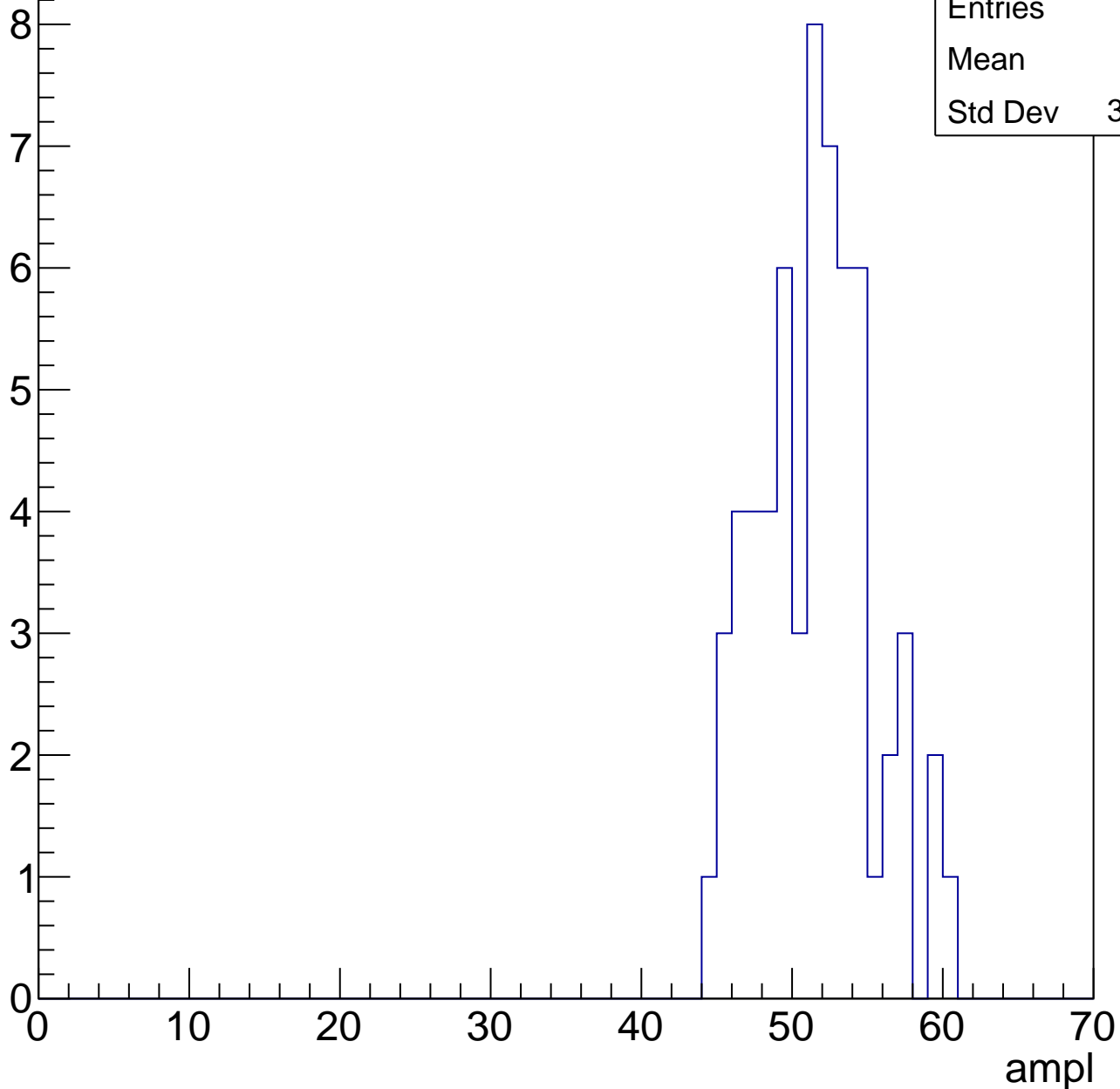


B0L001S, U14-ch85, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

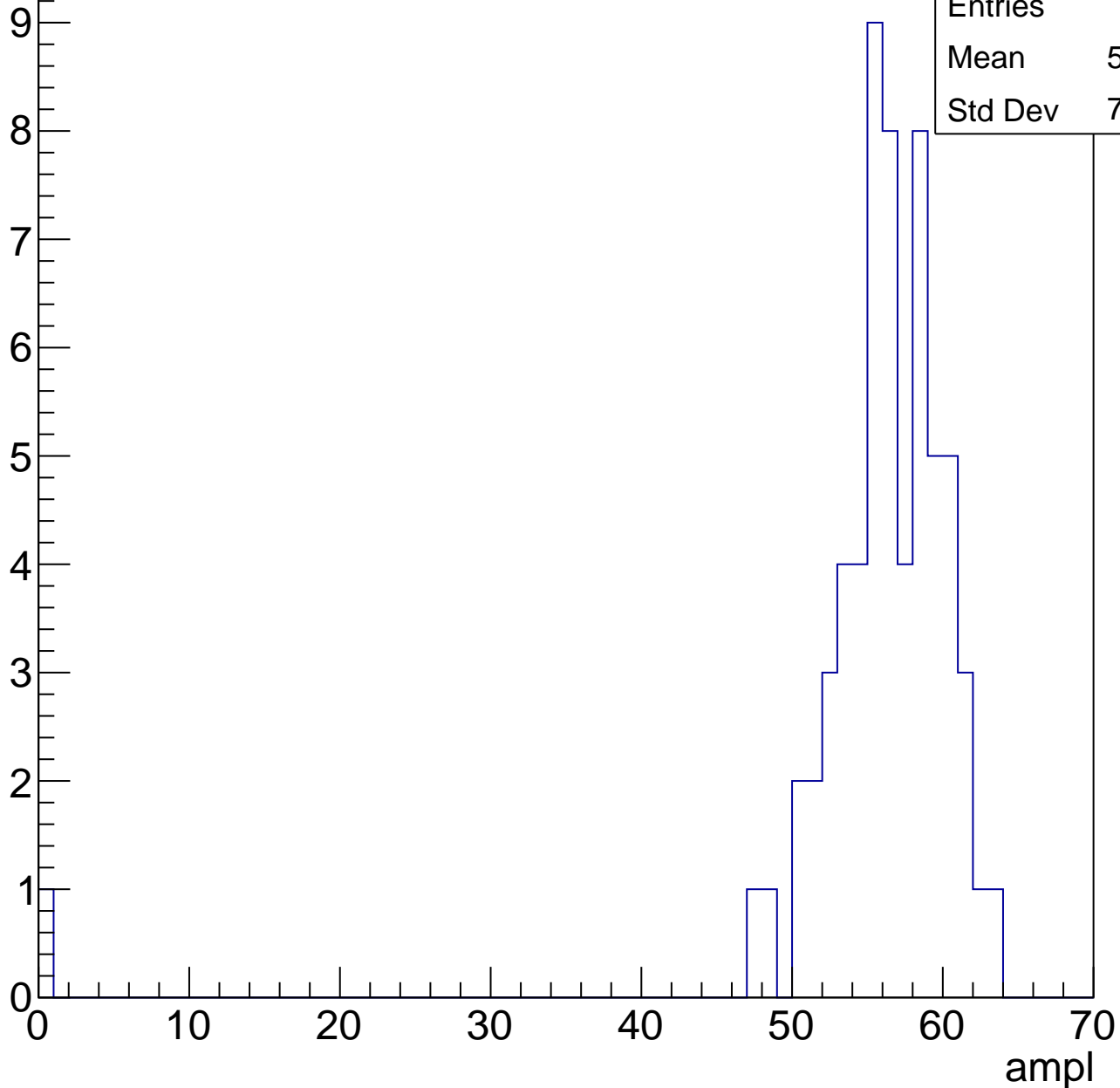
Entries	61
Mean	51.1
Std Dev	3.736



B0L001S, U14-ch85, adc4

calib_packv5_042523_0143.root, FC#9, port A1

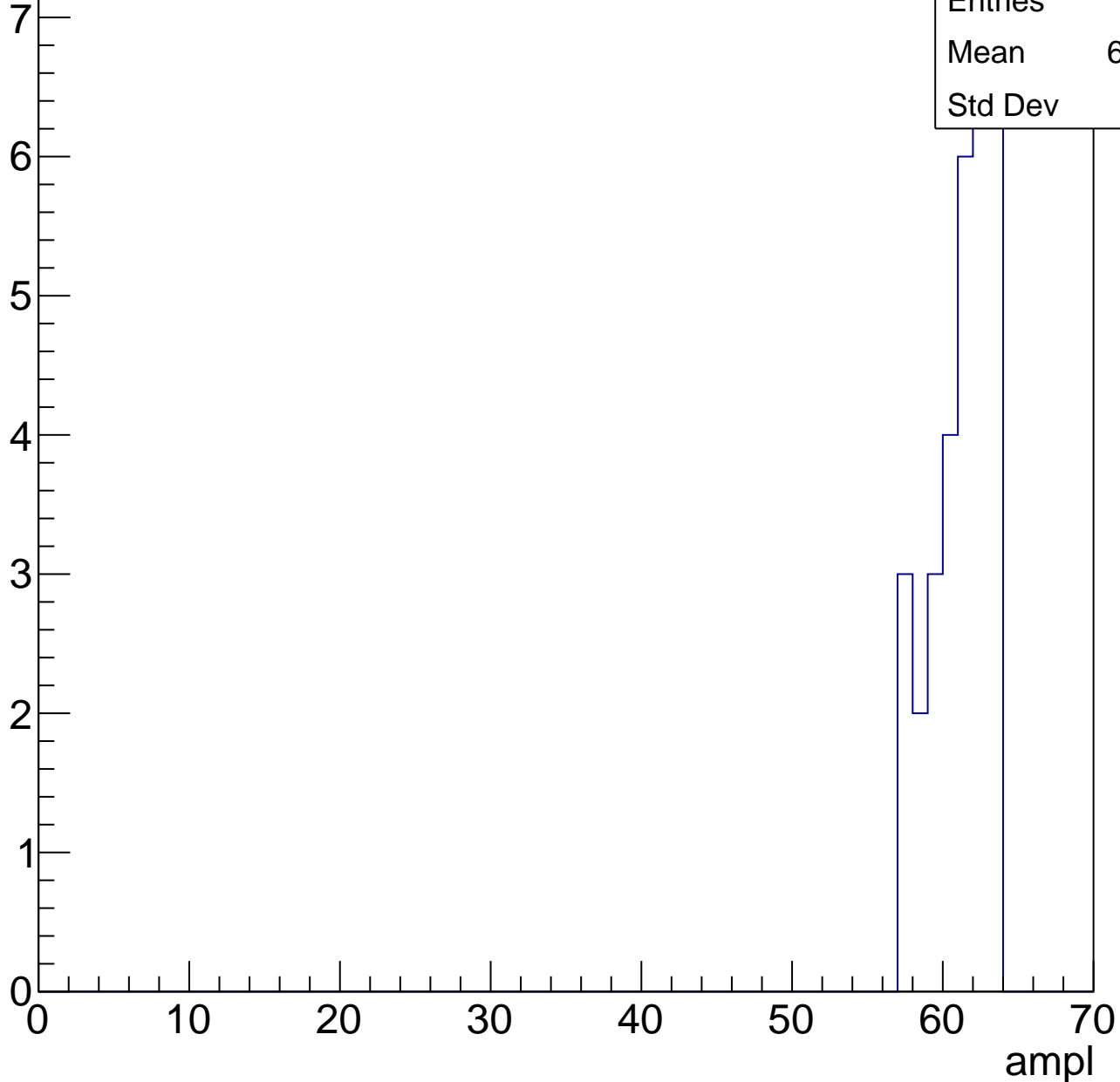
Entry



B0L001S, U14-ch85, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	32
Mean	60.78
Std Dev	1.9

B0L001S, U14-ch85, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch85, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch86, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	64
Mean	30.17
Std Dev	4.853

Gaus mean : 30.8644

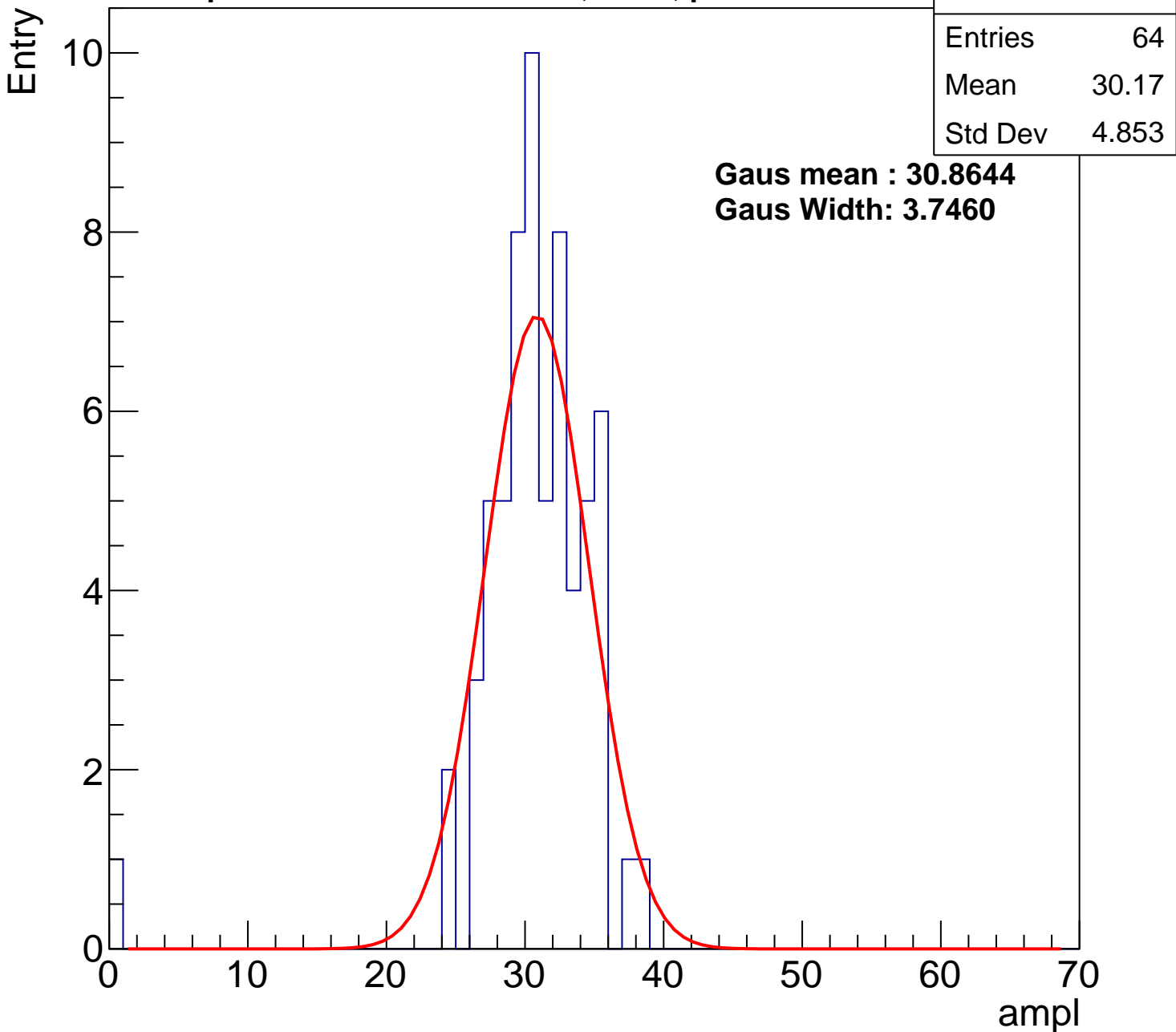
Gaus Width: 3.7460

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch86, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.35
Std Dev	3.311

Gaus mean : 38.0496

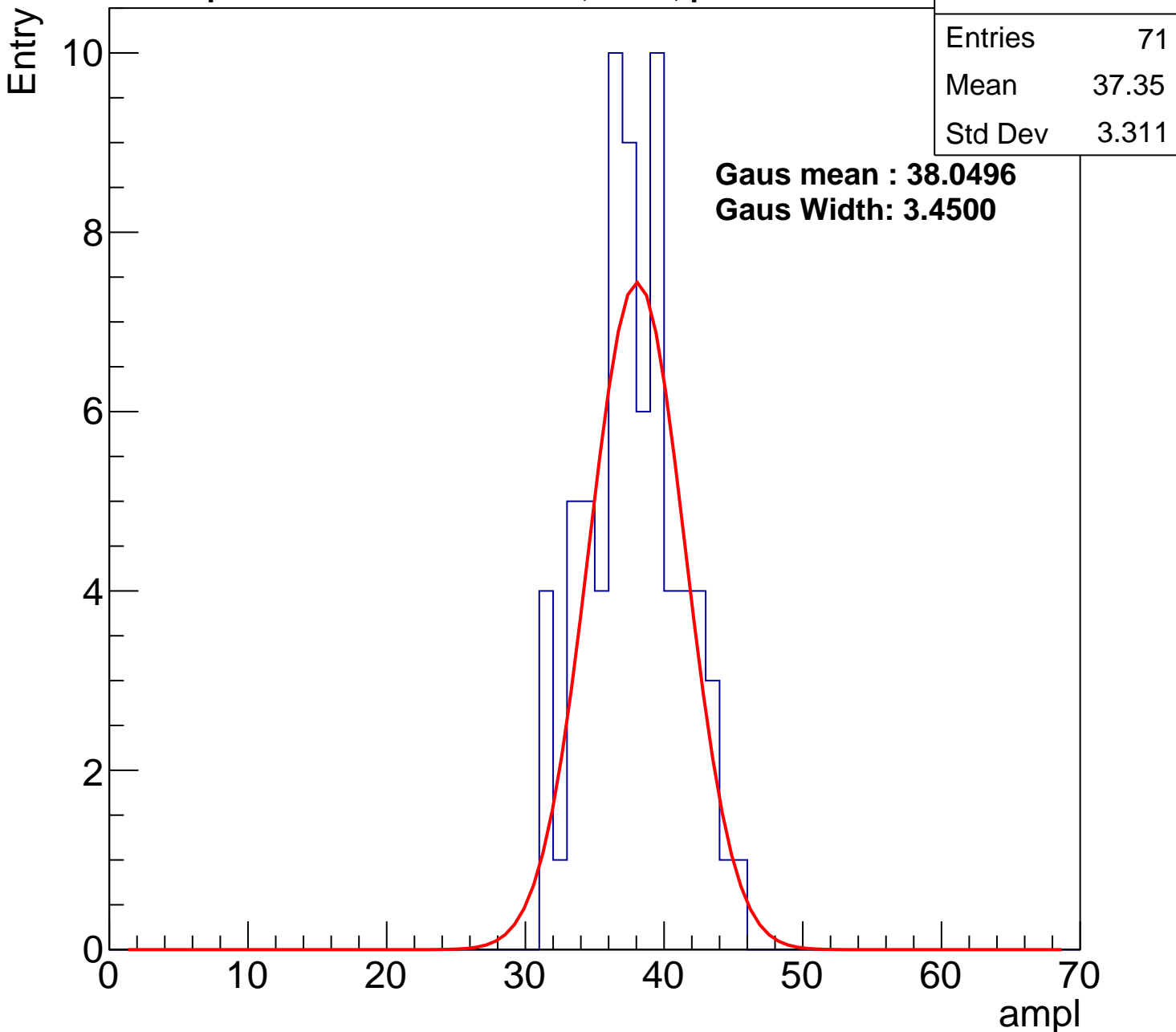
Gaus Width: 3.4500

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch86, adc2

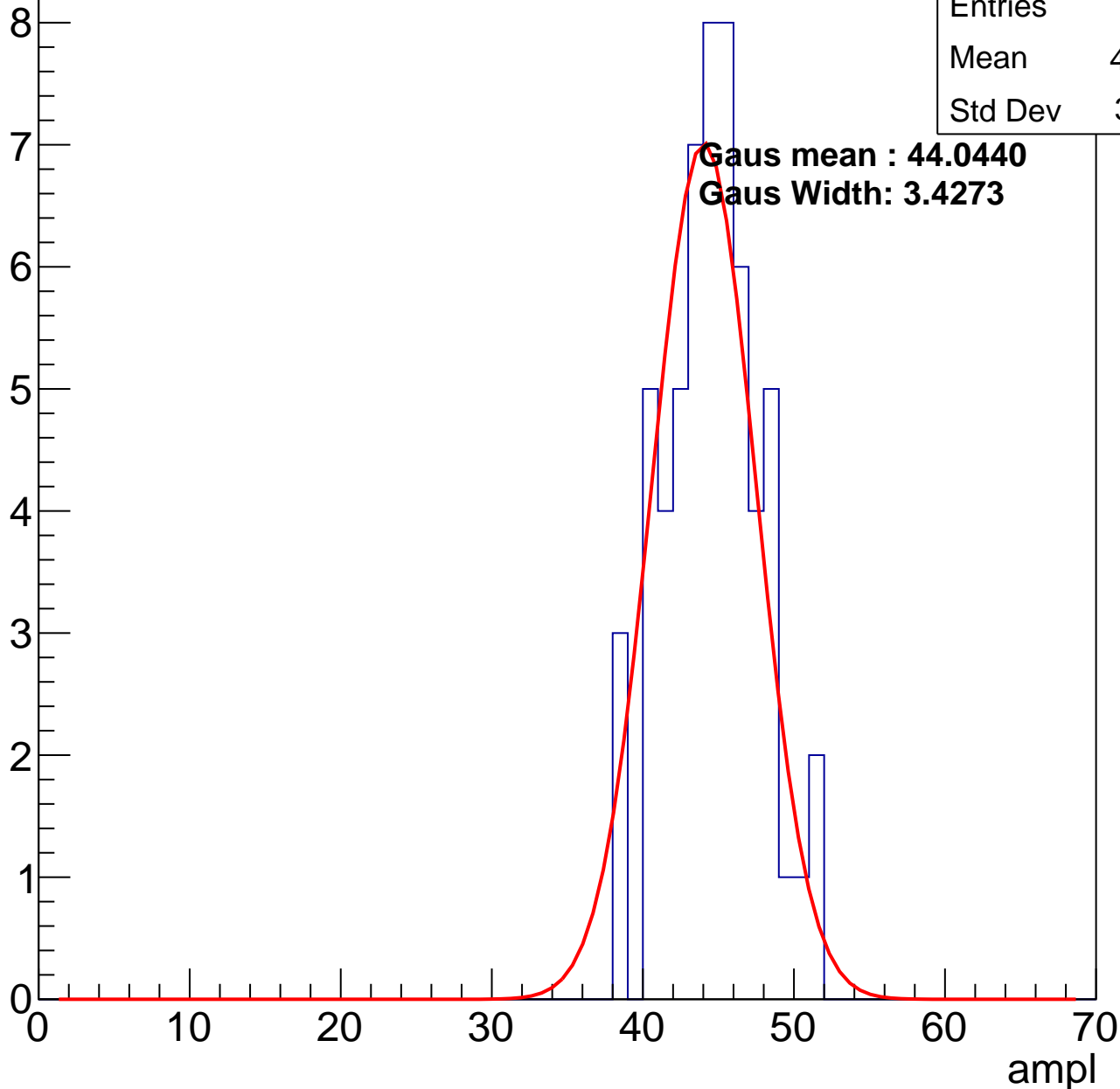
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	44.17
Std Dev	3.071

Gaus mean : 44.0440

Gaus Width: 3.4273

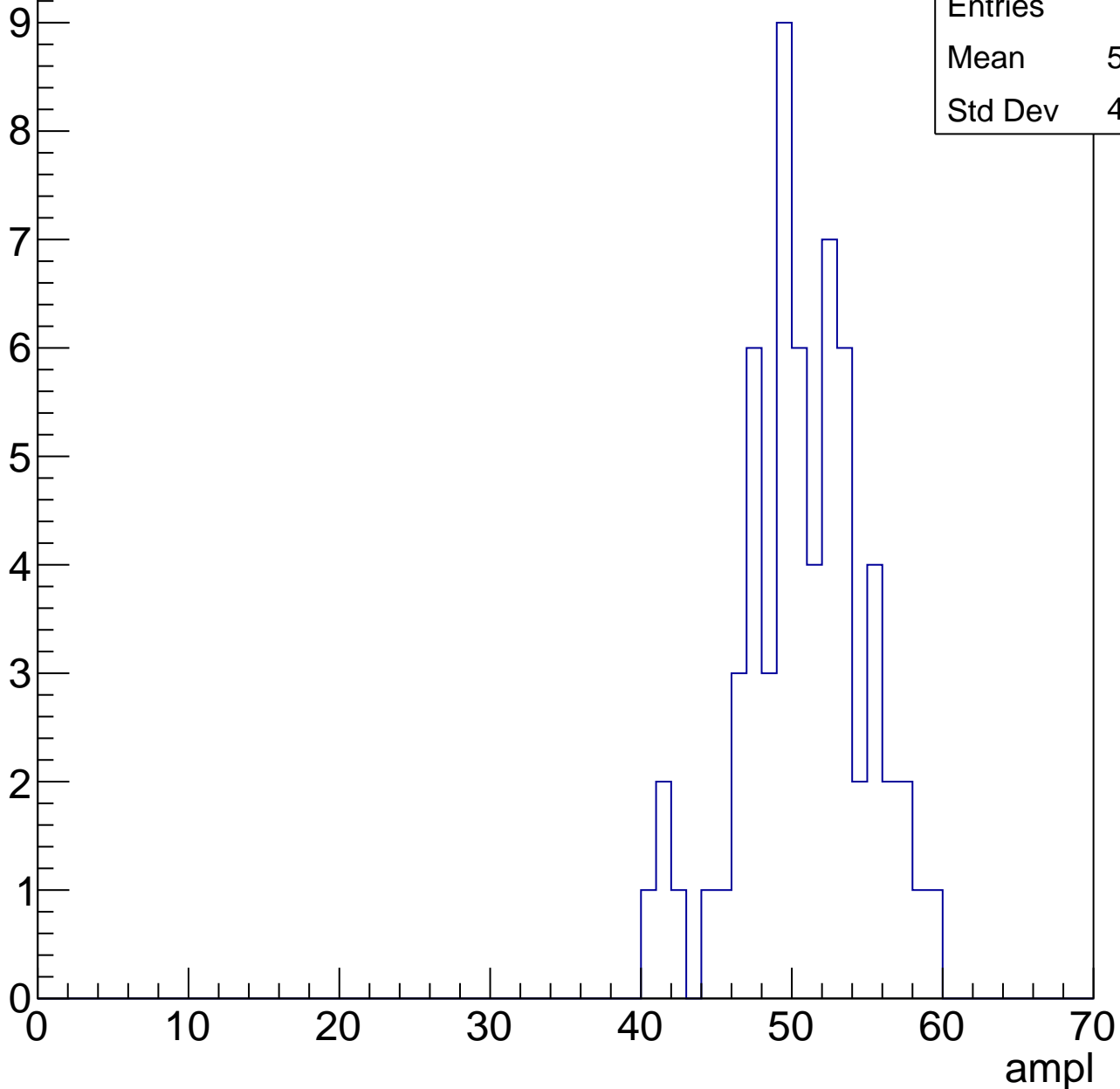


B0L001S, U14-ch86, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	50.24
Std Dev	4.094

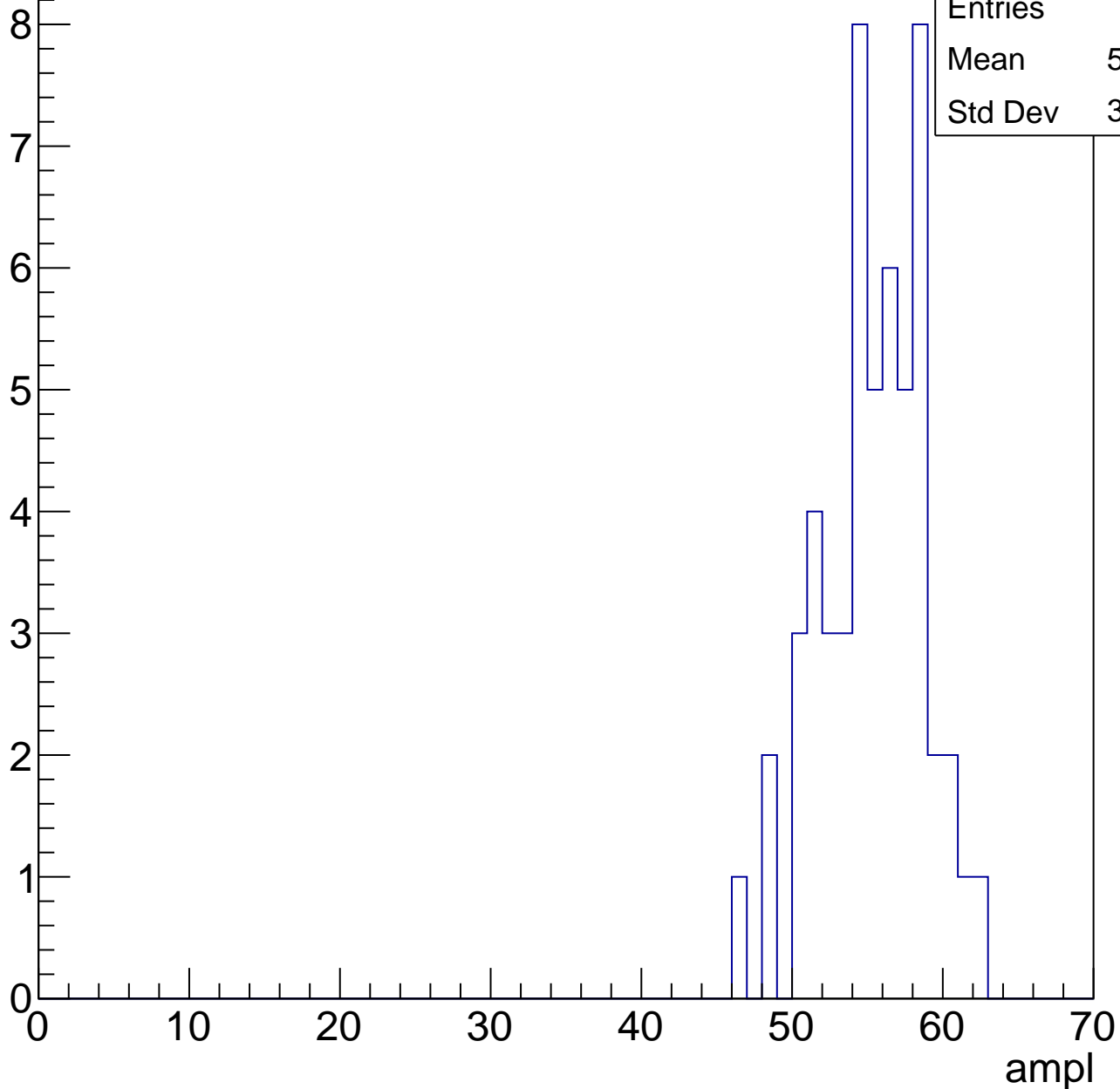


B0L001S, U14-ch86, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

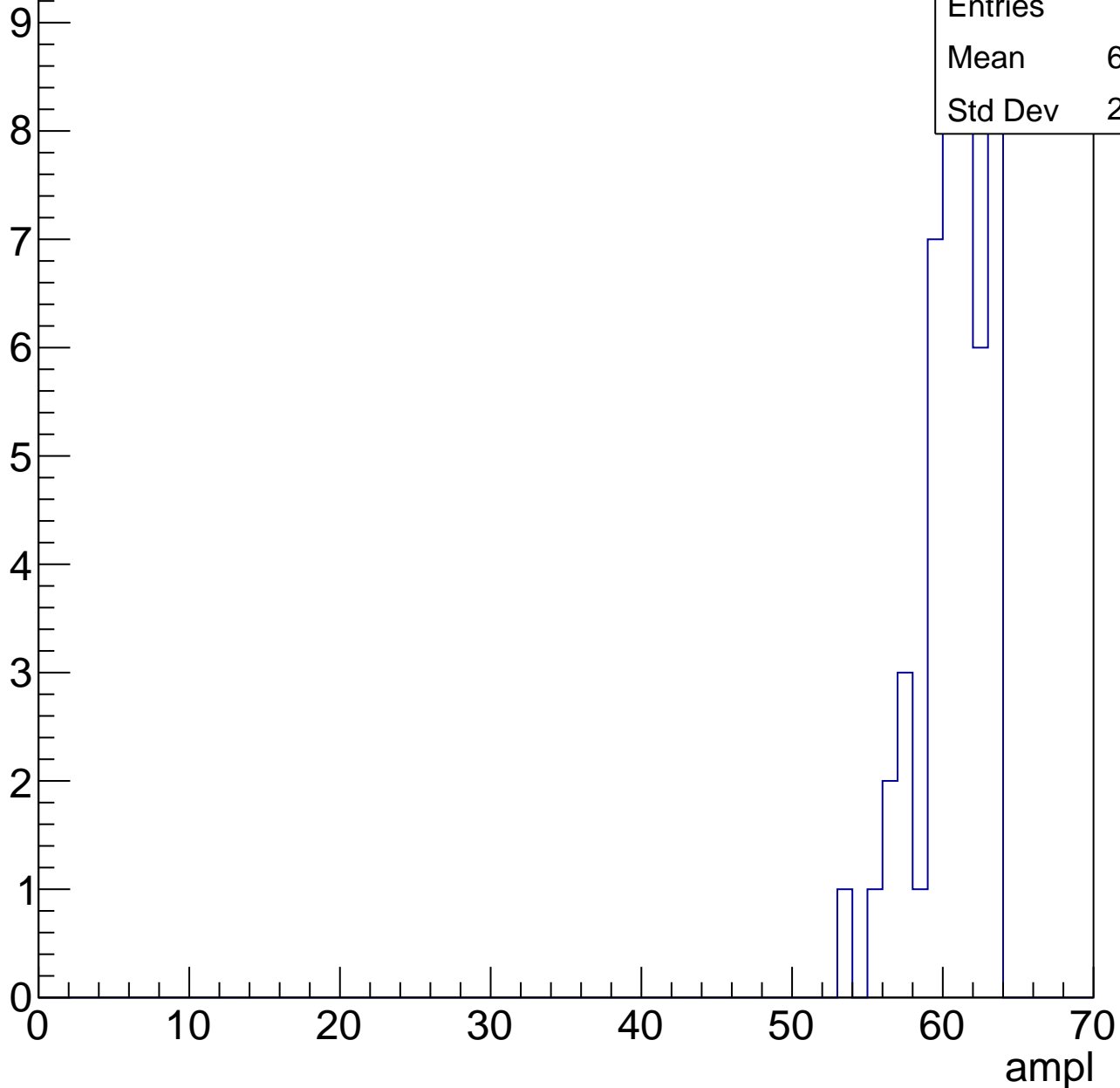
Entries	54
Mean	54.89
Std Dev	3.414



B0L001S, U14-ch86, adc5

calib_packv5_042523_0143.root, FC#9, port A1

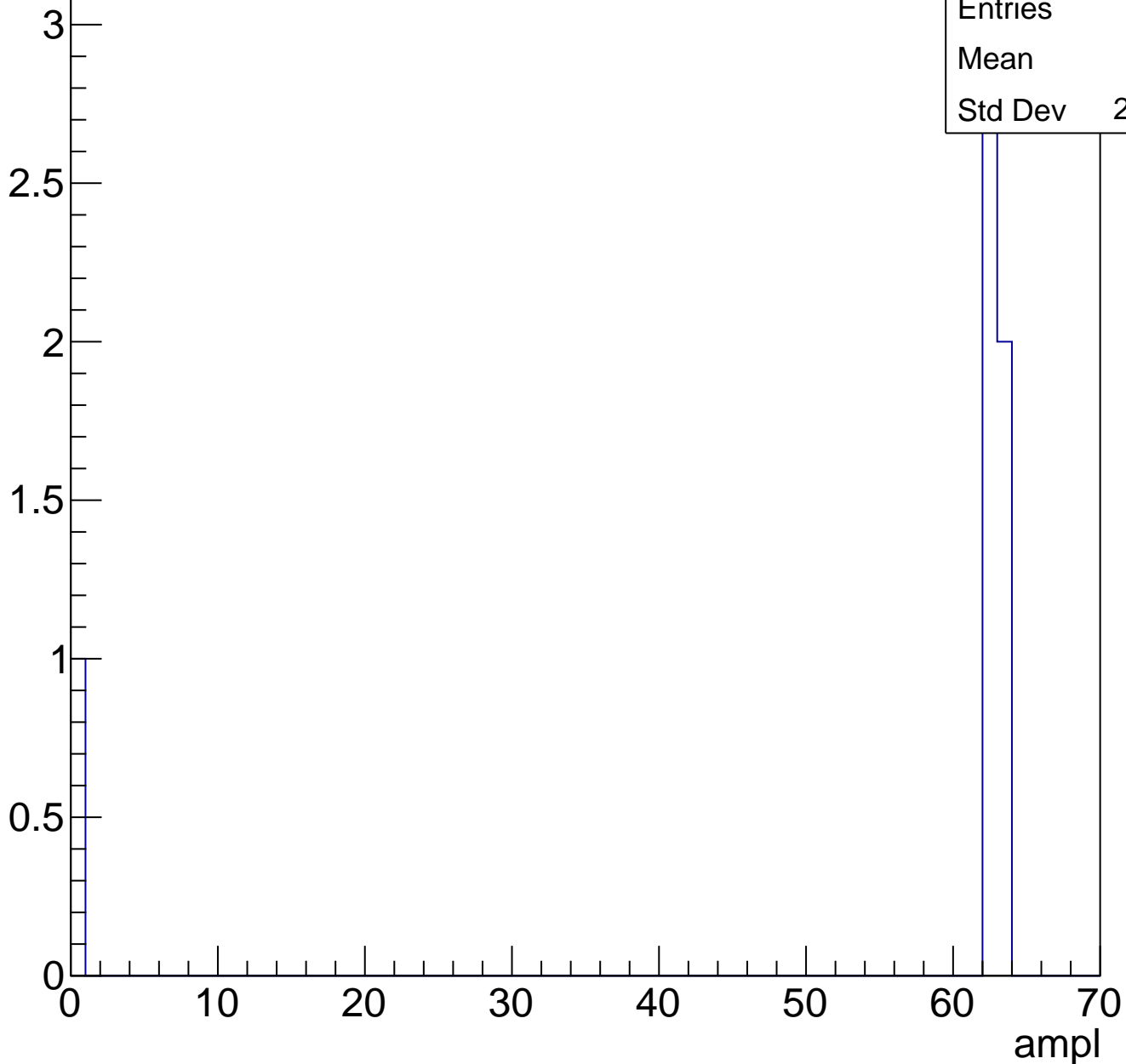
Entry



B0L001S, U14-ch86, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch86, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch87, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	78
Mean	31.87
Std Dev	3.884

Gaus mean : 31.8974

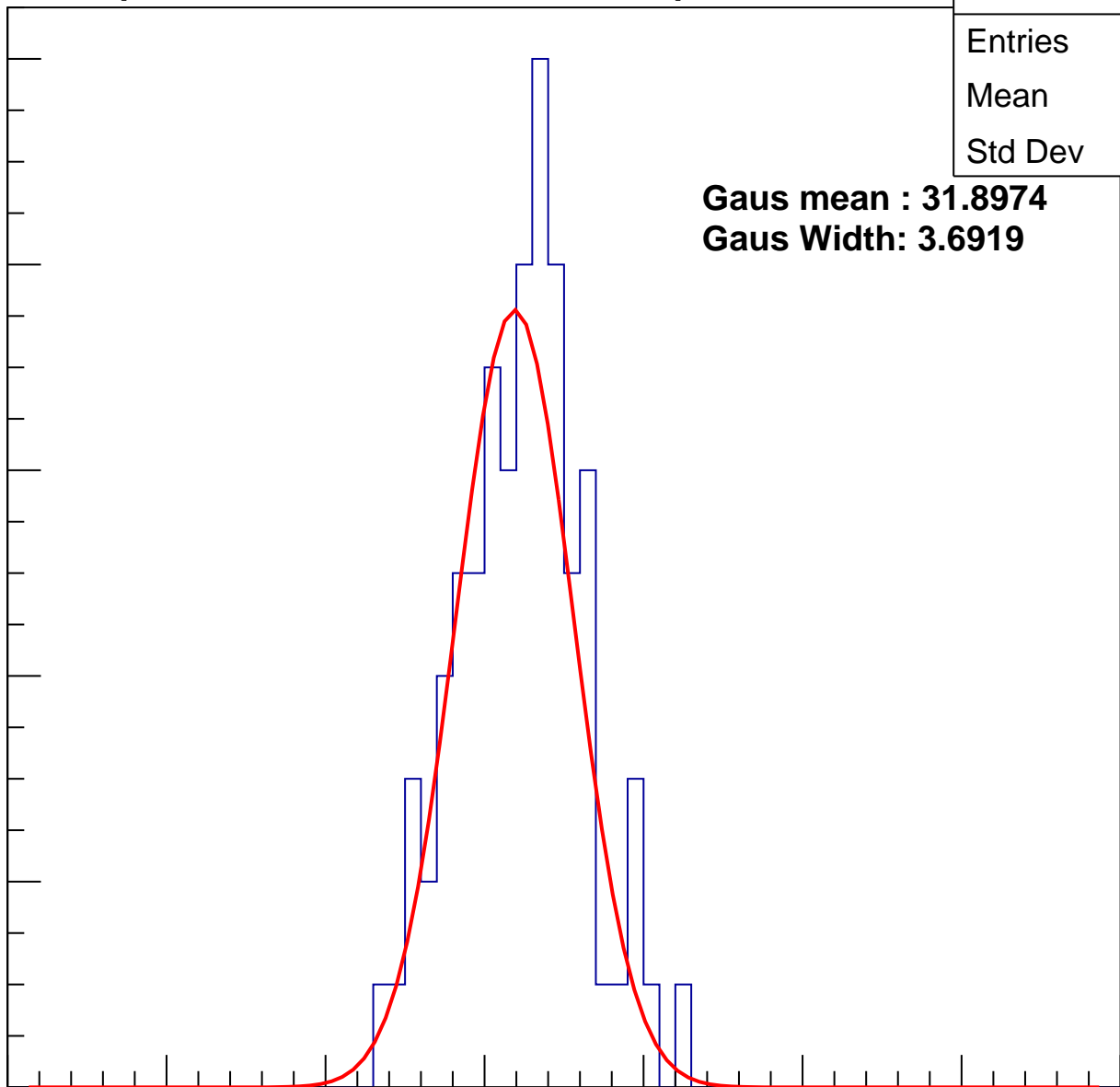
Gaus Width: 3.6919

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch87, adc1

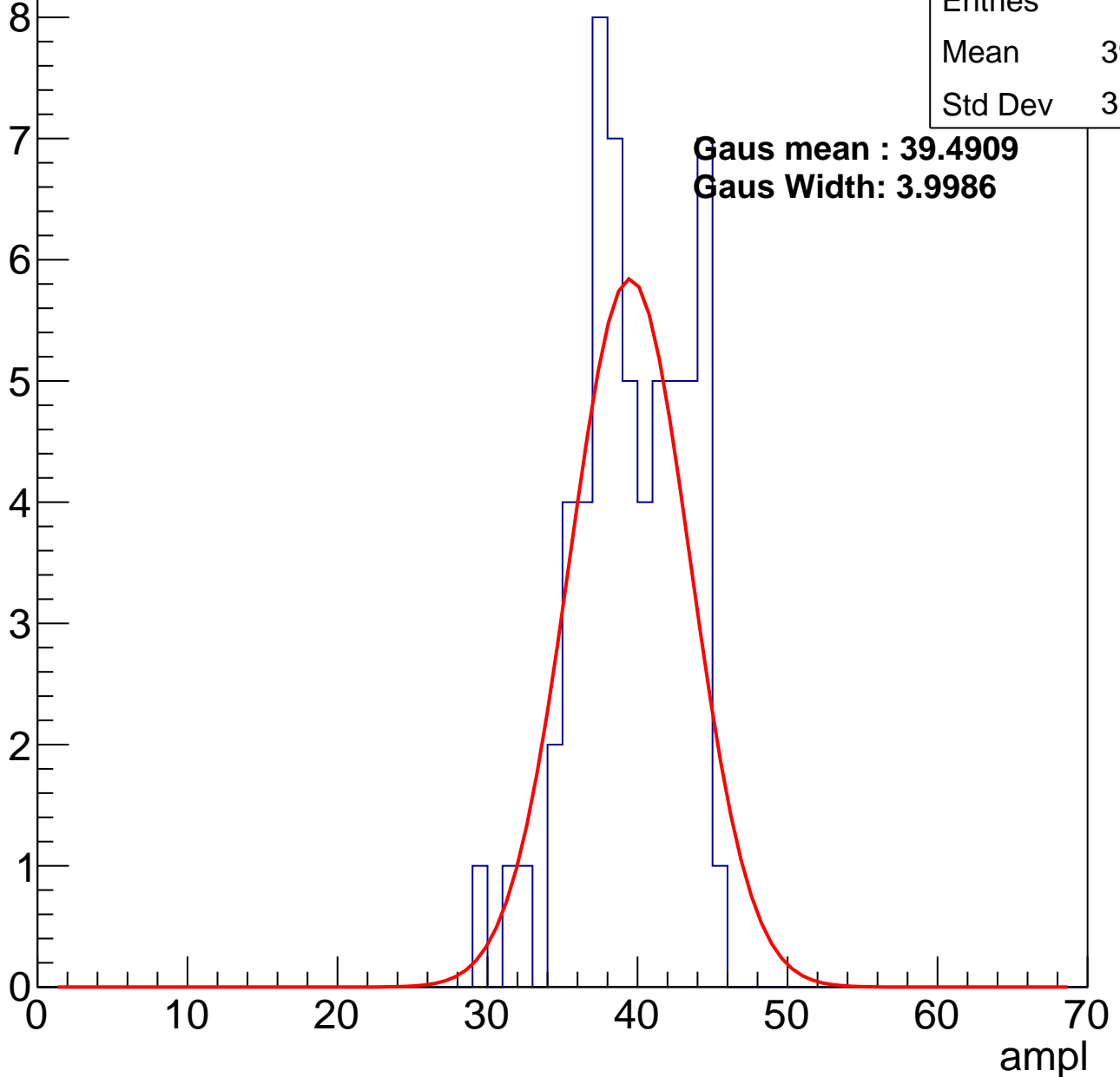
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	39.07
Std Dev	3.563

Gaus mean : 39.4909

Gaus Width: 3.9986



B0L001S, U14-ch87, adc2

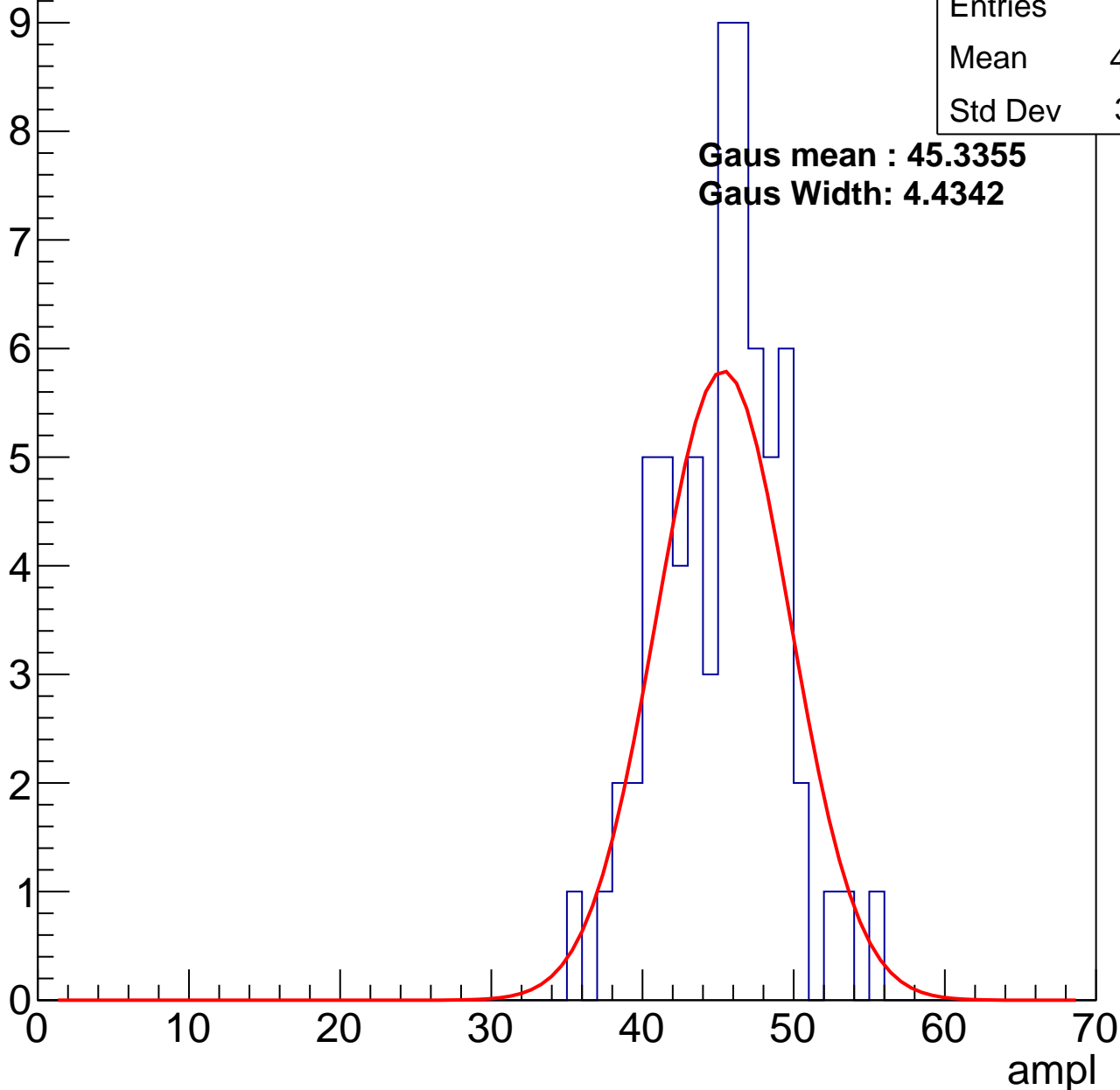
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.72
Std Dev	3.891

Gaus mean : 45.3355

Gaus Width: 4.4342



B0L001S, U14-ch87, adc3

calib_packv5_042523_0143.root, FC#9, port A1

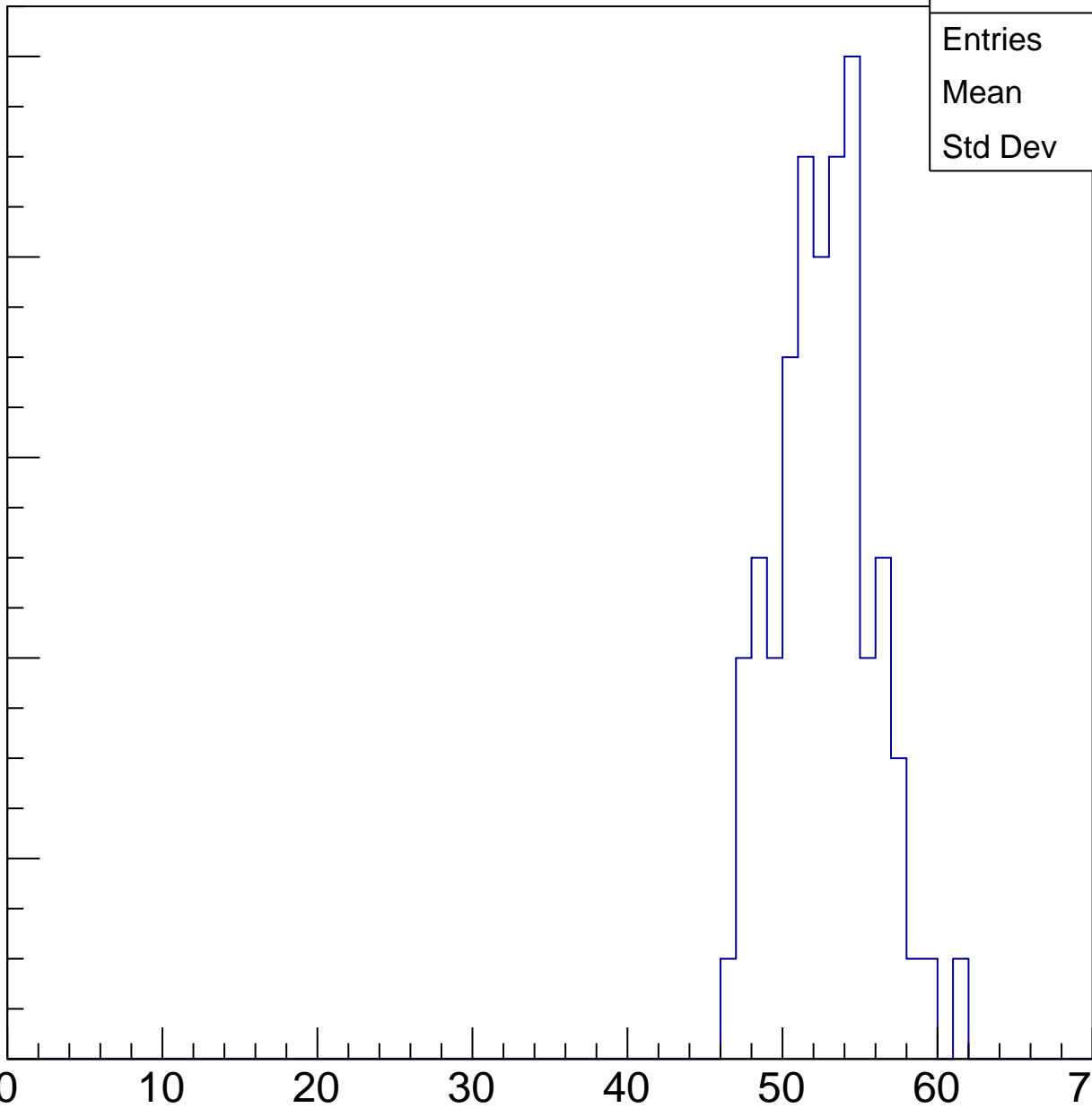
Entries	72
Mean	52.24
Std Dev	3.098

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

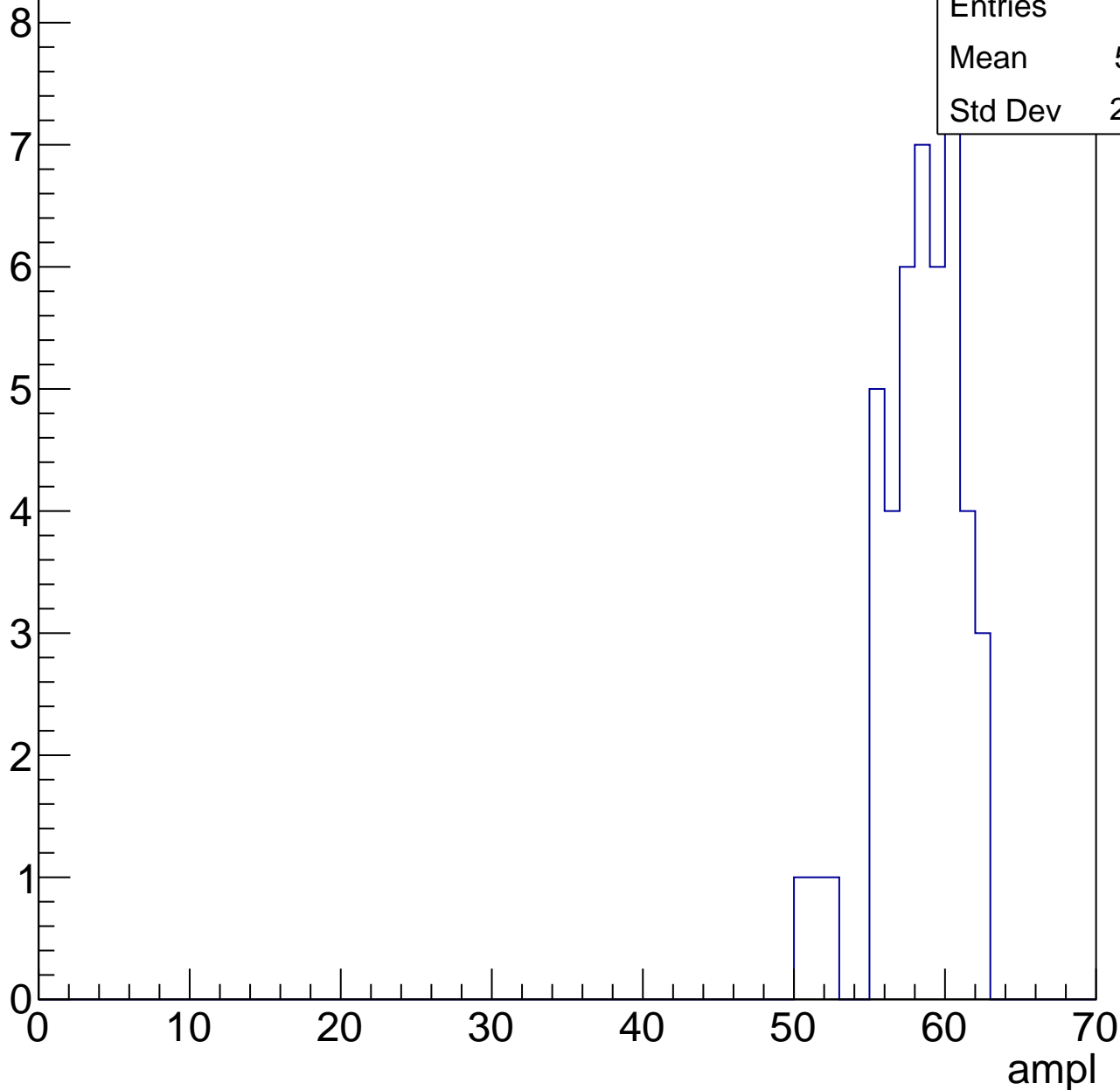


B0L001S, U14-ch87, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	57.91
Std Dev	2.709

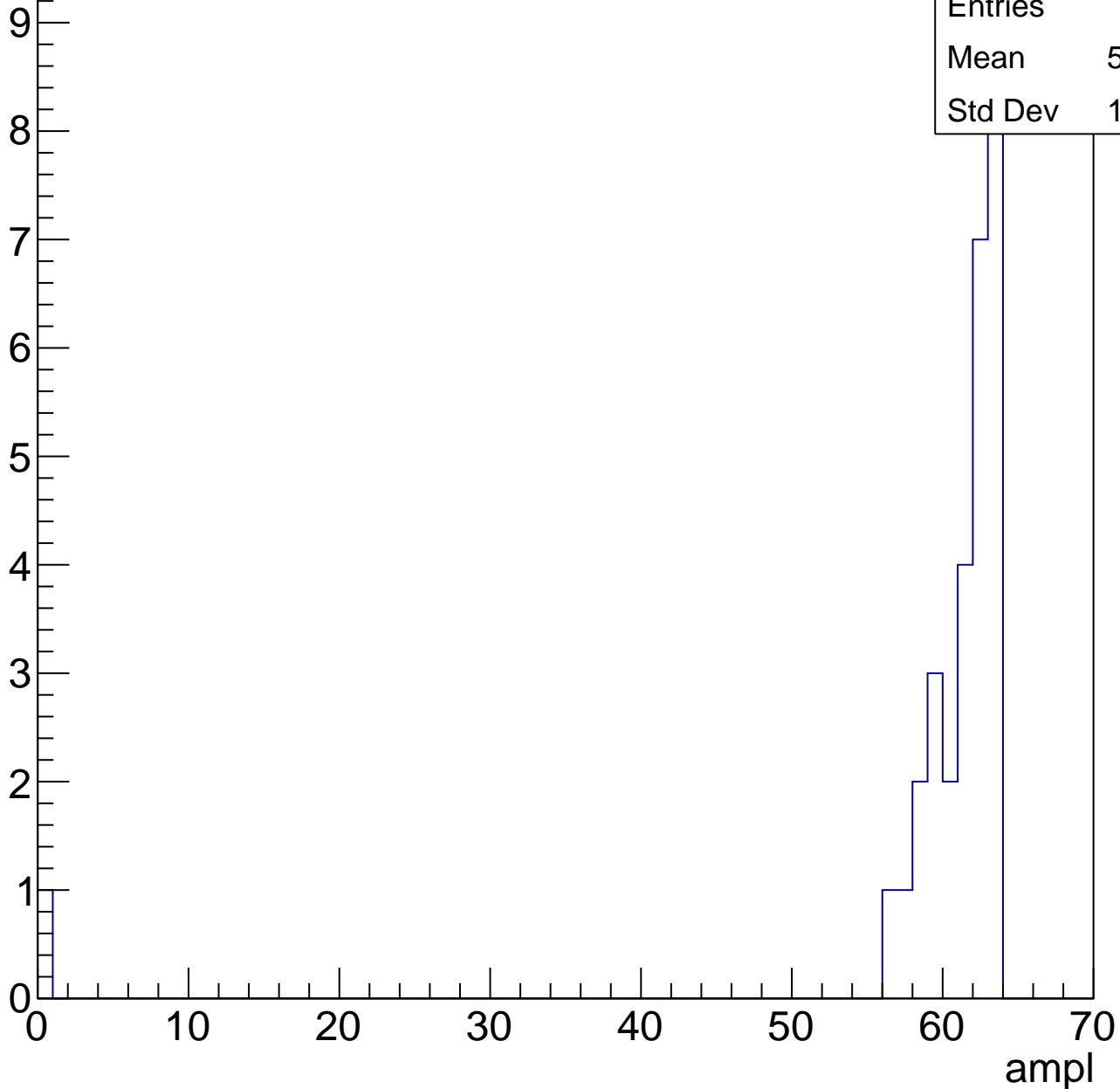


B0L001S, U14-ch87, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	30
Mean	59.03
Std Dev	11.14



B0L001S, U14-ch87, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch87, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch88, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	30.44
Std Dev	4.827

Gaus mean : 31.2169

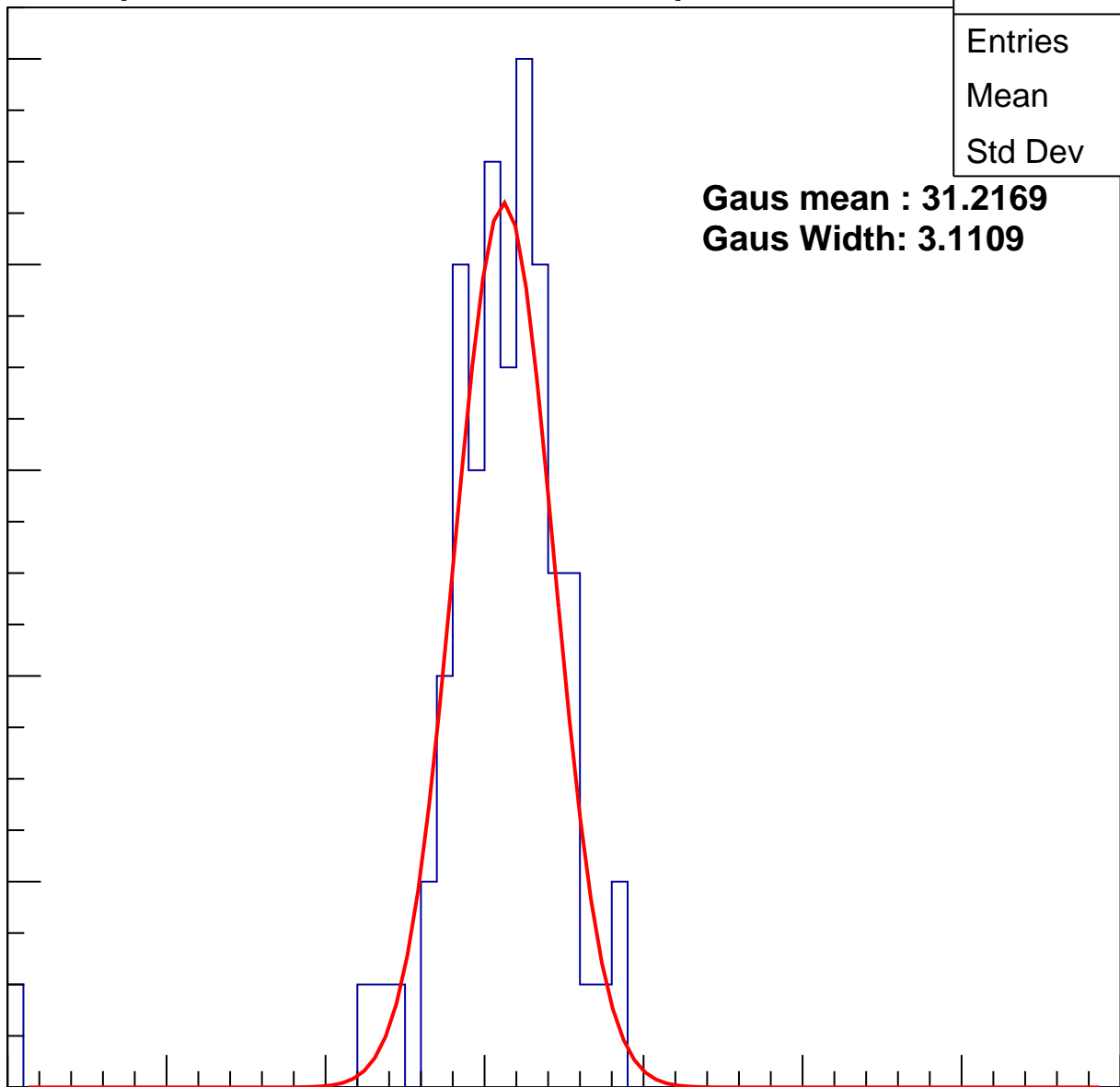
Gaus Width: 3.1109

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch88, adc1

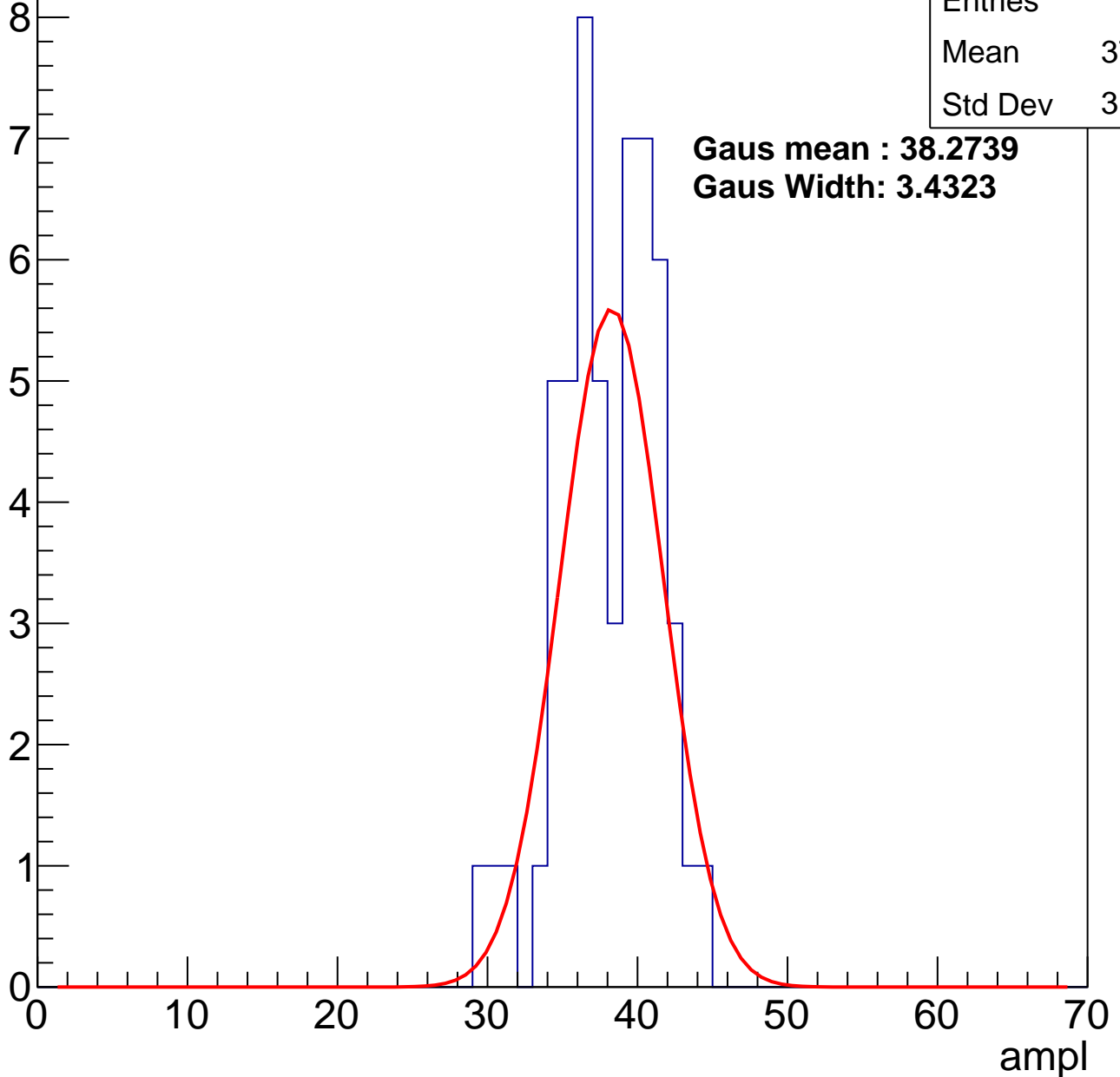
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.58
Std Dev	3.218

Gaus mean : 38.2739

Gaus Width: 3.4323



B0L001S, U14-ch88, adc2

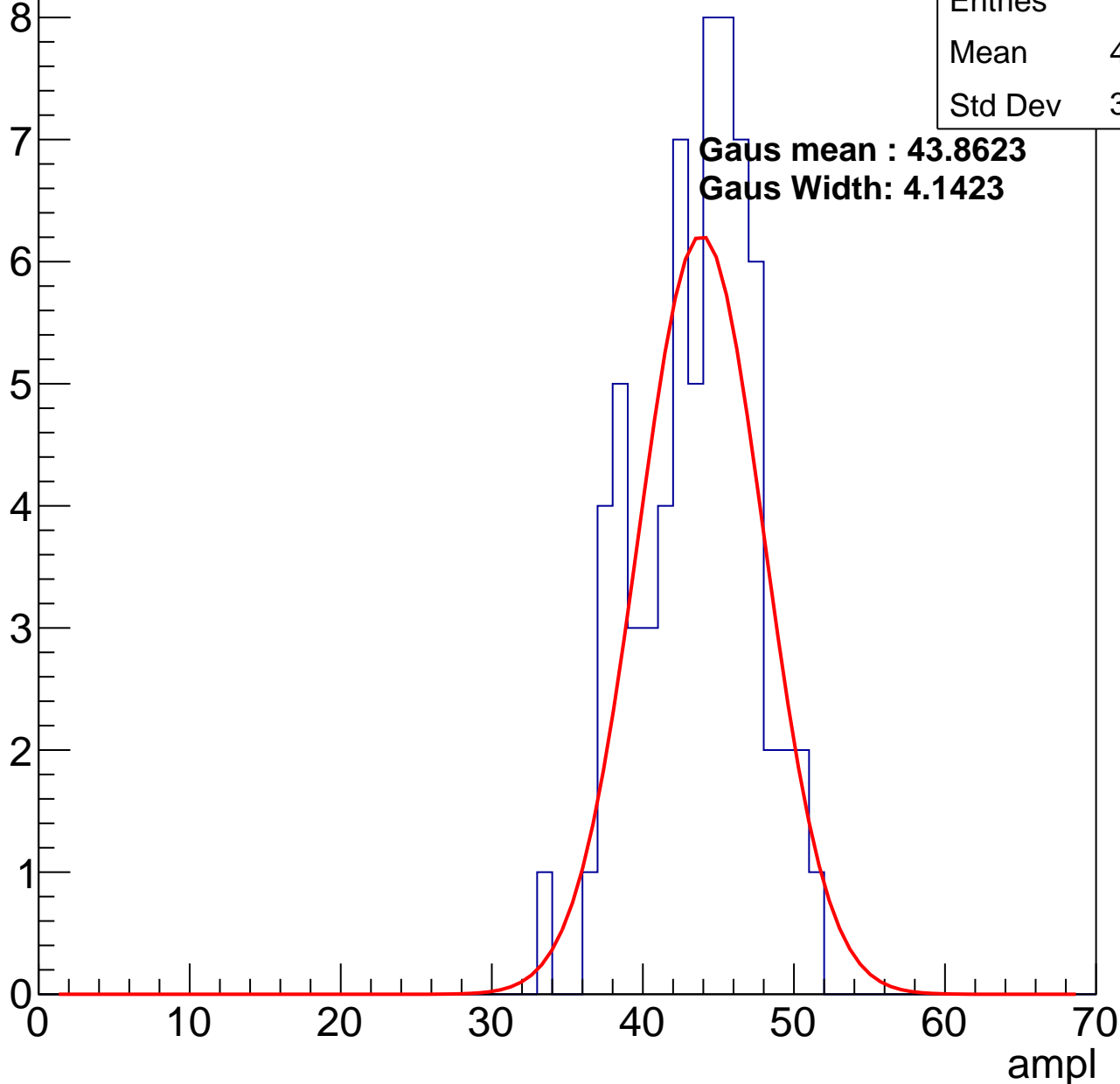
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	43.16
Std Dev	3.802

Gaus mean : 43.8623

Gaus Width: 4.1423

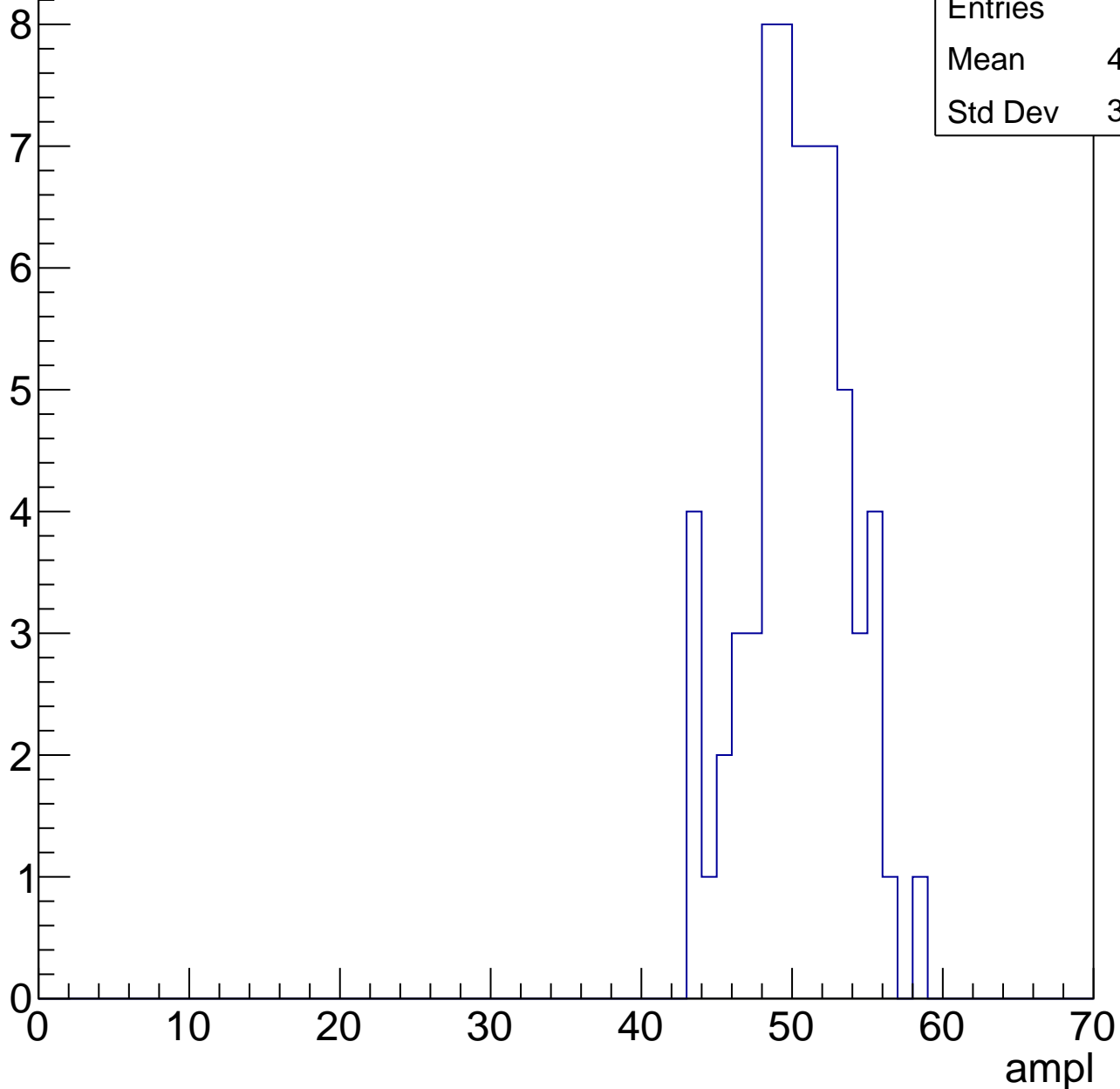


B0L001S, U14-ch88, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.89
Std Dev	3.364

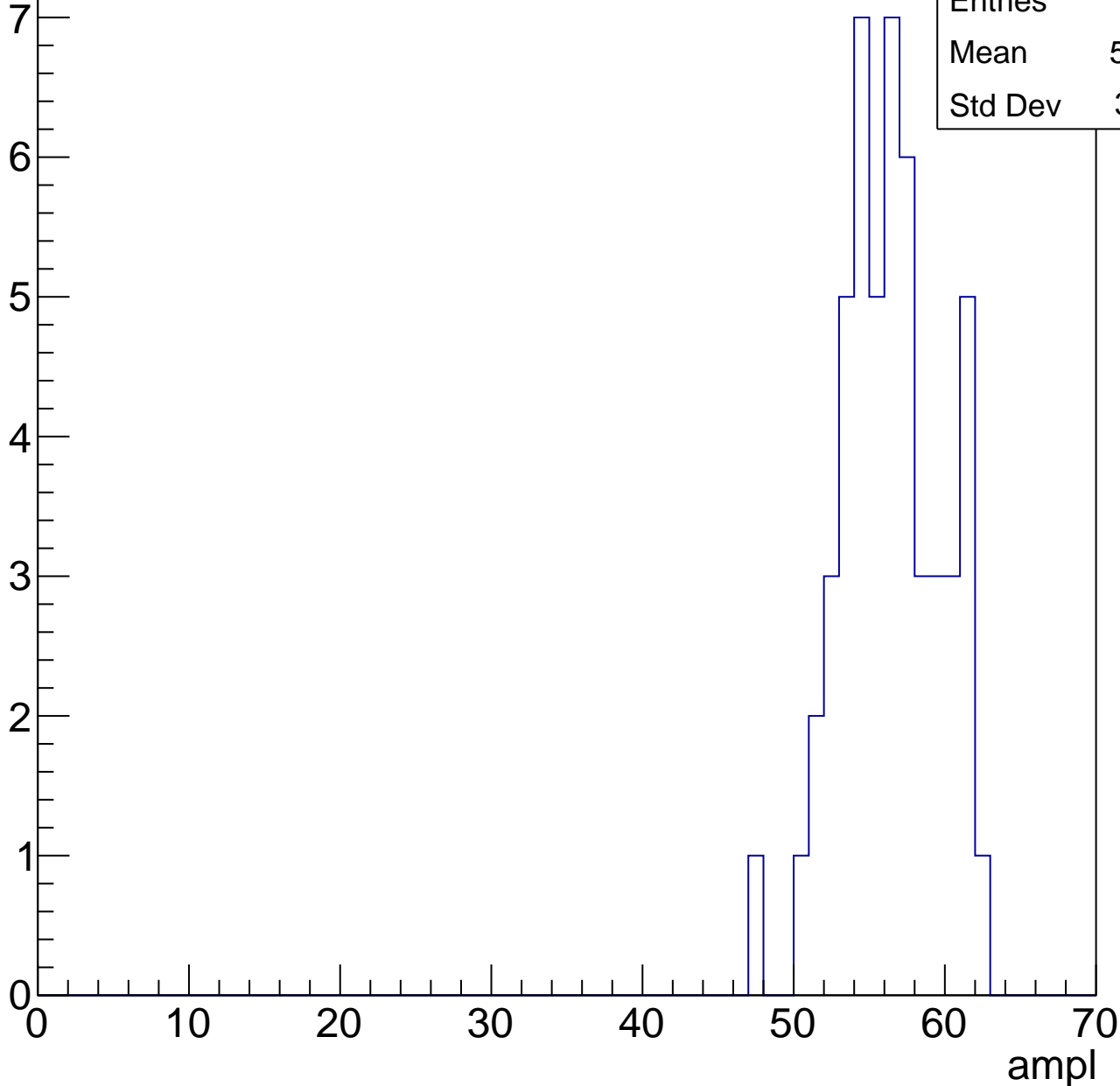


B0L001S, U14-ch88, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	55.87
Std Dev	3.241

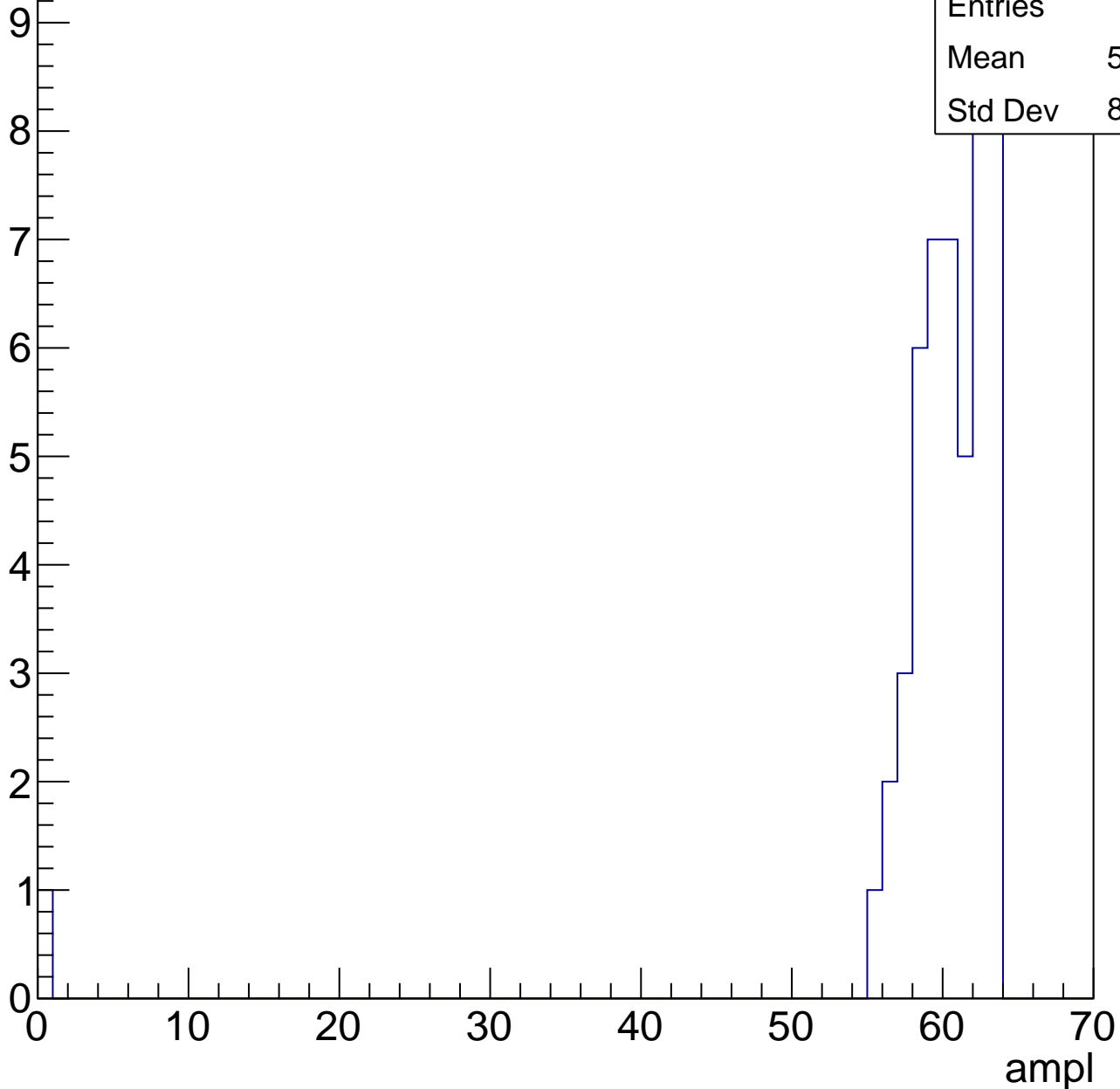


B0L001S, U14-ch88, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

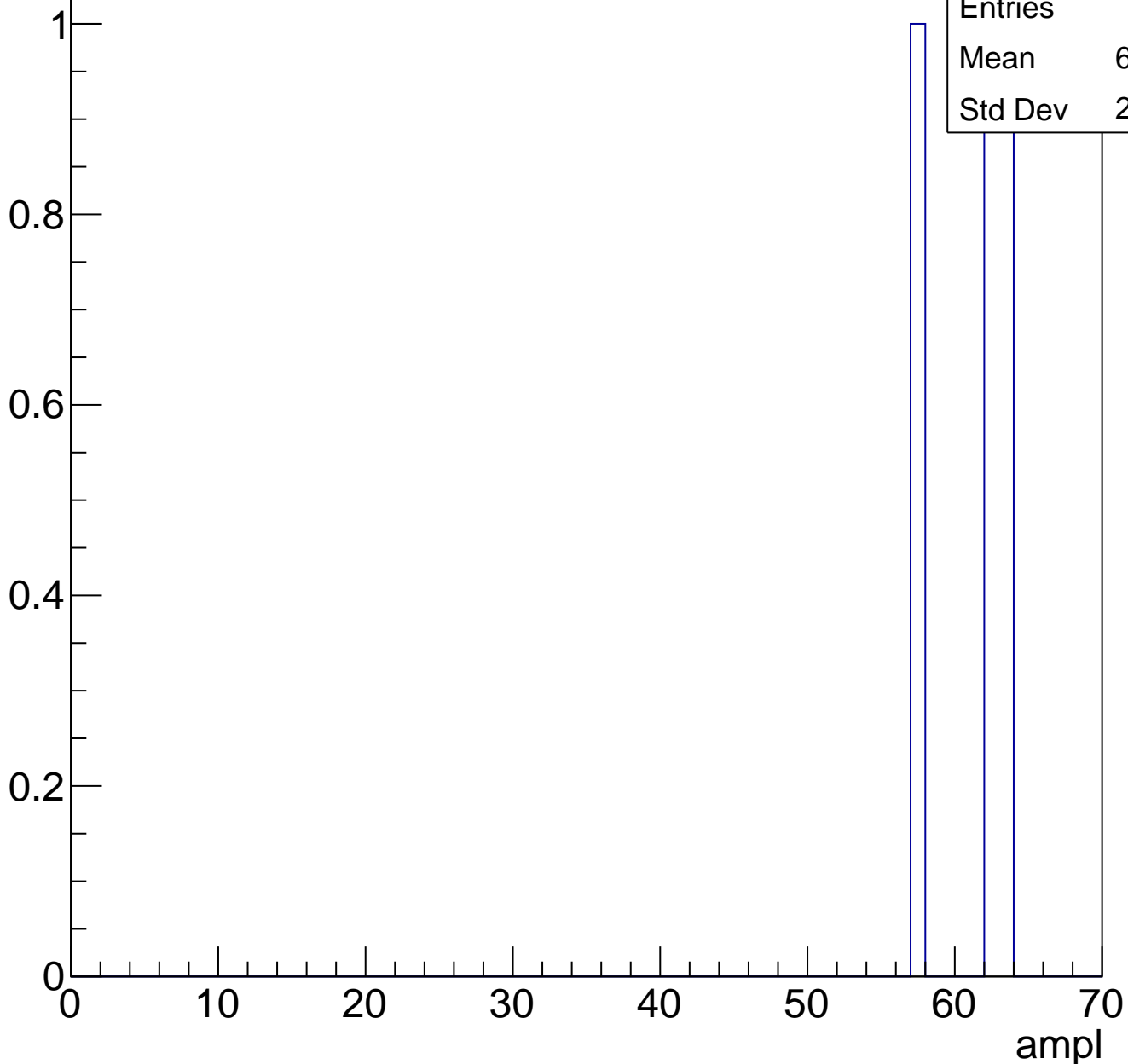
Entries	49
Mean	58.92
Std Dev	8.778



B0L001S, U14-ch88, adc6

calib_packv5_042523_0143.root, FC#9, port A1

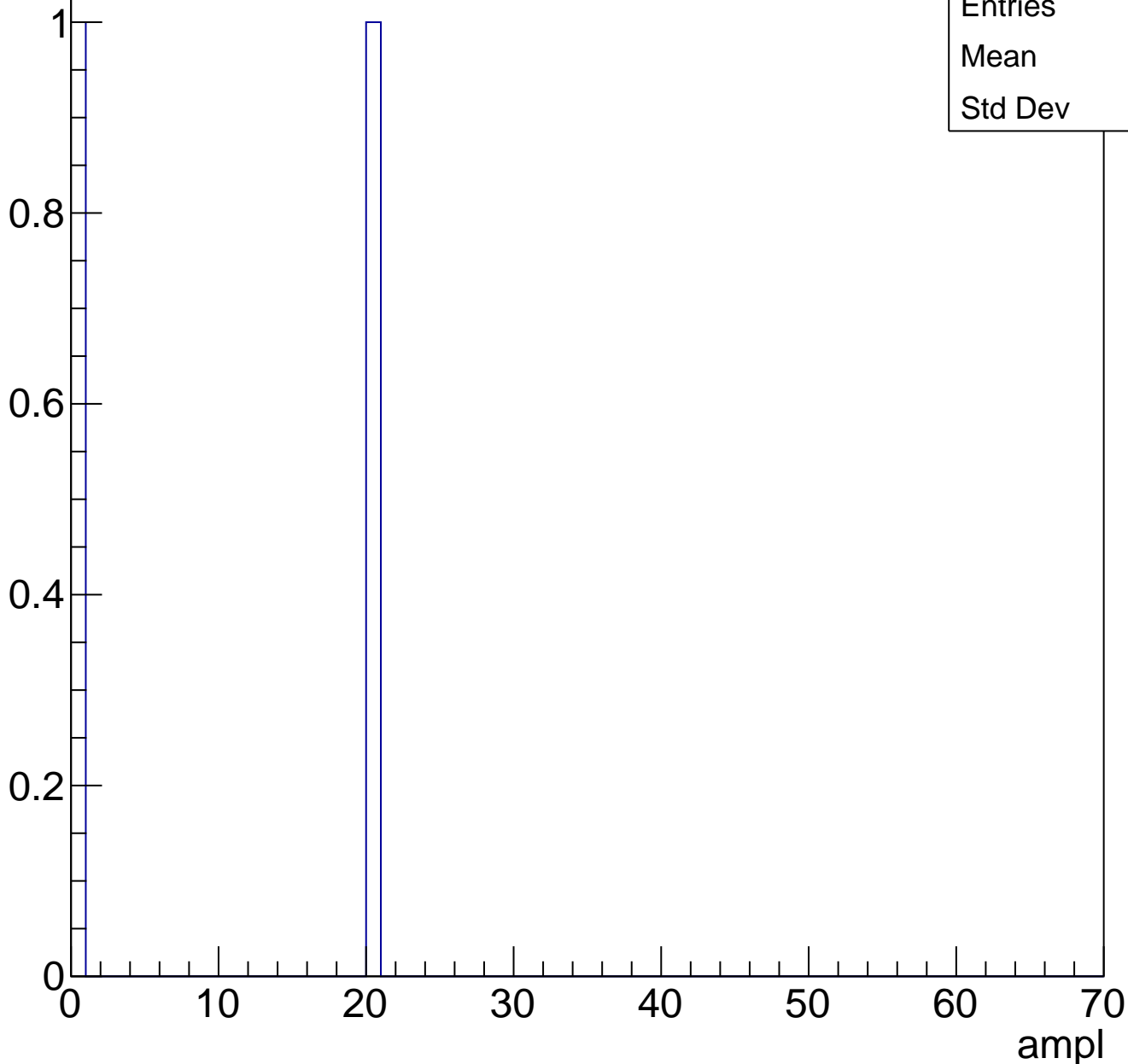
Entry



B0L001S, U14-ch88, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	10
Std Dev	10

B0L001S, U14-ch89, adc0

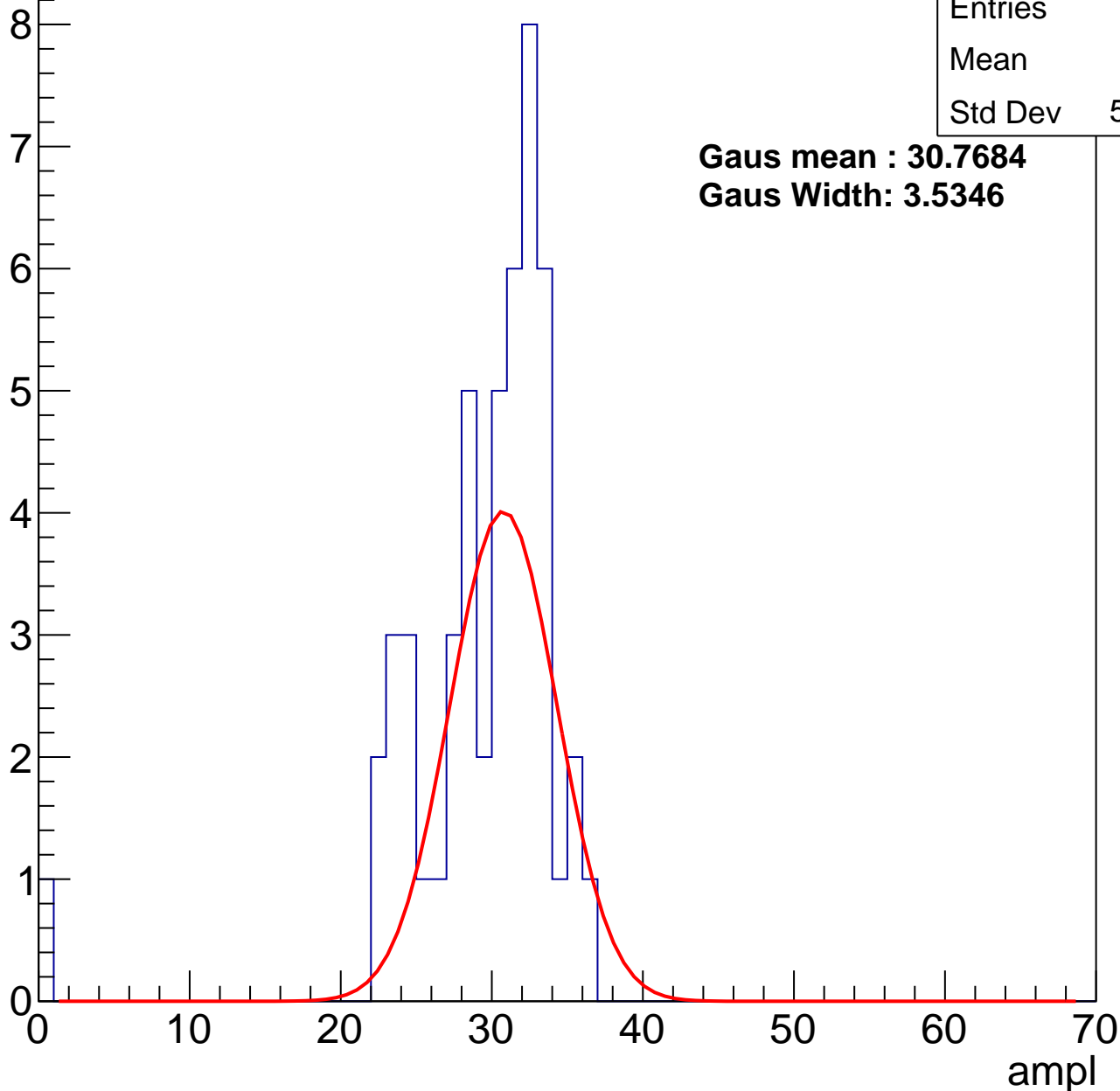
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	28.9
Std Dev	5.489

Gaus mean : 30.7684

Gaus Width: 3.5346



B0L001S, U14-ch89, adc1

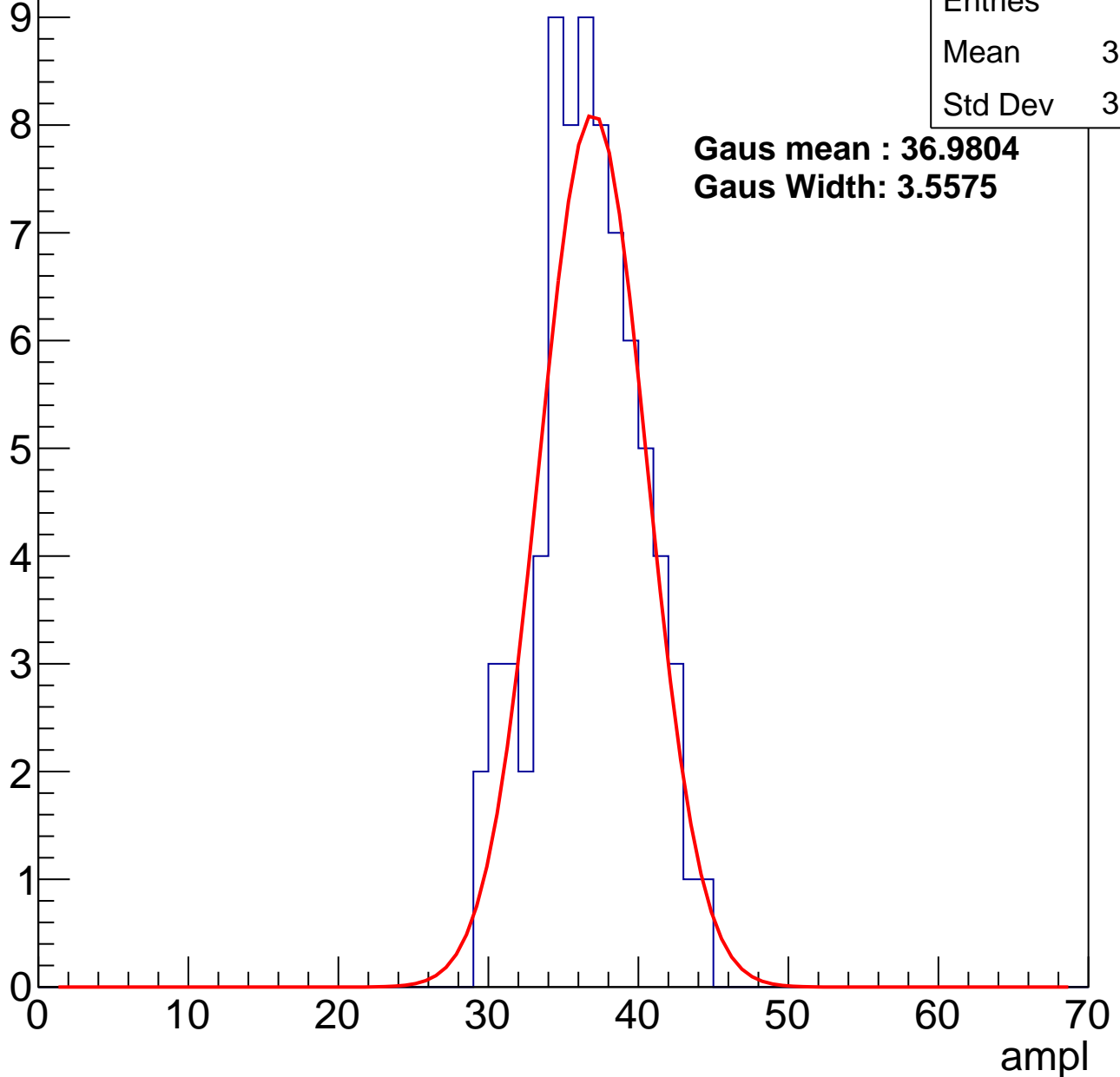
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	75
Mean	36.27
Std Dev	3.423

Gaus mean : 36.9804

Gaus Width: 3.5575



B0L001S, U14-ch89, adc2

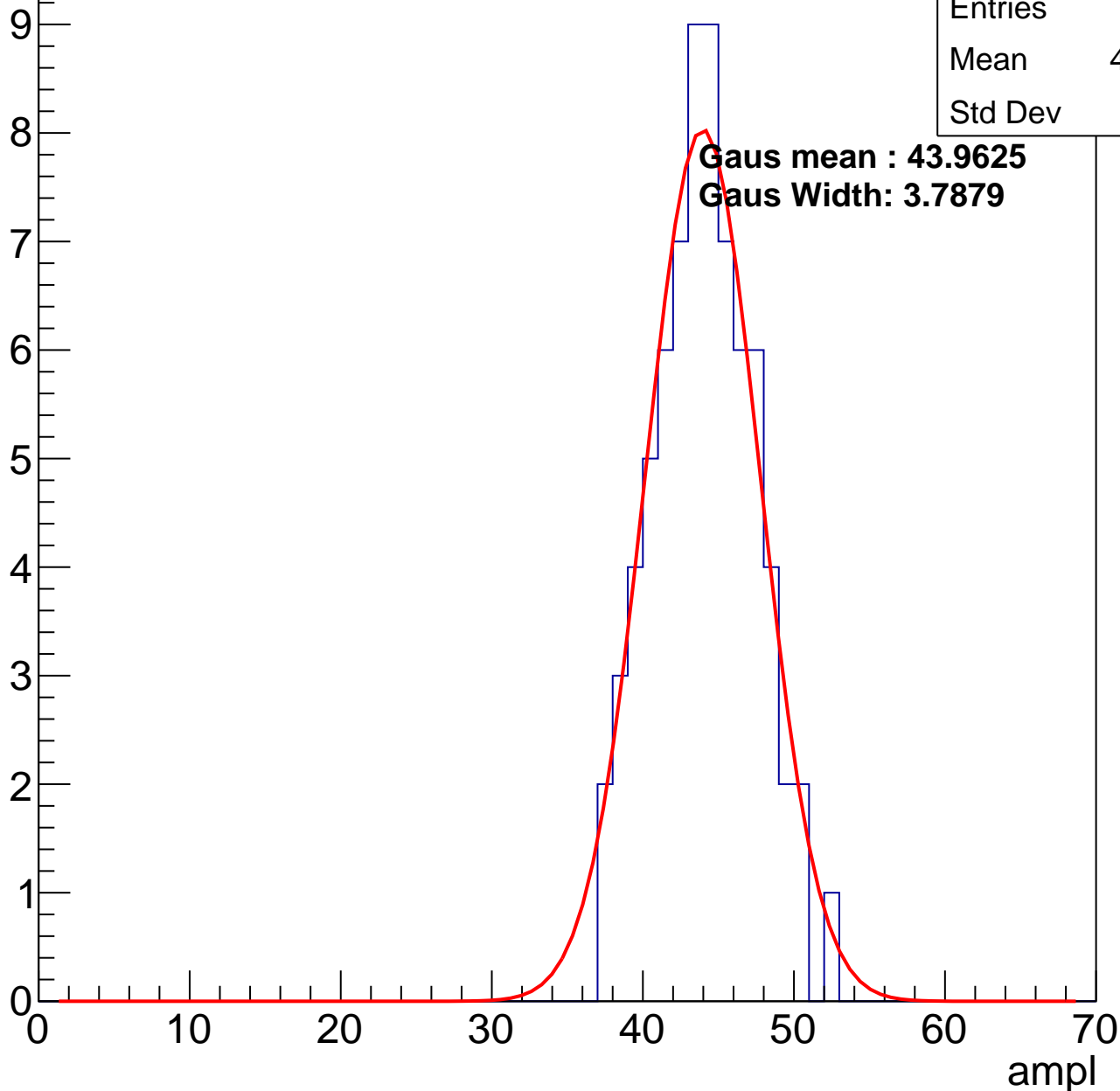
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	43.59
Std Dev	3.31

Gaus mean : 43.9625

Gaus Width: 3.7879



B0L001S, U14-ch89, adc3

calib_packv5_042523_0143.root, FC#9, port A1

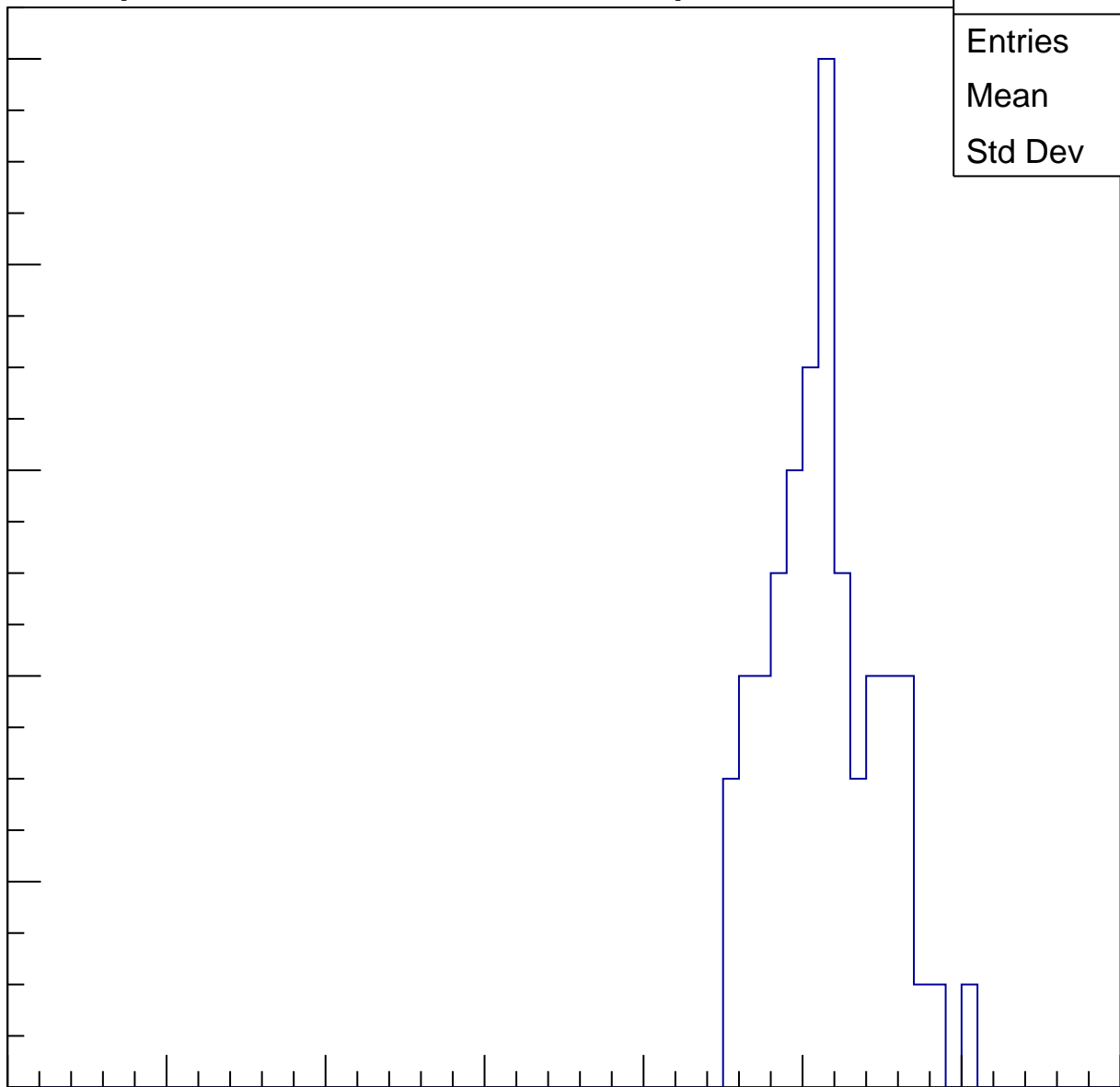
Entries	62
Mean	50.89
Std Dev	3.432

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

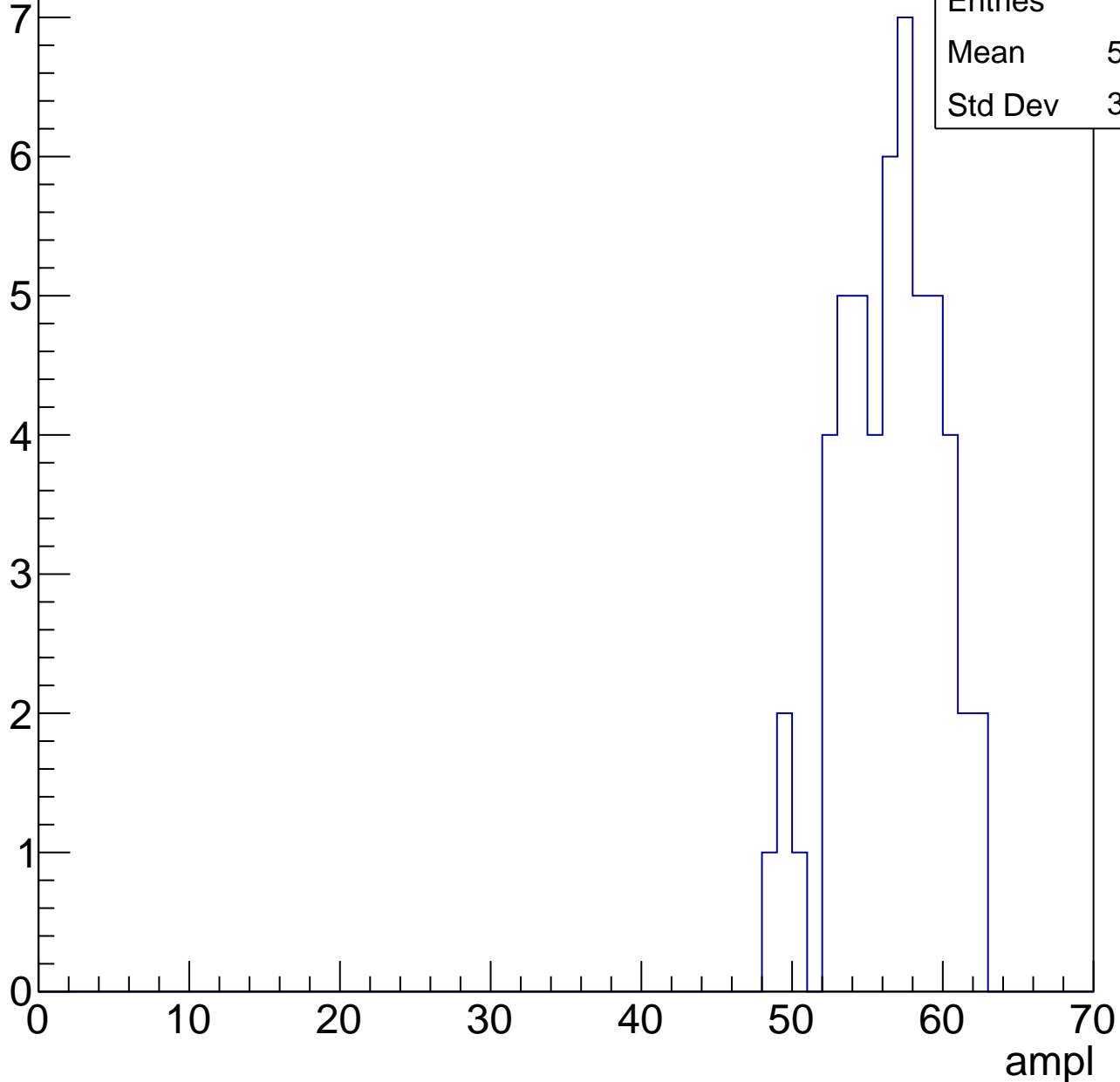
ampl



B0L001S, U14-ch89, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch89, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	45
Mean	58.69
Std Dev	9.273

Entry

10

8

6

4

2

0

0

10

20

30

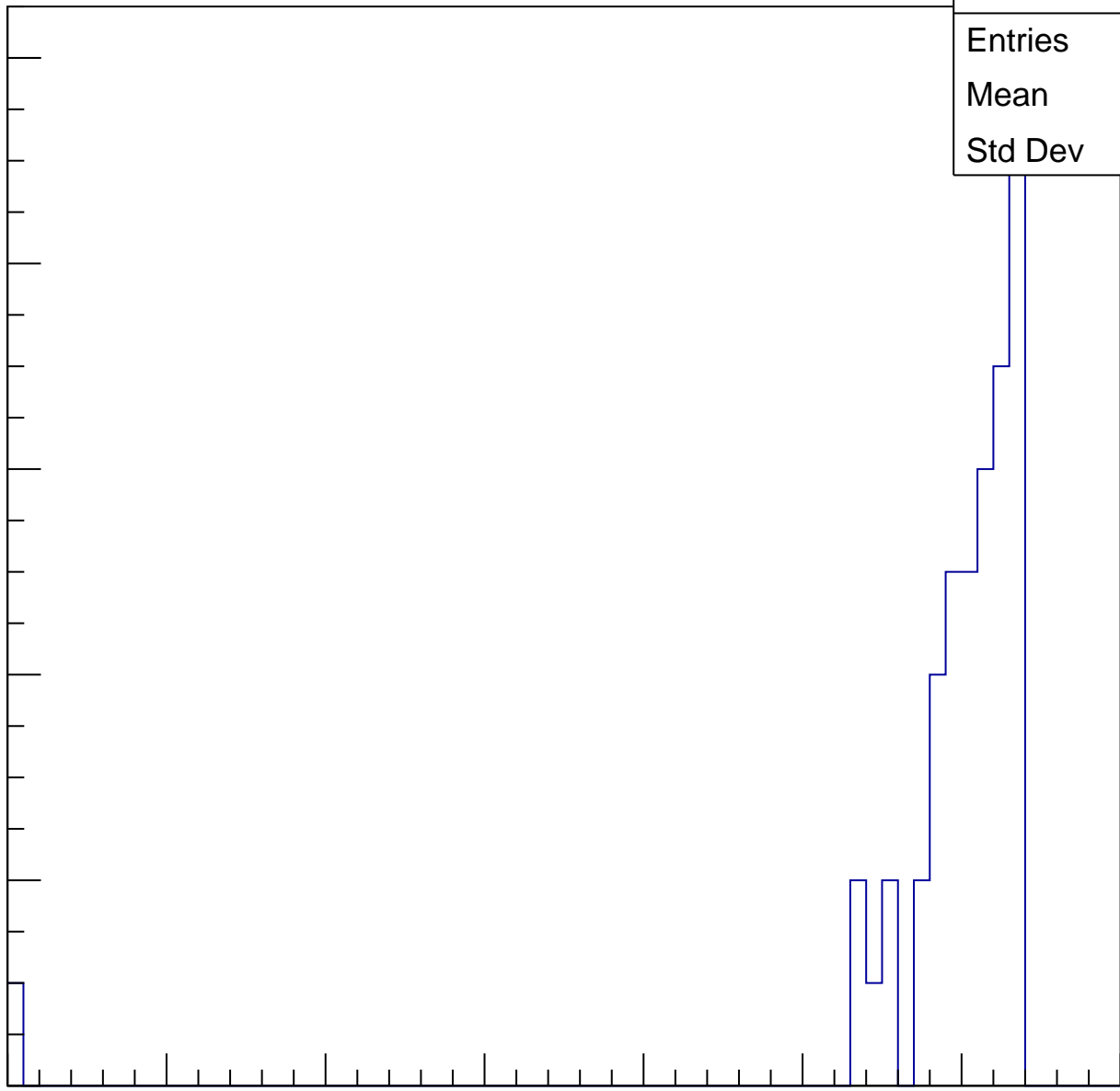
40

50

60

70

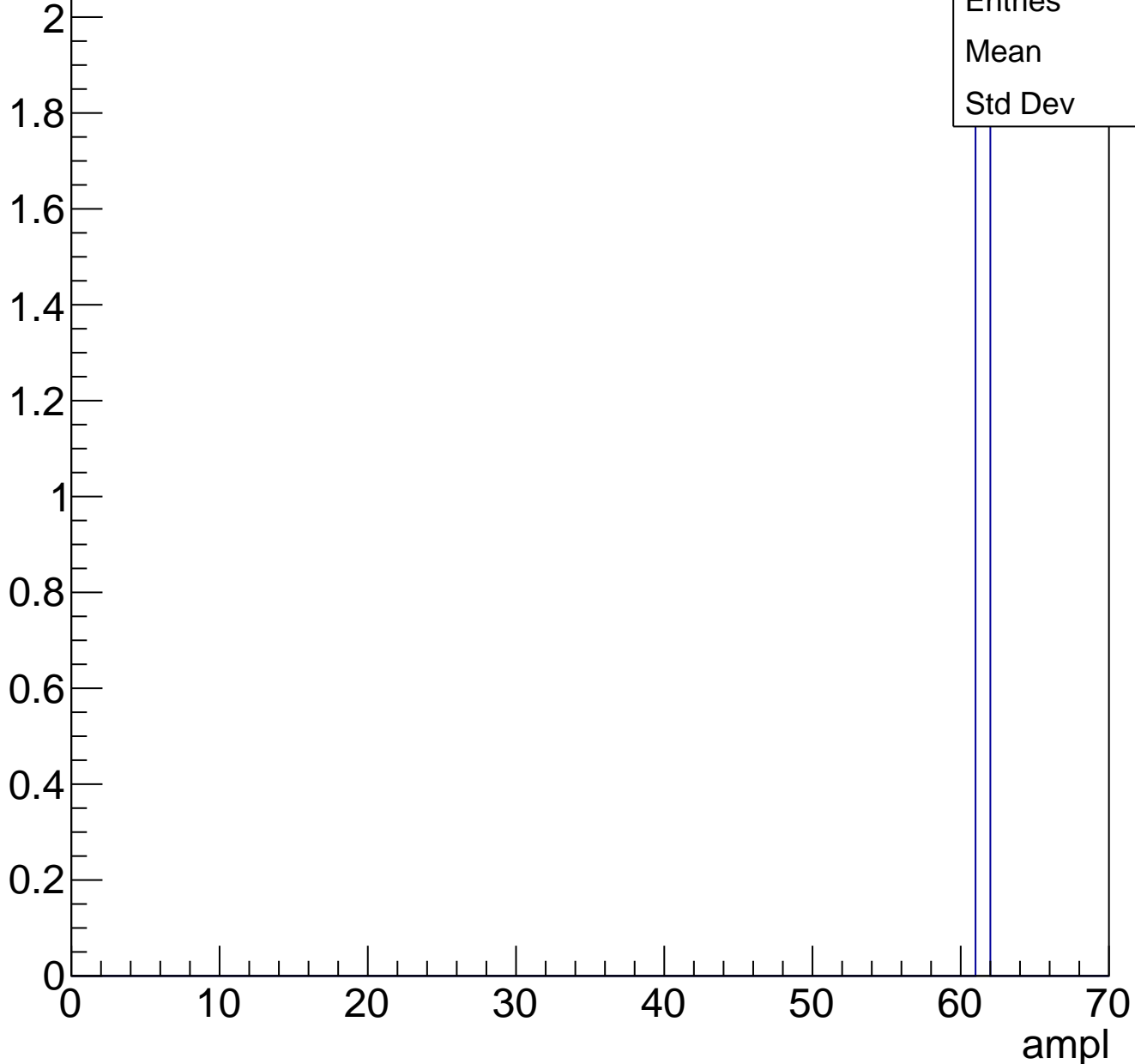
ampl



B0L001S, U14-ch89, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch89, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L001S, U14-ch90, adc0

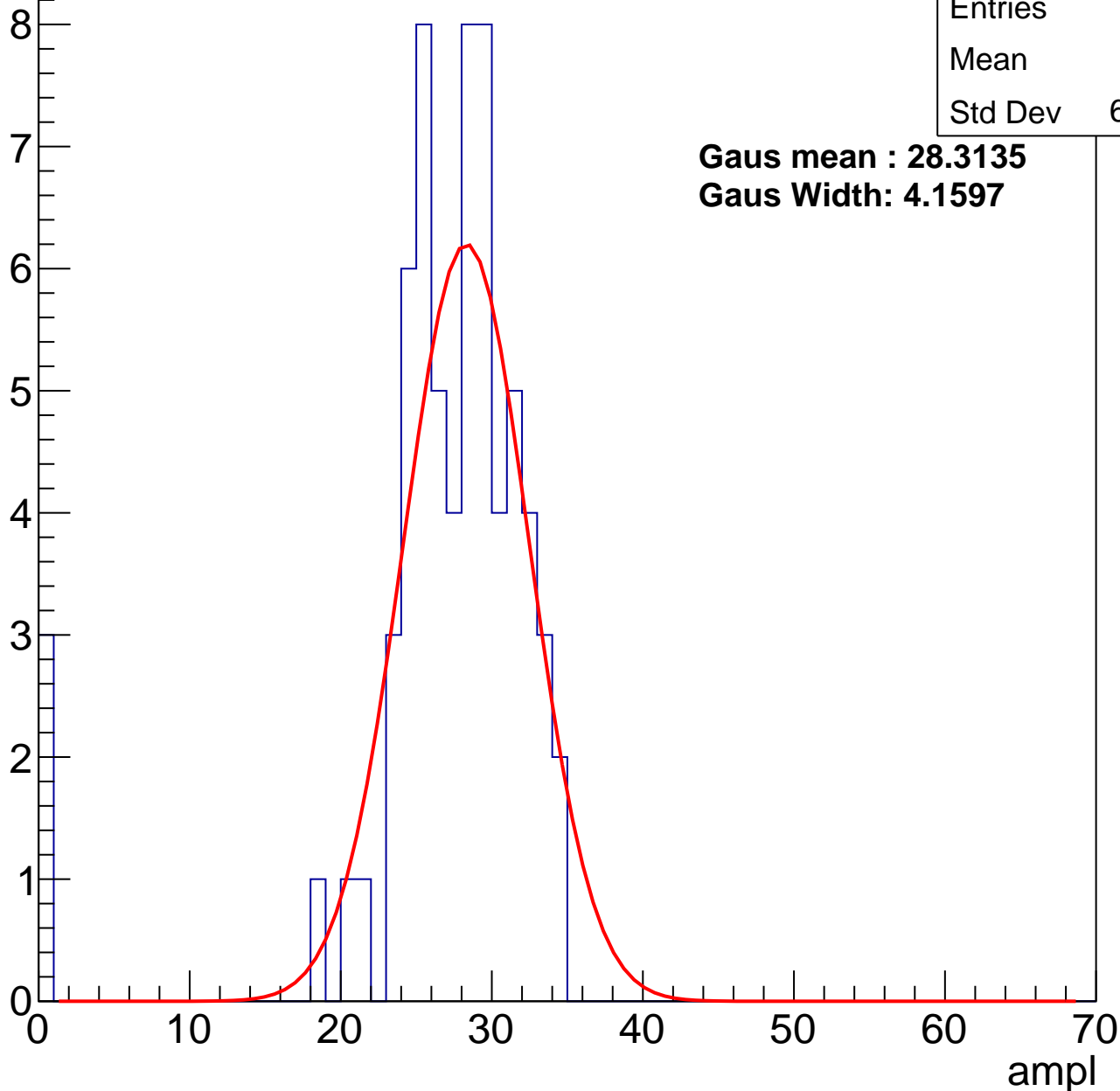
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	26.3
Std Dev	6.654

Gaus mean : 28.3135

Gaus Width: 4.1597



B0L001S, U14-ch90, adc1

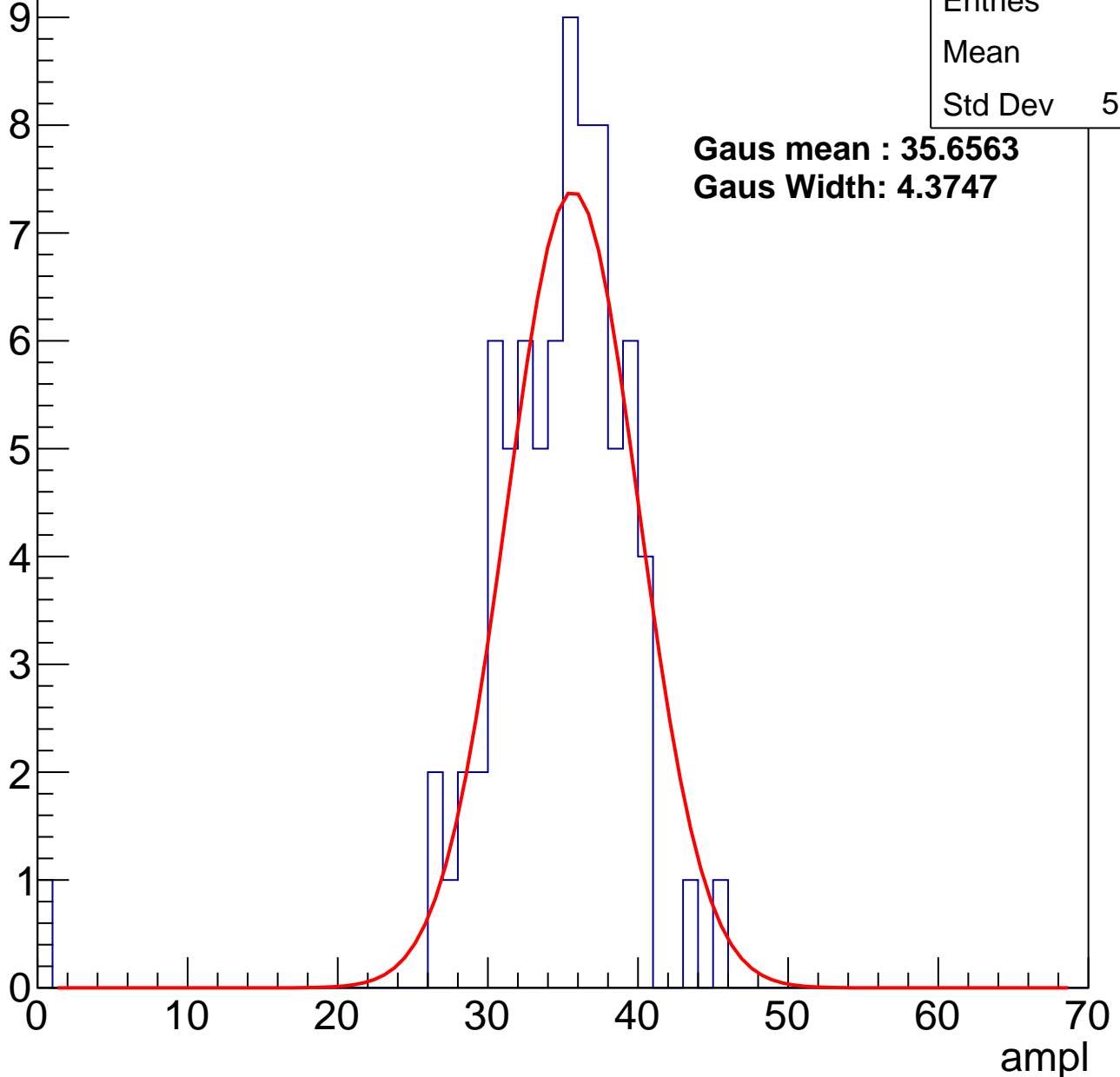
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	34.1
Std Dev	5.446

Gaus mean : 35.6563

Gaus Width: 4.3747



B0L001S, U14-ch90, adc2

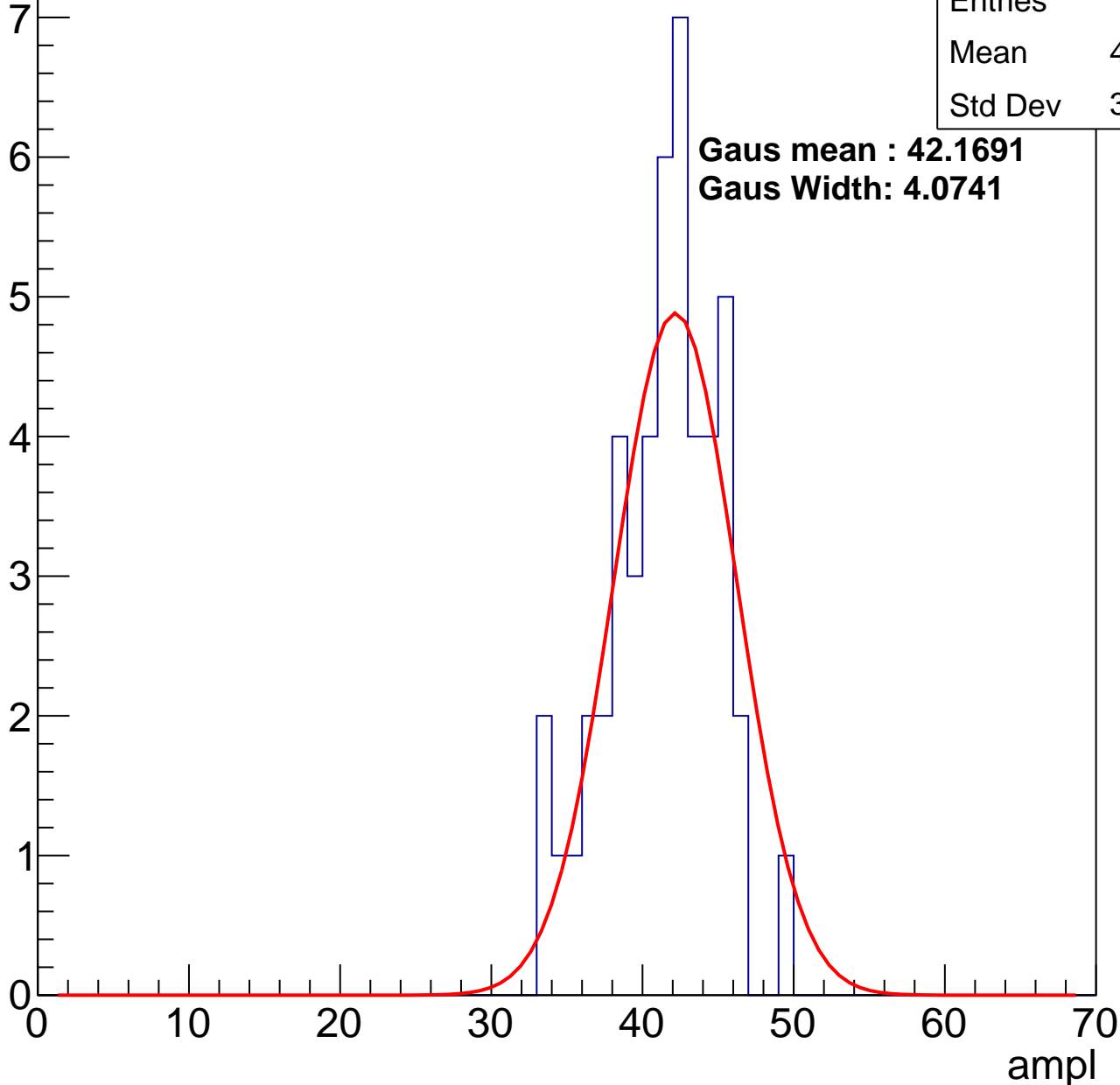
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	40.92
Std Dev	3.535

Gaus mean : 42.1691

Gaus Width: 4.0741

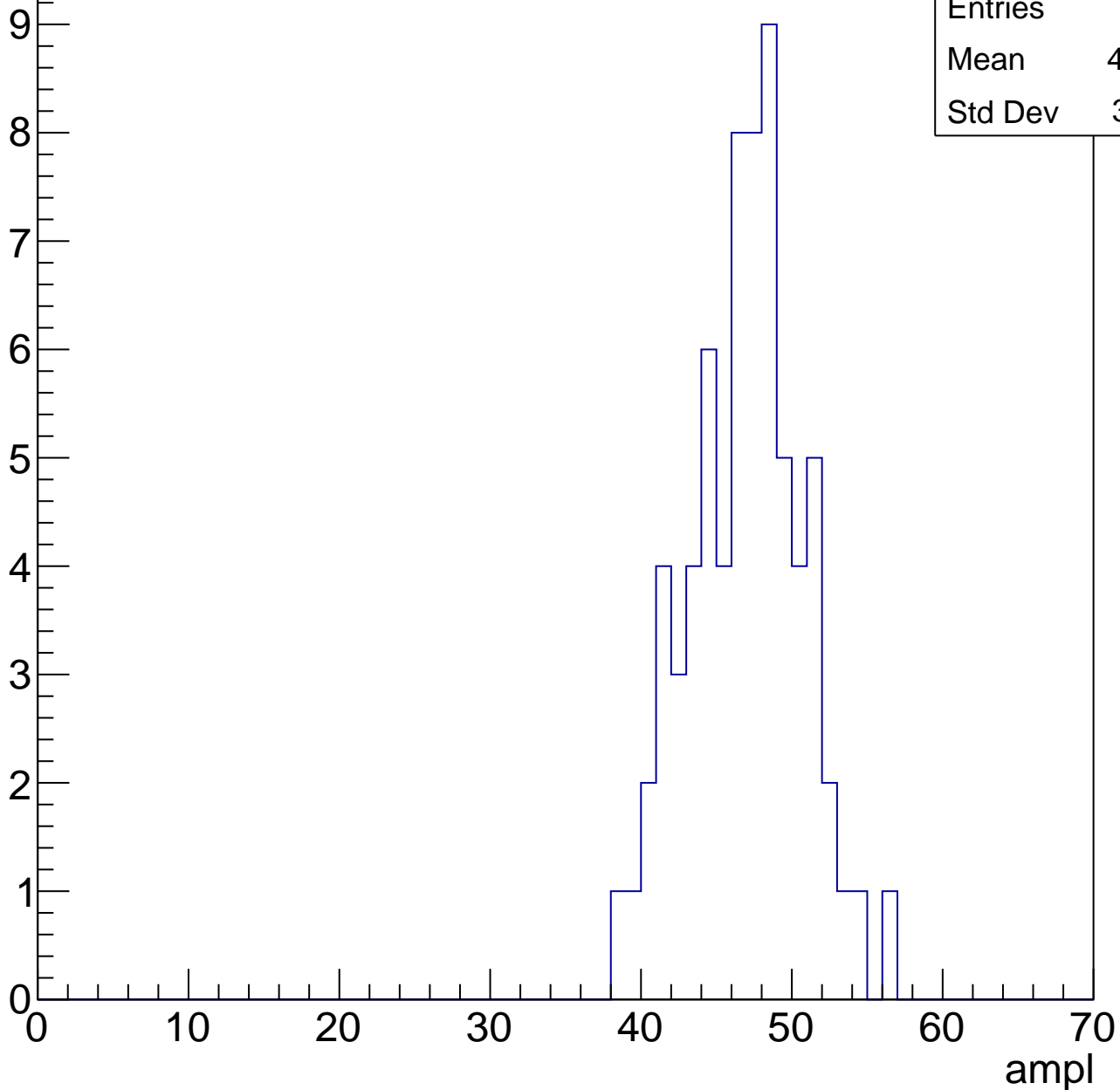


B0L001S, U14-ch90, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	46.46
Std Dev	3.701

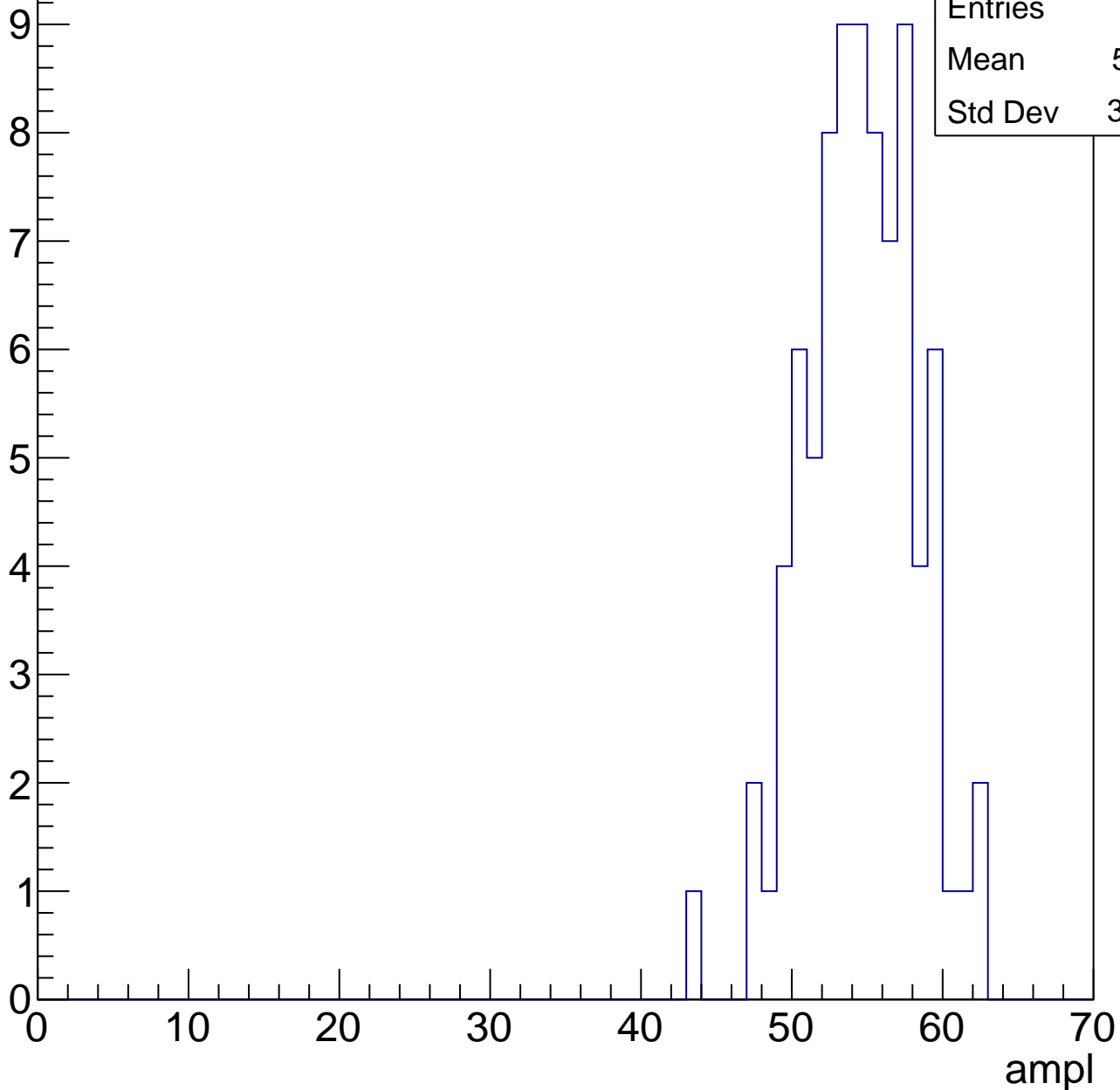


B0L001S, U14-ch90, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

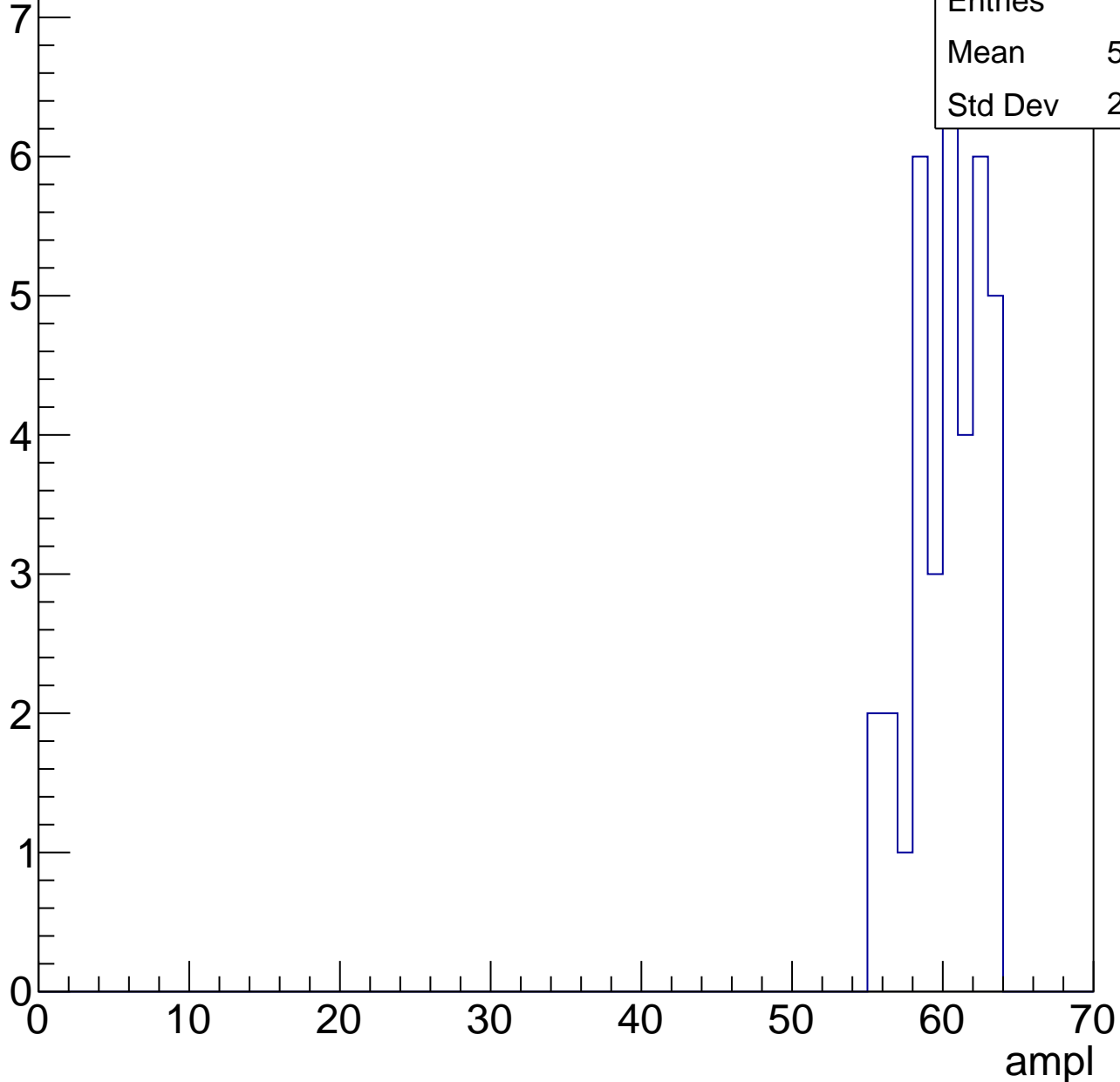
Entries	83
Mean	54.11
Std Dev	3.607



B0L001S, U14-ch90, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

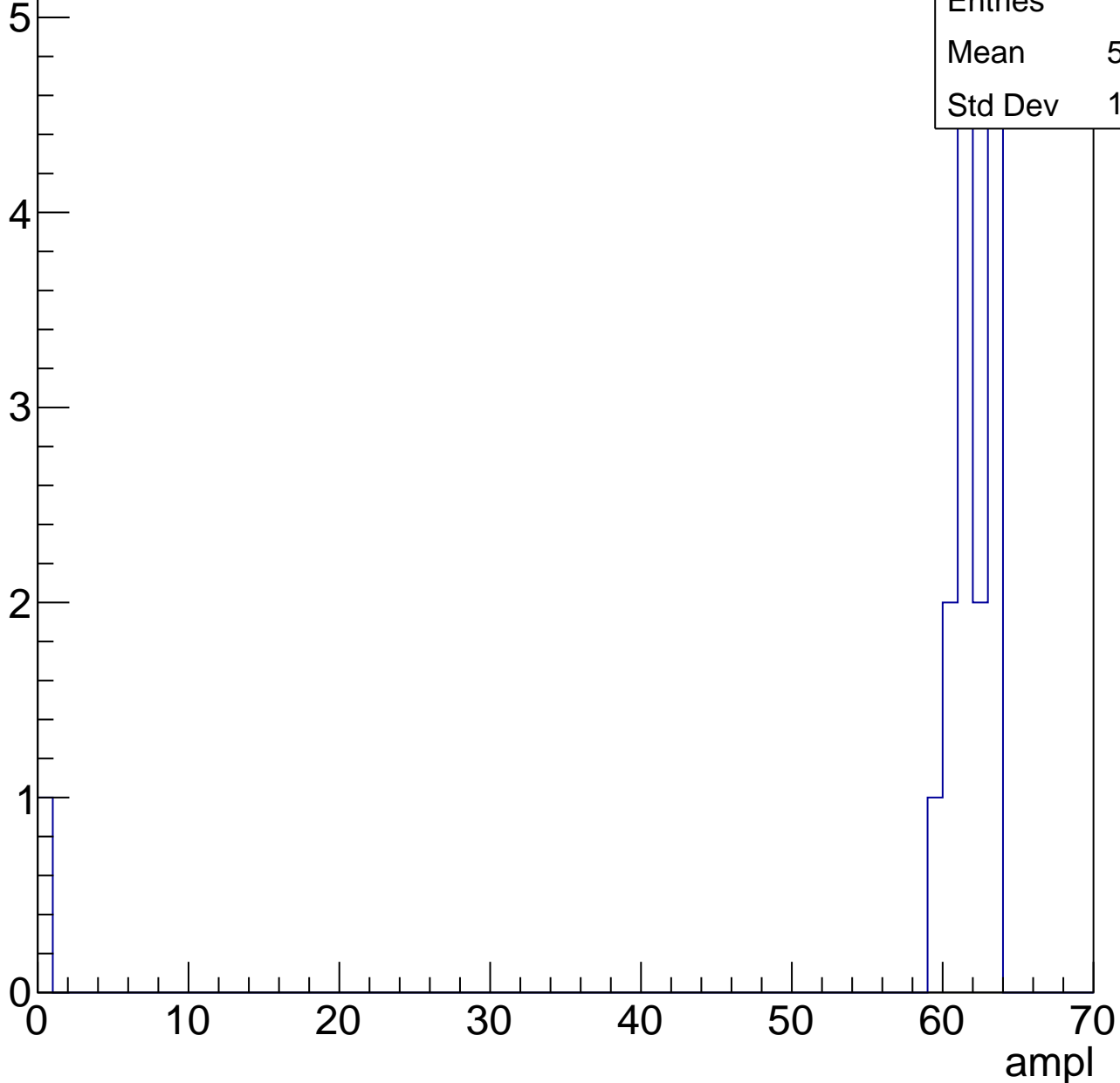


B0L001S, U14-ch90, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	16
Mean	57.69
Std Dev	14.94



B0L001S, U14-ch90, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch91, adc0

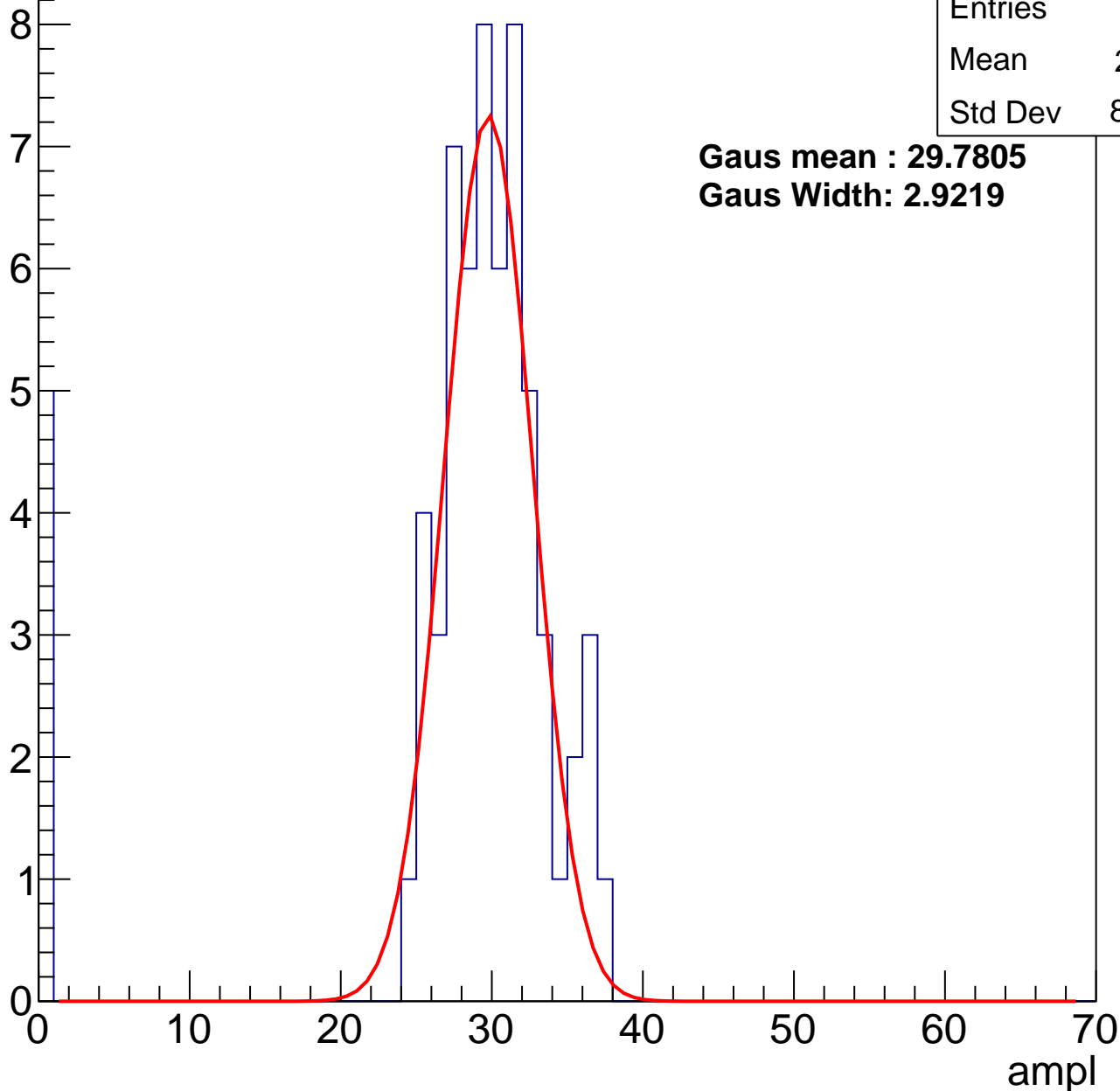
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	27.41
Std Dev	8.578

Gaus mean : 29.7805

Gaus Width: 2.9219



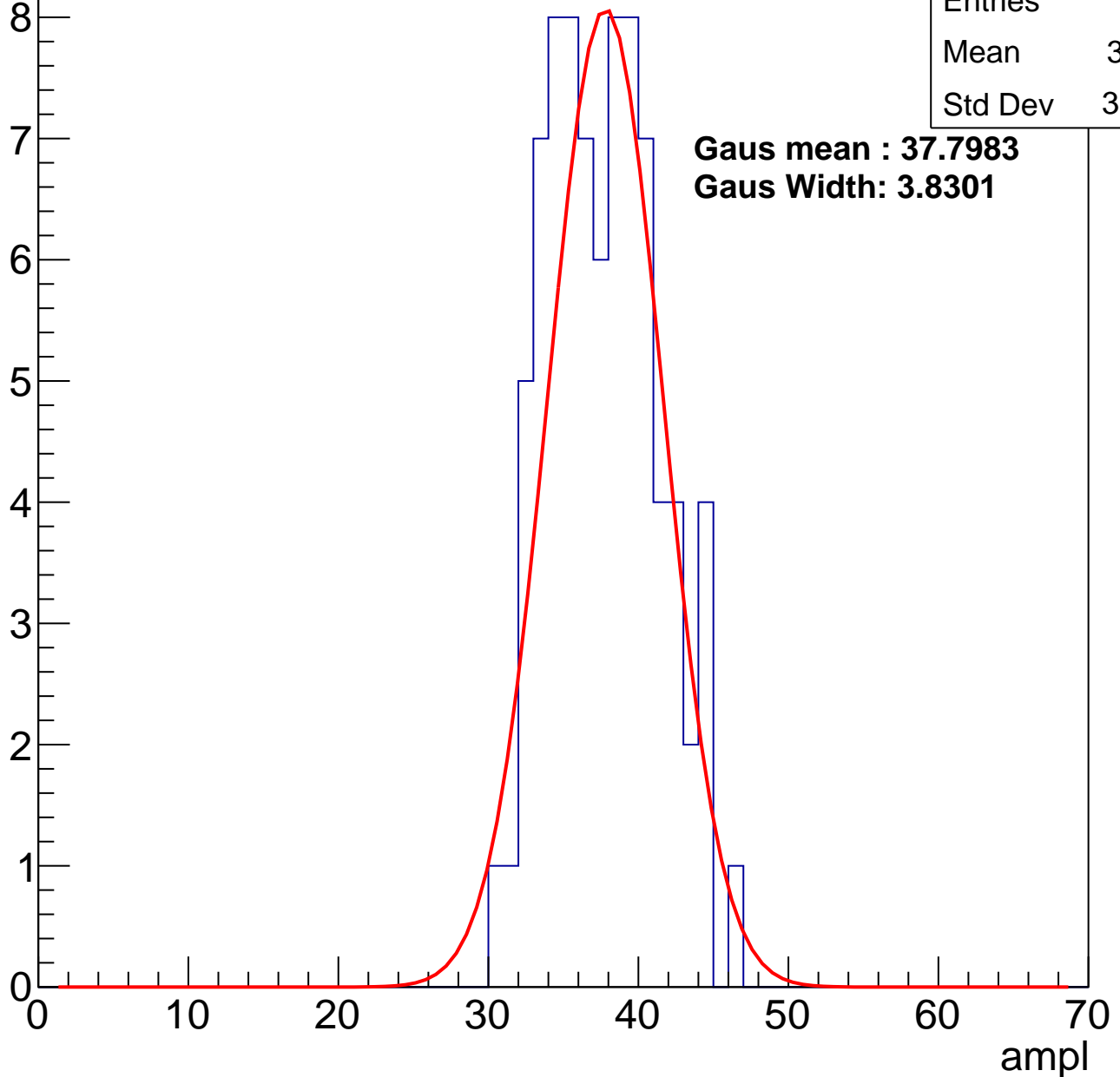
B0L001S, U14-ch91, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.21
Std Dev	3.593

Gaus mean : 37.7983
Gaus Width: 3.8301



B0L001S, U14-ch91, adc2

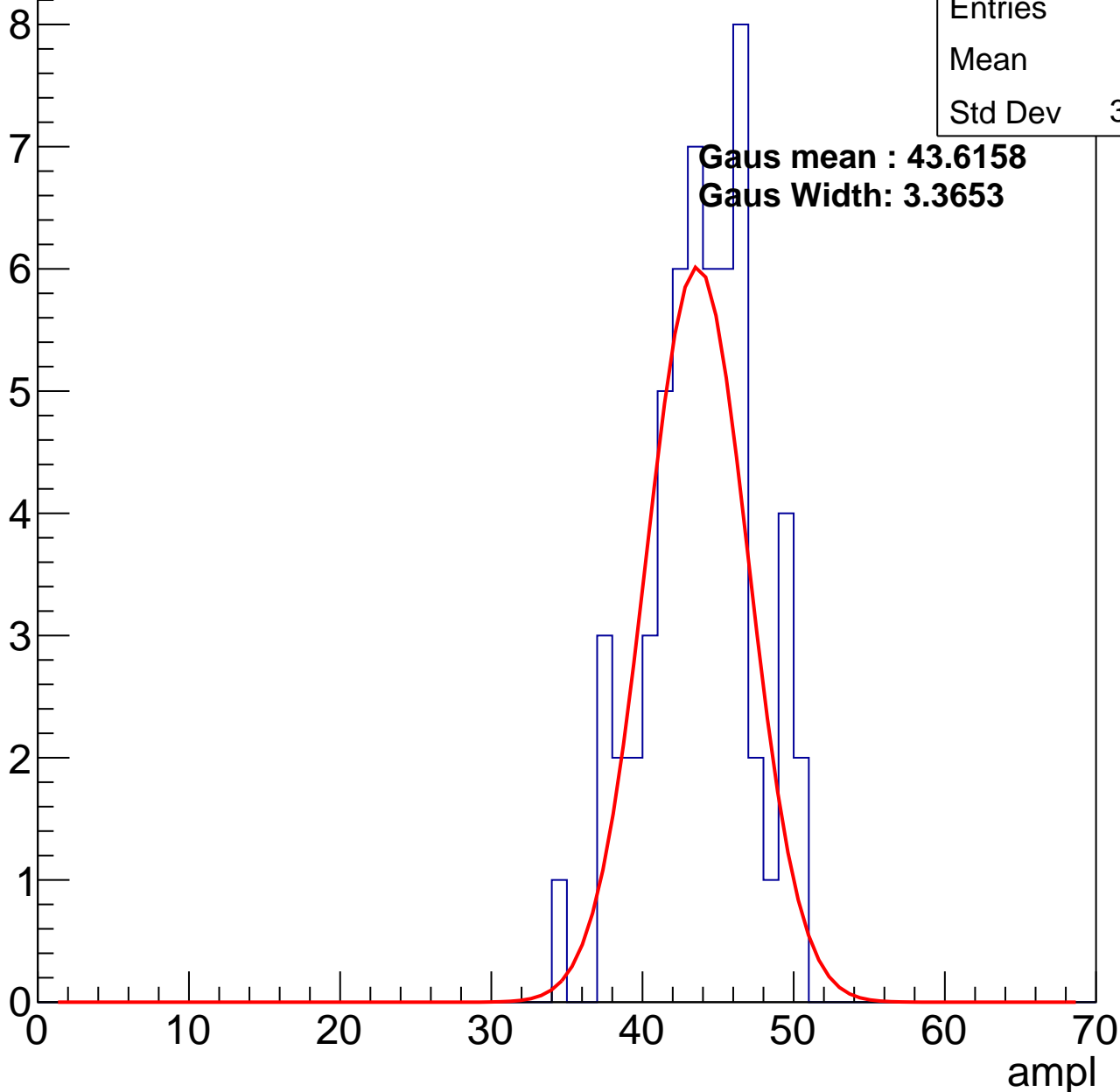
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.4
Std Dev	3.513

Gaus mean : 43.6158

Gaus Width: 3.3653

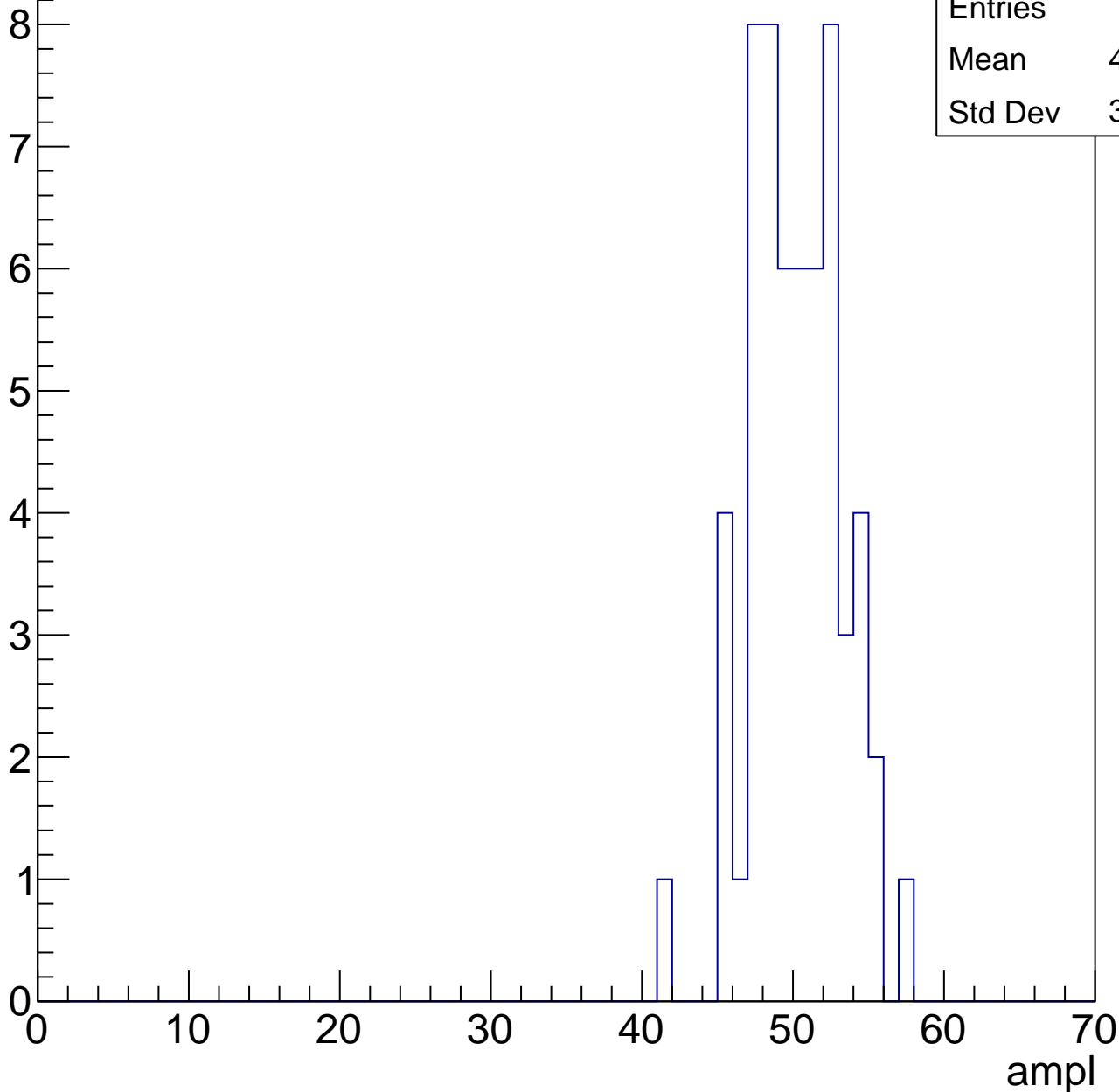


B0L001S, U14-ch91, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

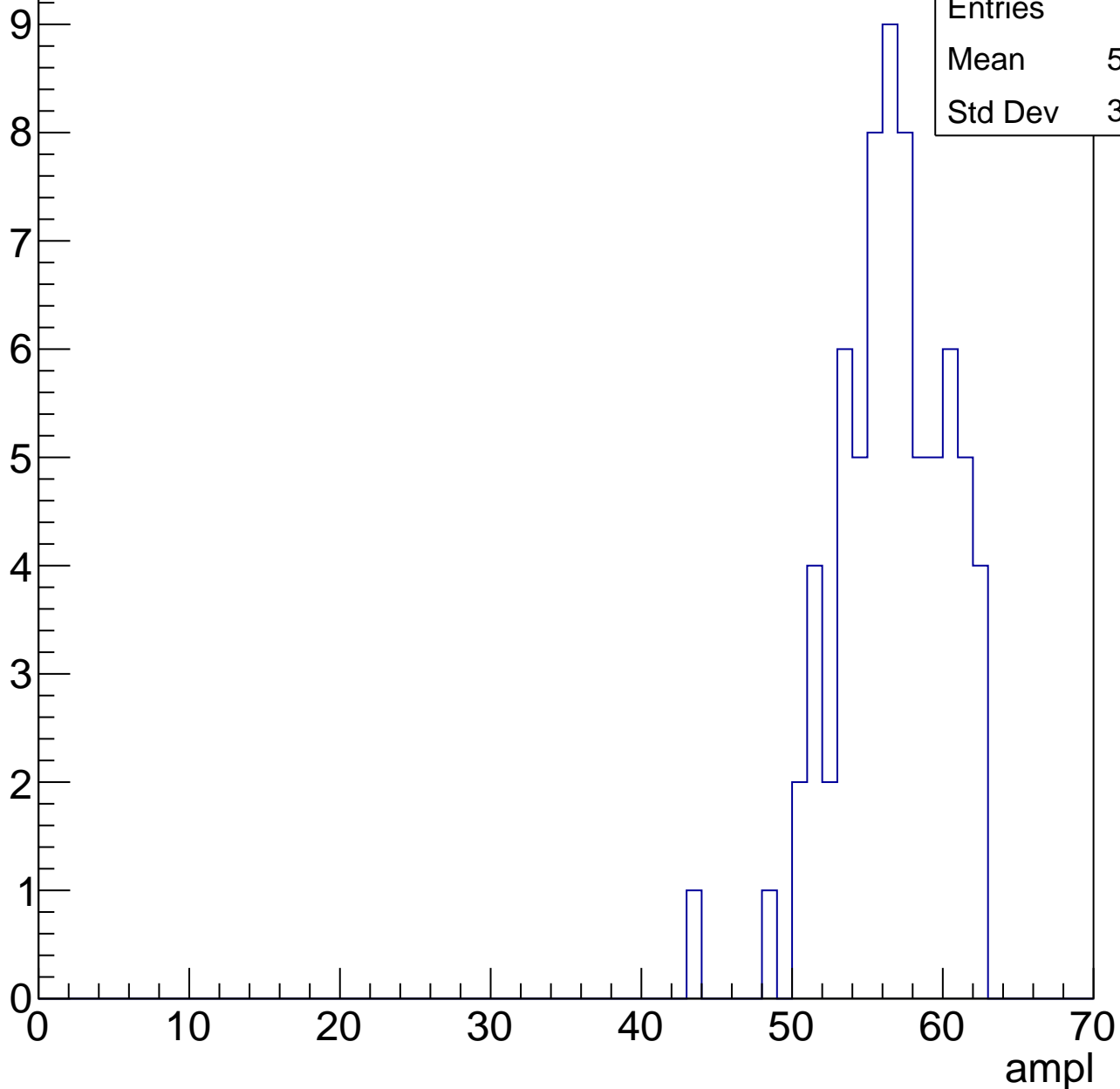
Entries	58
Mean	49.74
Std Dev	3.026



B0L001S, U14-ch91, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



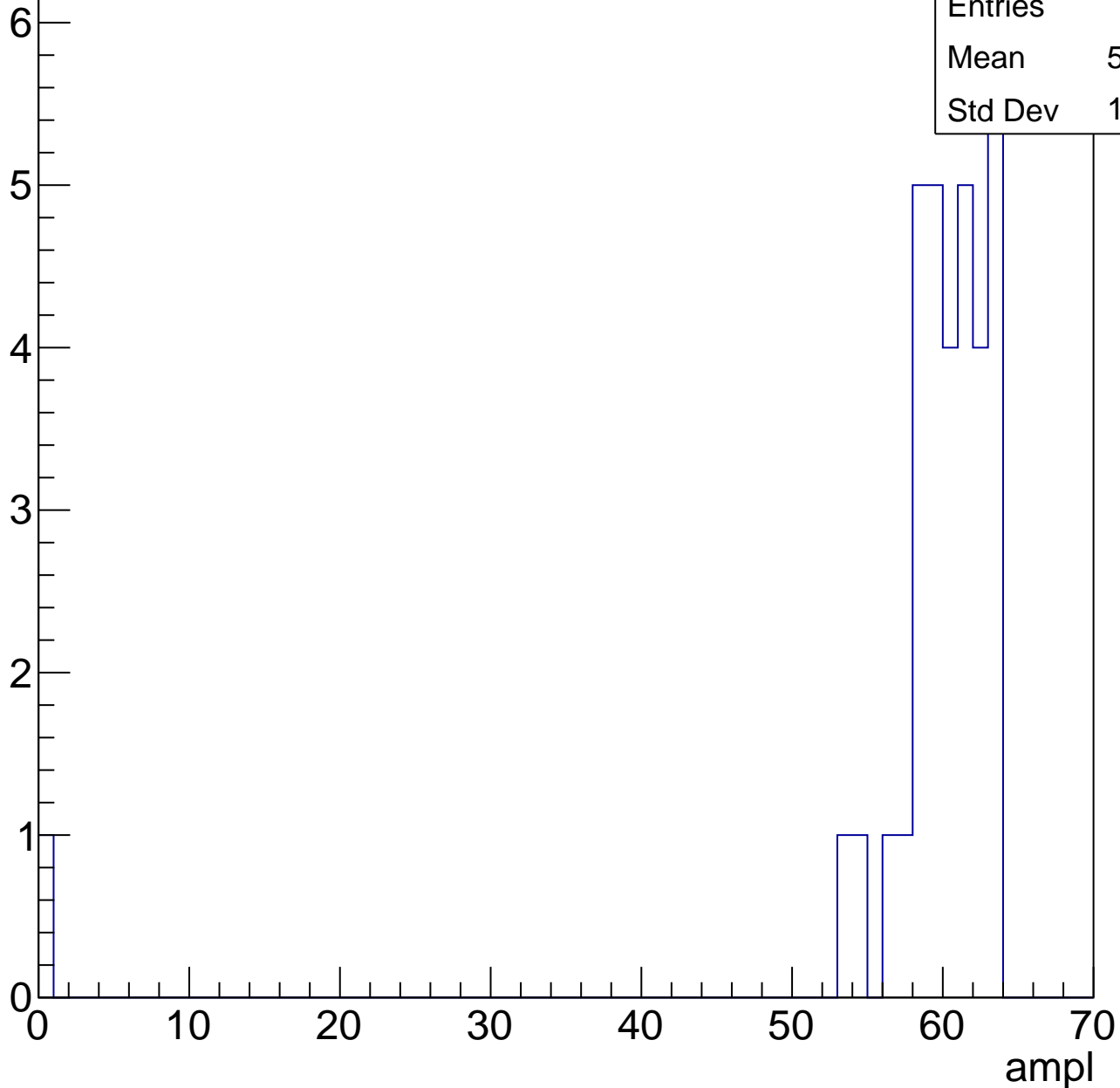
Entries	71
Mean	56.13
Std Dev	3.669

B0L001S, U14-ch91, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	34
Mean	58.12
Std Dev	10.42



B0L001S, U14-ch91, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	6
Mean	62.67
Std Dev	0.4714

B0L001S, U14-ch91, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch92, adc0

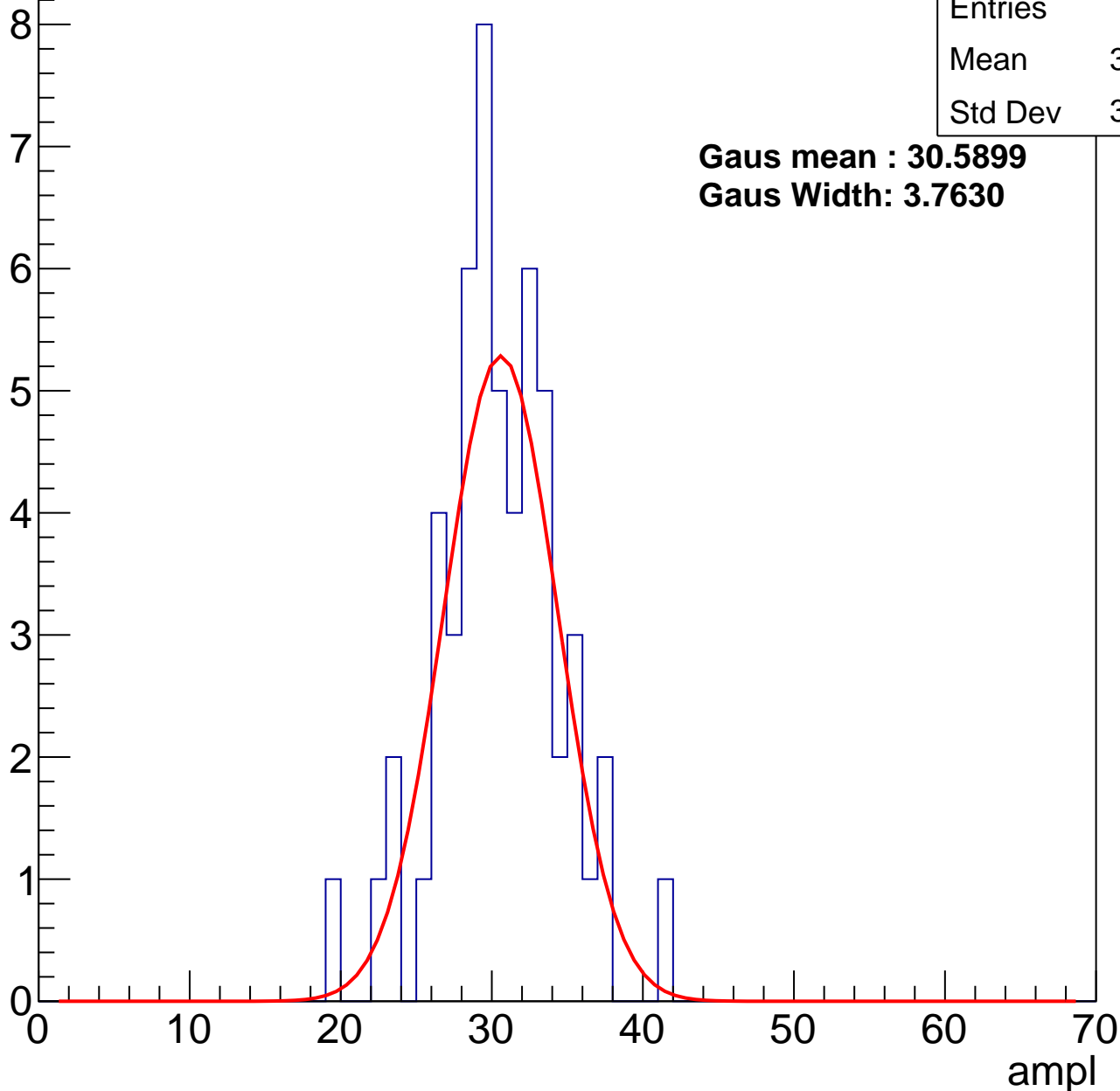
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	30.04
Std Dev	3.968

Gaus mean : 30.5899

Gaus Width: 3.7630



B0L001S, U14-ch92, adc1

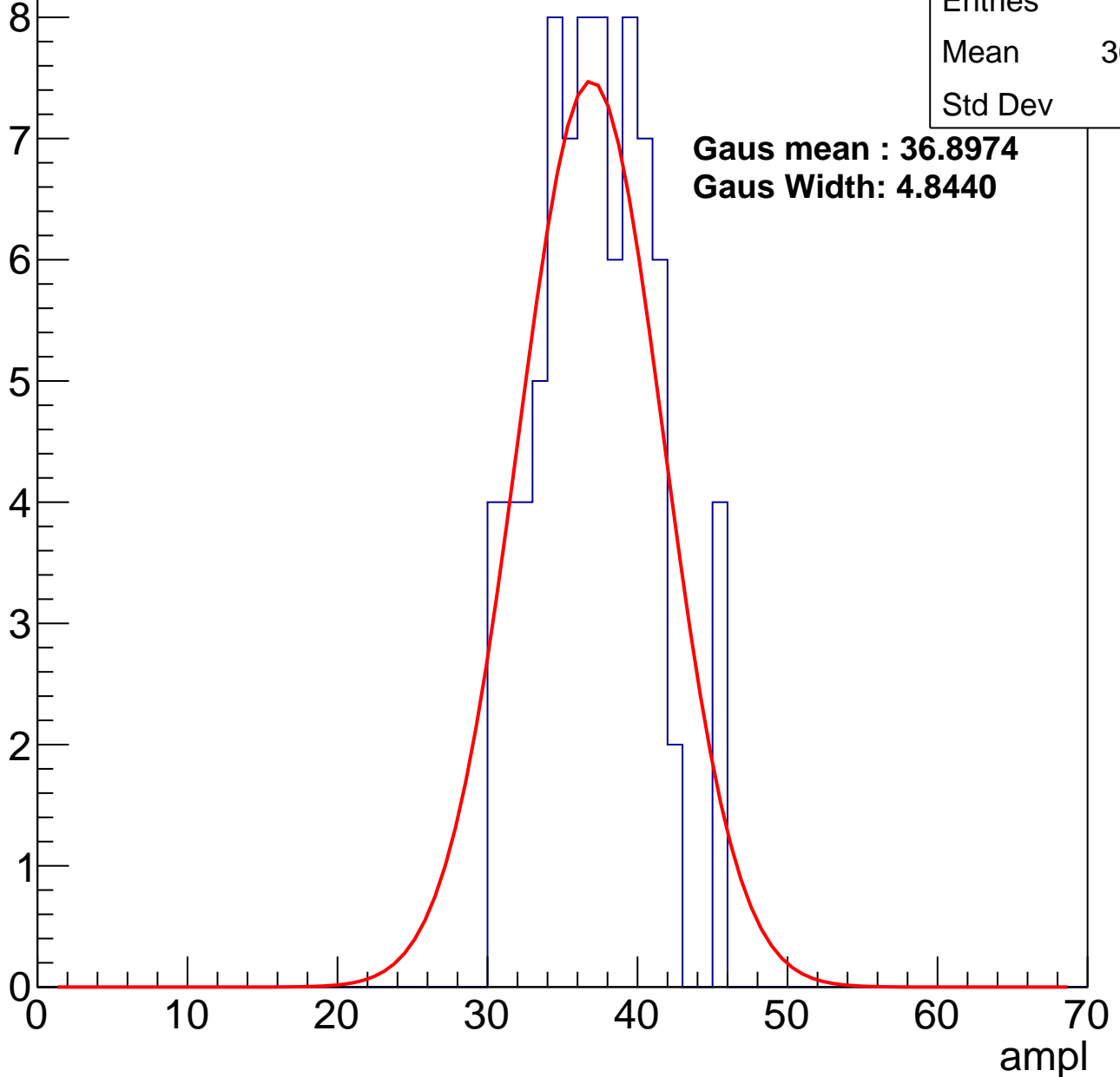
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	36.64
Std Dev	3.72

Gaus mean : 36.8974

Gaus Width: 4.8440



B0L001S, U14-ch92, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	59
Mean	43.69
Std Dev	3.371

Gaus mean : 44.0134

Gaus Width: 3.0303

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

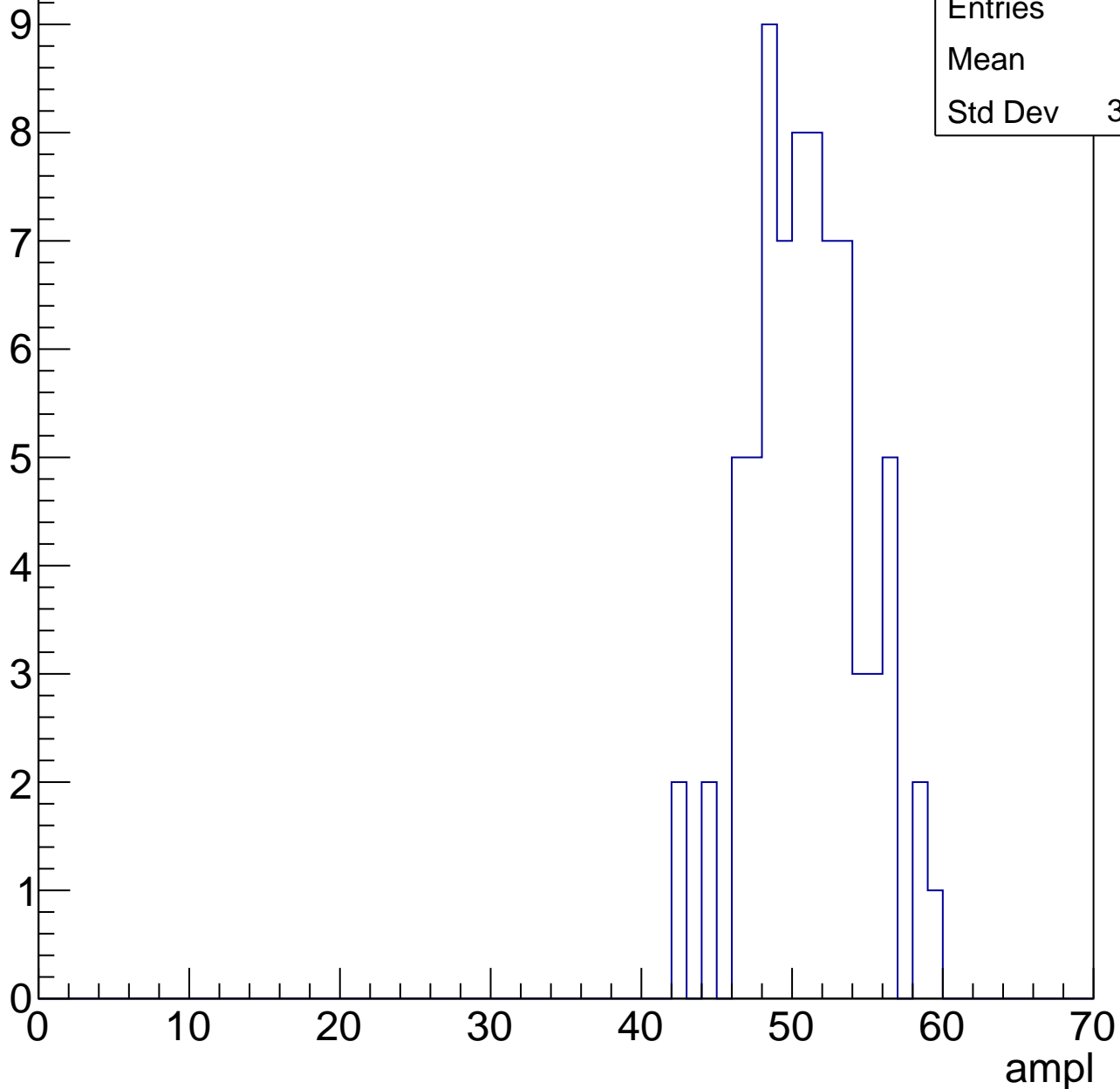
70

B0L001S, U14-ch92, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	50.5
Std Dev	3.603

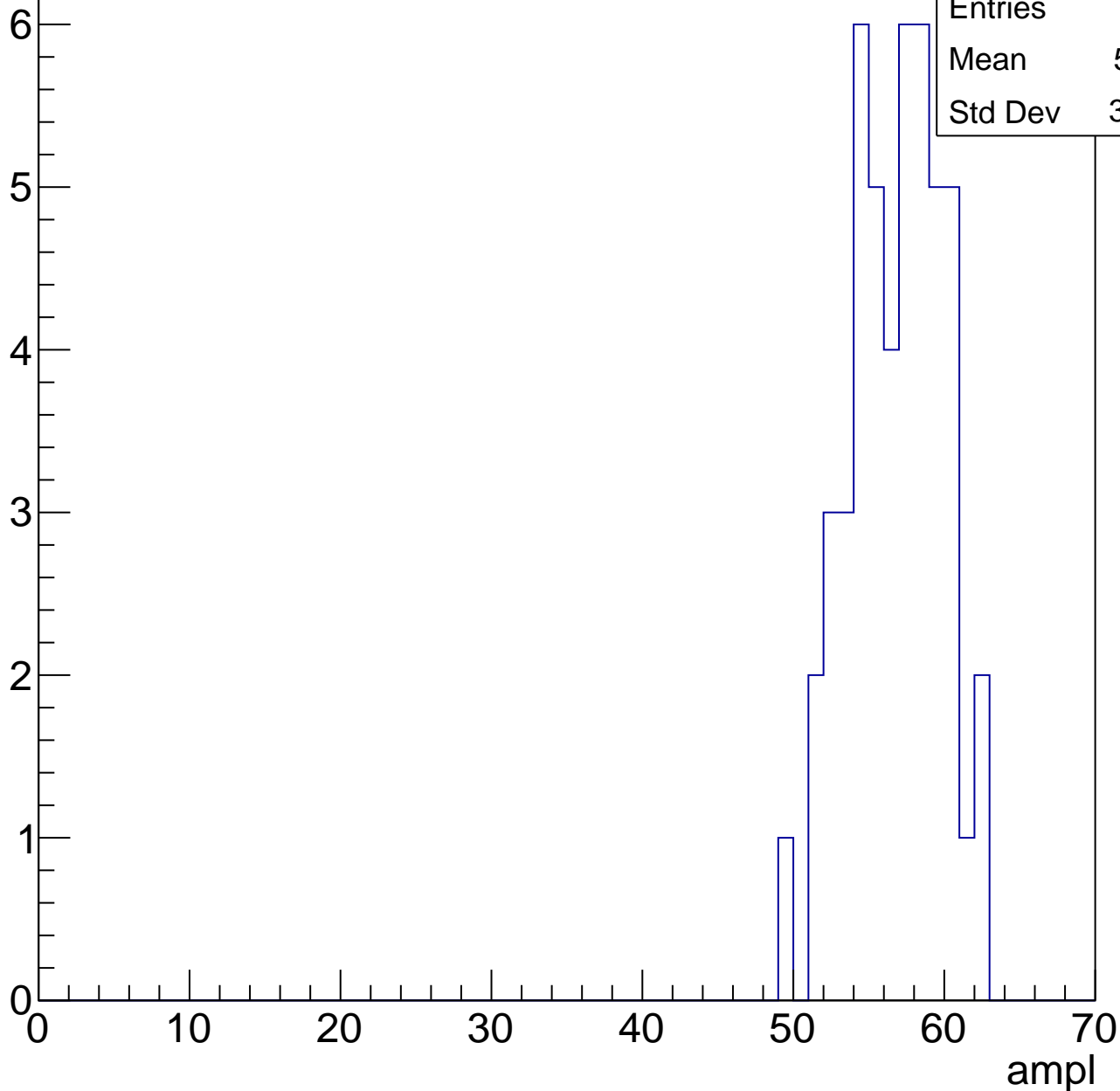


B0L001S, U14-ch92, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	56.31
Std Dev	3.032

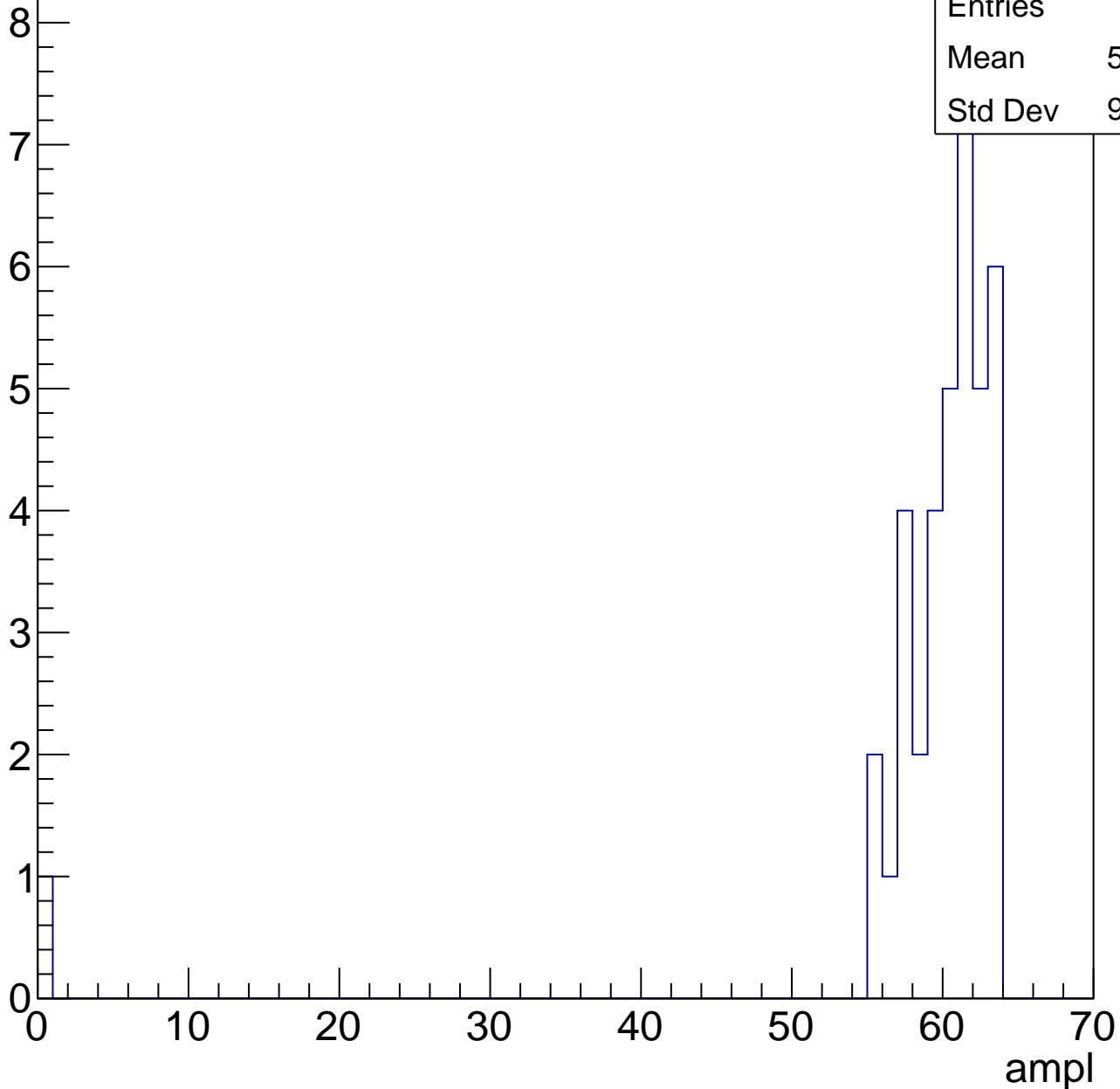


B0L001S, U14-ch92, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

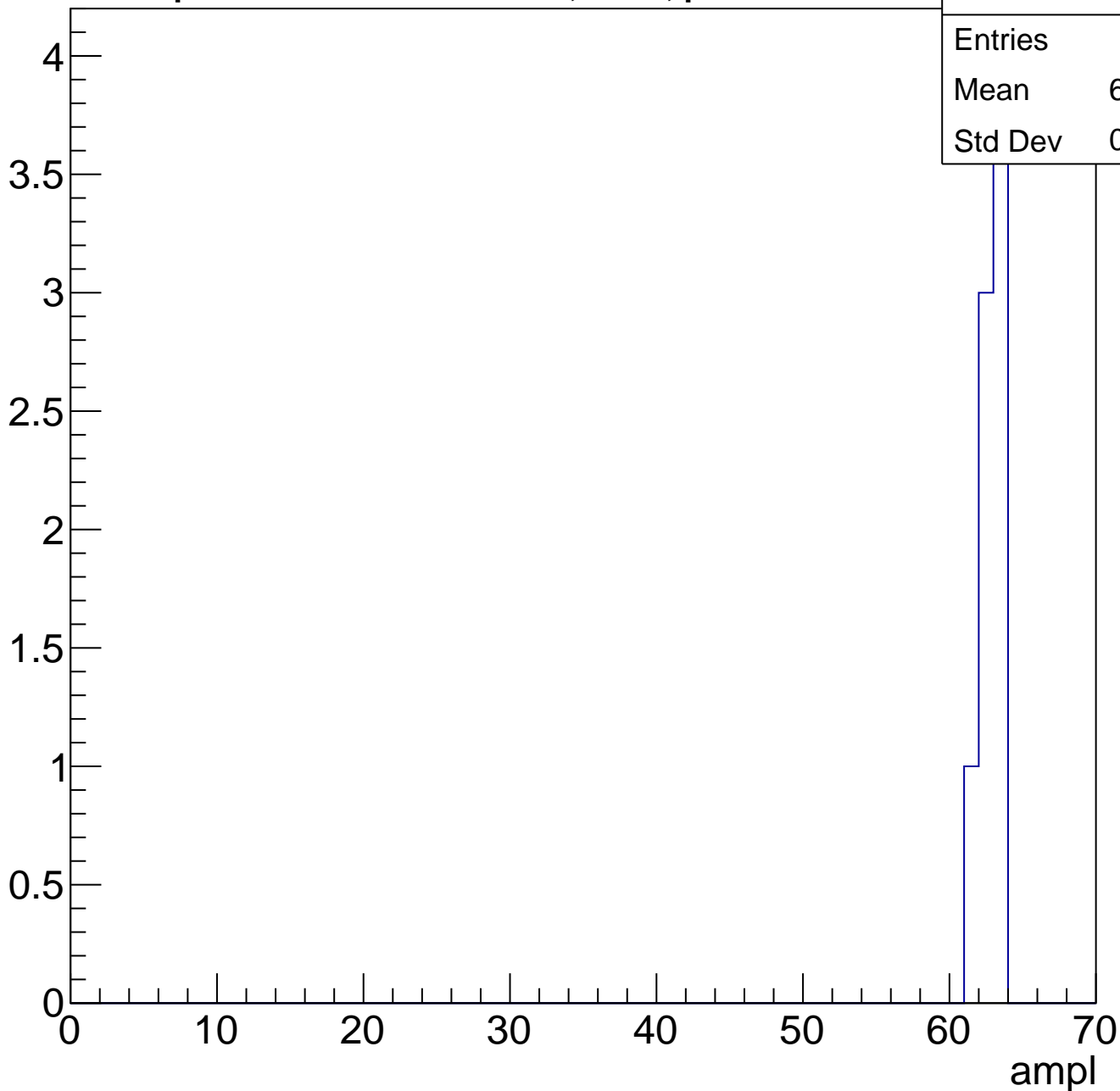
Entries	38
Mean	58.47
Std Dev	9.878



B0L001S, U14-ch92, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch92, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch93, adc0

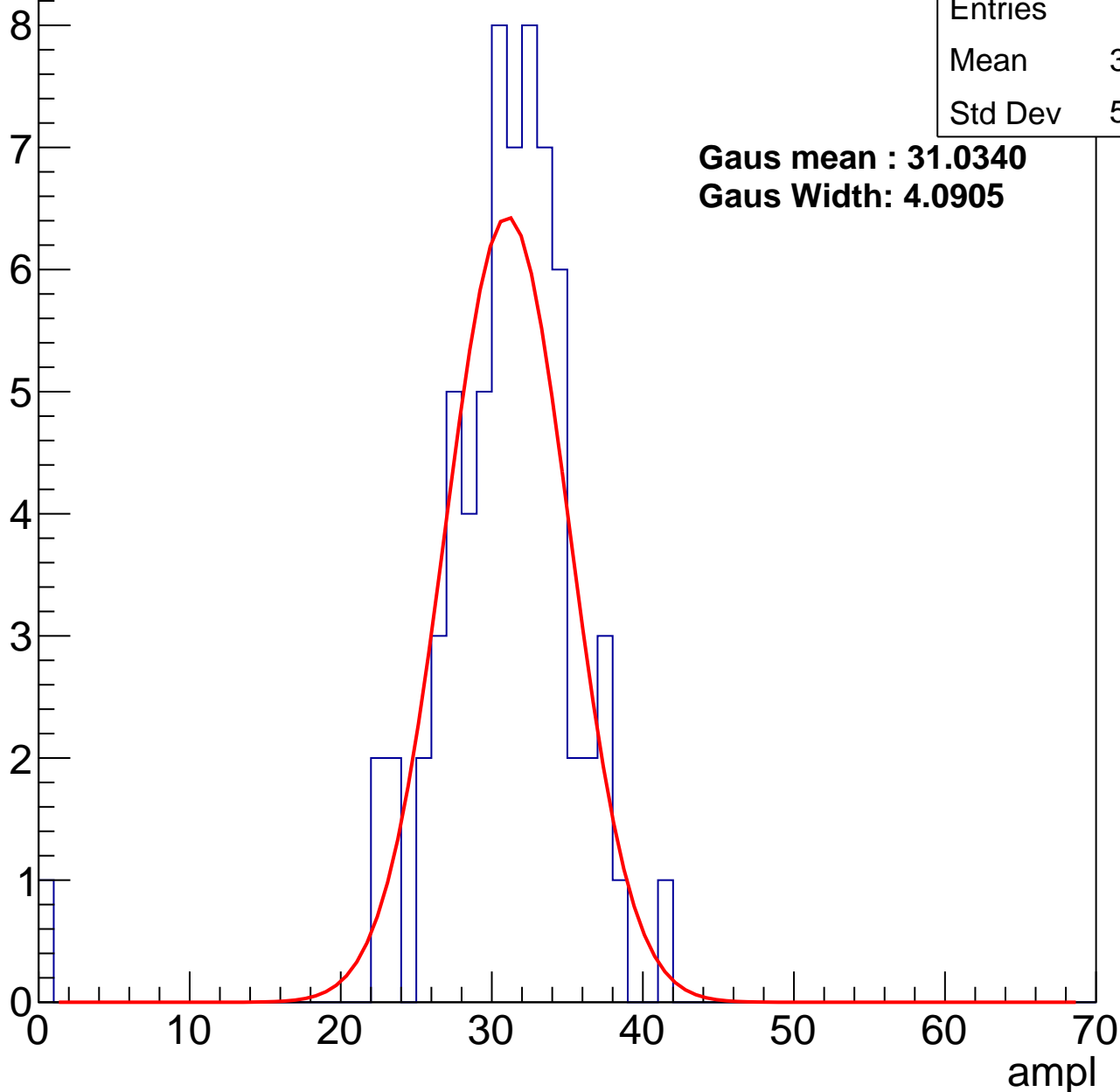
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	30.29
Std Dev	5.297

Gaus mean : 31.0340

Gaus Width: 4.0905



B0L001S, U14-ch93, adc1

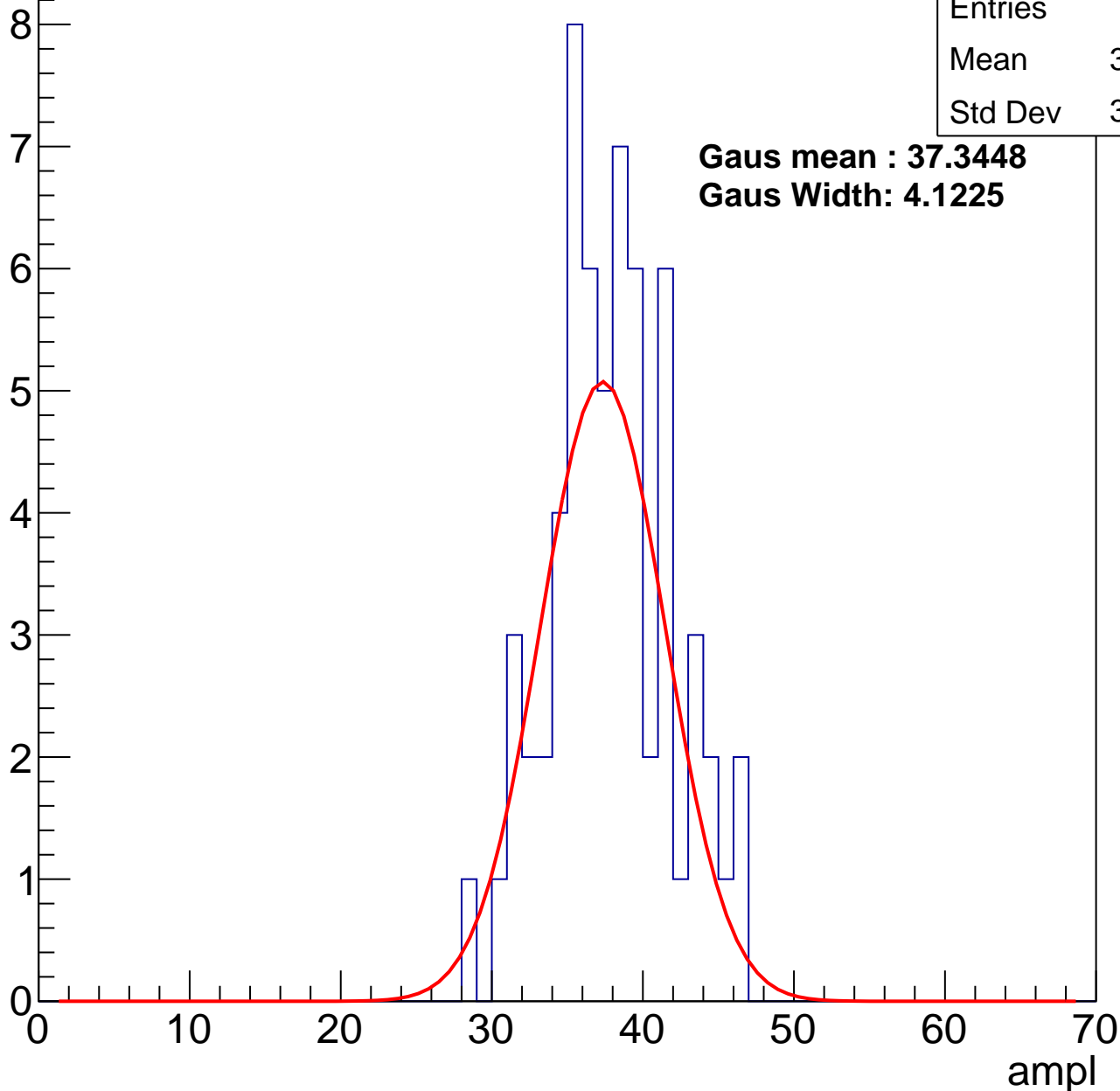
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	37.42
Std Dev	3.986

Gaus mean : 37.3448

Gaus Width: 4.1225



B0L001S, U14-ch93, adc2

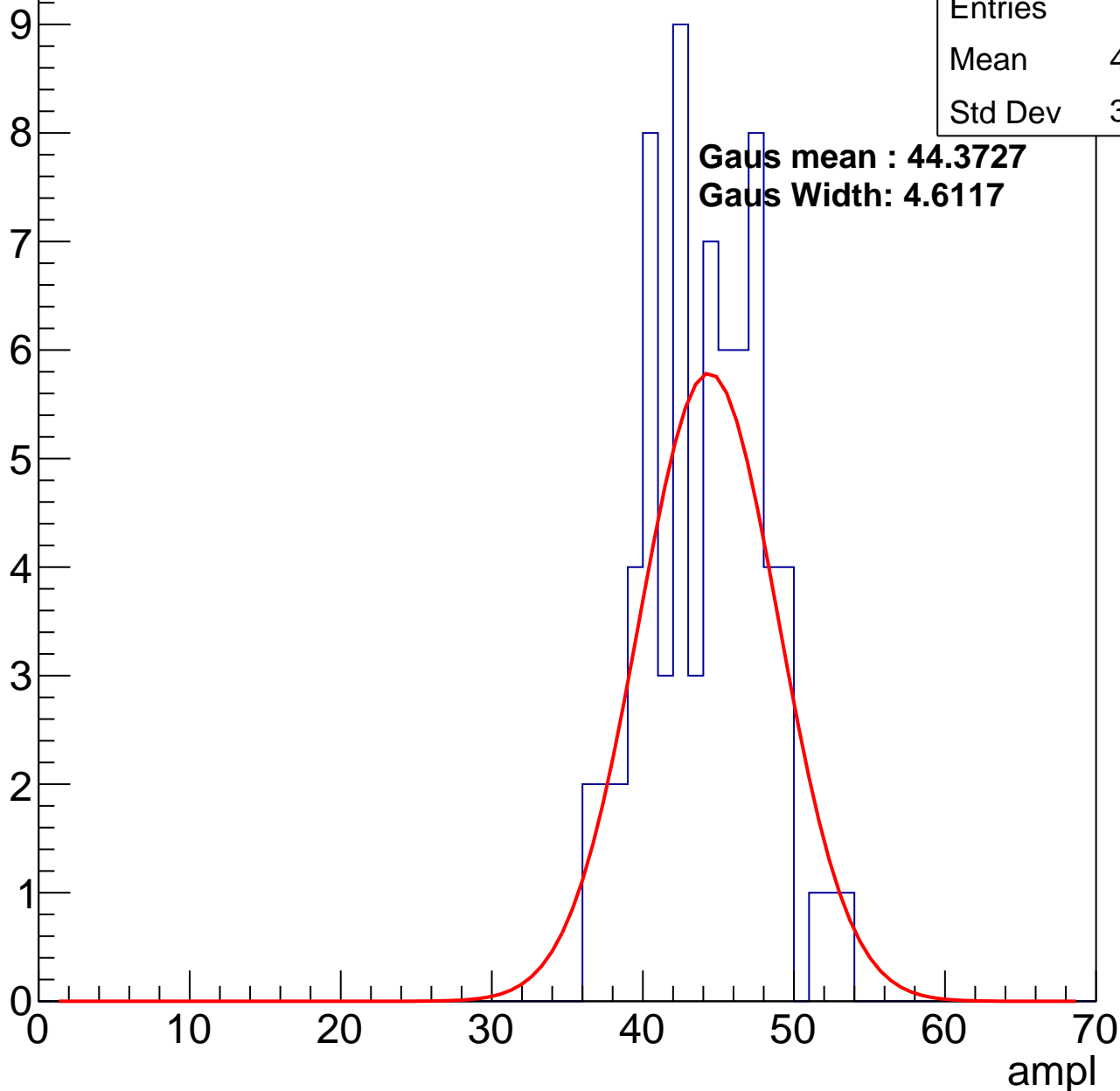
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	43.69
Std Dev	3.837

Gaus mean : 44.3727

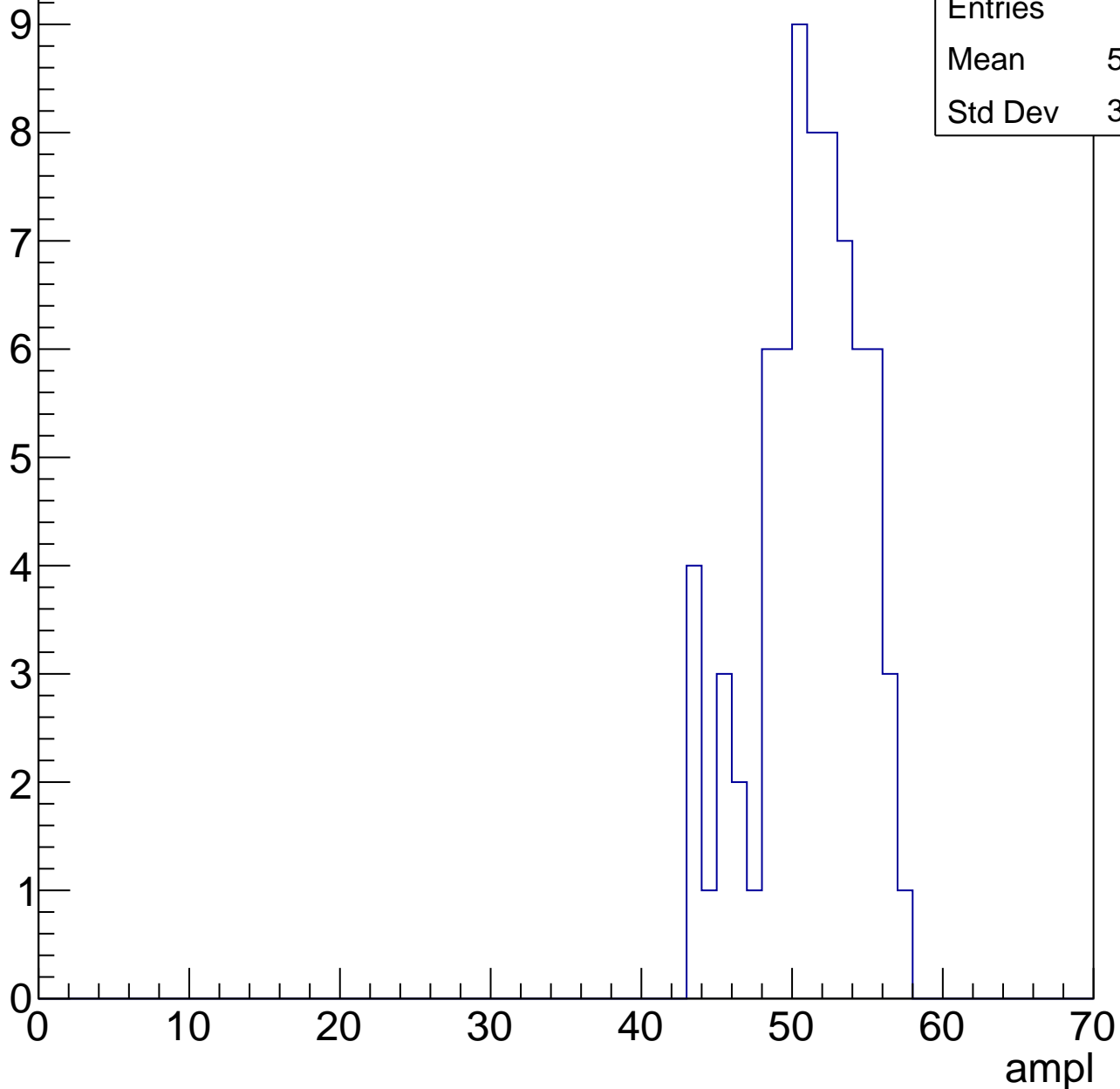
Gaus Width: 4.6117



B0L001S, U14-ch93, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry



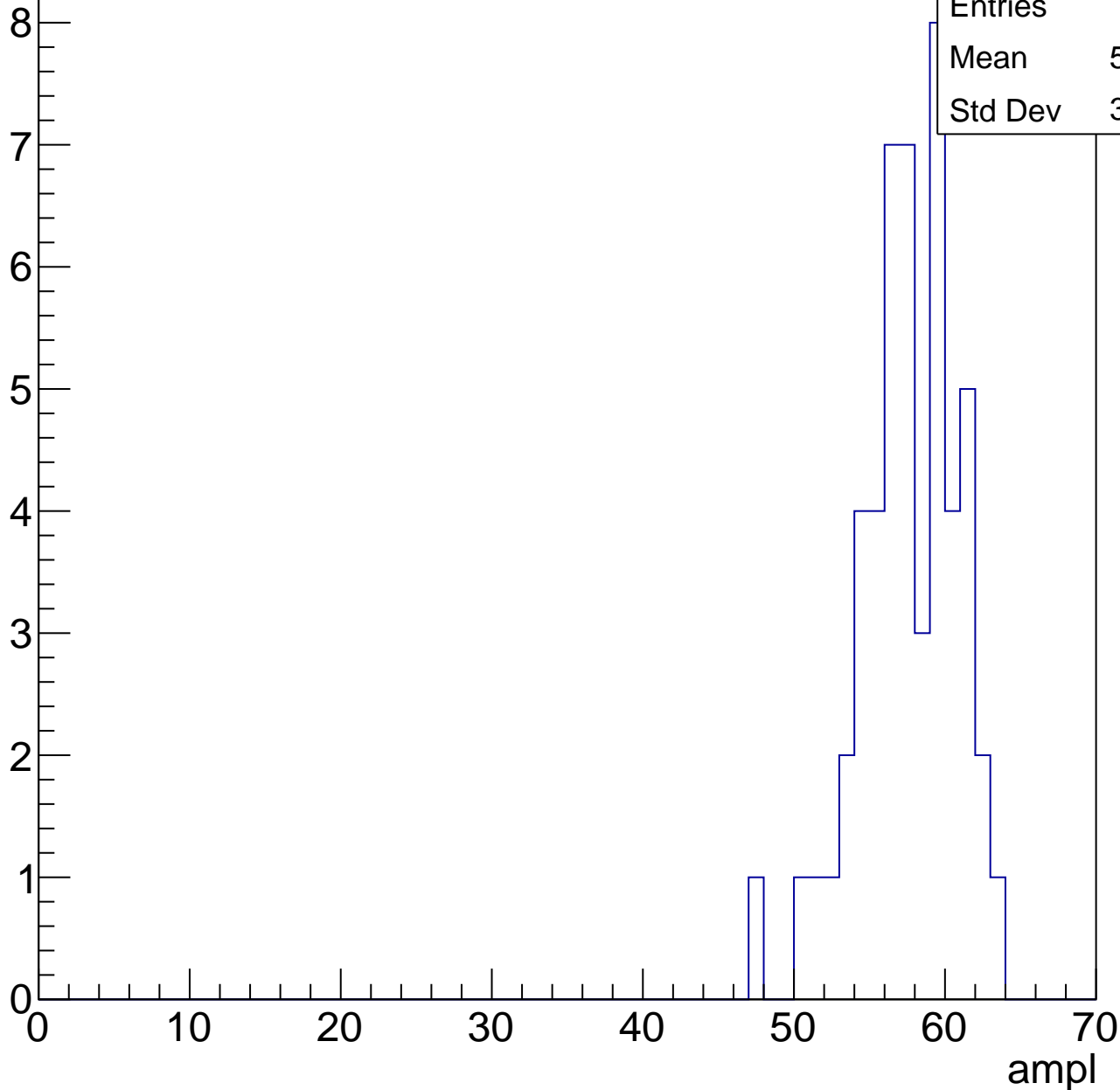
Entries	71
Mean	50.65
Std Dev	3.469

B0L001S, U14-ch93, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	57.08
Std Dev	3.247

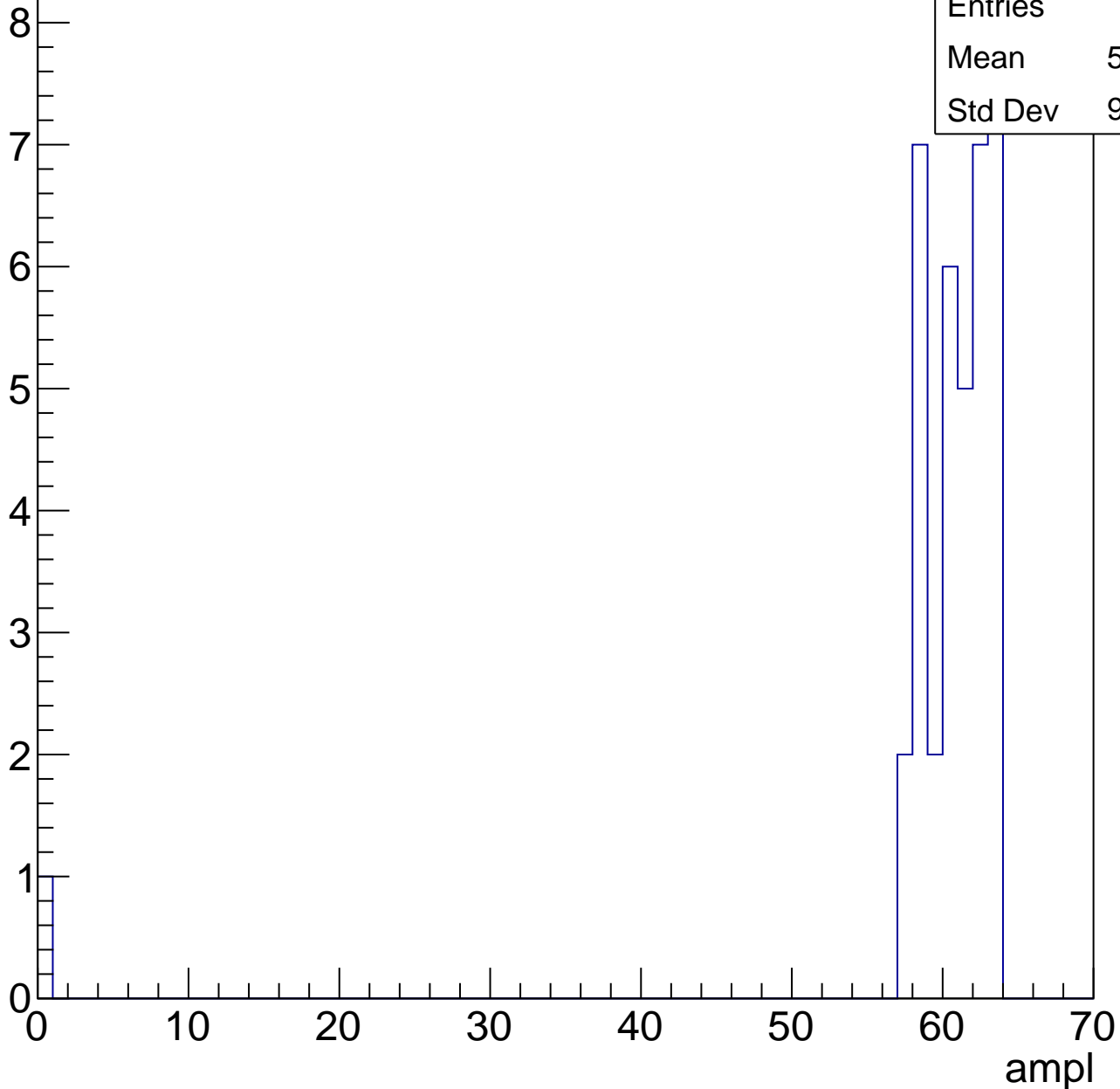


B0L001S, U14-ch93, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	58.97
Std Dev	9.885



B0L001S, U14-ch93, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch93, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L001S, U14-ch94, adc0

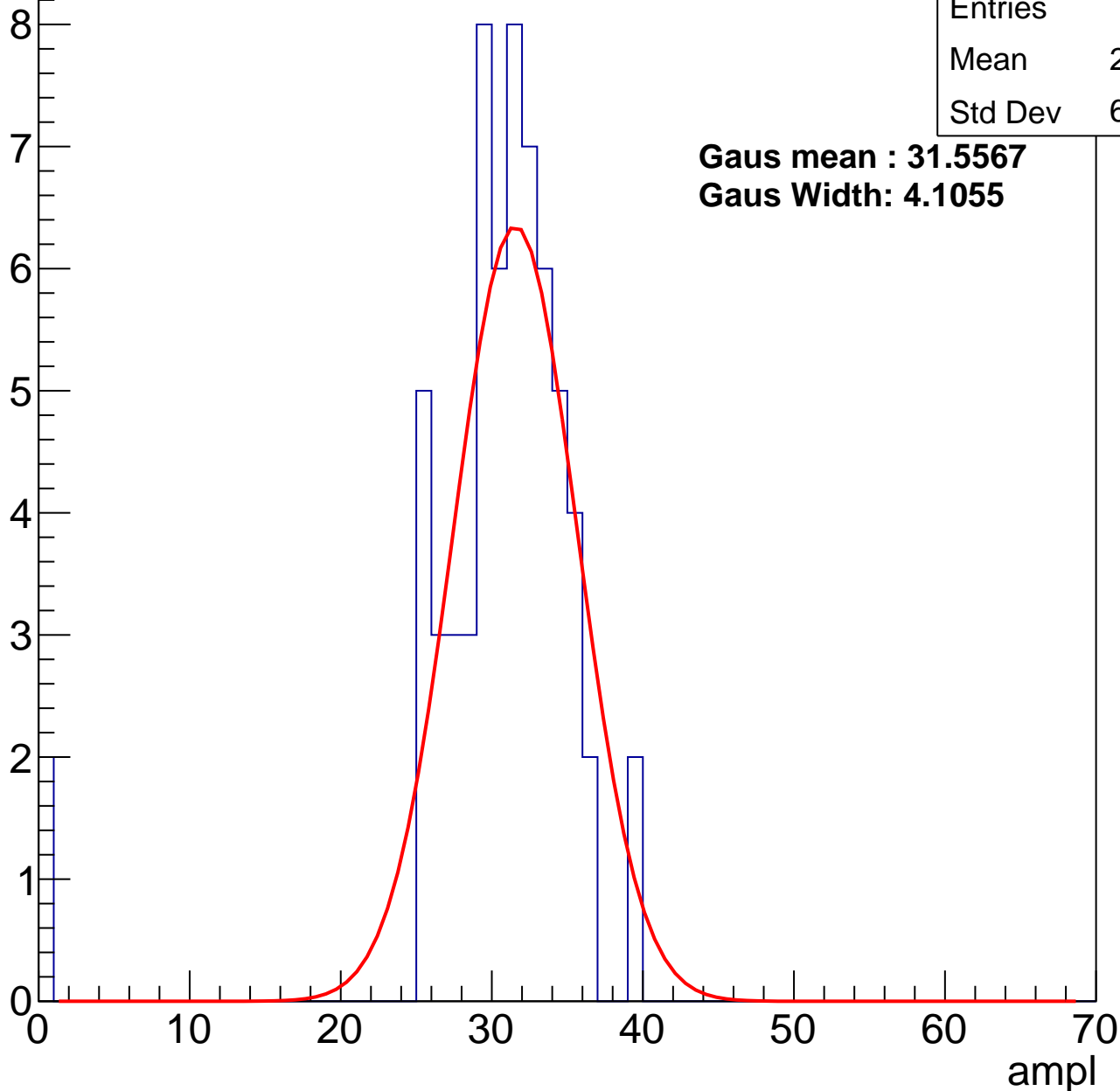
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	29.84
Std Dev	6.278

Gaus mean : 31.5567

Gaus Width: 4.1055



B0L001S, U14-ch94, adc1

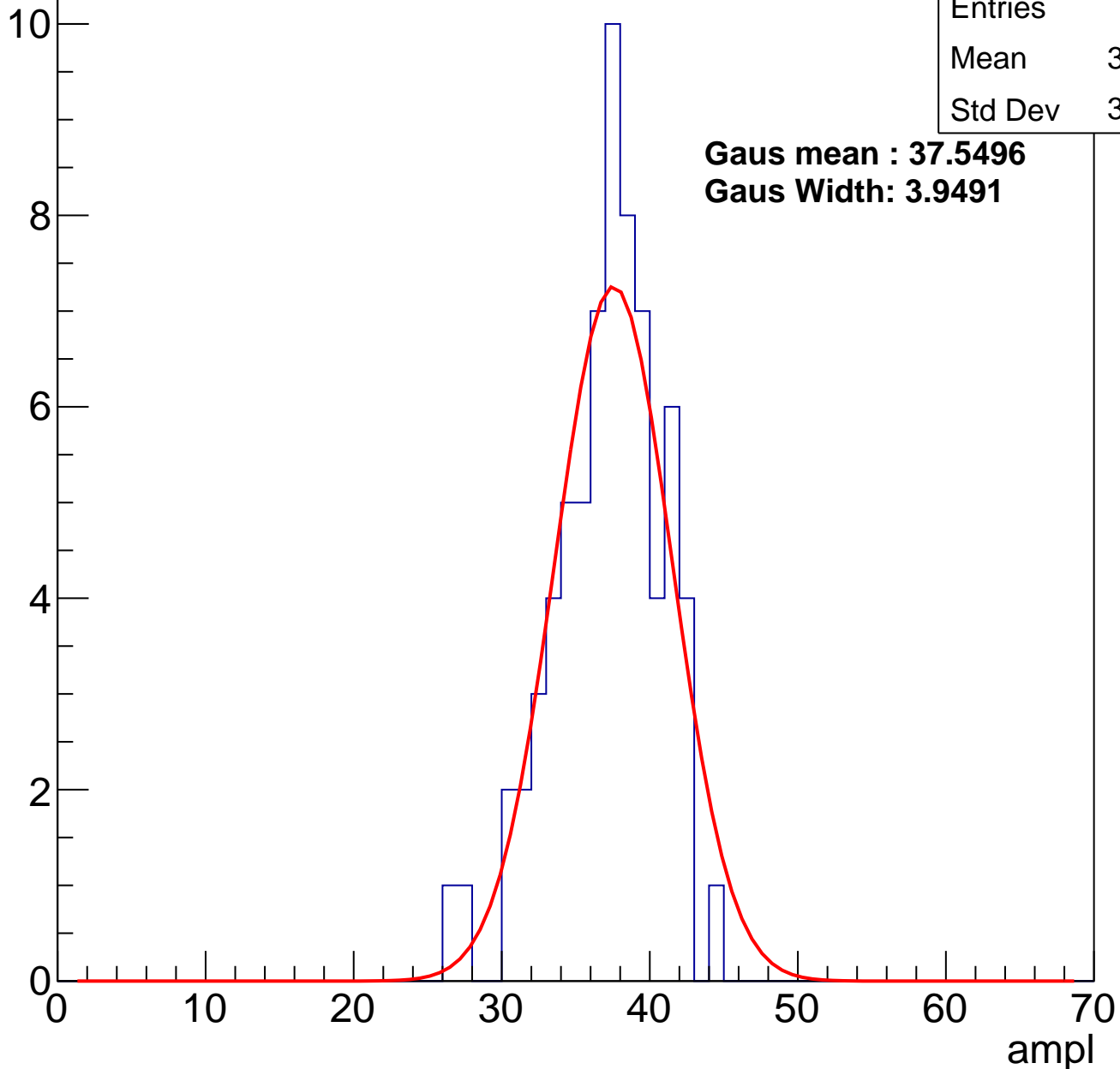
calib_packv5_042523_0143.root, FC#9, port A1

Entries	70
Mean	36.64
Std Dev	3.606

Gaus mean : 37.5496

Gaus Width: 3.9491

Entry

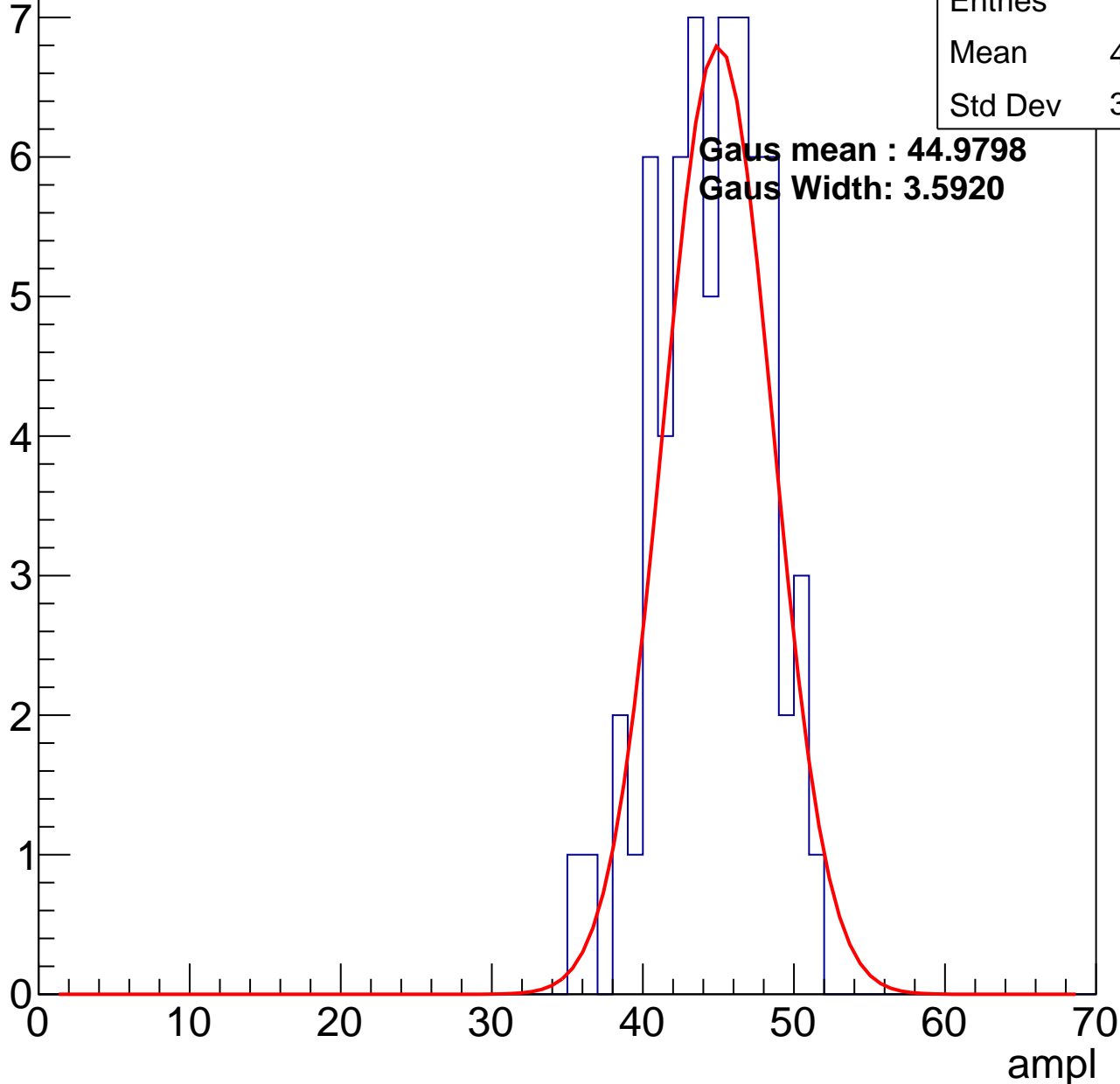


B0L001S, U14-ch94, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

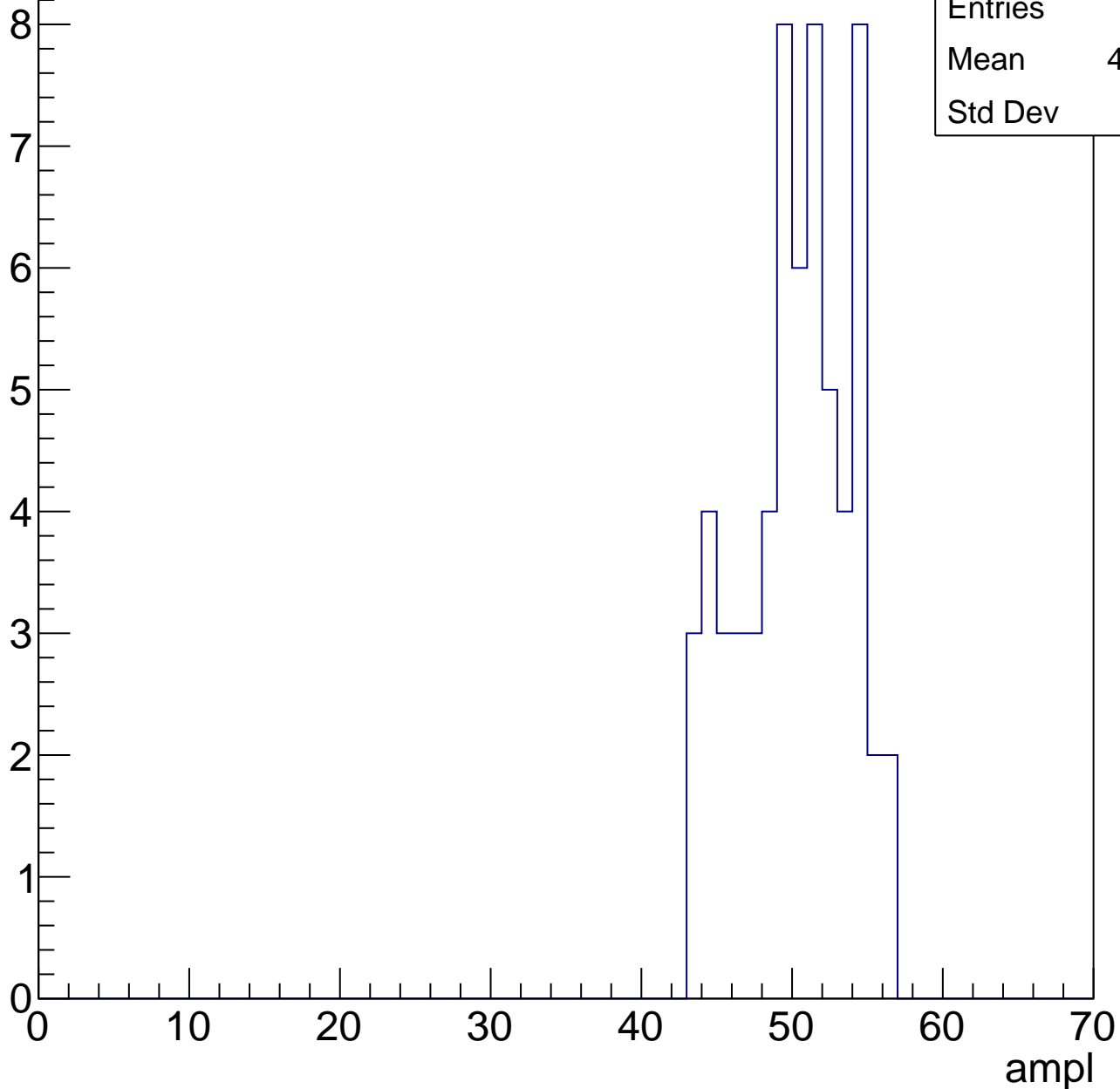
Entries	65
Mean	44.14
Std Dev	3.499



B0L001S, U14-ch94, adc3

calib_packv5_042523_0143.root, FC#9, port A1

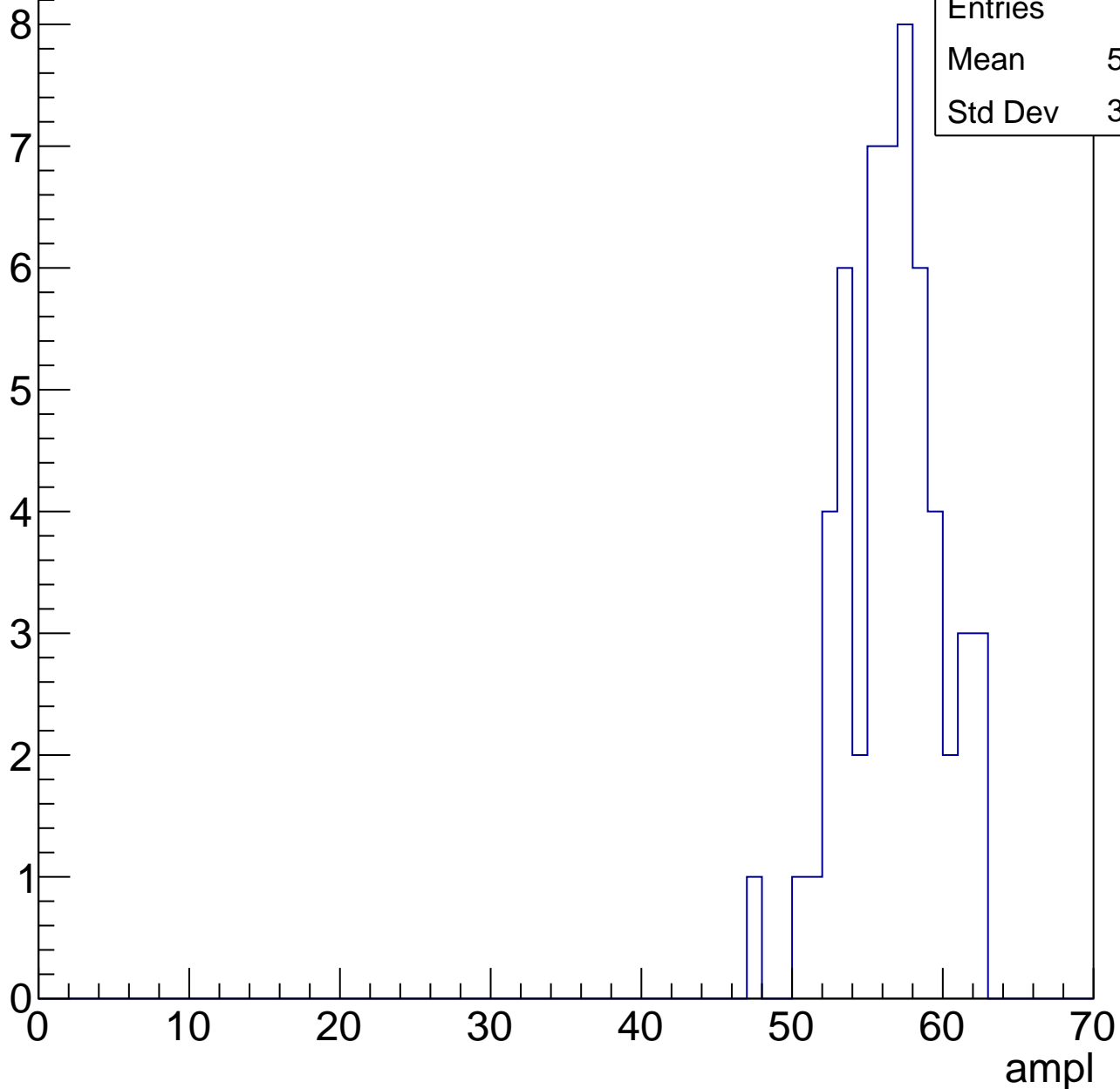
Entry



B0L001S, U14-ch94, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



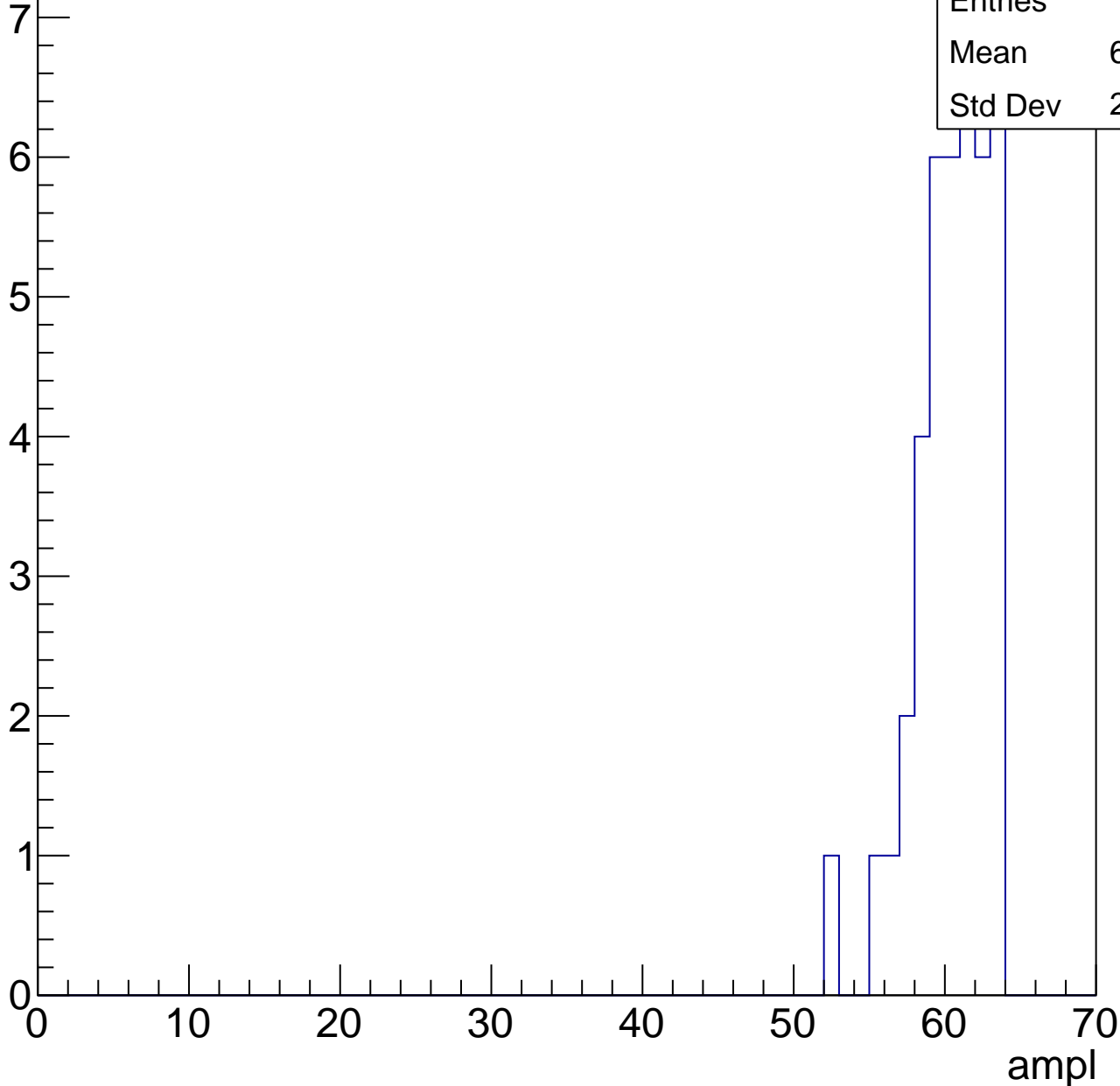
Entries	55
Mean	56.15
Std Dev	3.182

B0L001S, U14-ch94, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

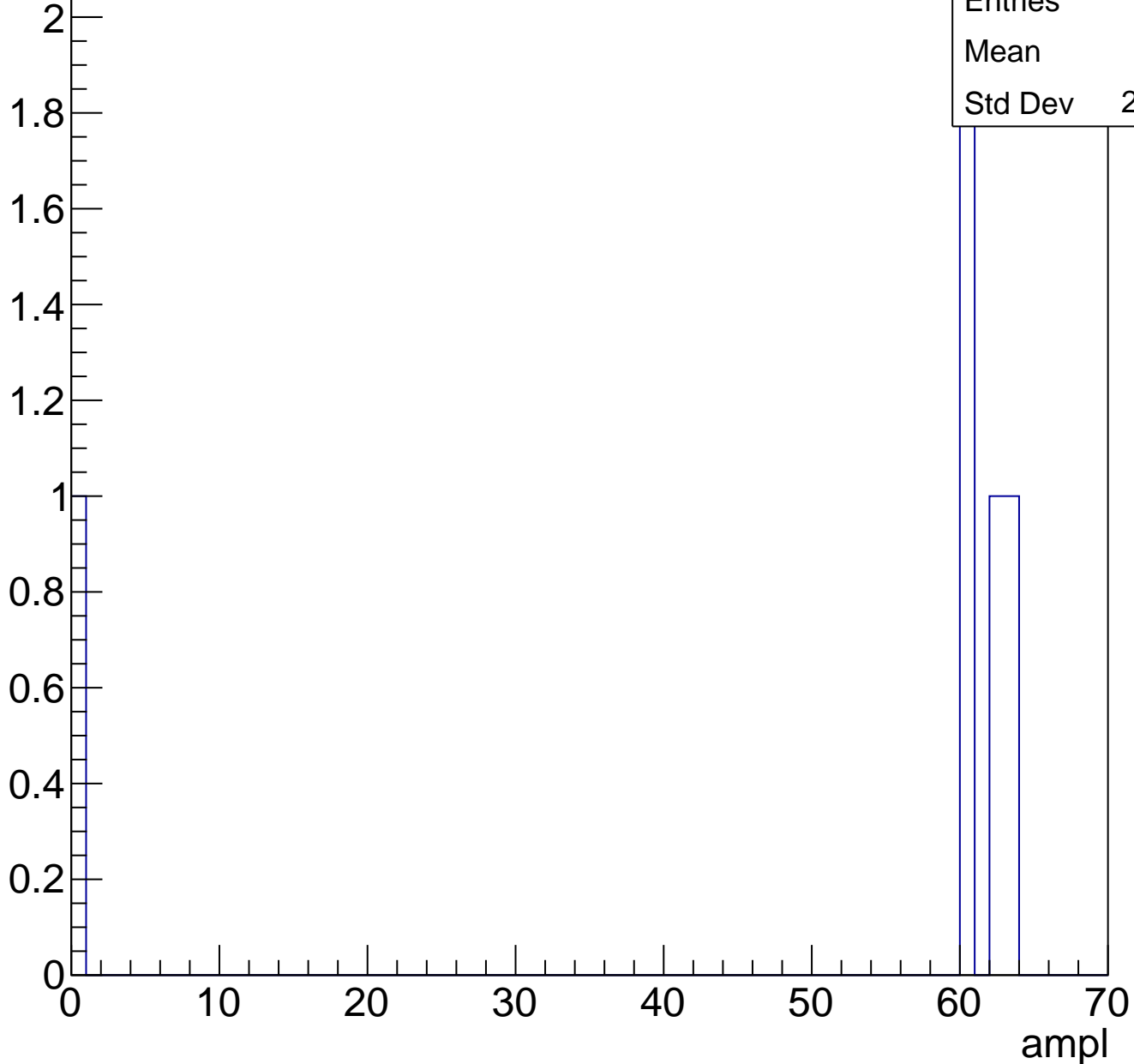
Entries	41
Mean	60.07
Std Dev	2.413



B0L001S, U14-ch94, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch94, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch95, adc0

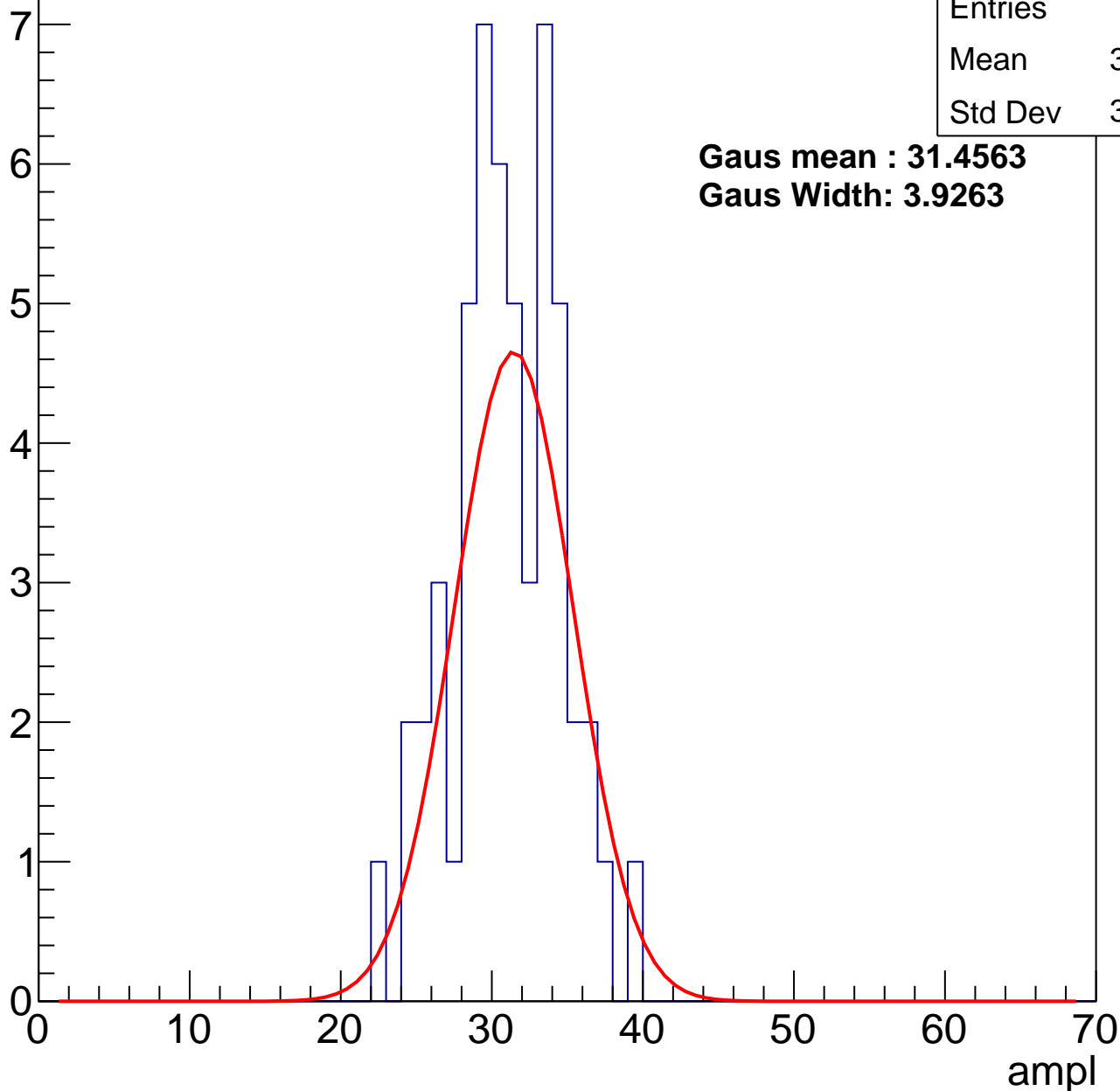
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.53
Std Dev	3.548

Gaus mean : 31.4563

Gaus Width: 3.9263



B0L001S, U14-ch95, adc1

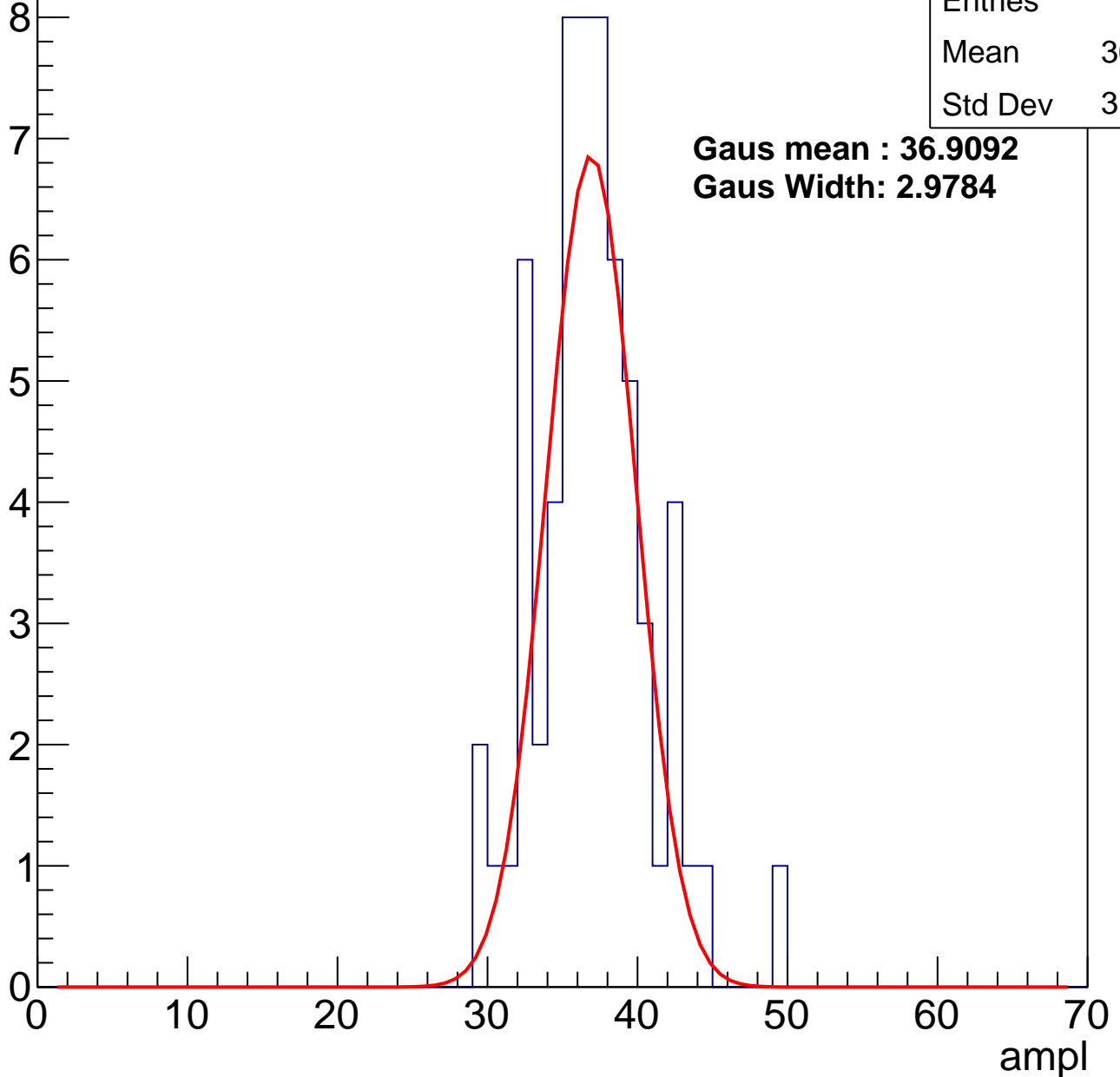
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	36.53
Std Dev	3.714

Gaus mean : 36.9092

Gaus Width: 2.9784



B0L001S, U14-ch95, adc2

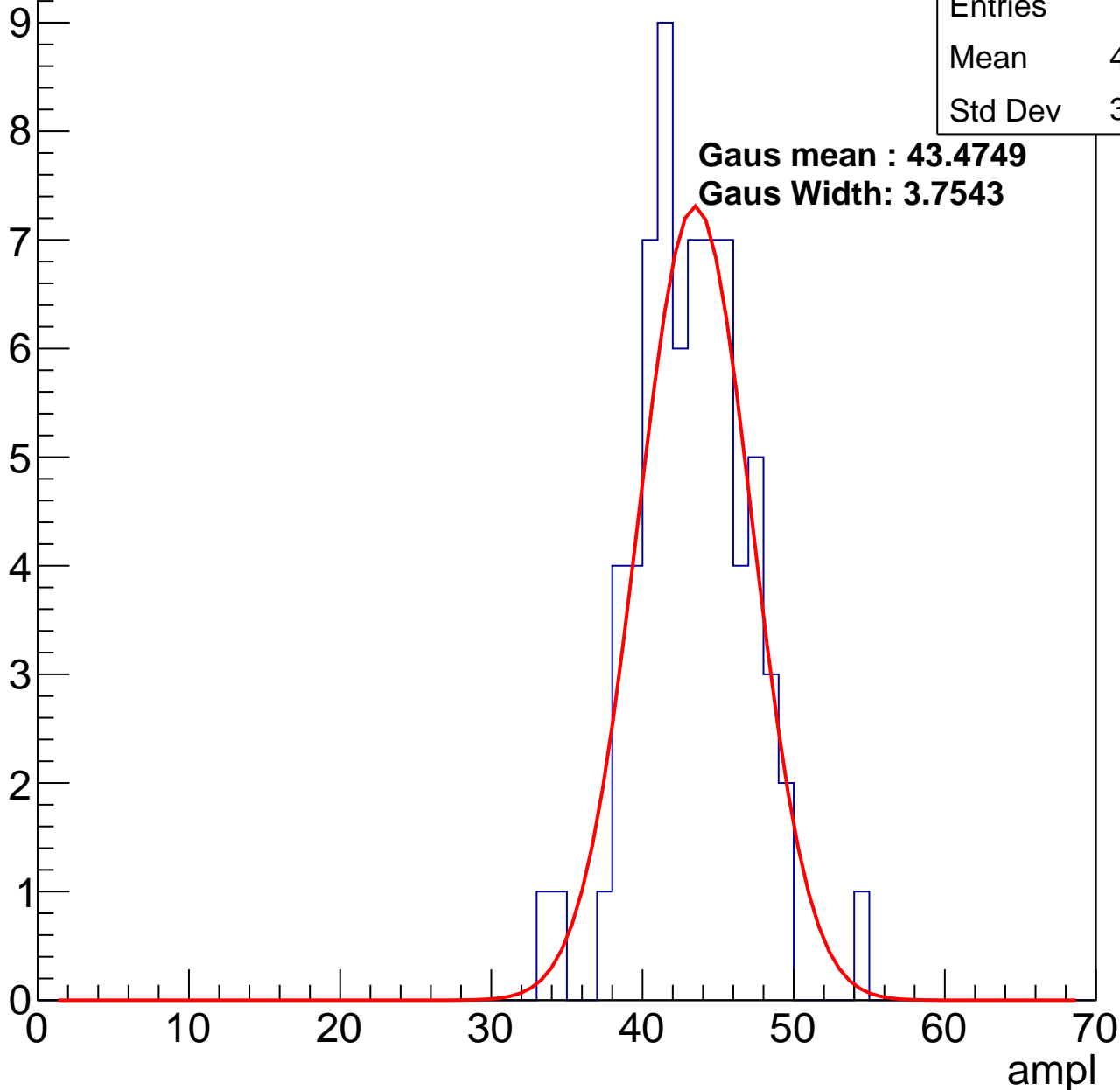
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	42.78
Std Dev	3.615

Gaus mean : 43.4749

Gaus Width: 3.7543

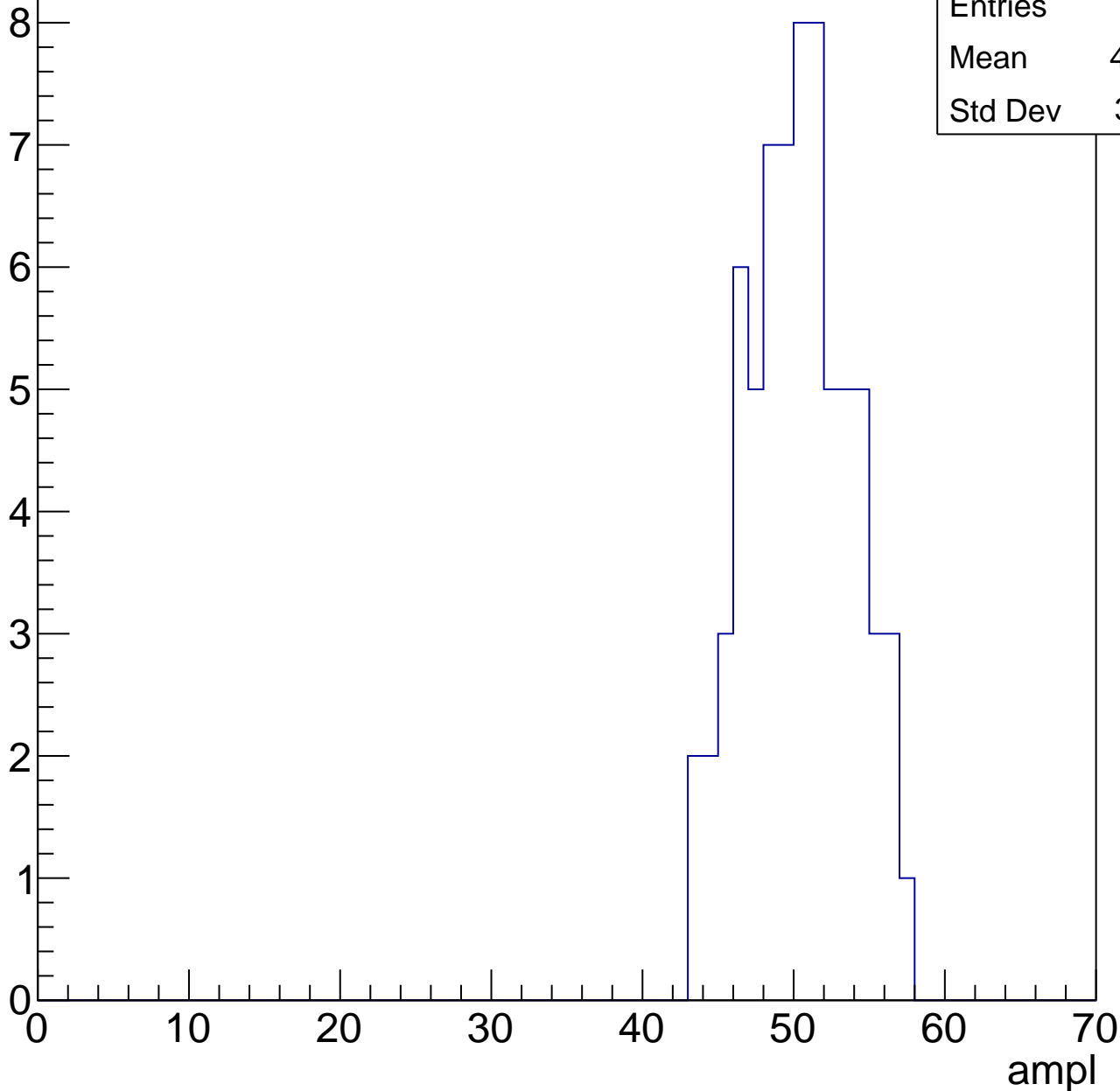


B0L001S, U14-ch95, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.89
Std Dev	3.391

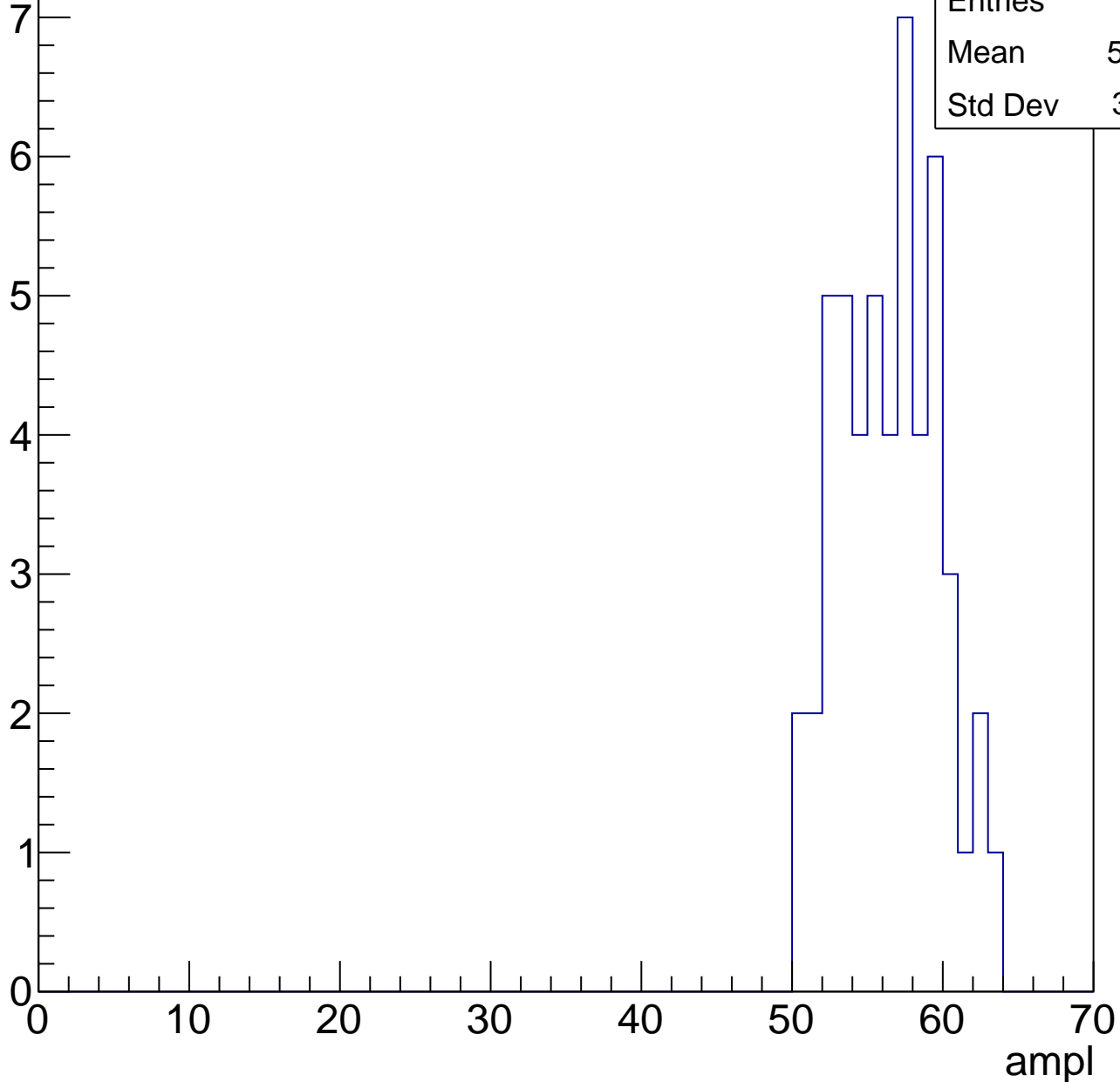


B0L001S, U14-ch95, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	55.98
Std Dev	3.251

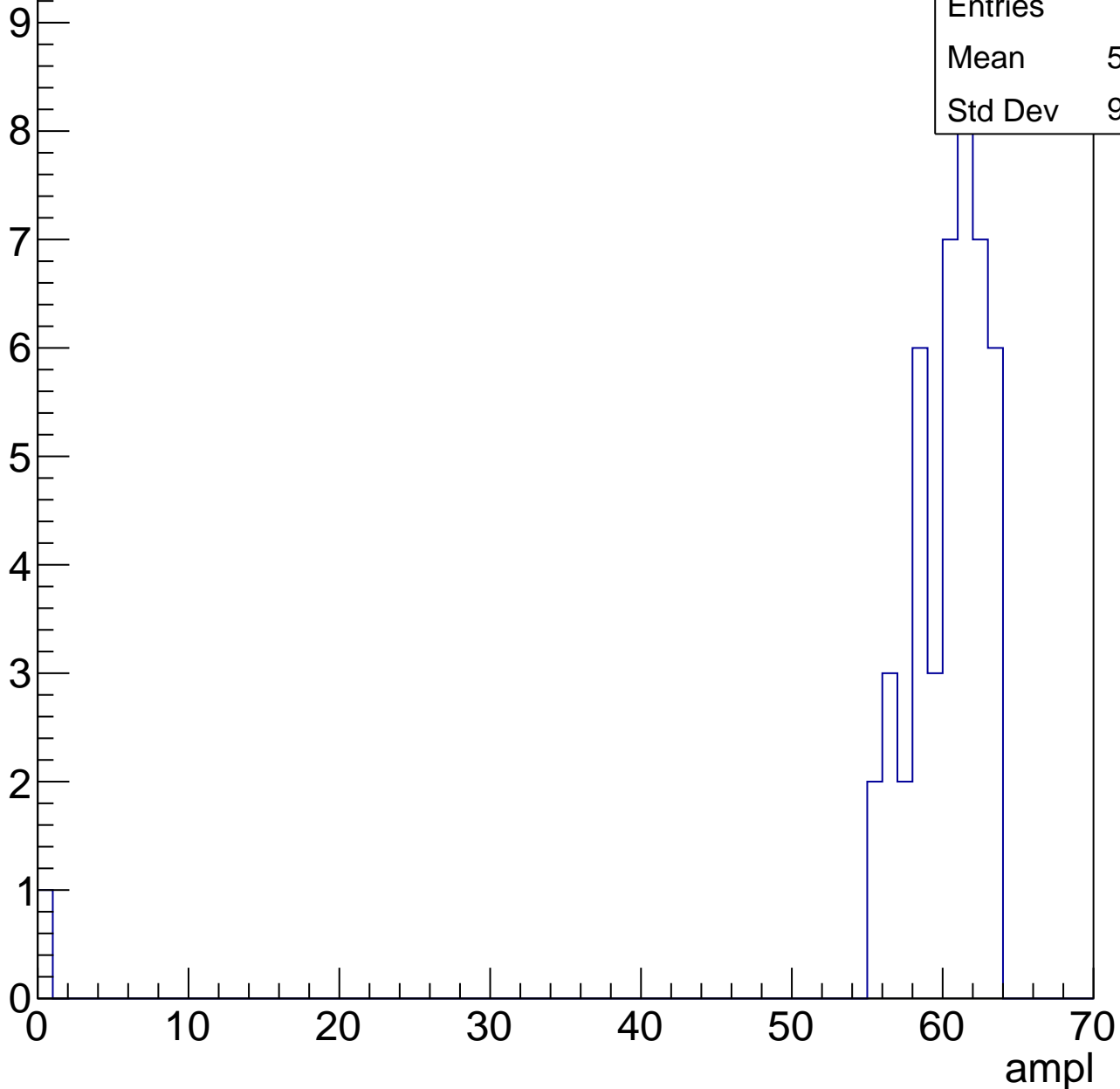


B0L001S, U14-ch95, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

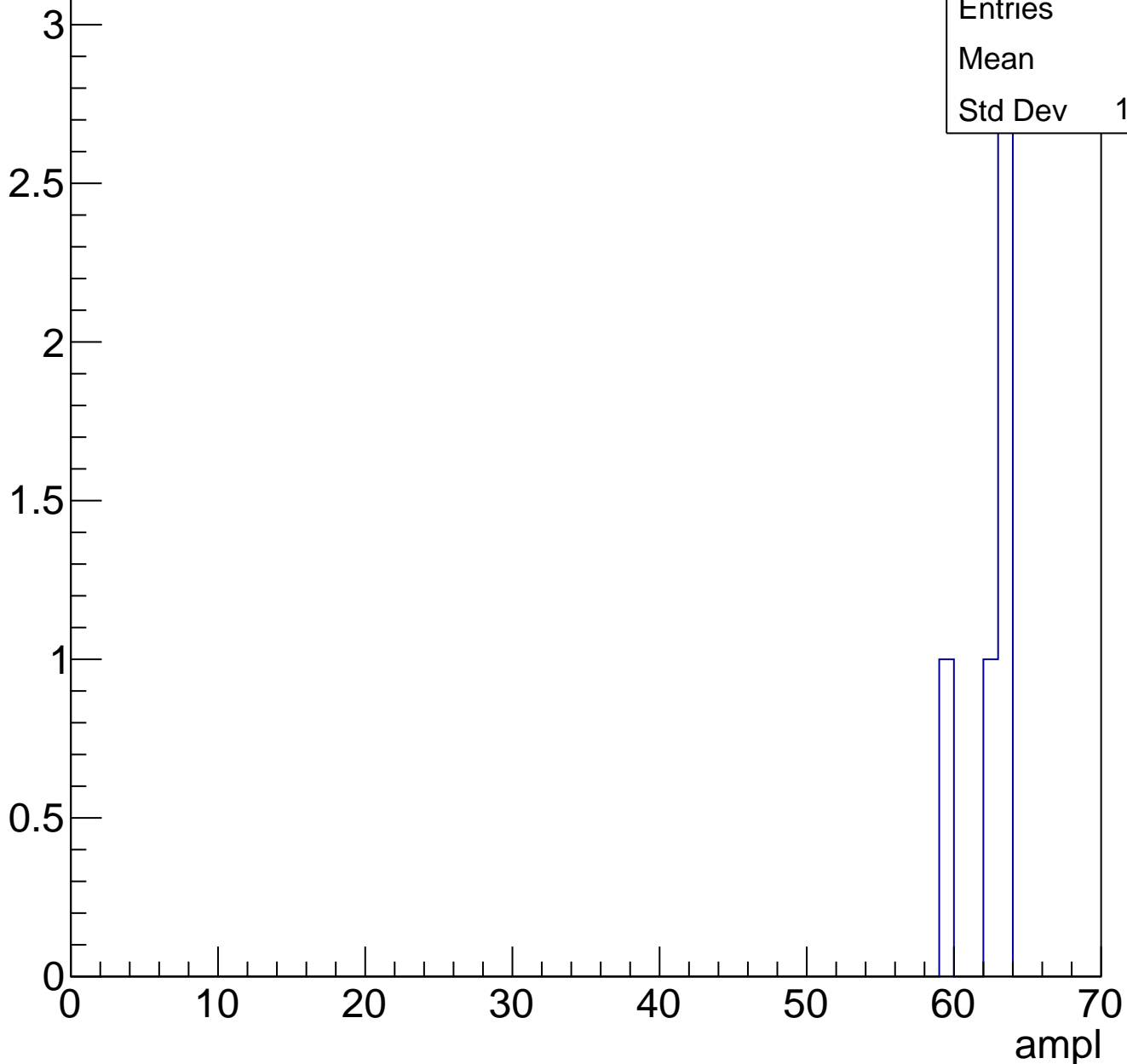
Entries	46
Mean	58.65
Std Dev	9.029



B0L001S, U14-ch95, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch95, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch96, adc0

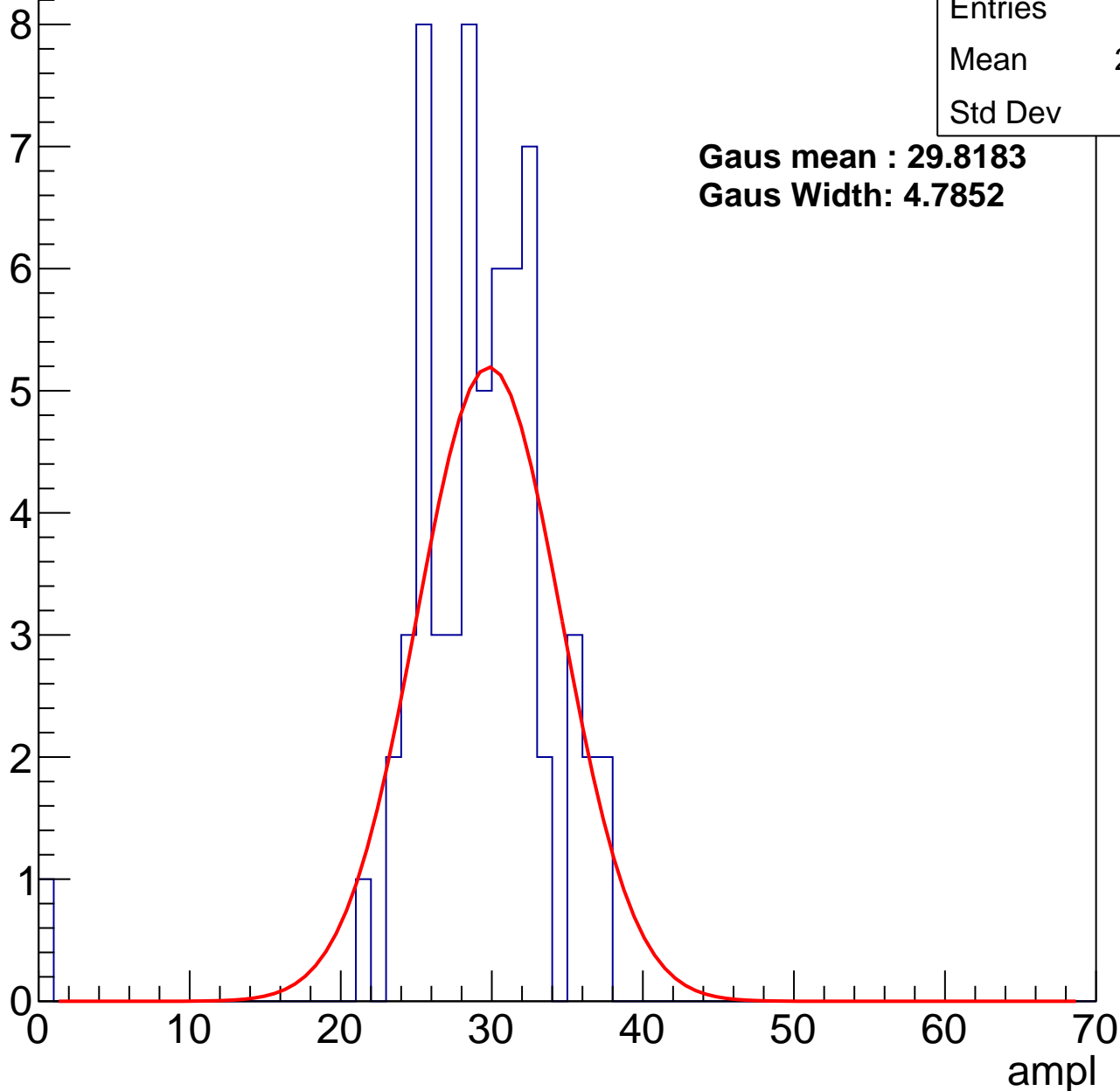
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	28.61
Std Dev	5.2

Gaus mean : 29.8183

Gaus Width: 4.7852



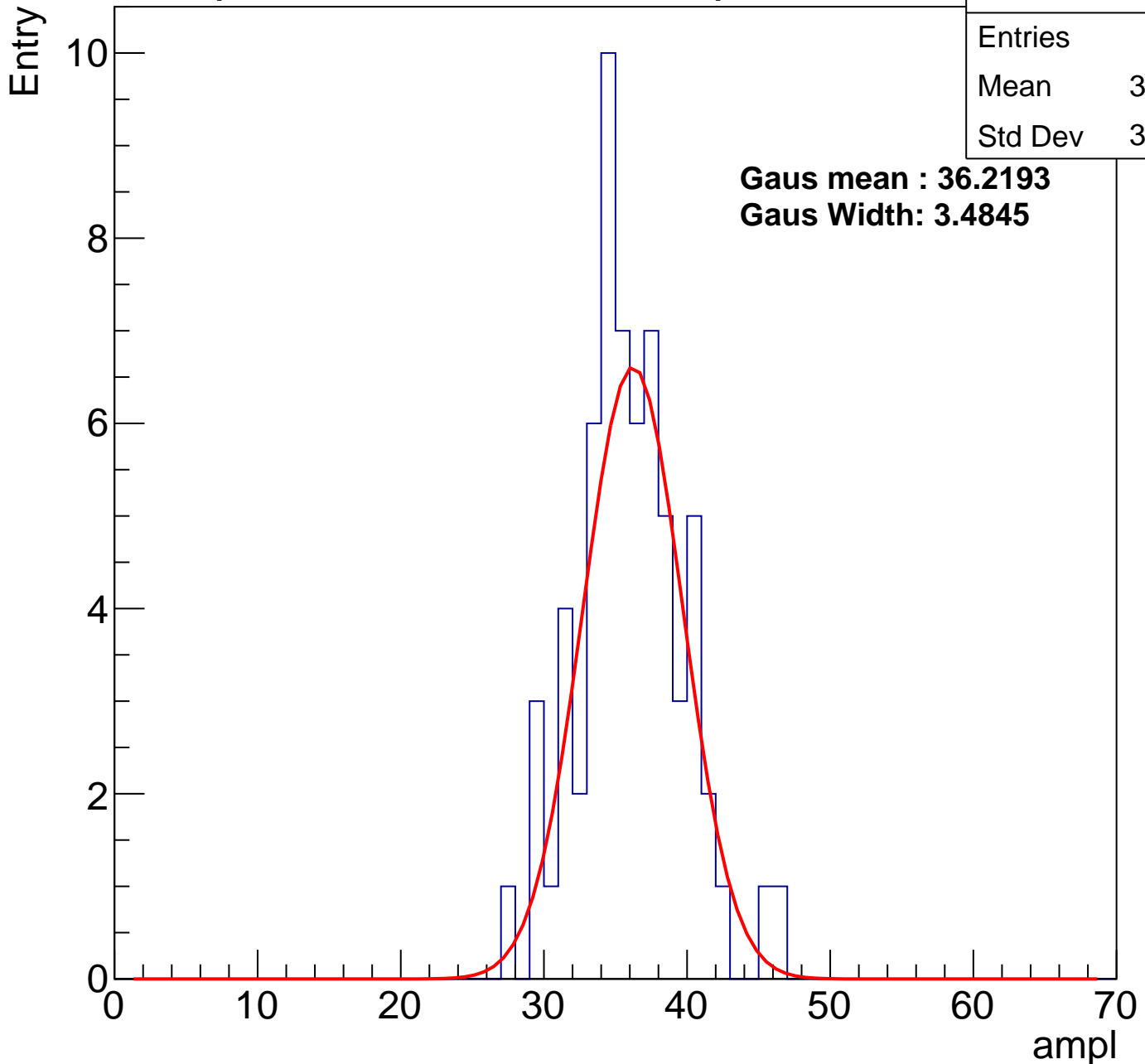
B0L001S, U14-ch96, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	35.57
Std Dev	3.692

Gaus mean : 36.2193

Gaus Width: 3.4845



B0L001S, U14-ch96, adc2

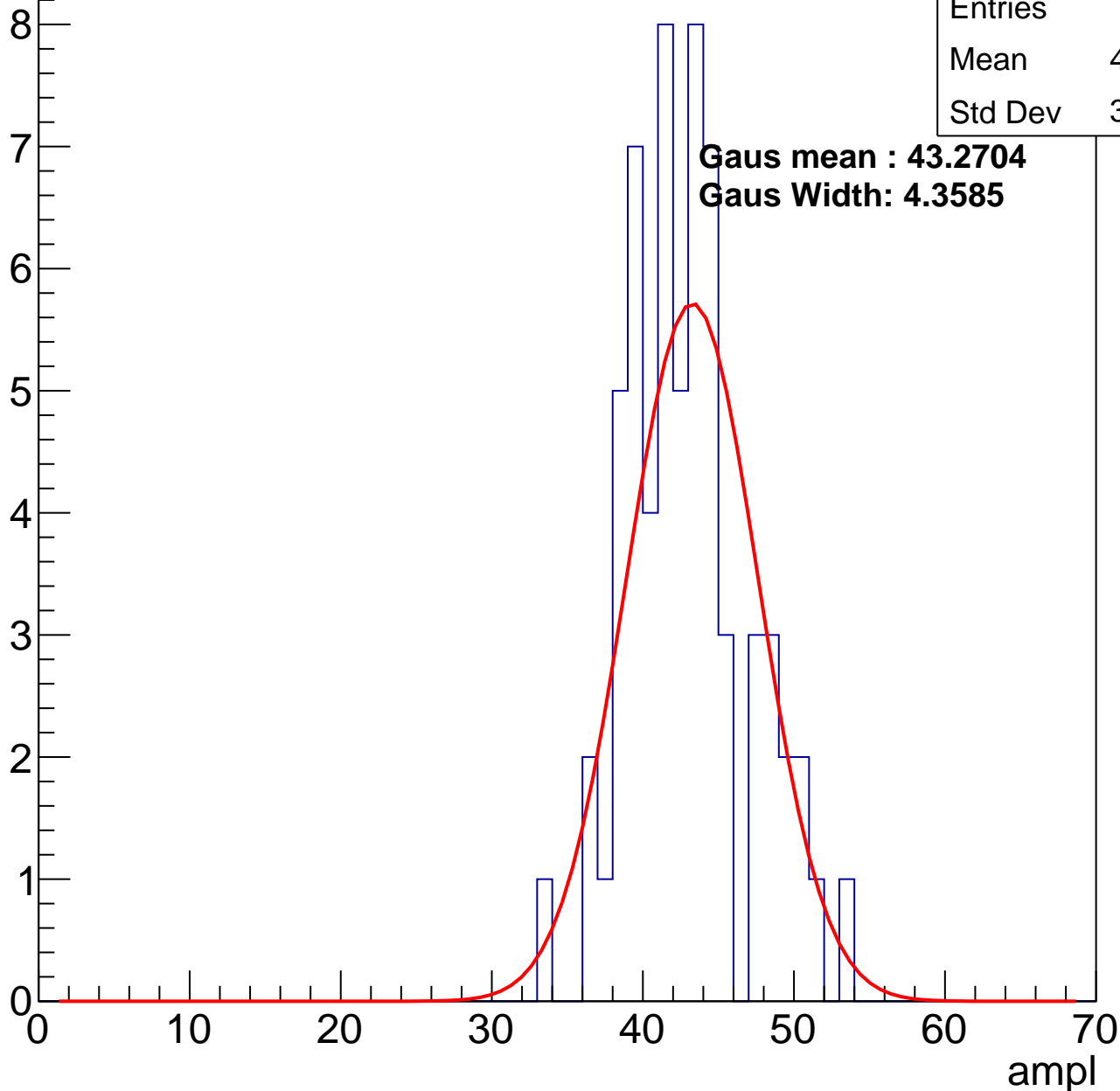
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.49
Std Dev	3.992

Gaus mean : 43.2704

Gaus Width: 4.3585

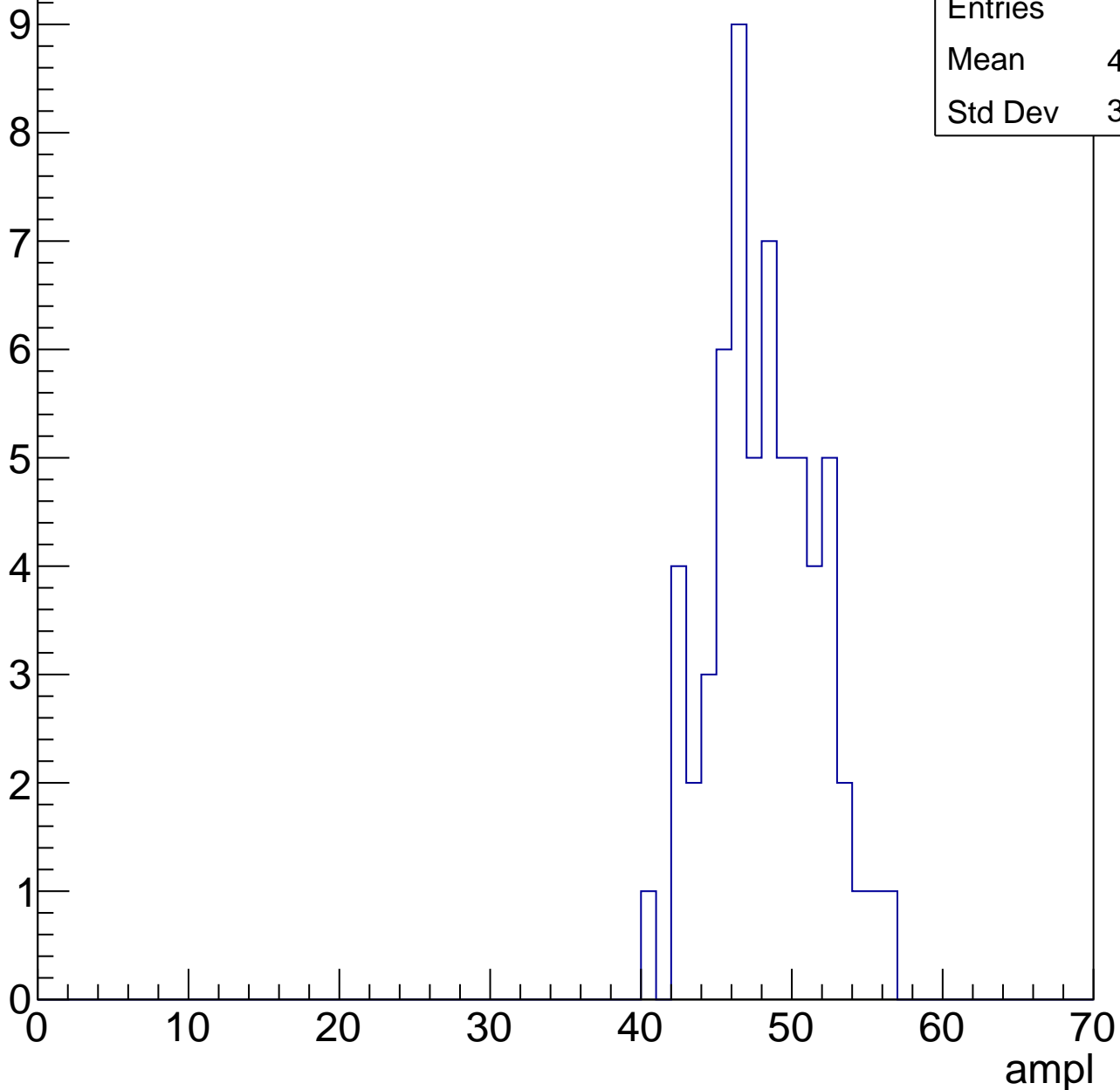


B0L001S, U14-ch96, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	47.72
Std Dev	3.474

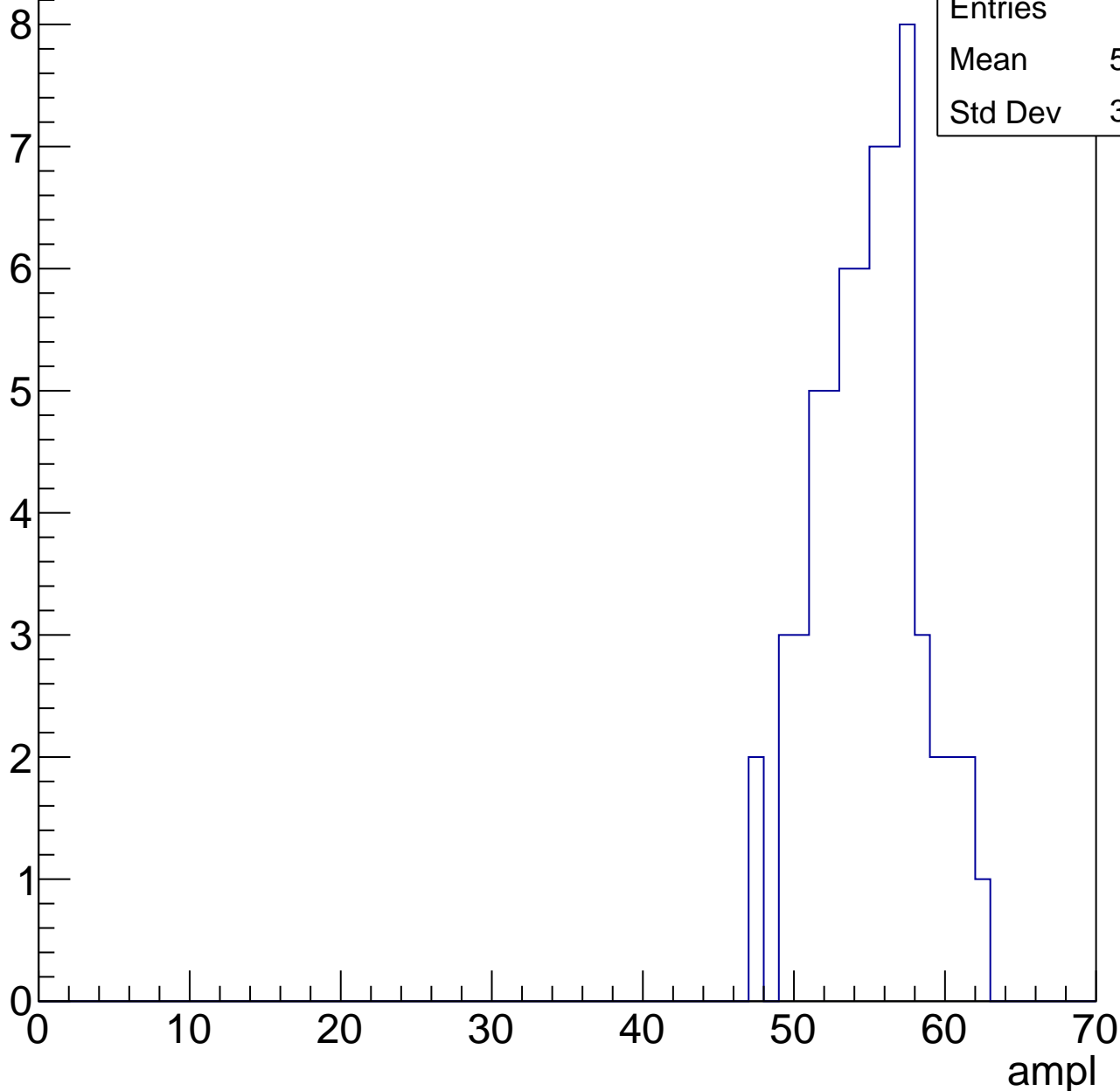


B0L001S, U14-ch96, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	54.47
Std Dev	3.402

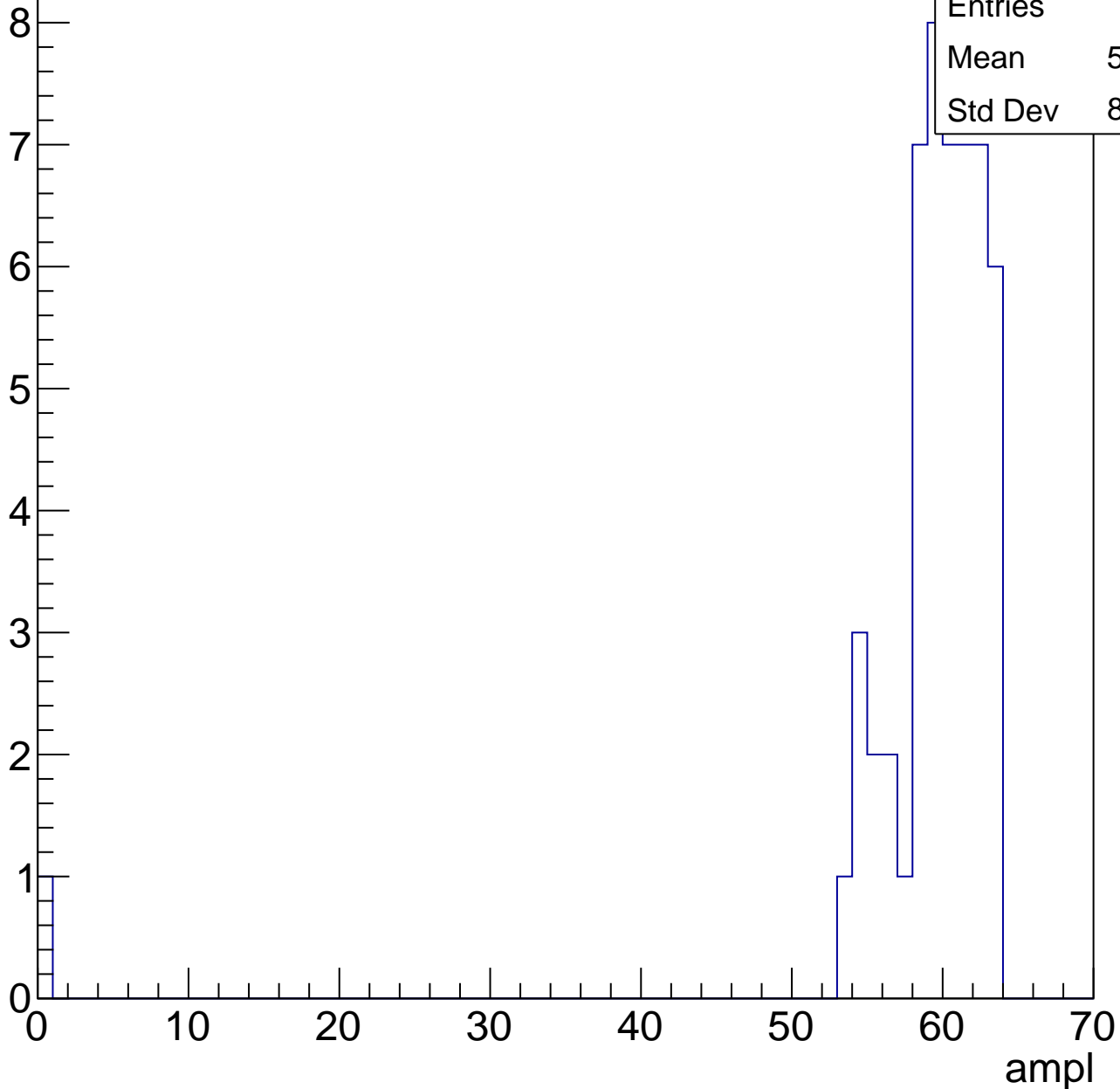


B0L001S, U14-ch96, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	58.29
Std Dev	8.572



B0L001S, U14-ch96, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

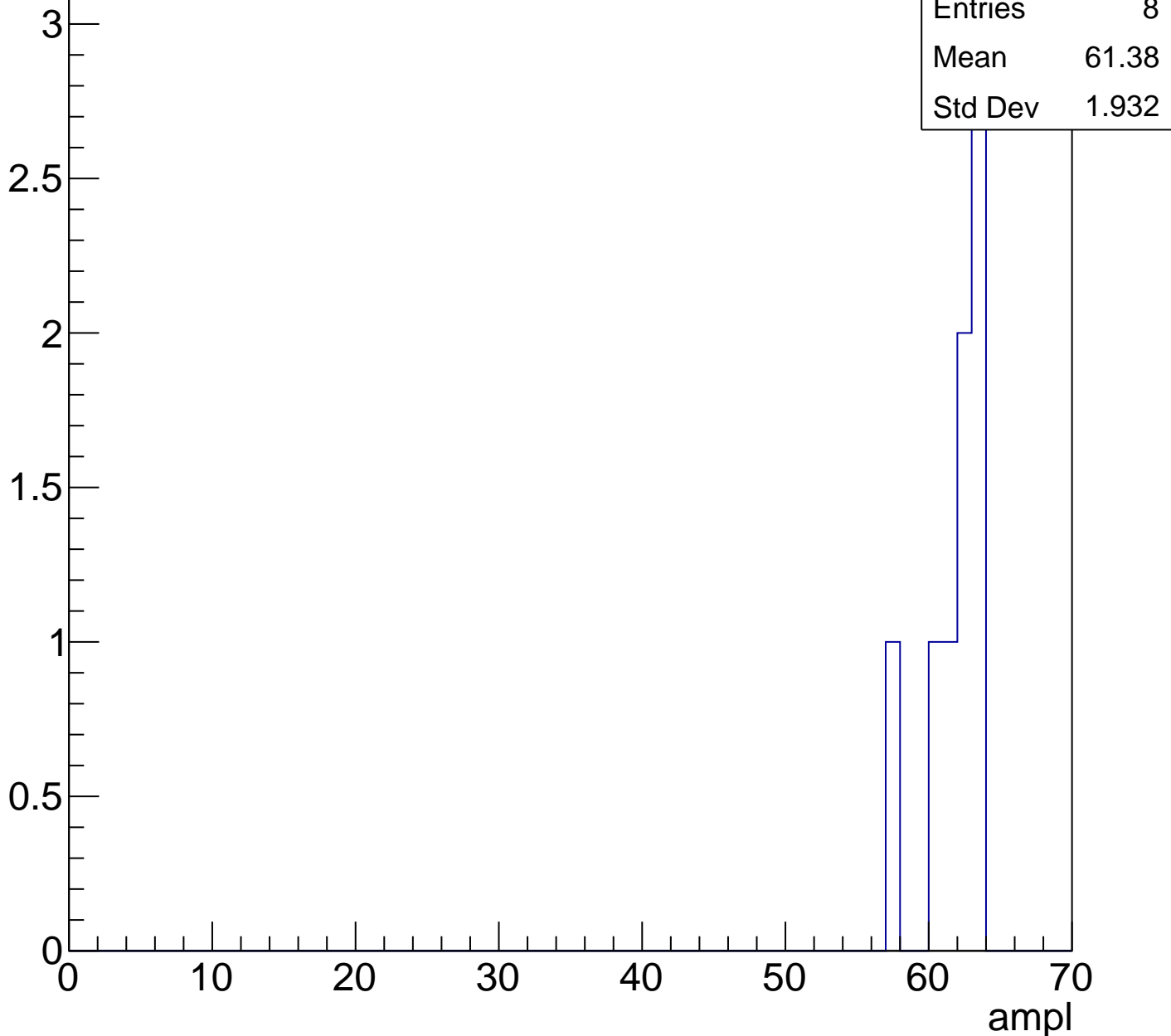
8

Mean

61.38

Std Dev

1.932



B0L001S, U14-ch96, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch97, adc0

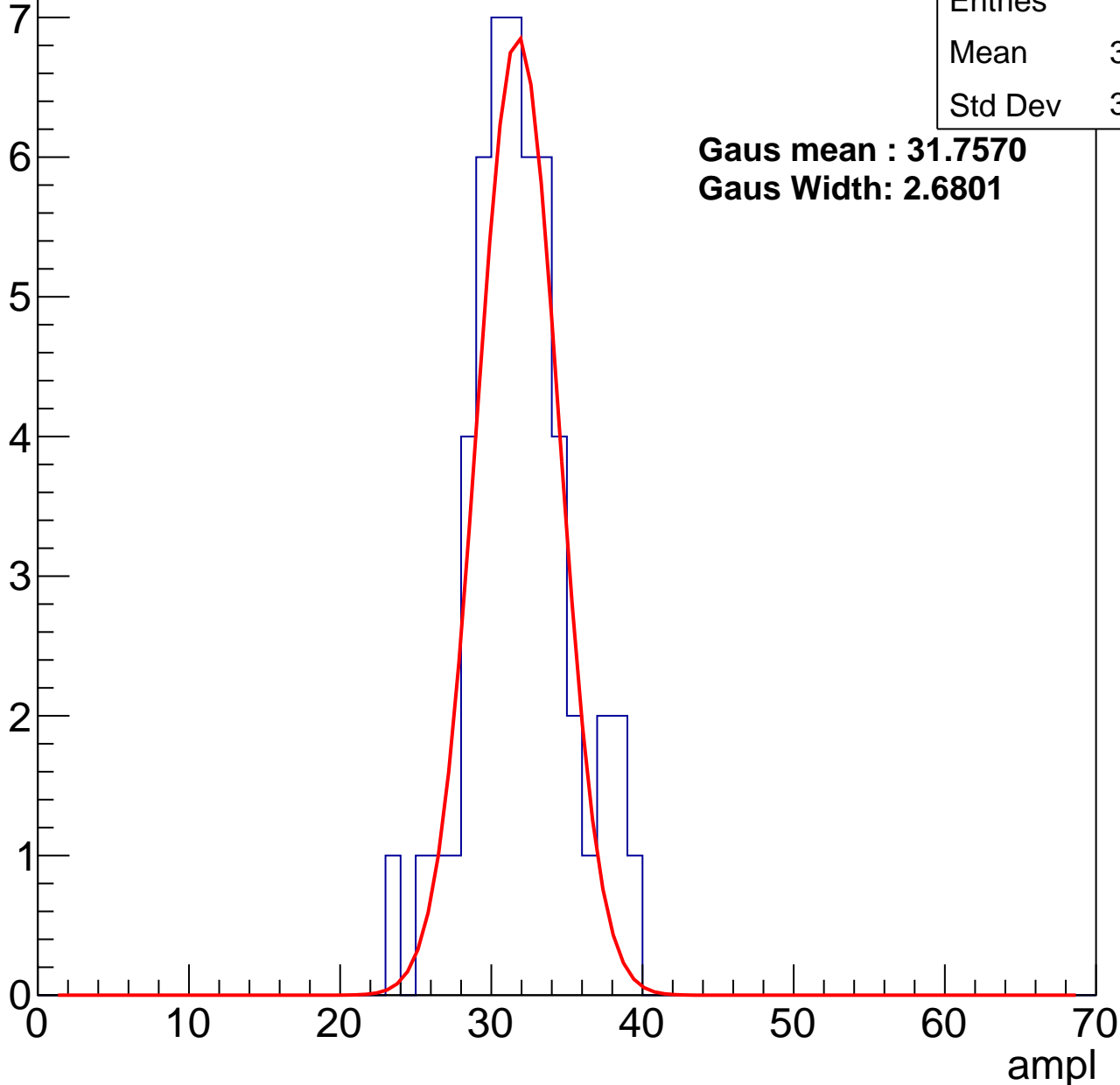
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	31.44
Std Dev	3.266

Gaus mean : 31.7570

Gaus Width: 2.6801



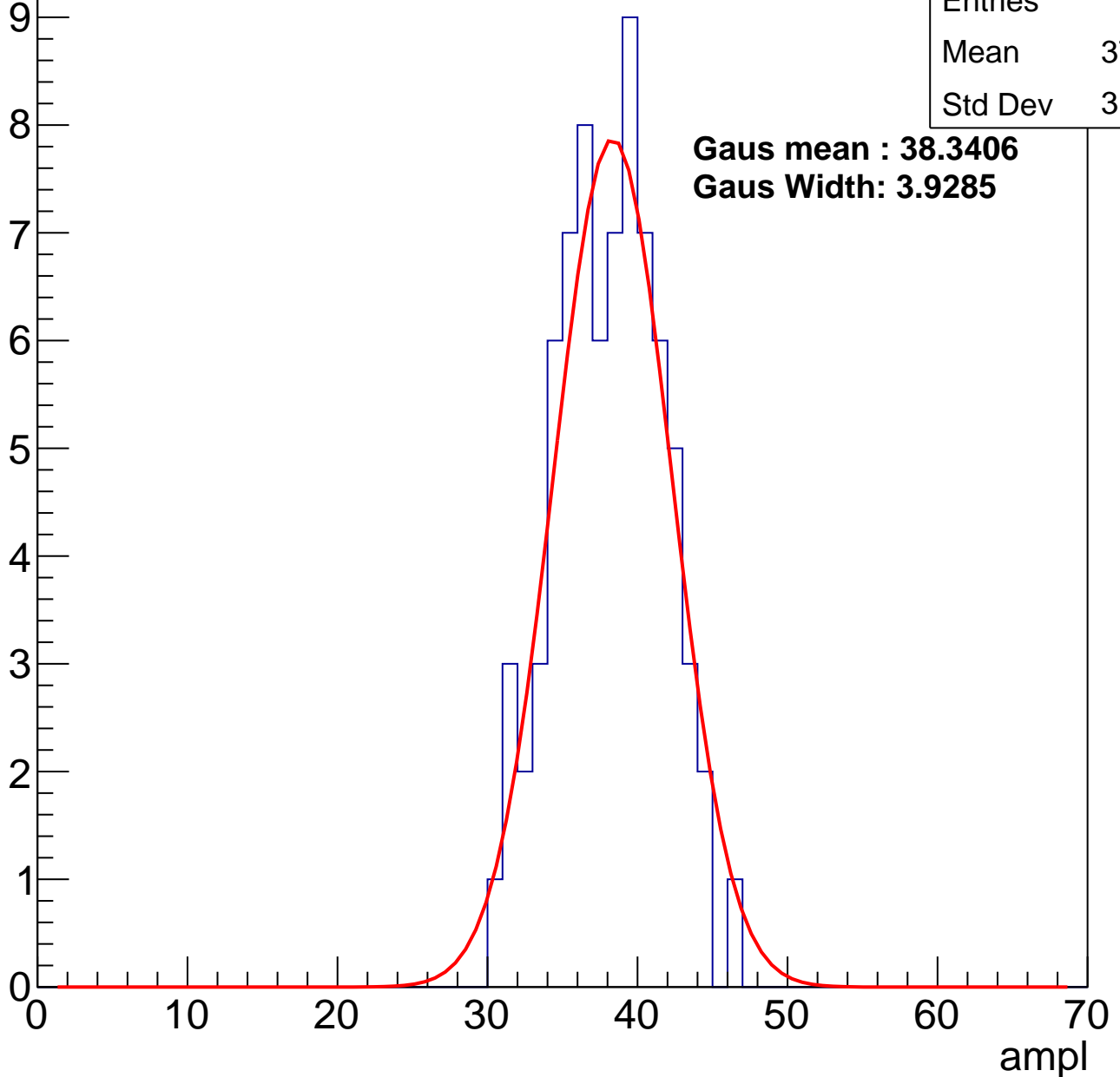
B0L001S, U14-ch97, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	37.64
Std Dev	3.493

Gaus mean : 38.3406
Gaus Width: 3.9285



B0L001S, U14-ch97, adc2

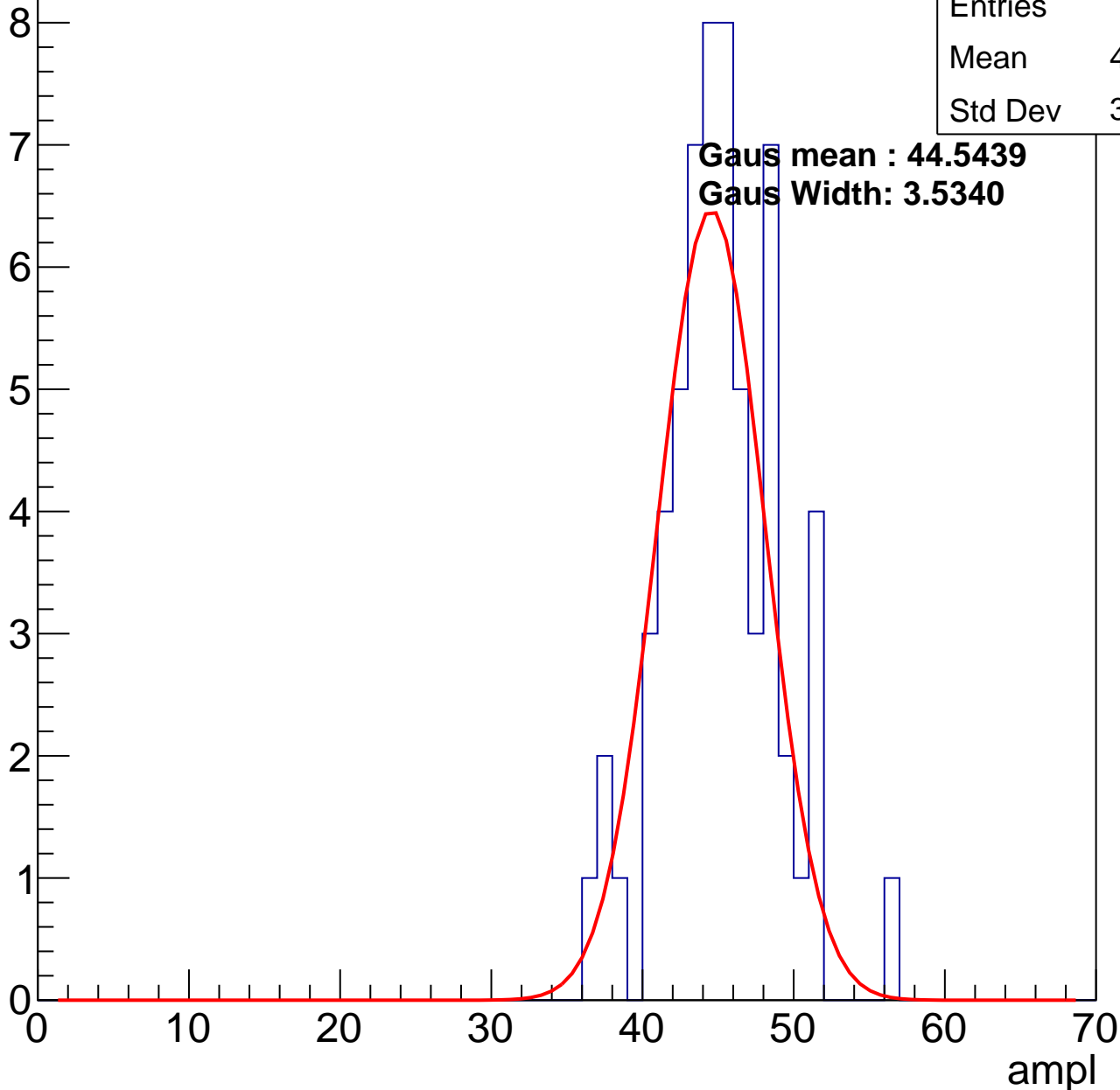
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	44.68
Std Dev	3.754

Gaus mean : 44.5439

Gaus Width: 3.5340

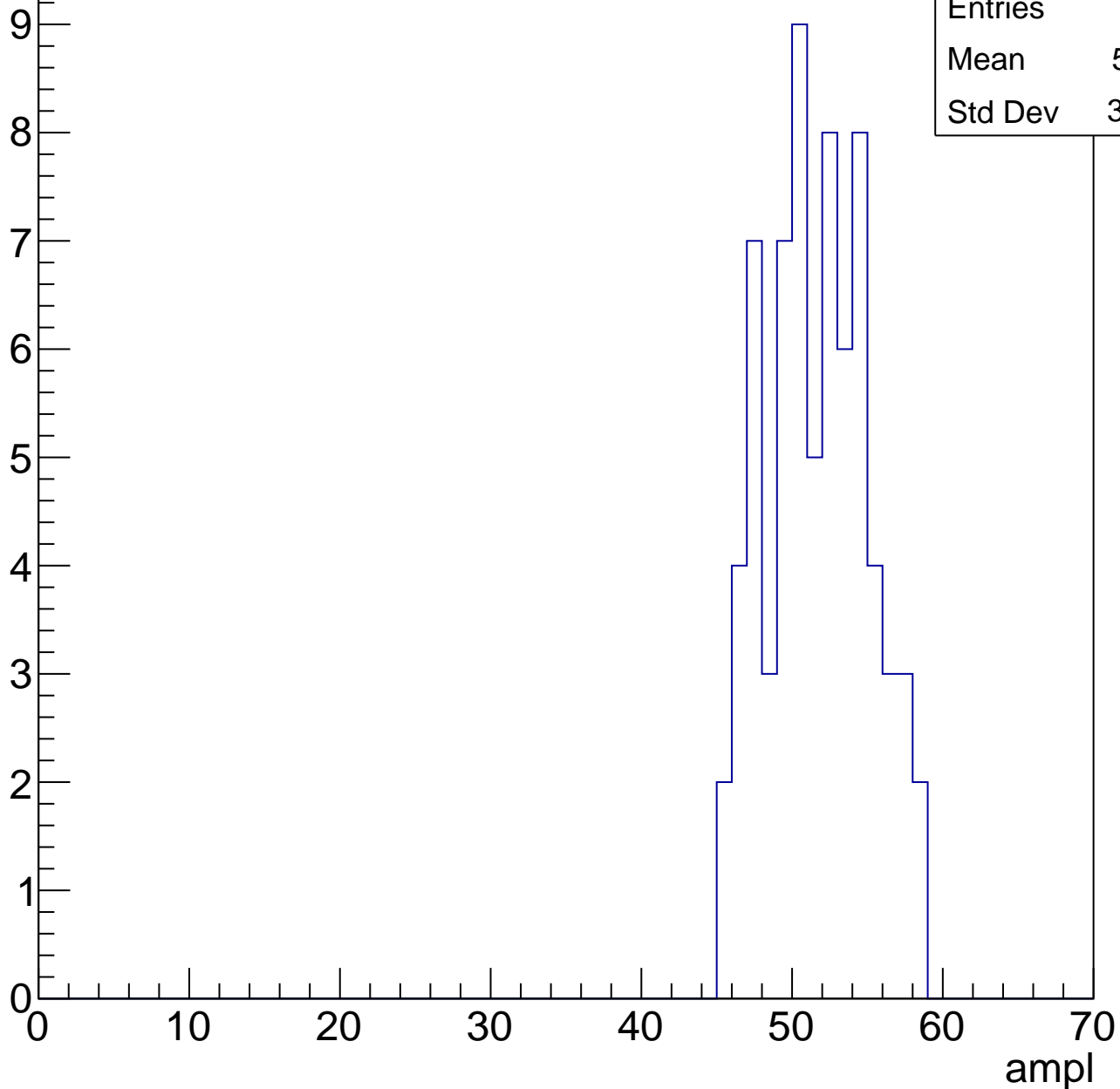


B0L001S, U14-ch97, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	71
Mean	51.21
Std Dev	3.344

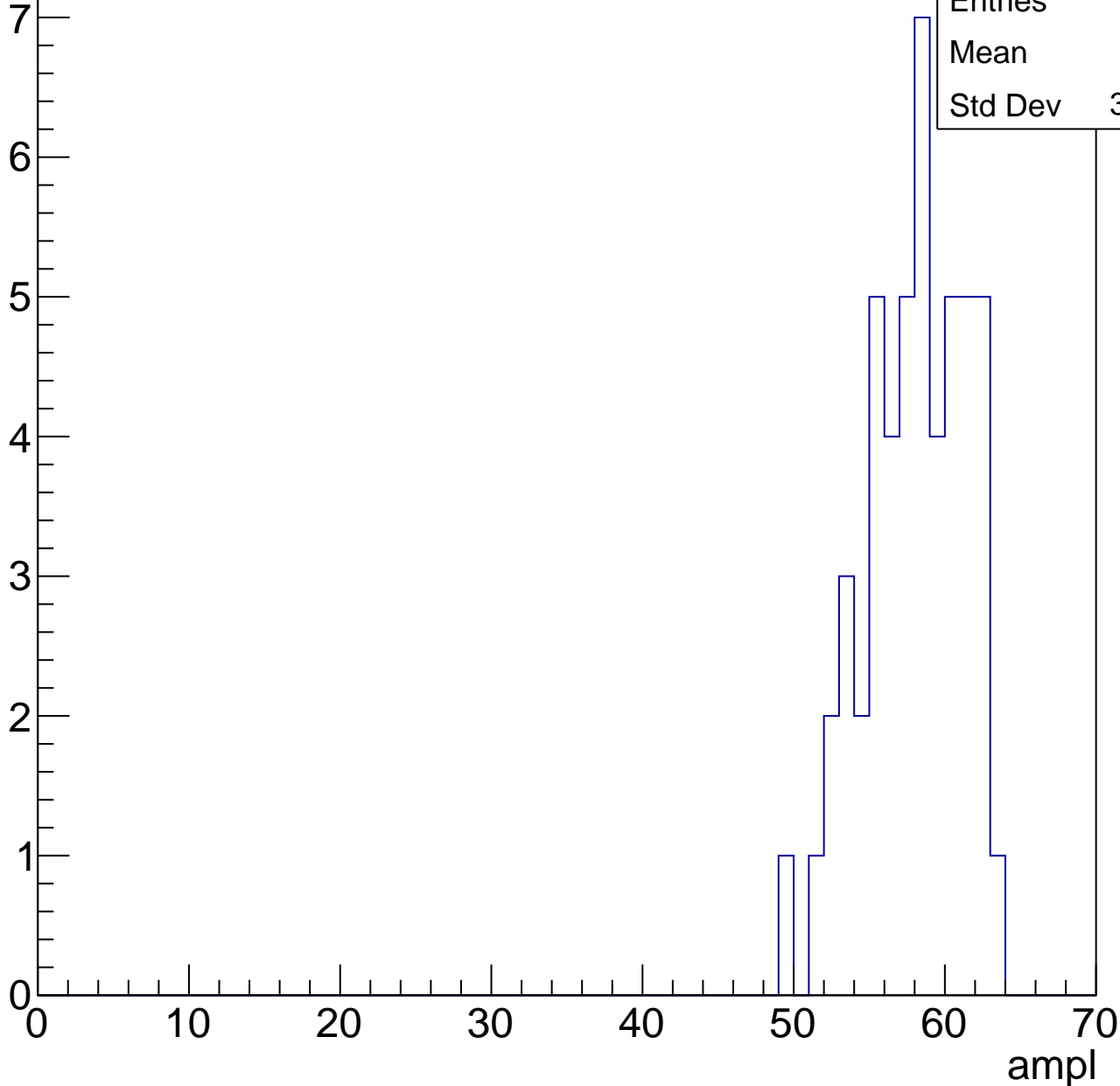


B0L001S, U14-ch97, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	57.5
Std Dev	3.276

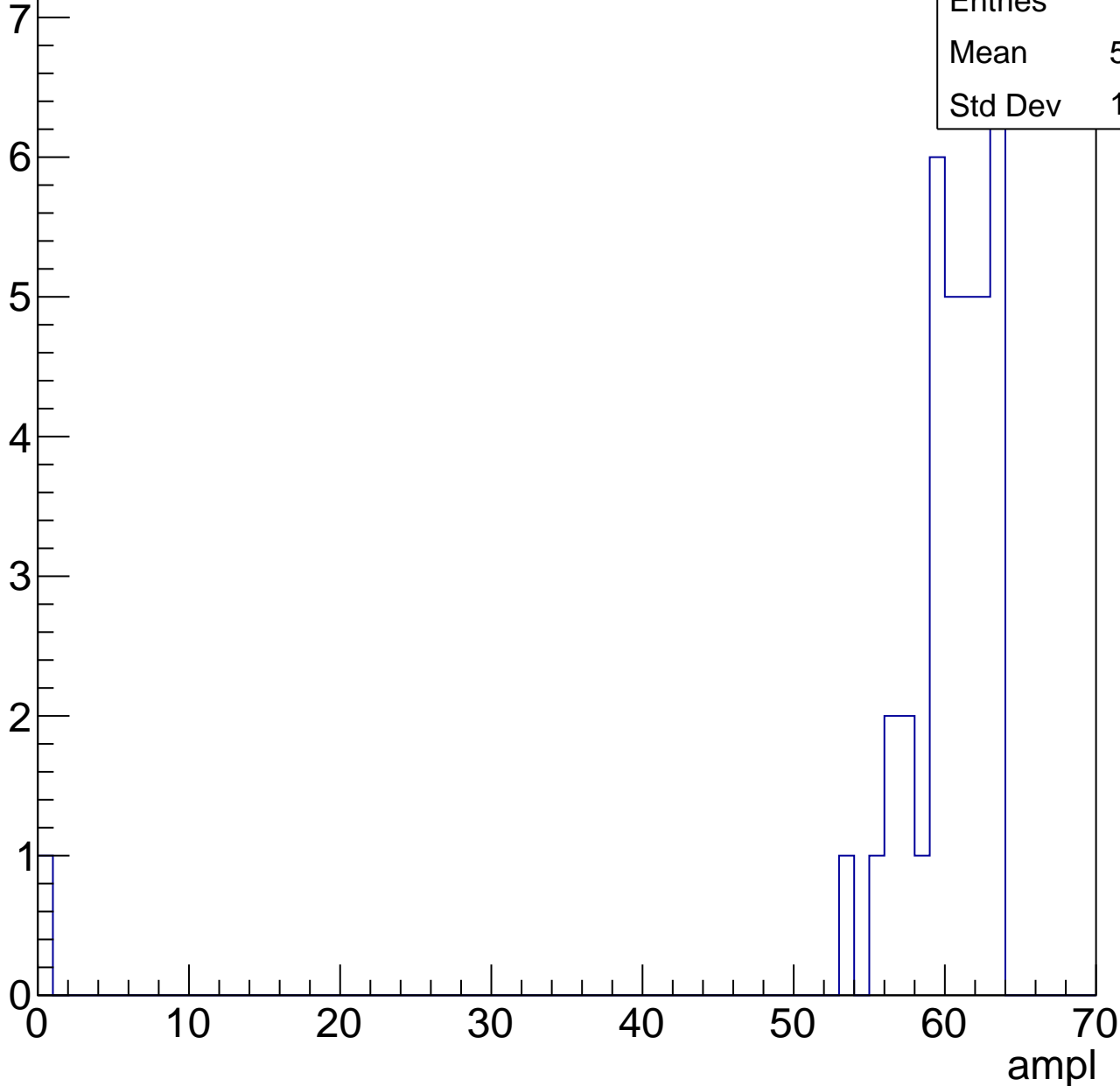


B0L001S, U14-ch97, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	36
Mean	58.39
Std Dev	10.18



B0L001S, U14-ch97, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B0L001S, U14-ch97, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch98, adc0

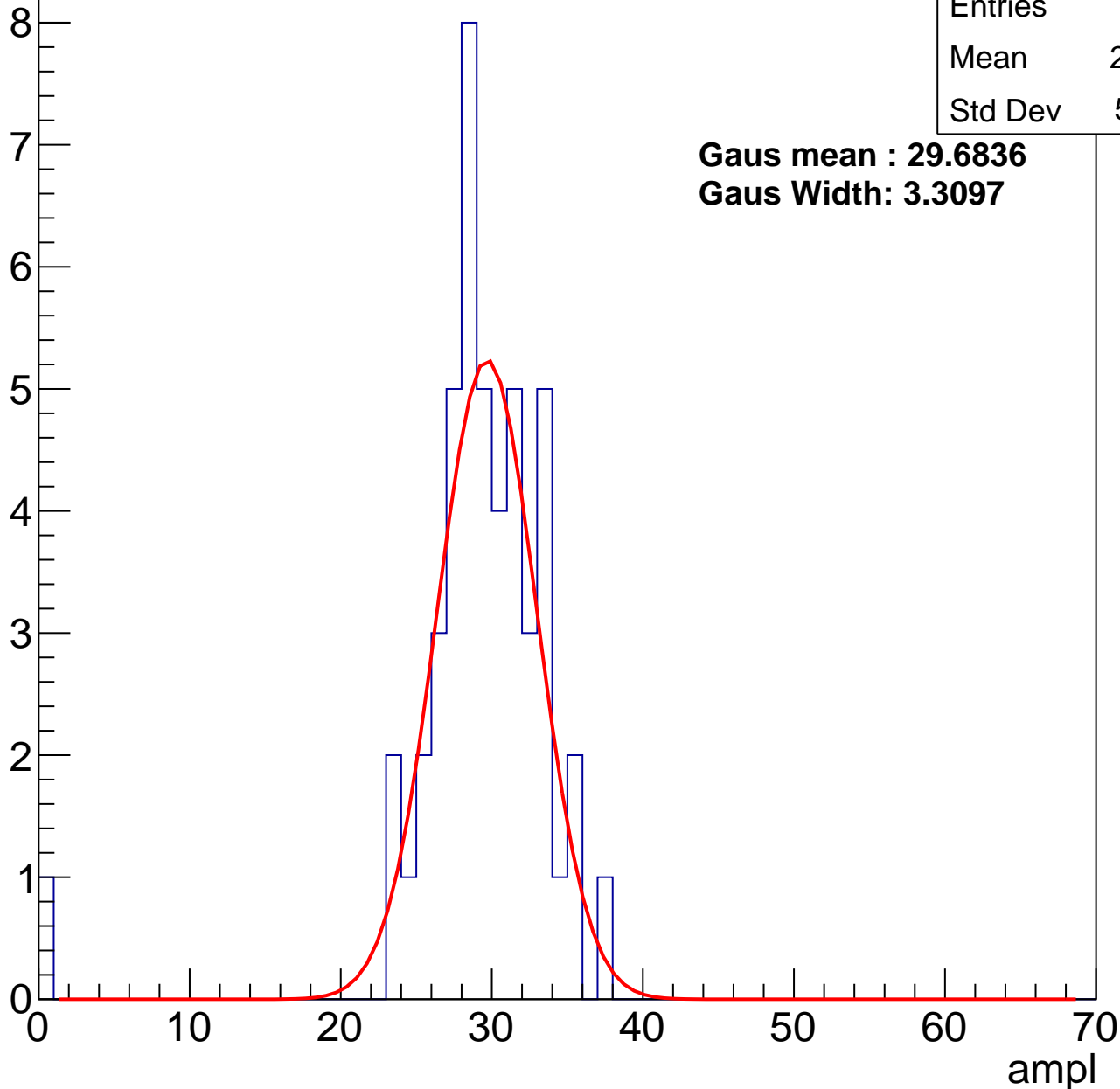
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	28.73
Std Dev	5.231

Gaus mean : 29.6836

Gaus Width: 3.3097



B0L001S, U14-ch98, adc1

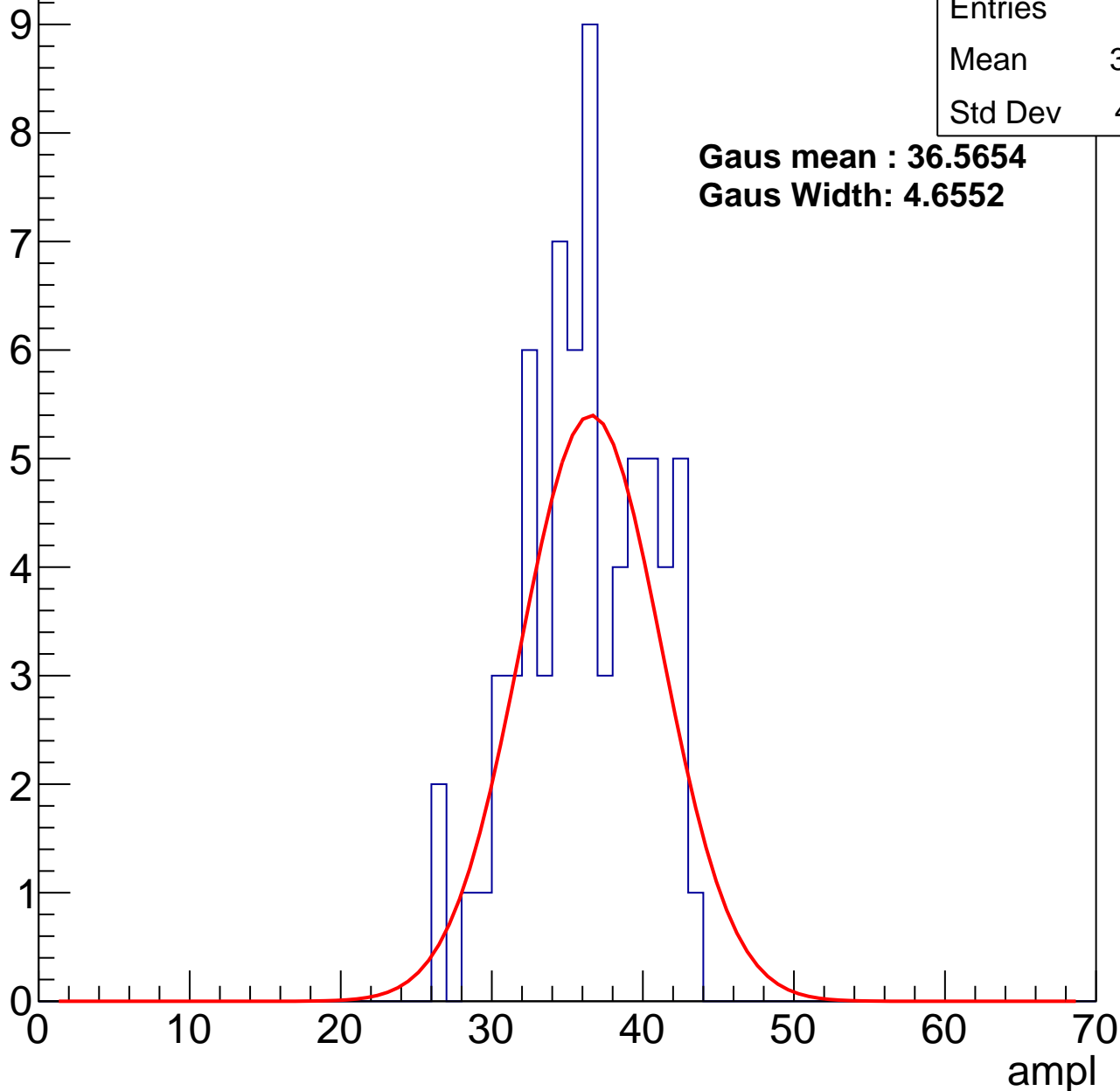
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	35.74
Std Dev	4.061

Gaus mean : 36.5654

Gaus Width: 4.6552



B0L001S, U14-ch98, adc2

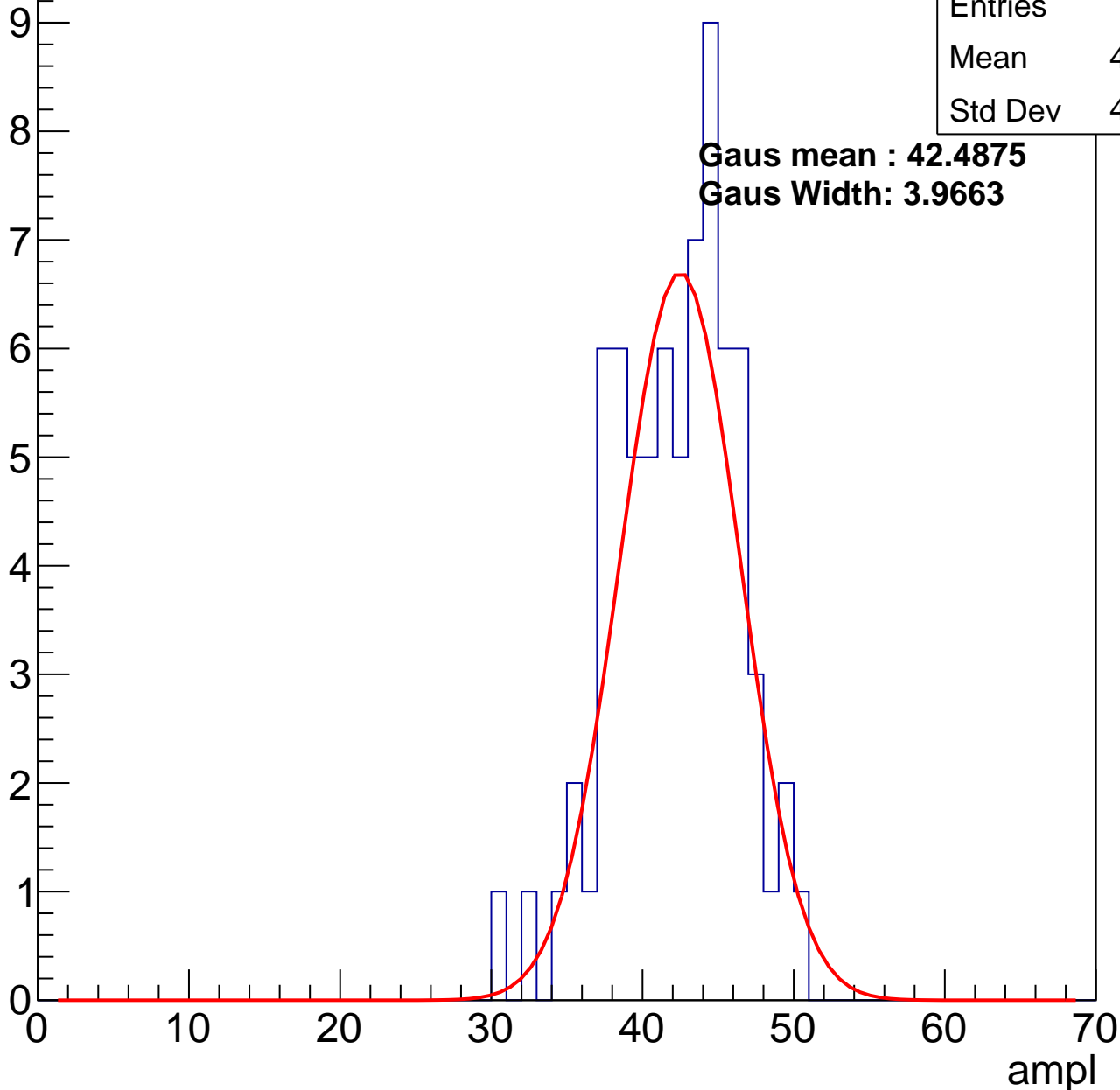
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	41.66
Std Dev	4.055

Gaus mean : 42.4875

Gaus Width: 3.9663

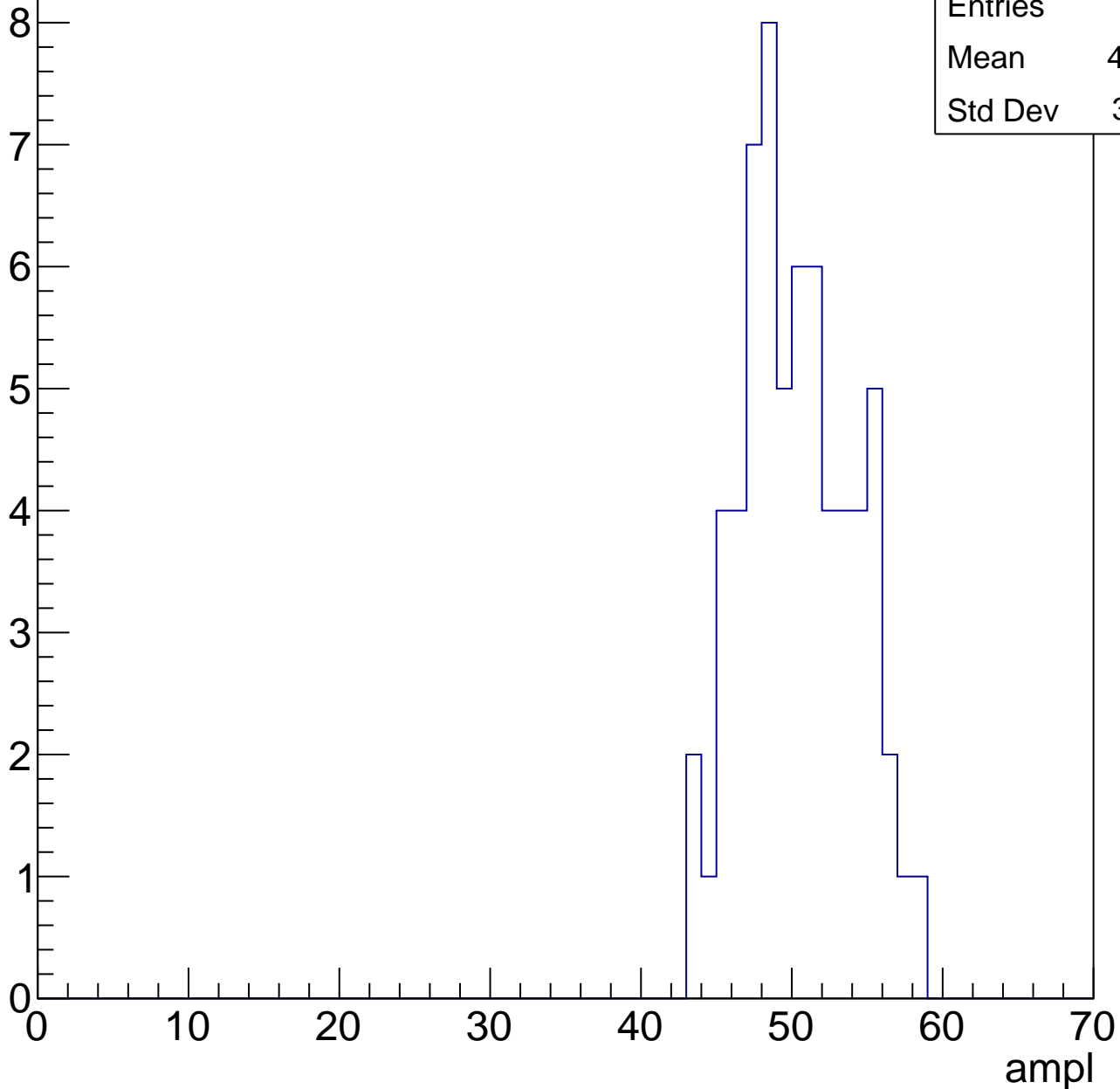


B0L001S, U14-ch98, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.94
Std Dev	3.601

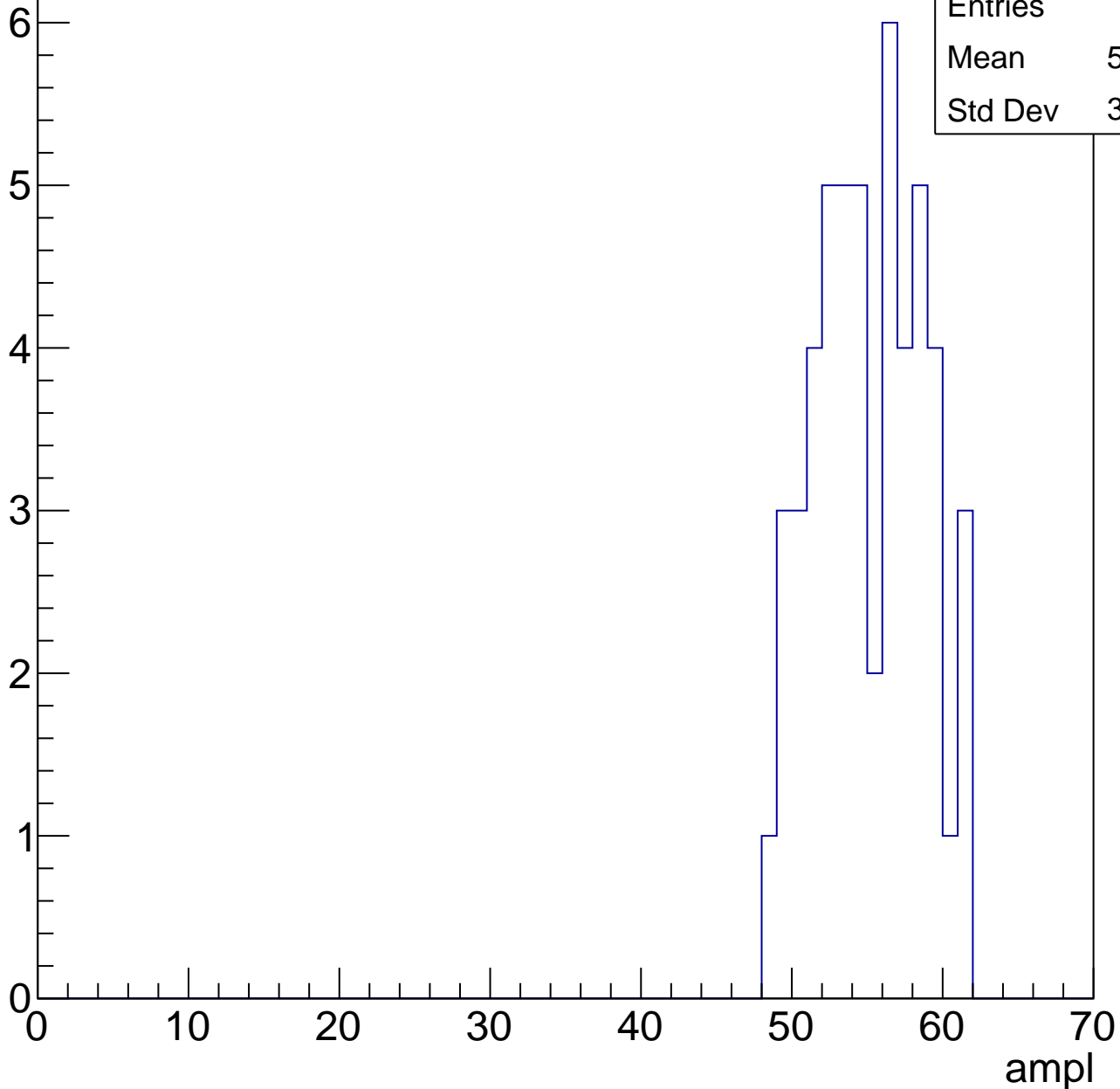


B0L001S, U14-ch98, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	54.65
Std Dev	3.497

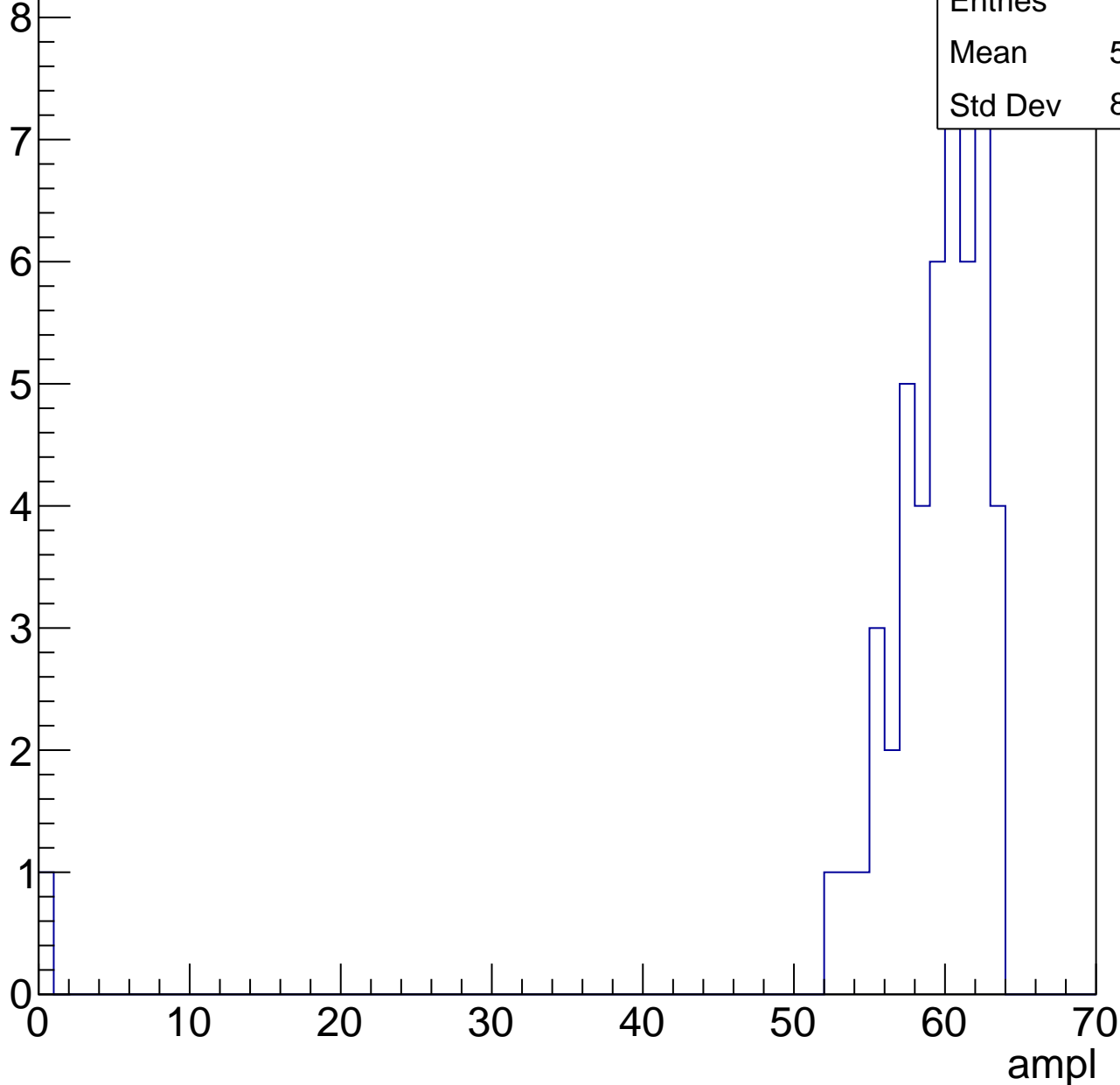


B0L001S, U14-ch98, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	58.02
Std Dev	8.719

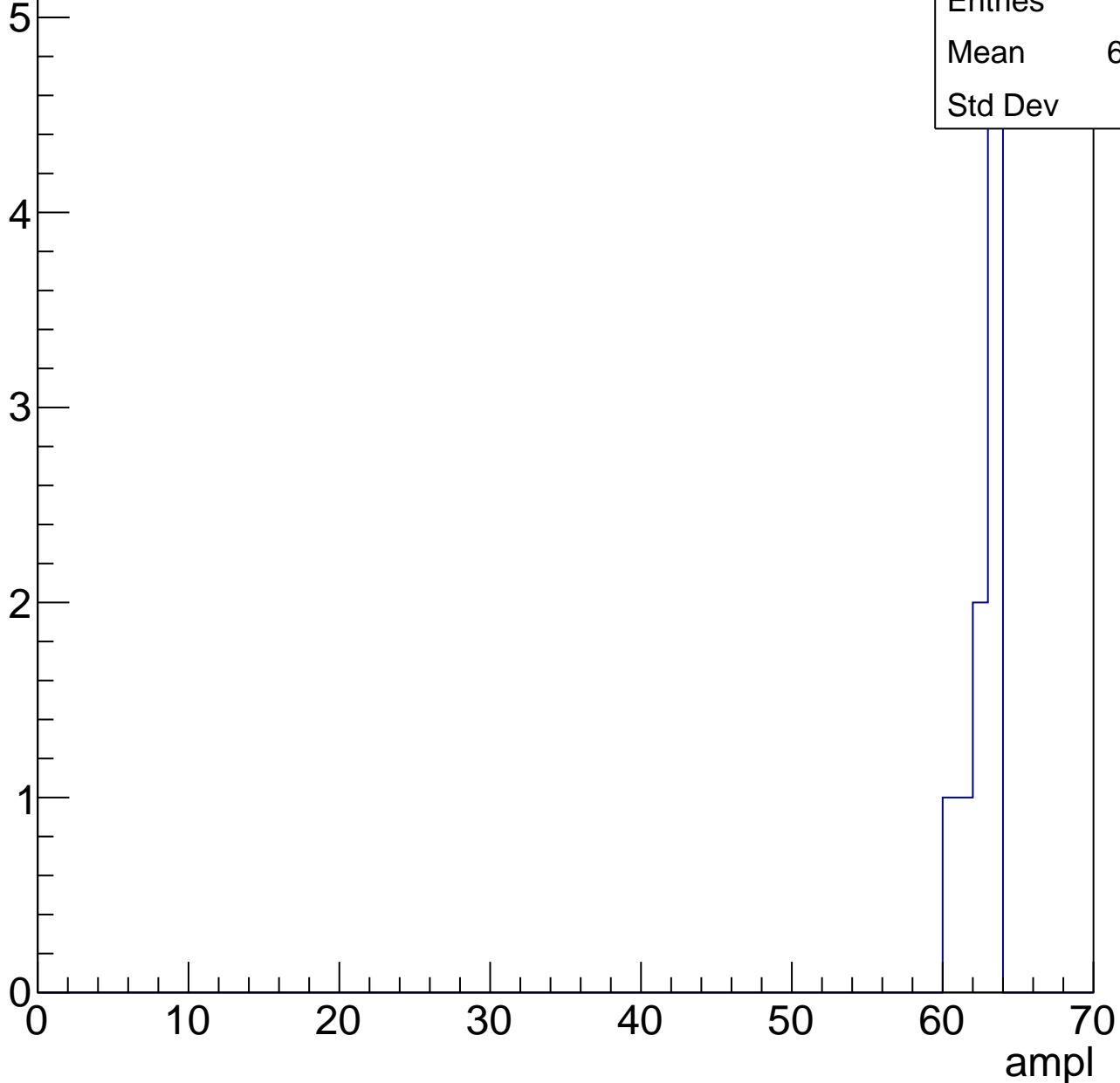


B0L001S, U14-ch98, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

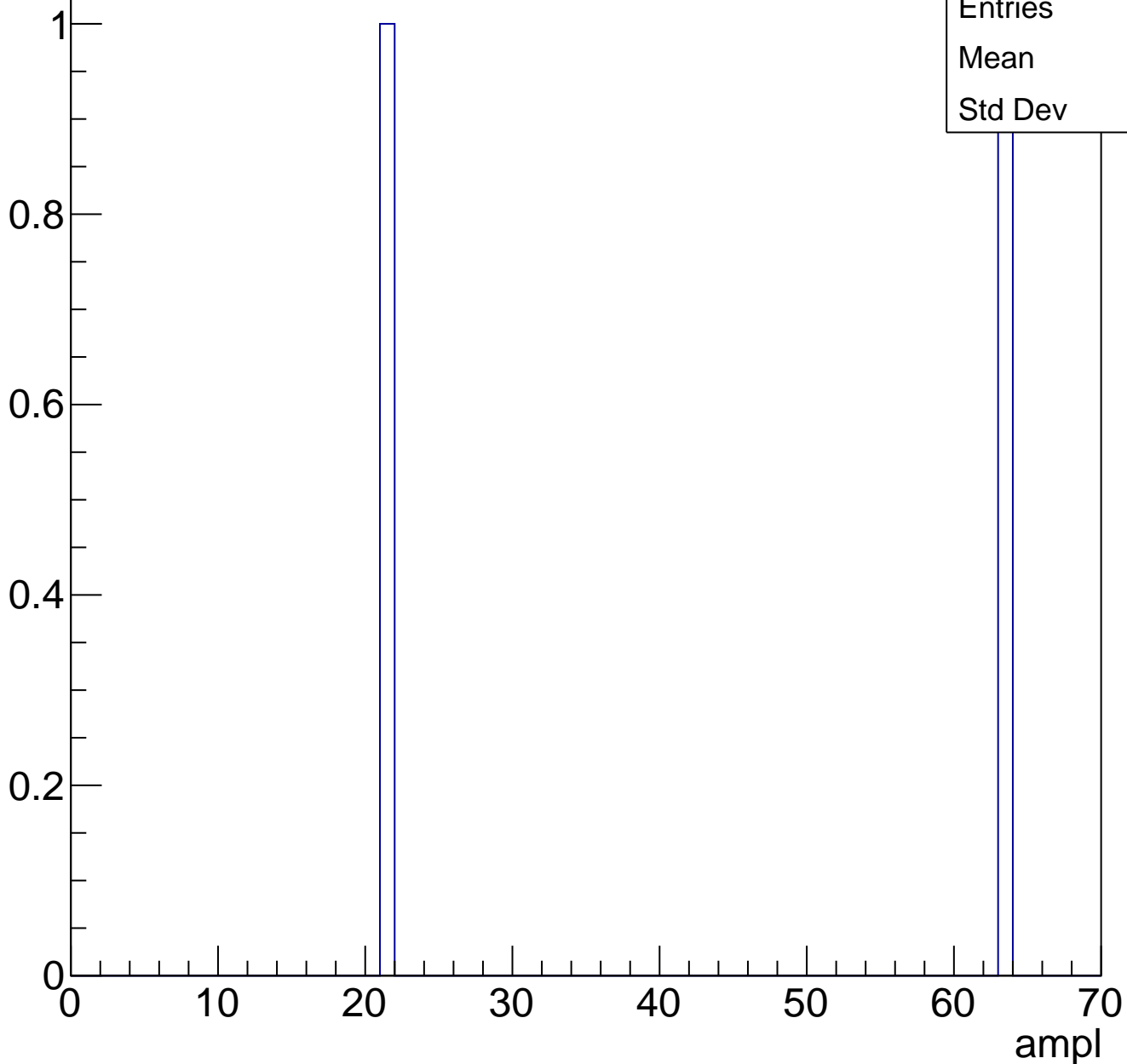
Entries	9
Mean	62.22
Std Dev	1.03



B0L001S, U14-ch98, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch99, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	76
Mean	31.67
Std Dev	3.397

Gaus mean : 32.3235

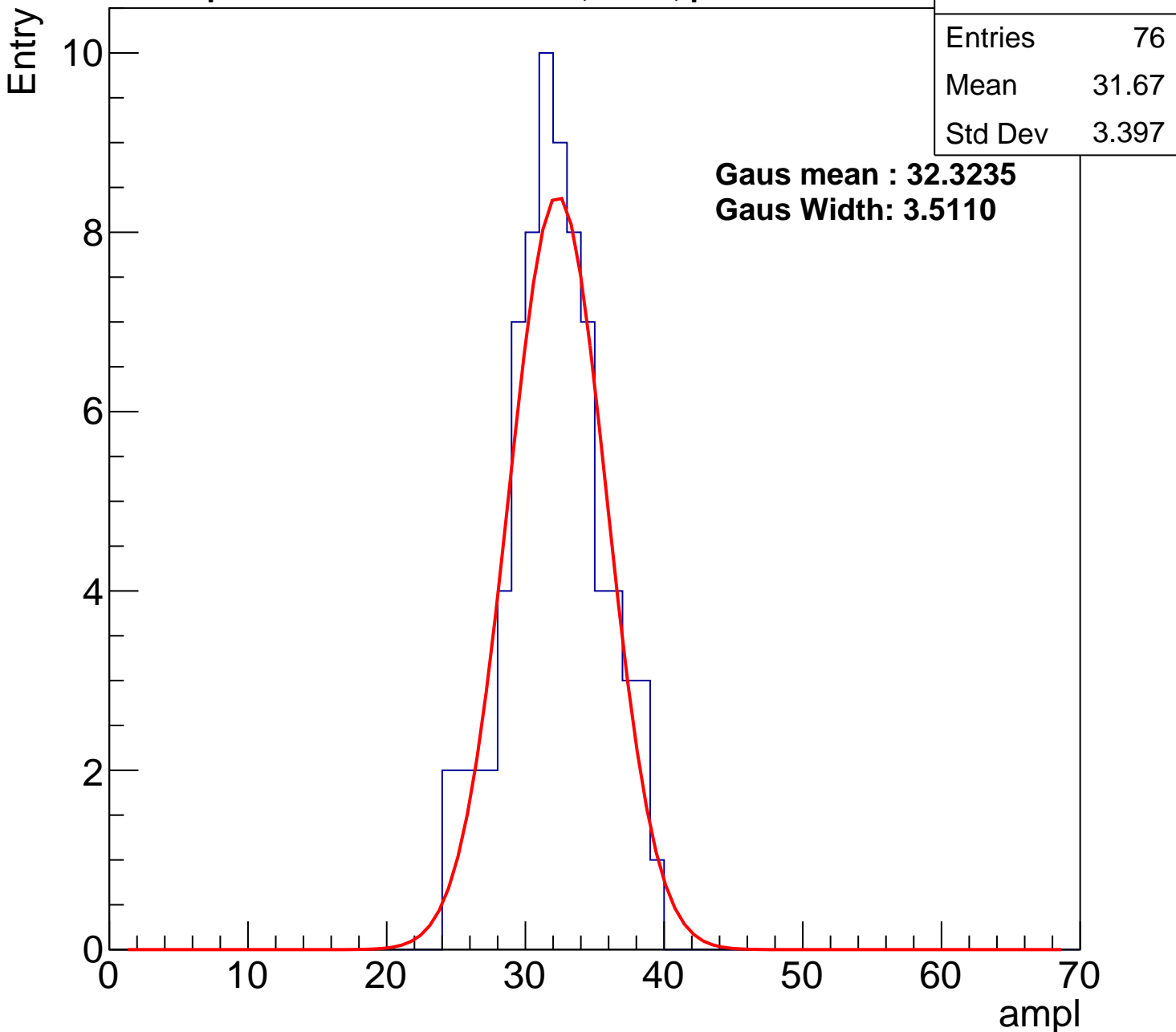
Gaus Width: 3.5110

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



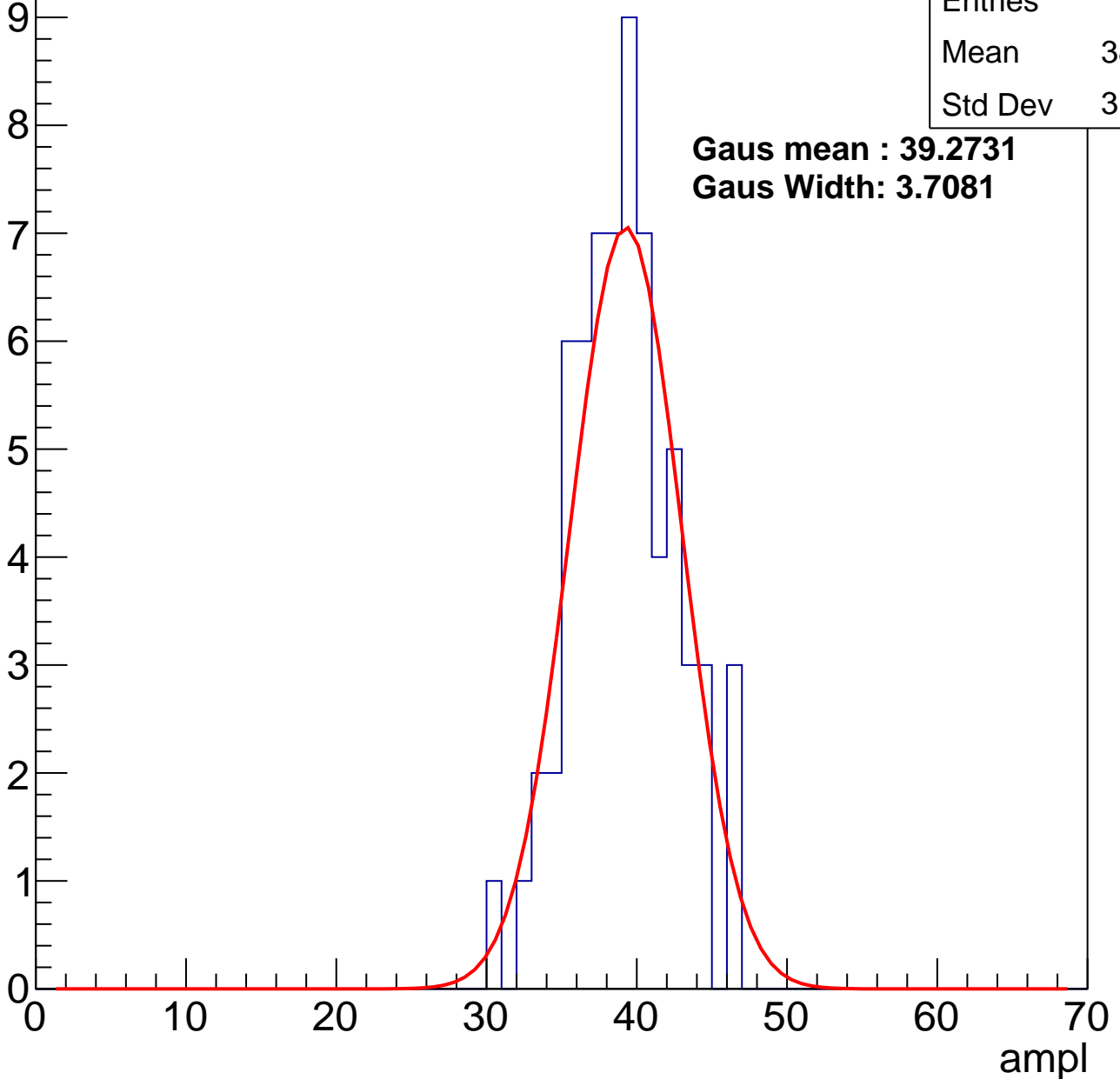
B0L001S, U14-ch99, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	38.65
Std Dev	3.409

Gaus mean : 39.2731
Gaus Width: 3.7081



B0L001S, U14-ch99, adc2

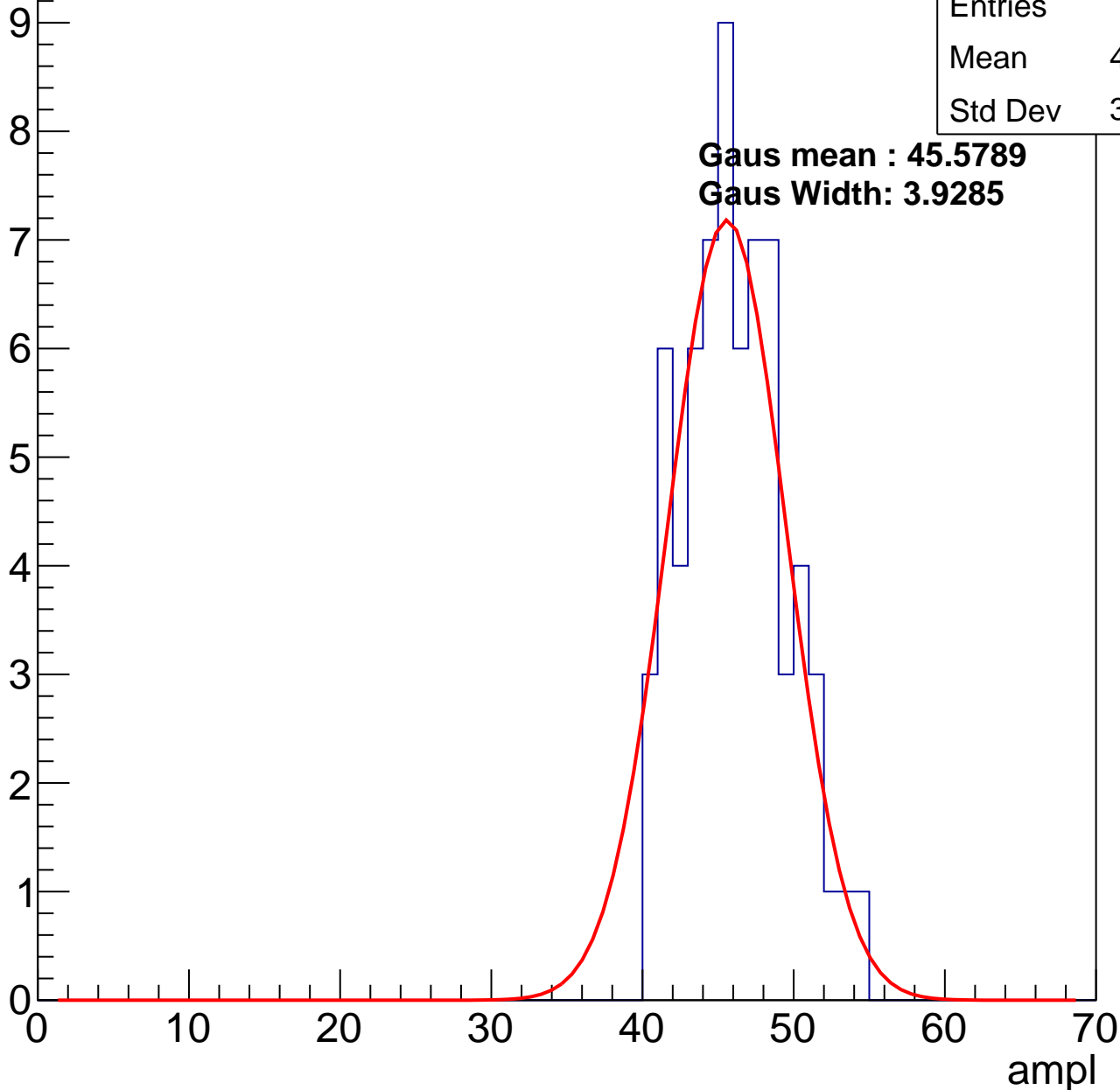
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	45.66
Std Dev	3.333

Gaus mean : 45.5789

Gaus Width: 3.9285

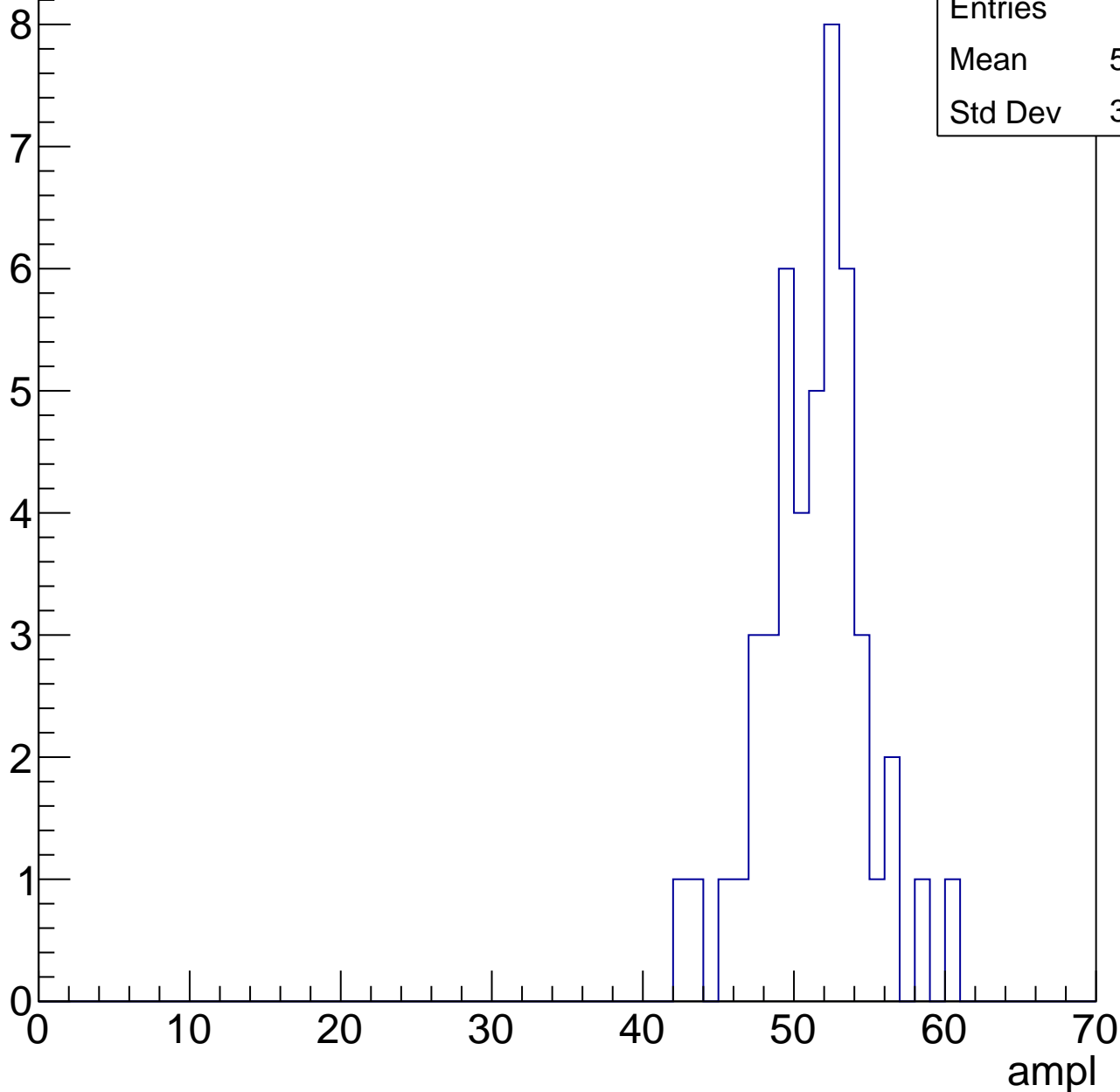


B0L001S, U14-ch99, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

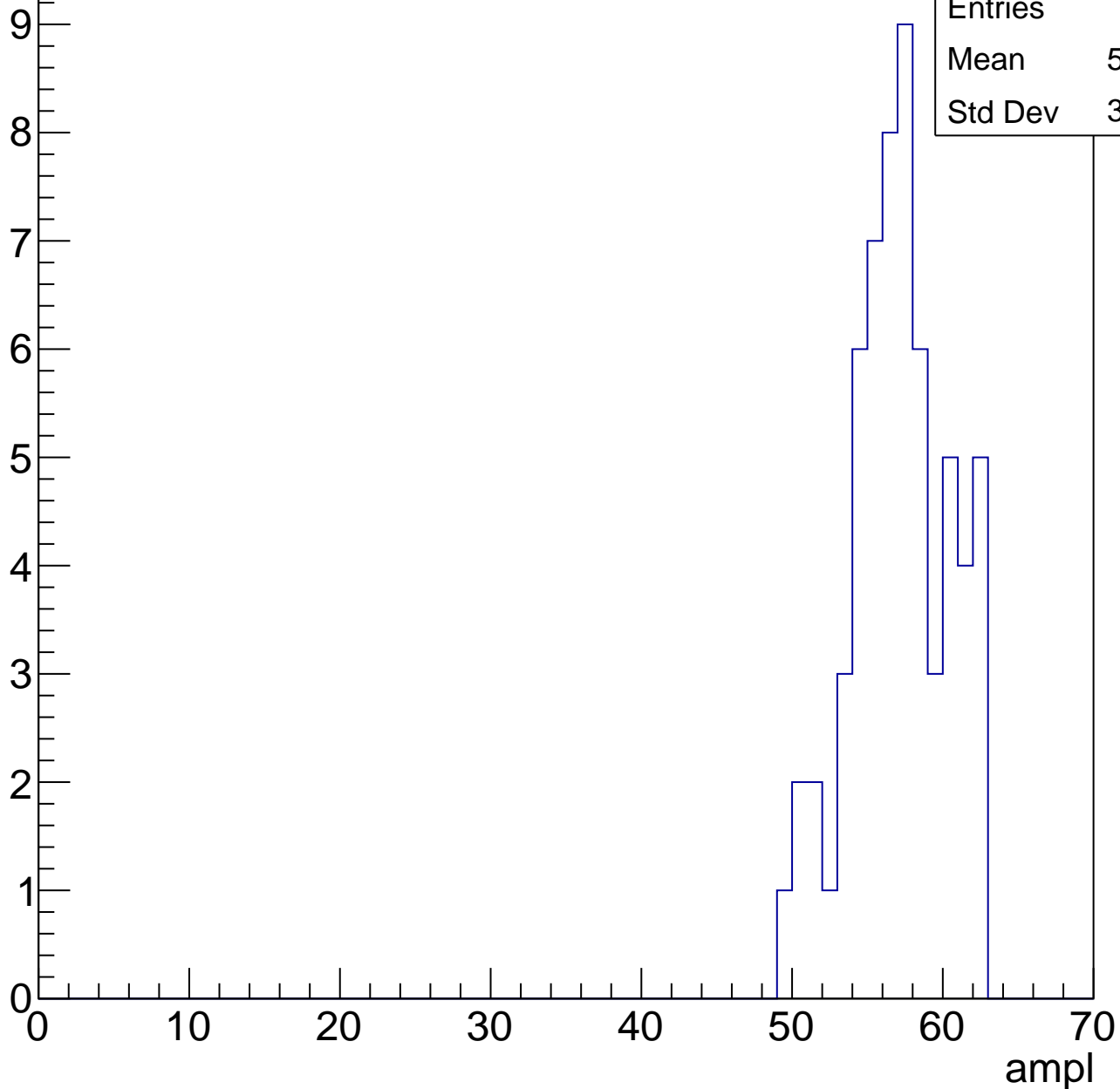
Entries	47
Mean	50.87
Std Dev	3.462



B0L001S, U14-ch99, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

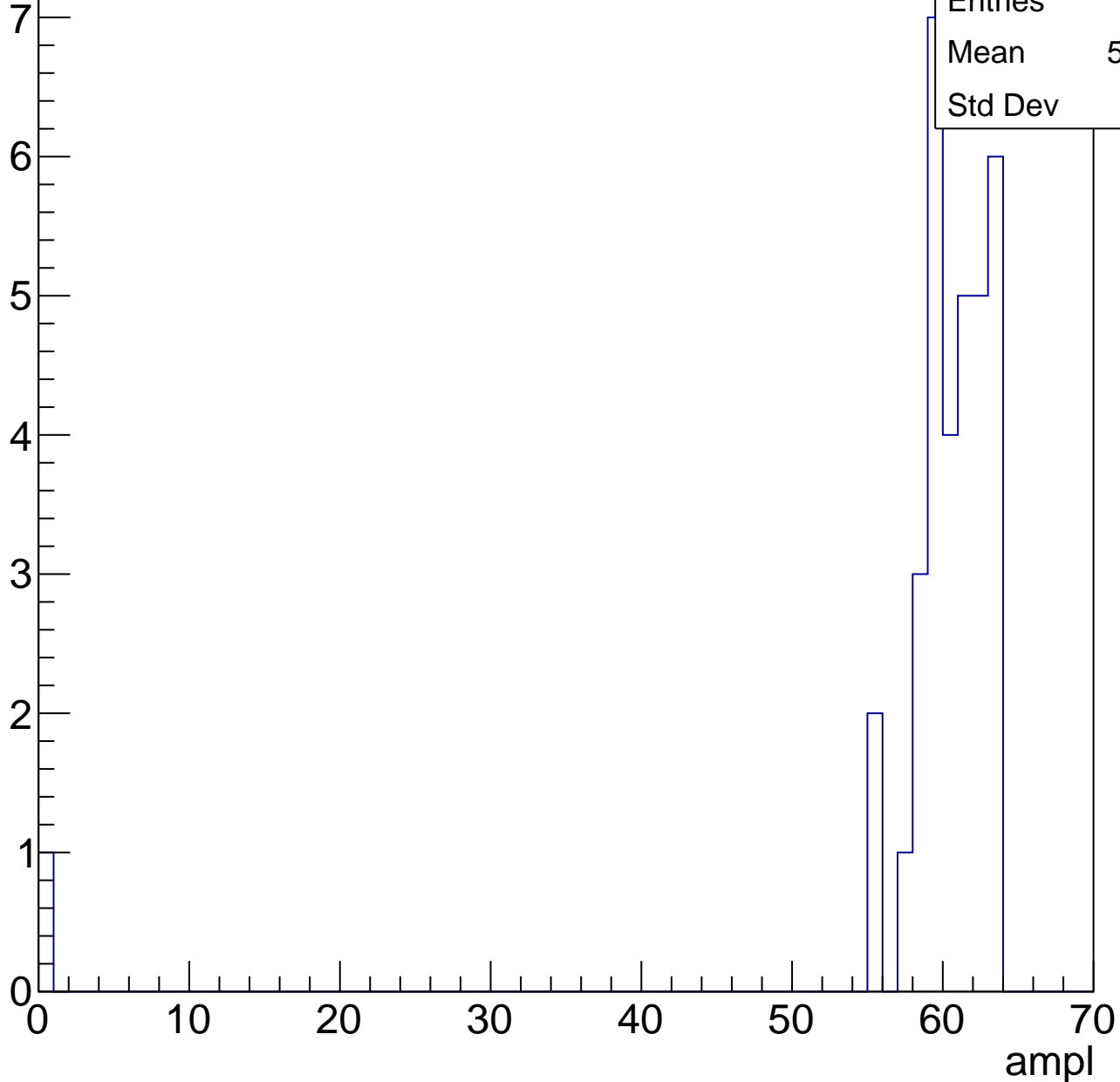


Entries	62
Mean	56.63
Std Dev	3.214

B0L001S, U14-ch99, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch99, adc6

calib_packv5_042523_0143.root, FC#9, port A1

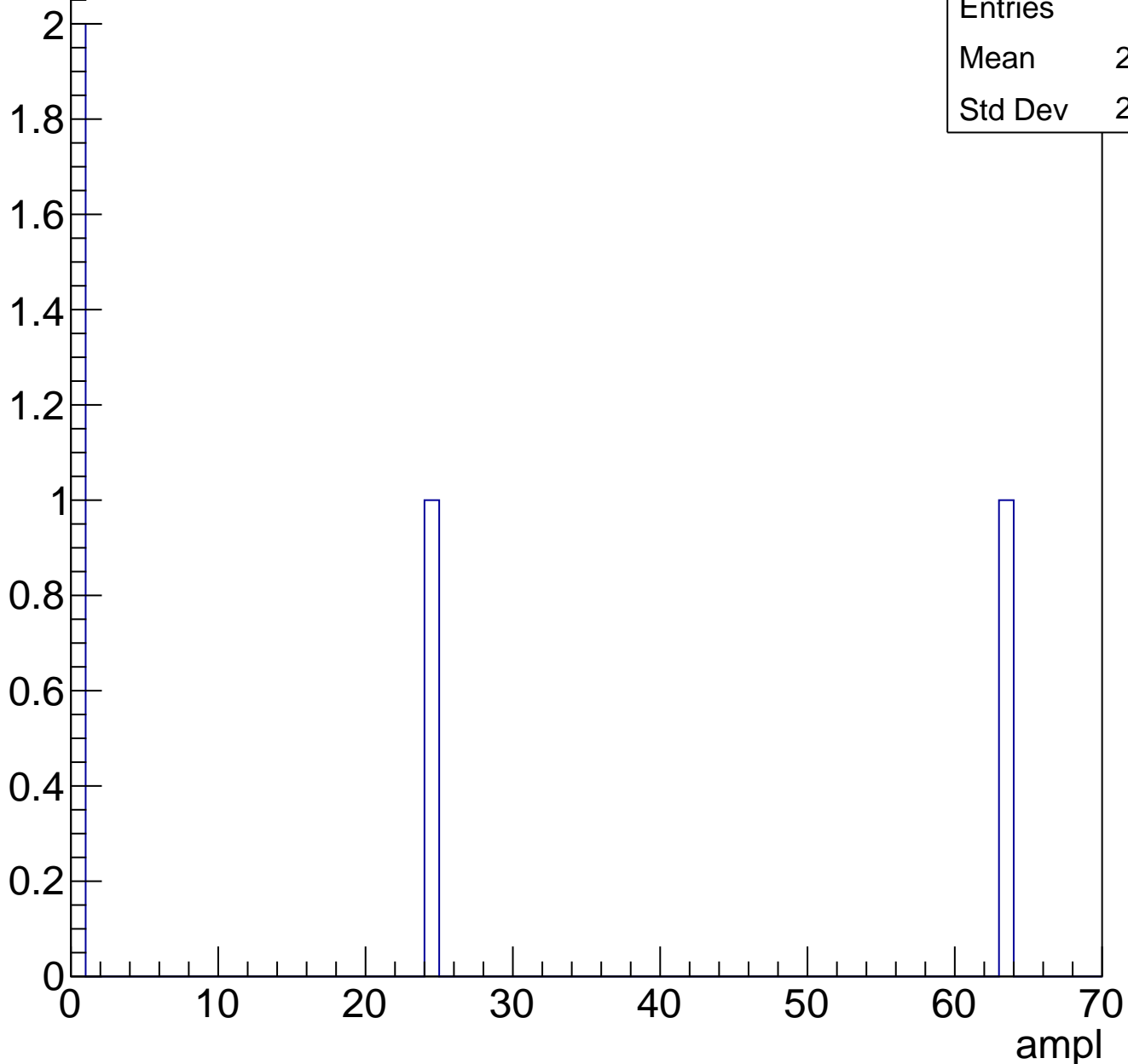
Entry



B0L001S, U14-ch99, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	21.75
Std Dev	25.75

B0L001S, U14-ch100, adc0

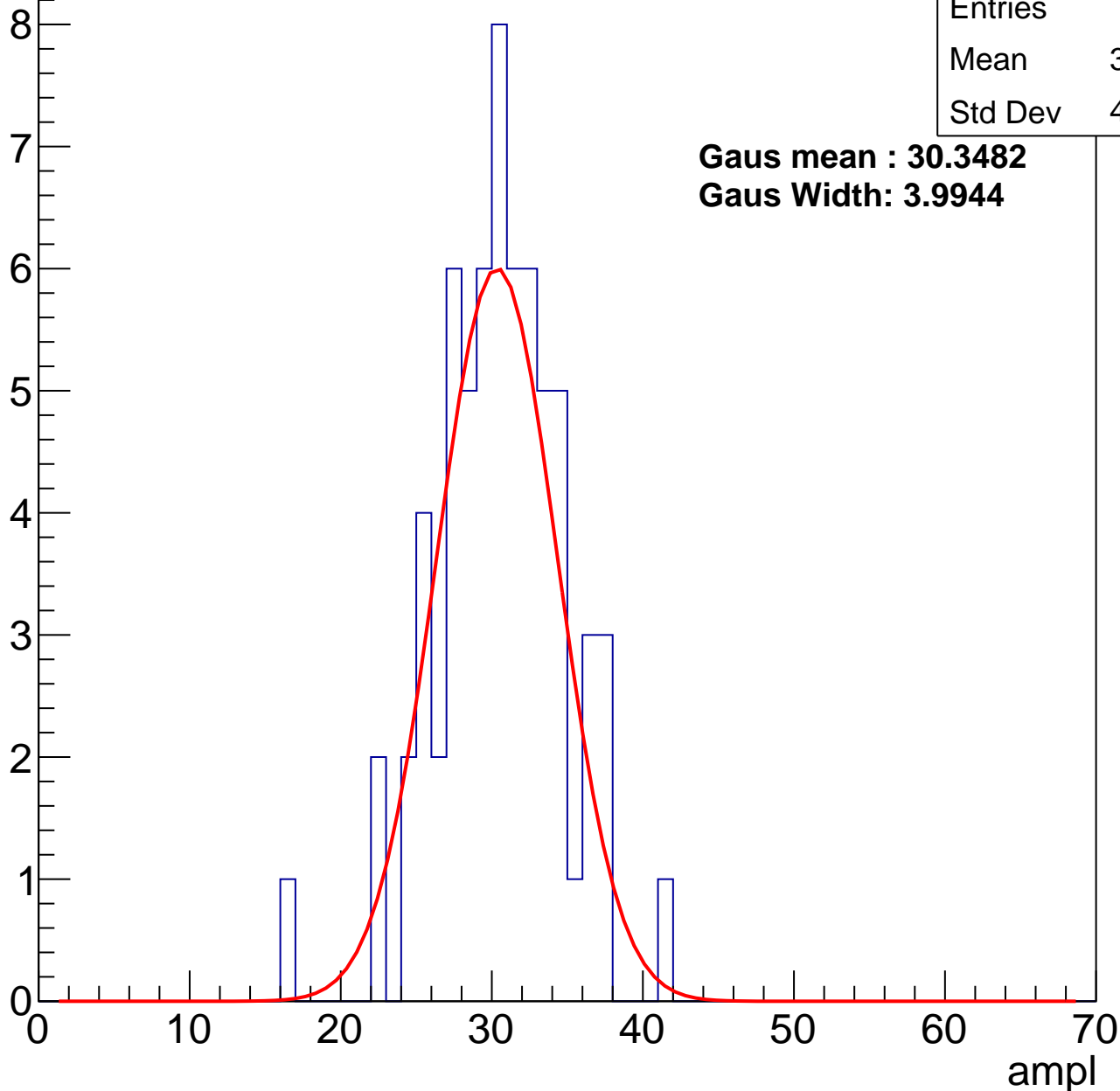
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	30.06
Std Dev	4.199

Gaus mean : 30.3482

Gaus Width: 3.9944



B0L001S, U14-ch100, adc1

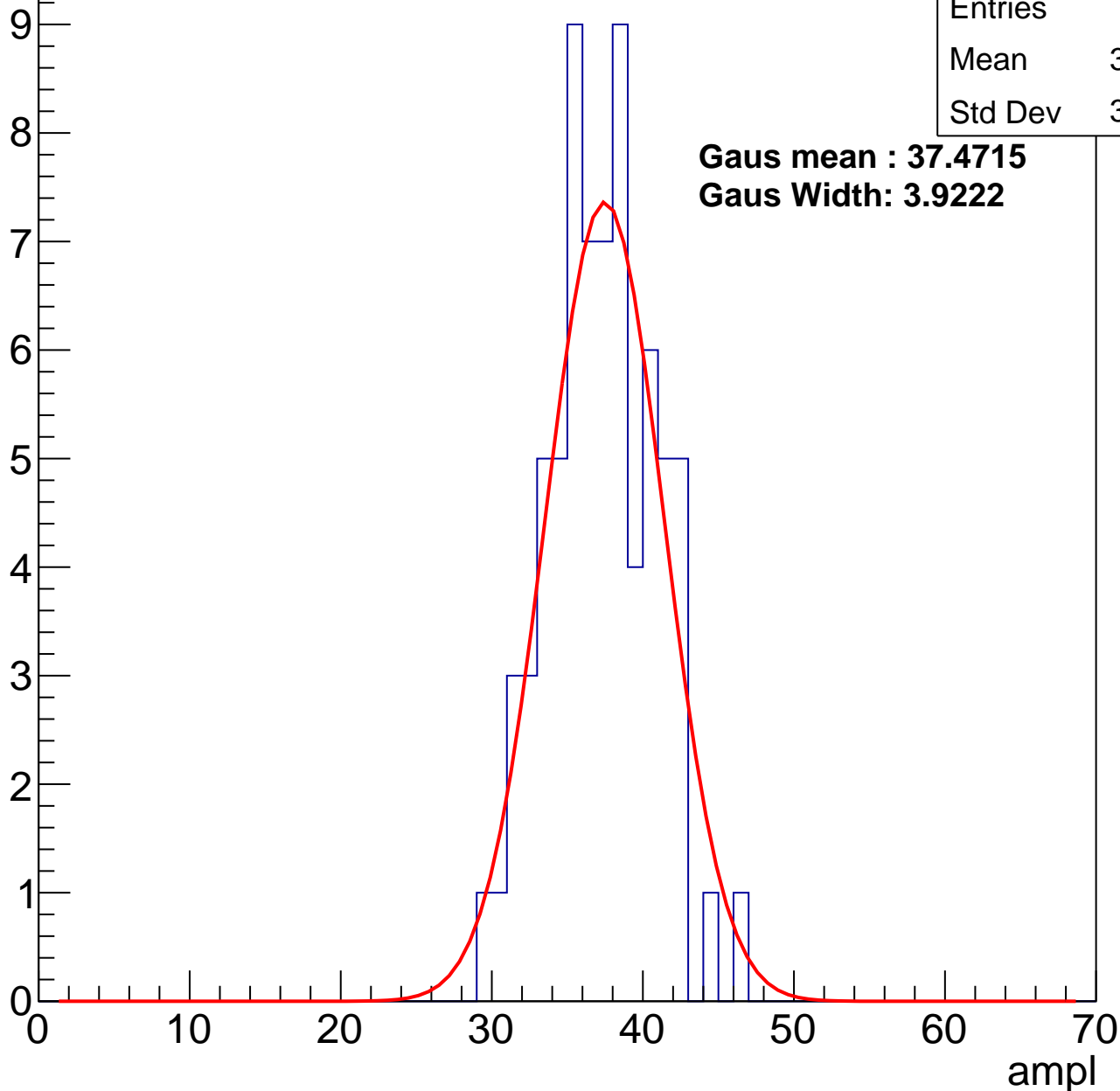
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.83
Std Dev	3.488

Gaus mean : 37.4715

Gaus Width: 3.9222



B0L001S, U14-ch100, adc2

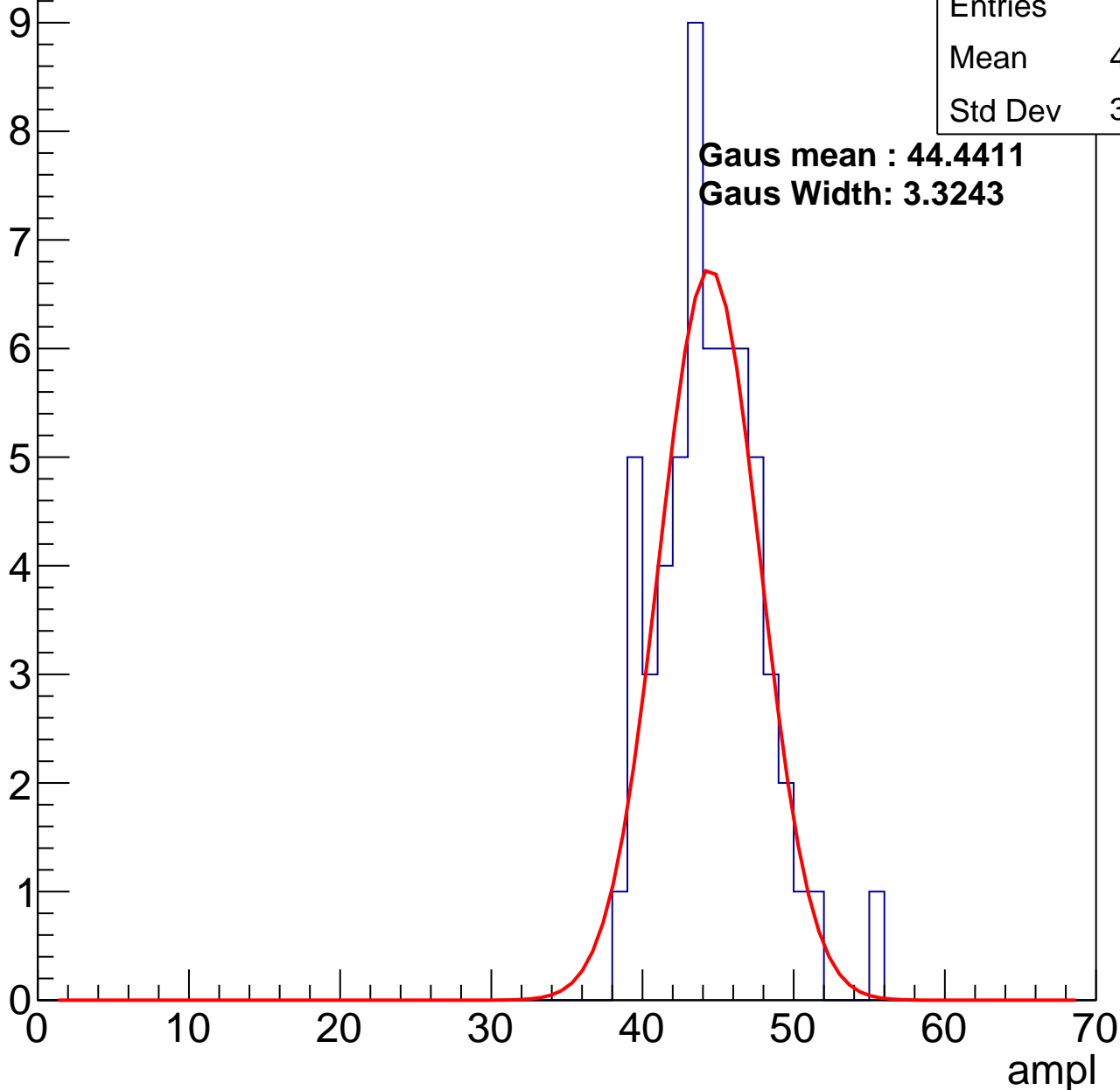
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	44.09
Std Dev	3.354

Gaus mean : 44.4411

Gaus Width: 3.3243

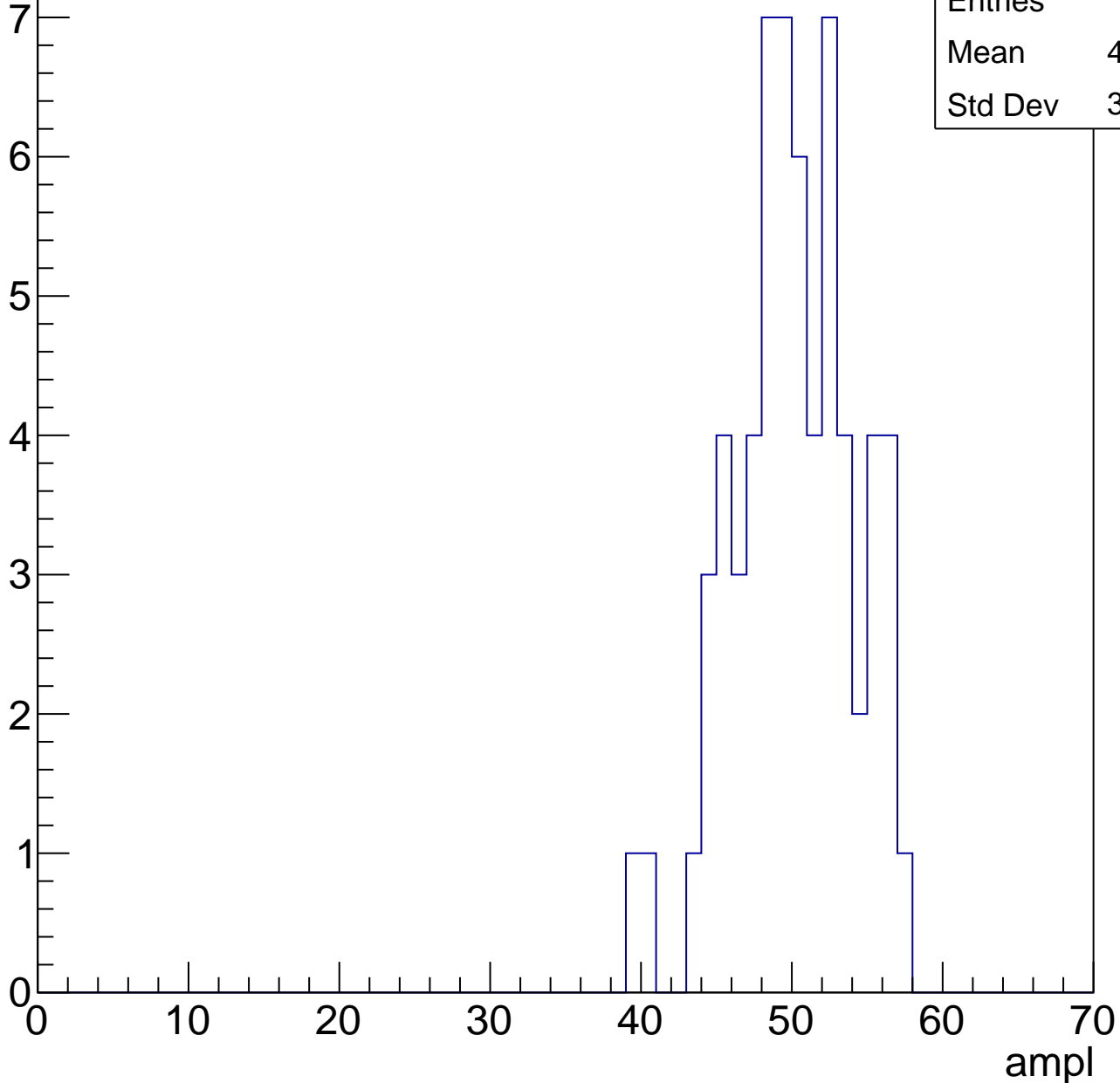


B0L001S, U14-ch100, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	49.65
Std Dev	3.949

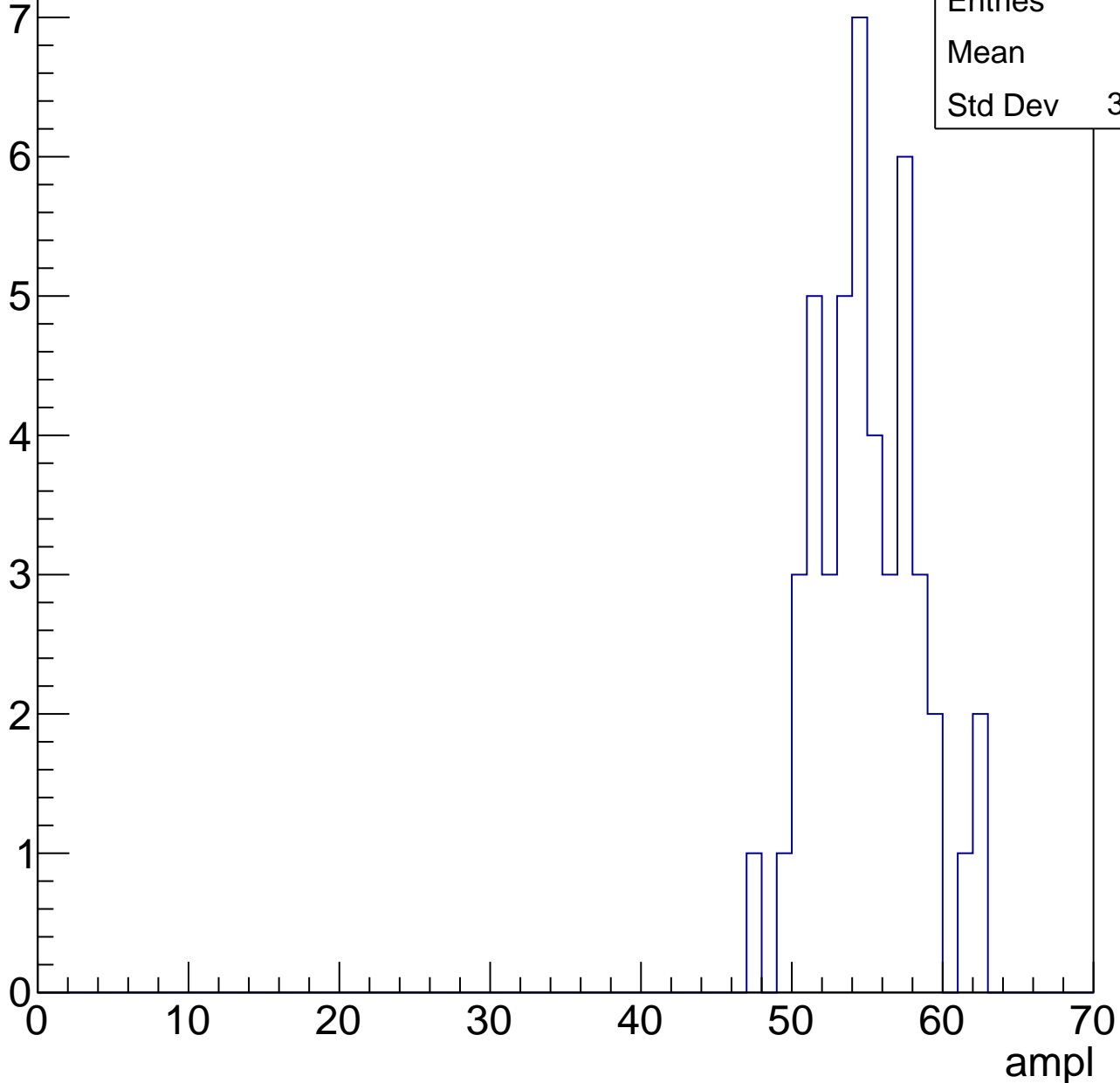


B0L001S, U14-ch100, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	46
Mean	54.5
Std Dev	3.354

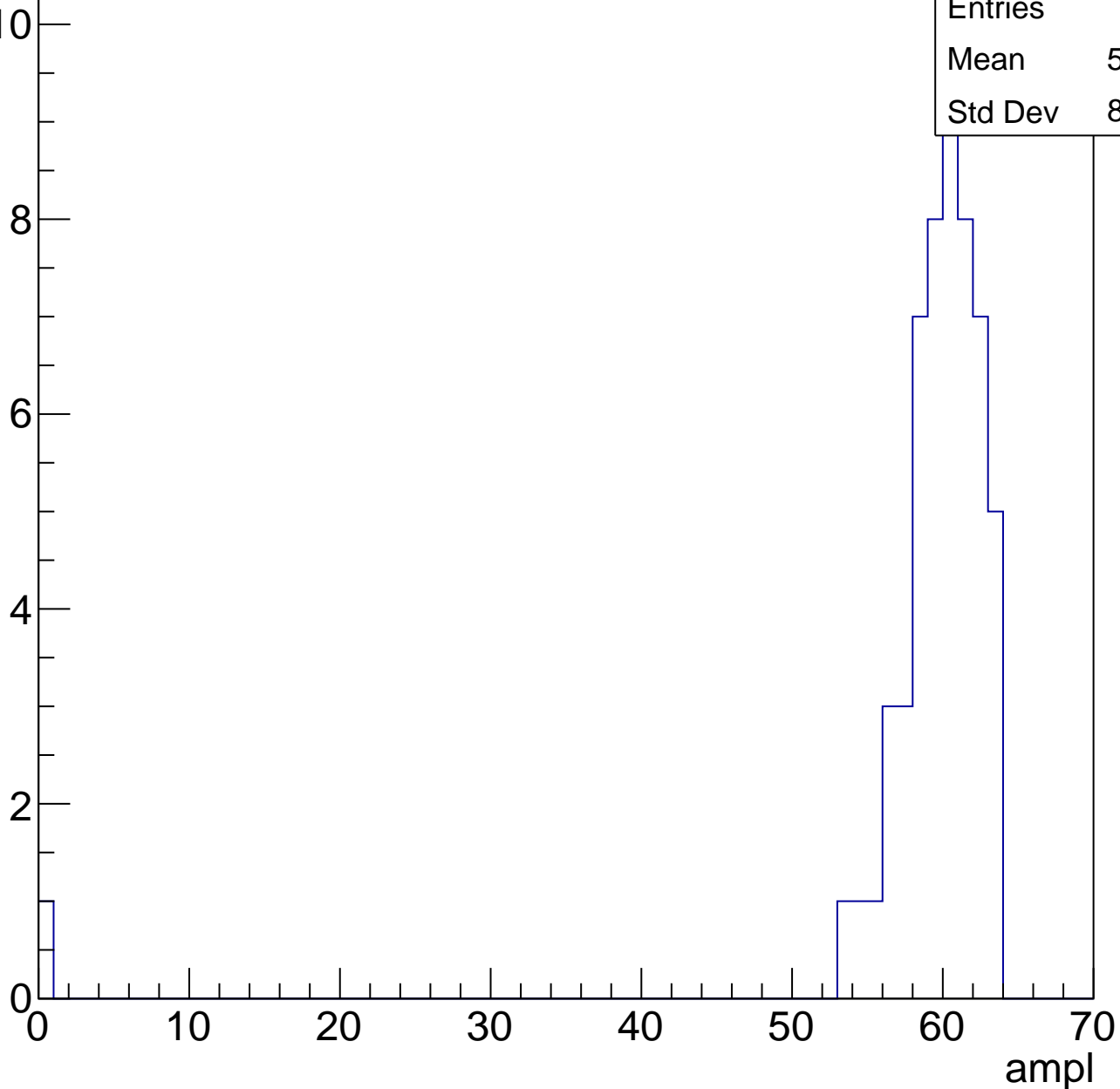


B0L001S, U14-ch100, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	58.47
Std Dev	8.283

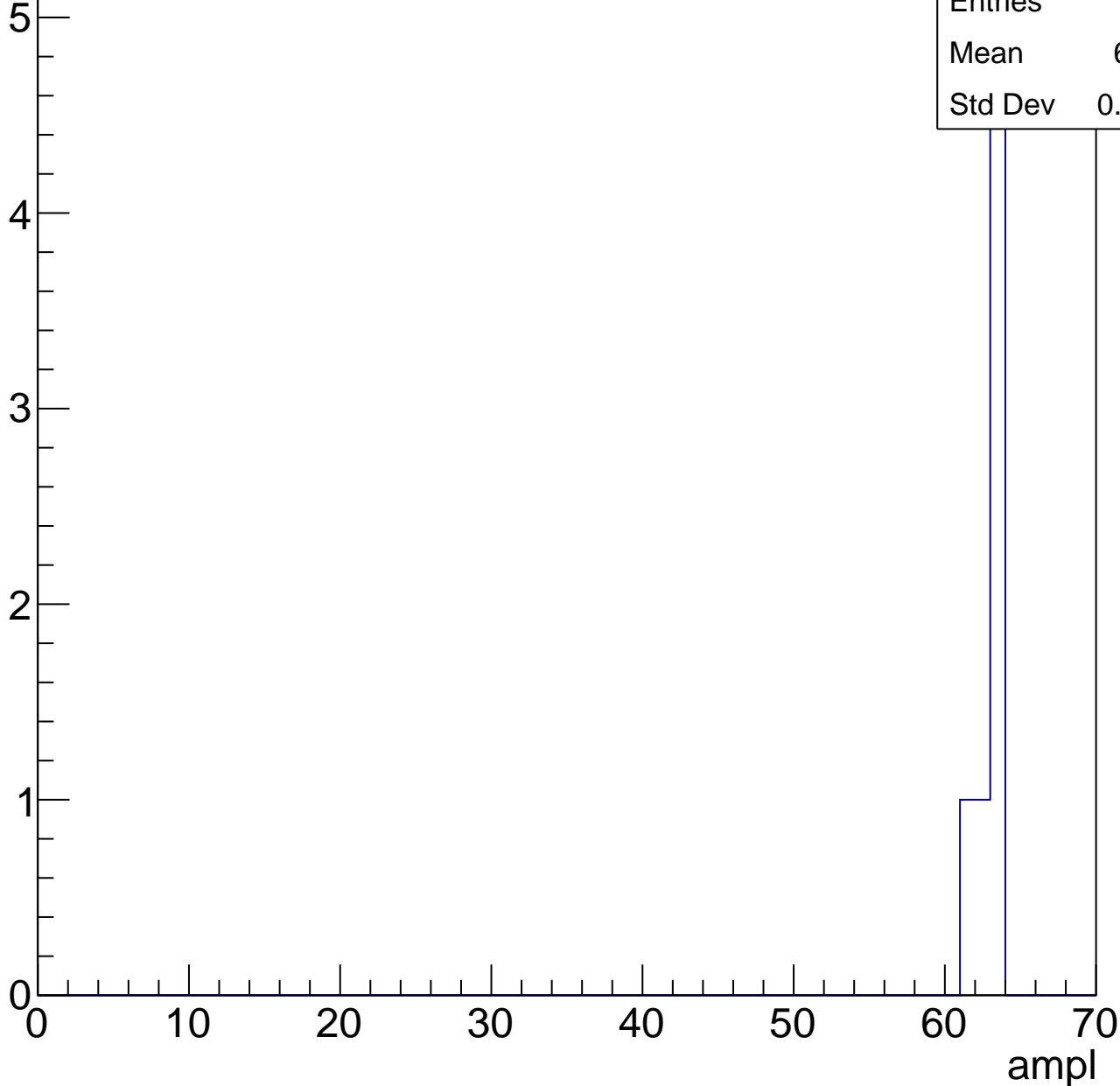


B0L001S, U14-ch100, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	7
Mean	62.57
Std Dev	0.7284



B0L001S, U14-ch100, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch101, adc0

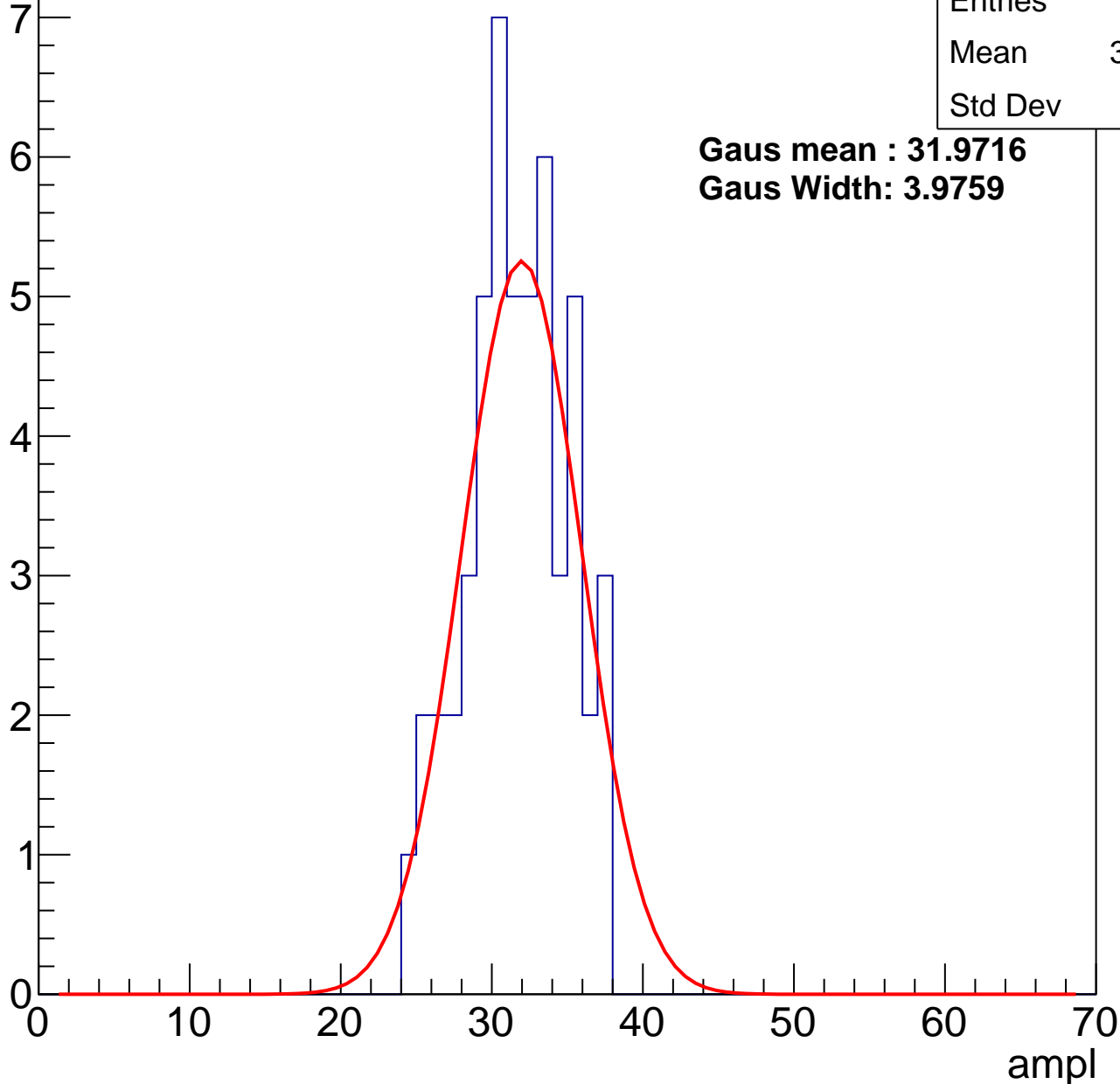
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	31.22
Std Dev	3.28

Gaus mean : 31.9716

Gaus Width: 3.9759



B0L001S, U14-ch101, adc1

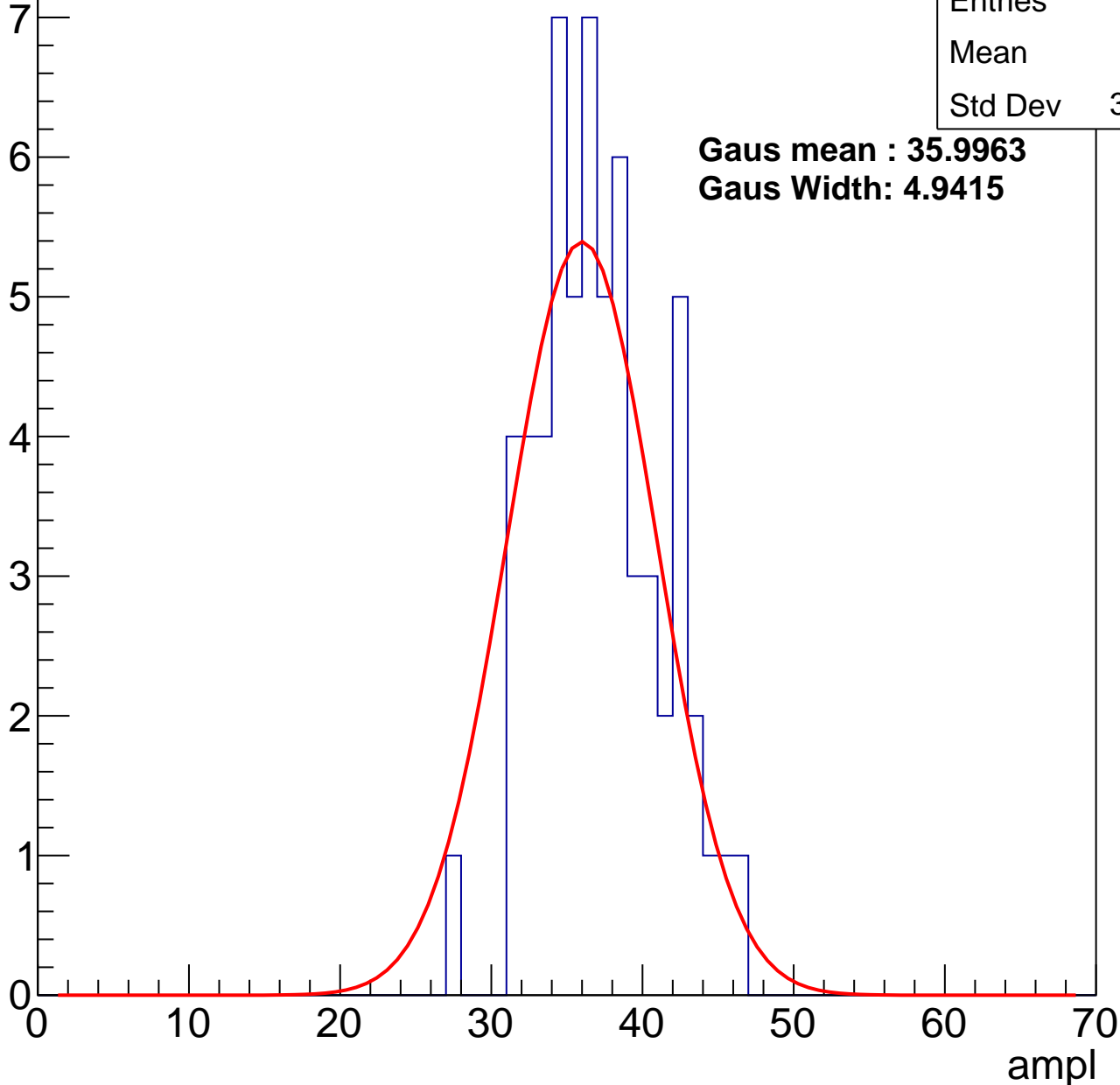
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	36.7
Std Dev	3.968

Gaus mean : 35.9963

Gaus Width: 4.9415



B0L001S, U14-ch101, adc2

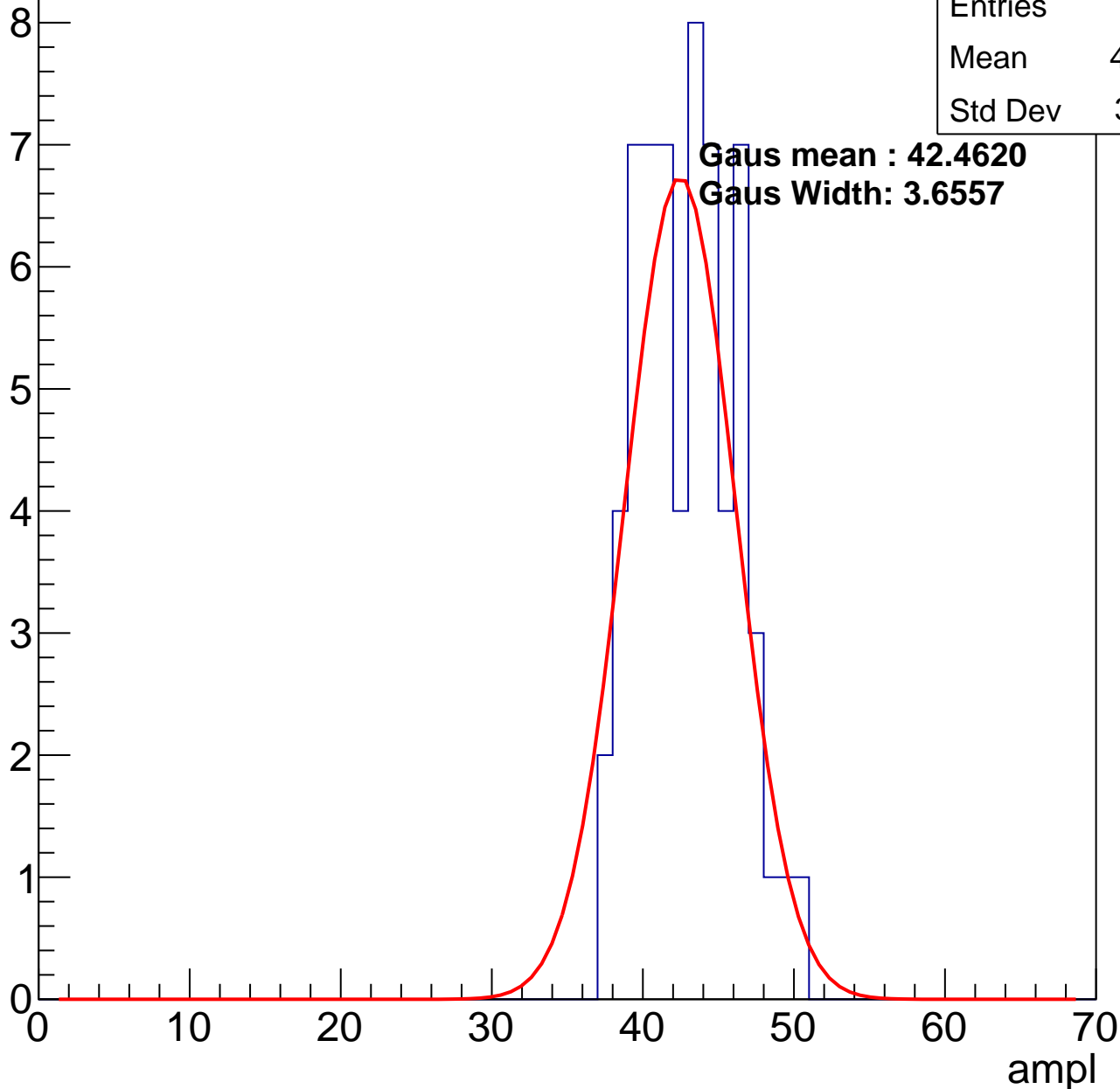
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	42.48
Std Dev	3.101

Gaus mean : 42.4620

Gaus Width: 3.6557

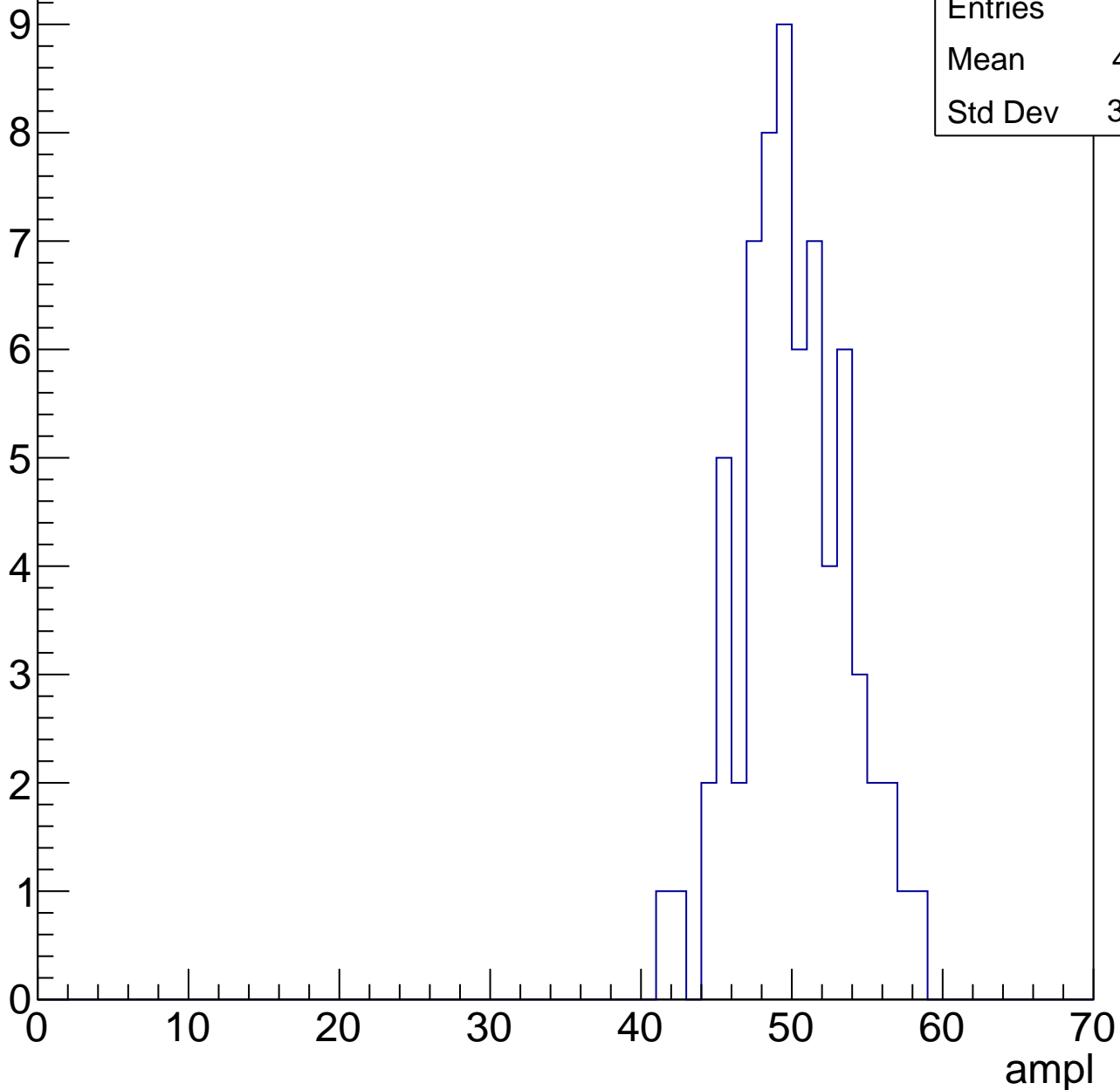


B0L001S, U14-ch101, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	49.61
Std Dev	3.515

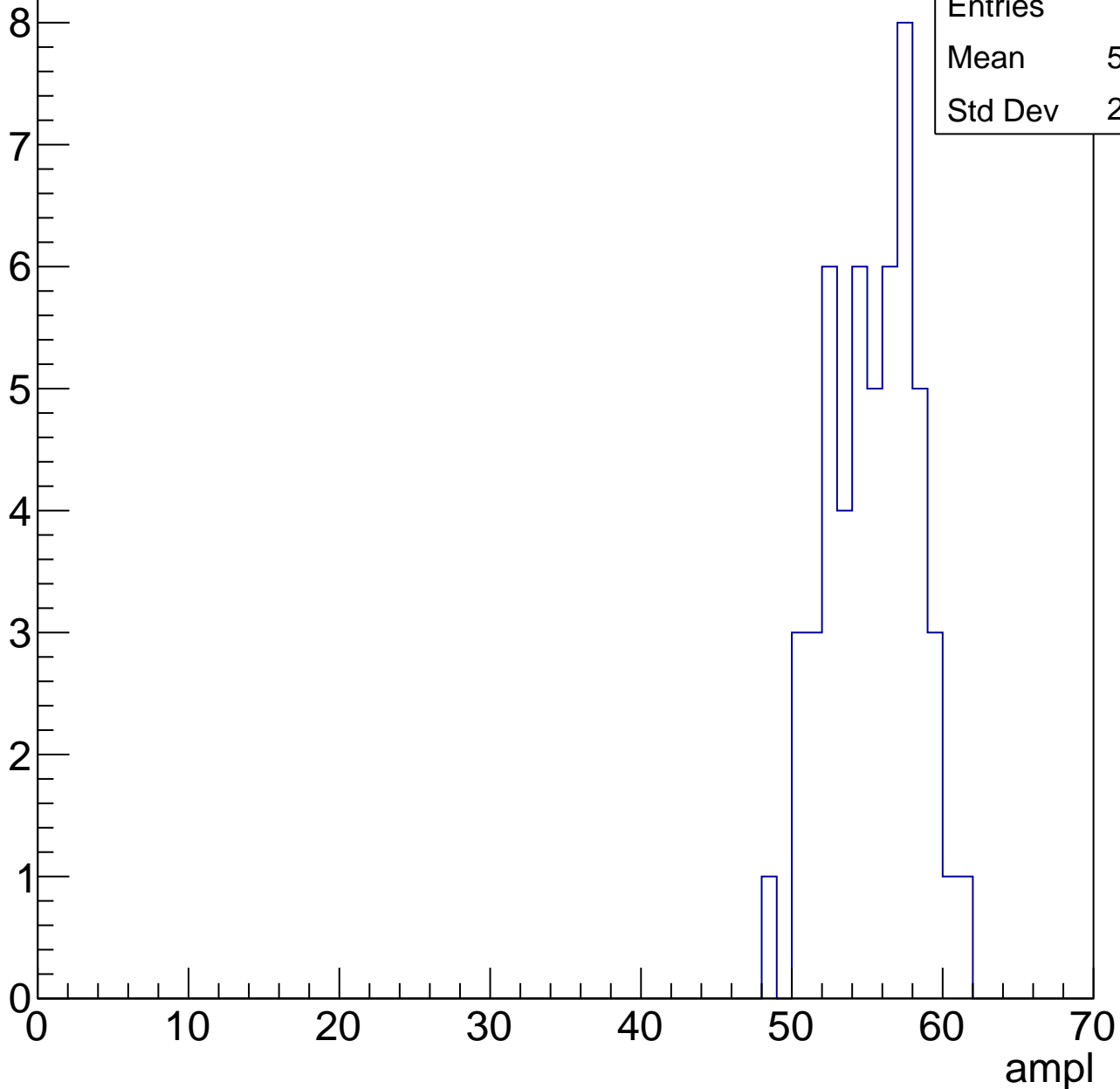


B0L001S, U14-ch101, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	54.88
Std Dev	2.907

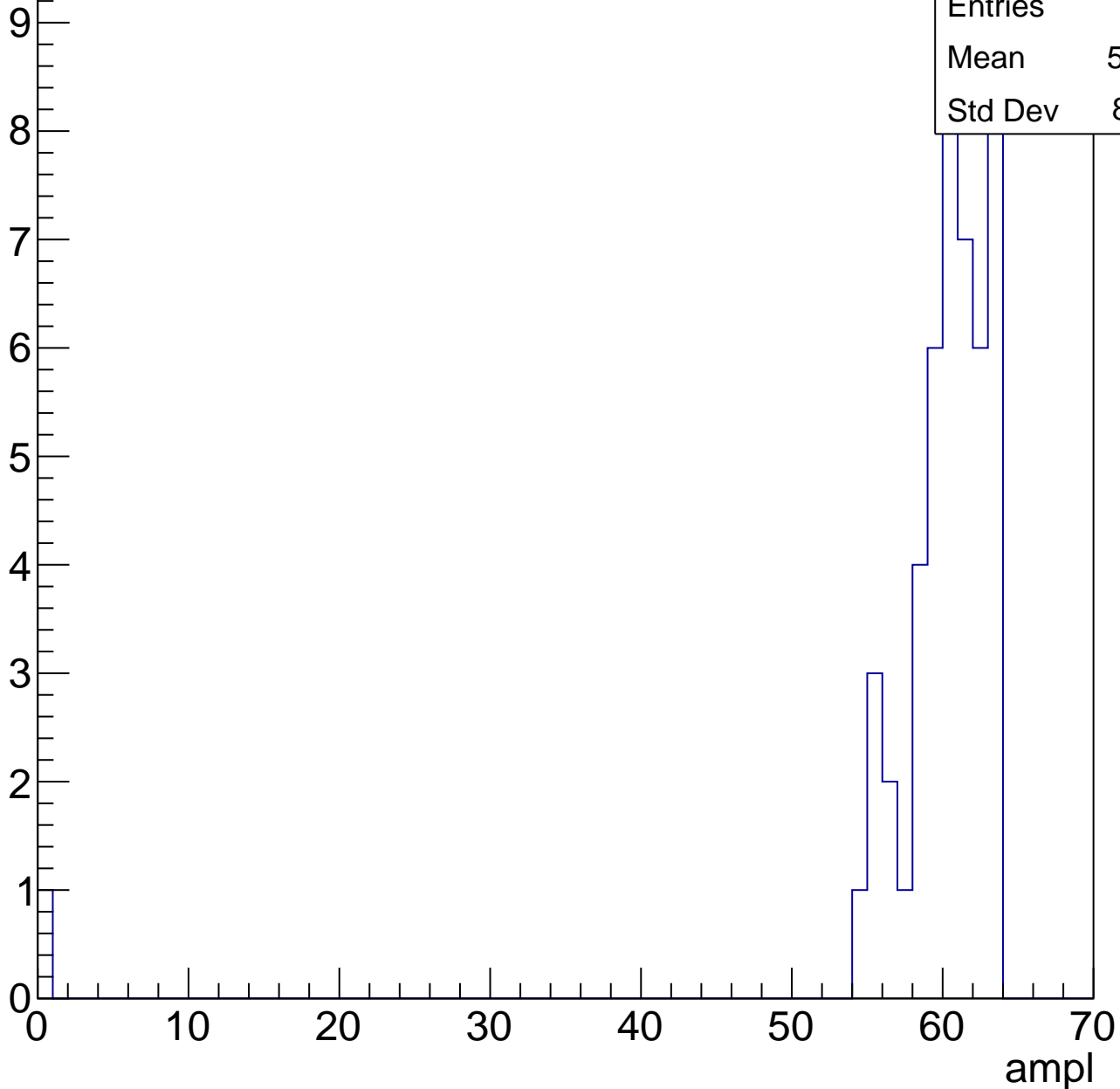


B0L001S, U14-ch101, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

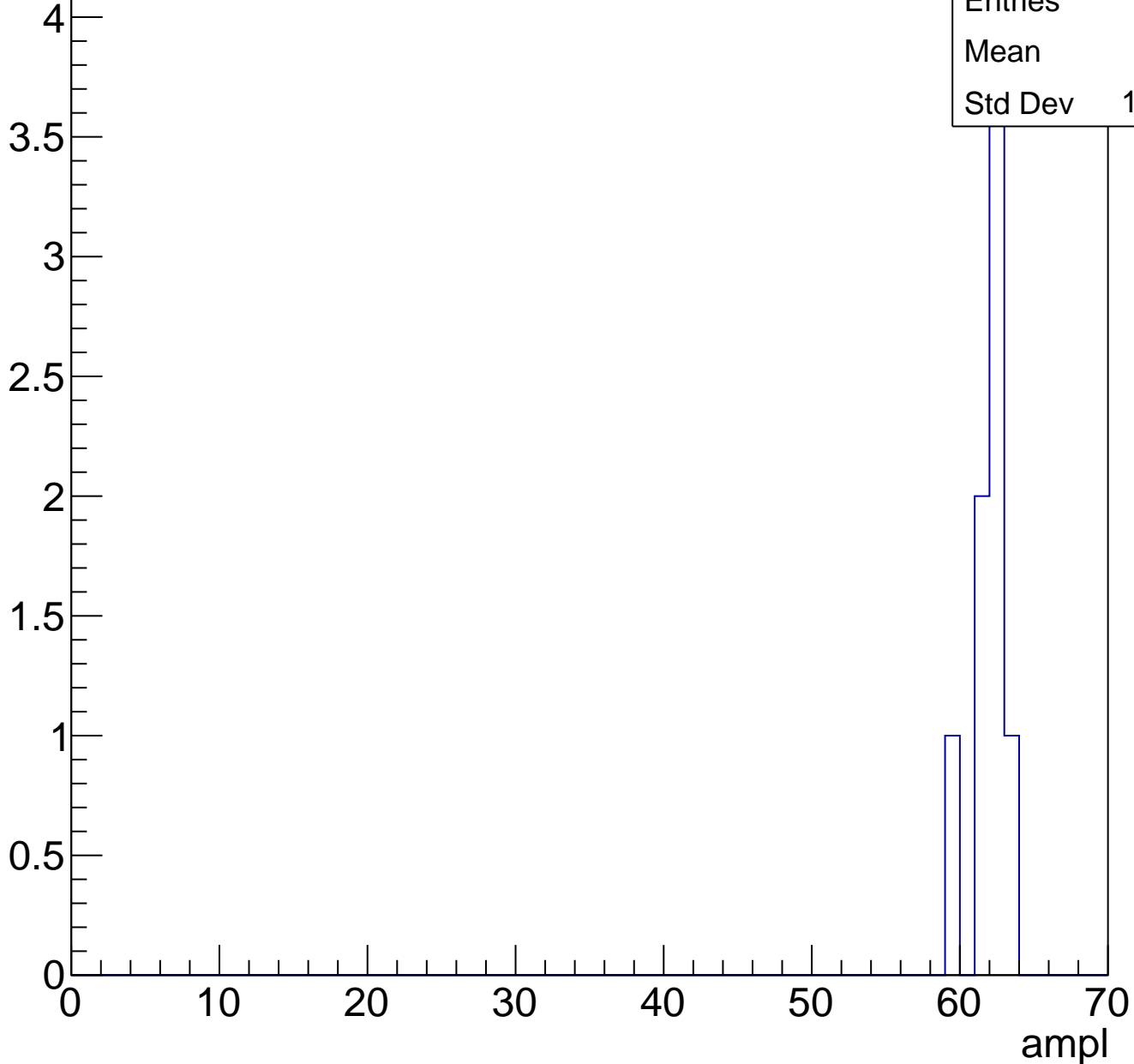
Entries	49
Mean	58.78
Std Dev	8.821



B0L001S, U14-ch101, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch101, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	23
Std Dev	0

B0L001S, U14-ch102, adc0

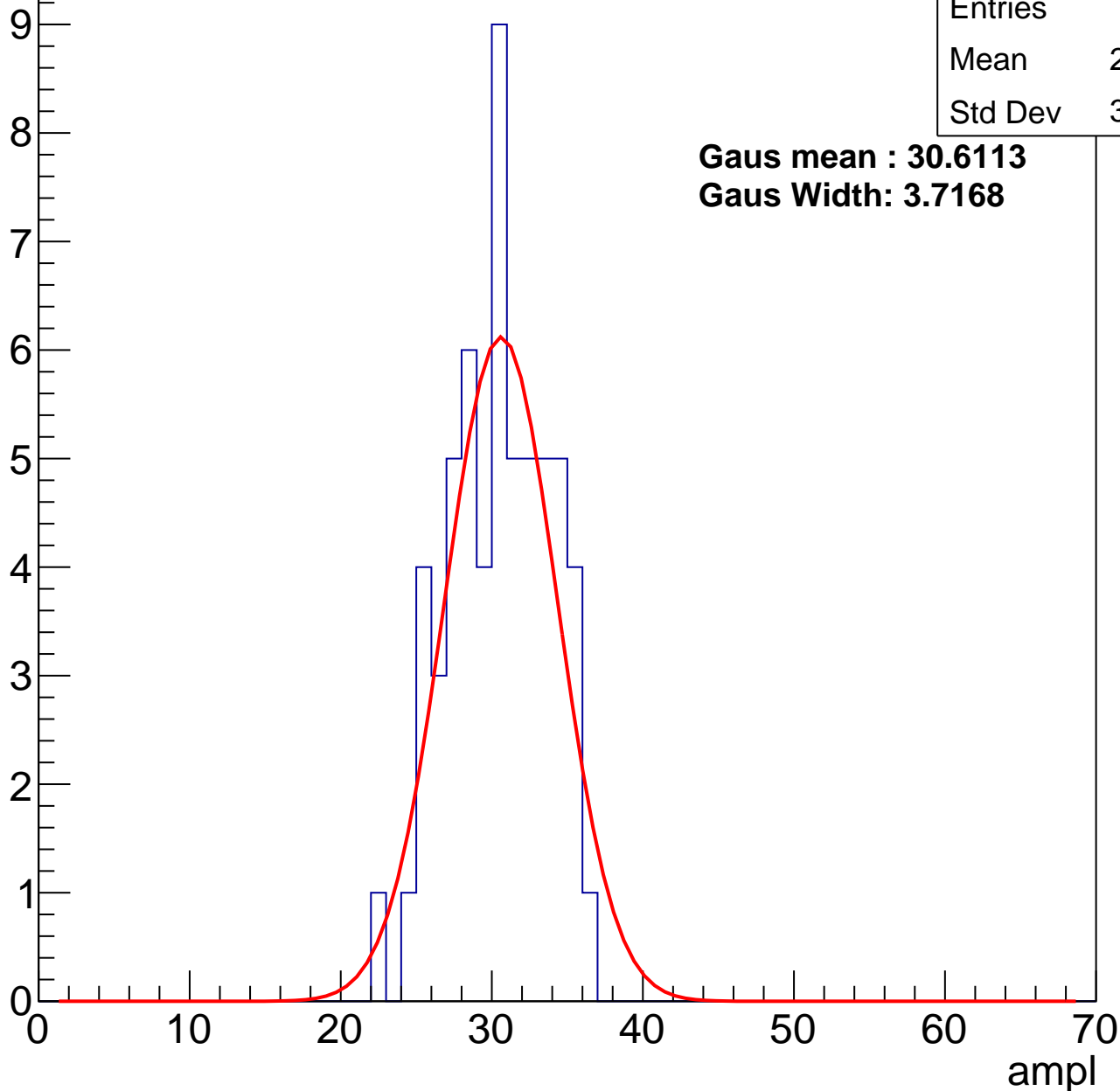
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	29.98
Std Dev	3.235

Gaus mean : 30.6113

Gaus Width: 3.7168



B0L001S, U14-ch102, adc1

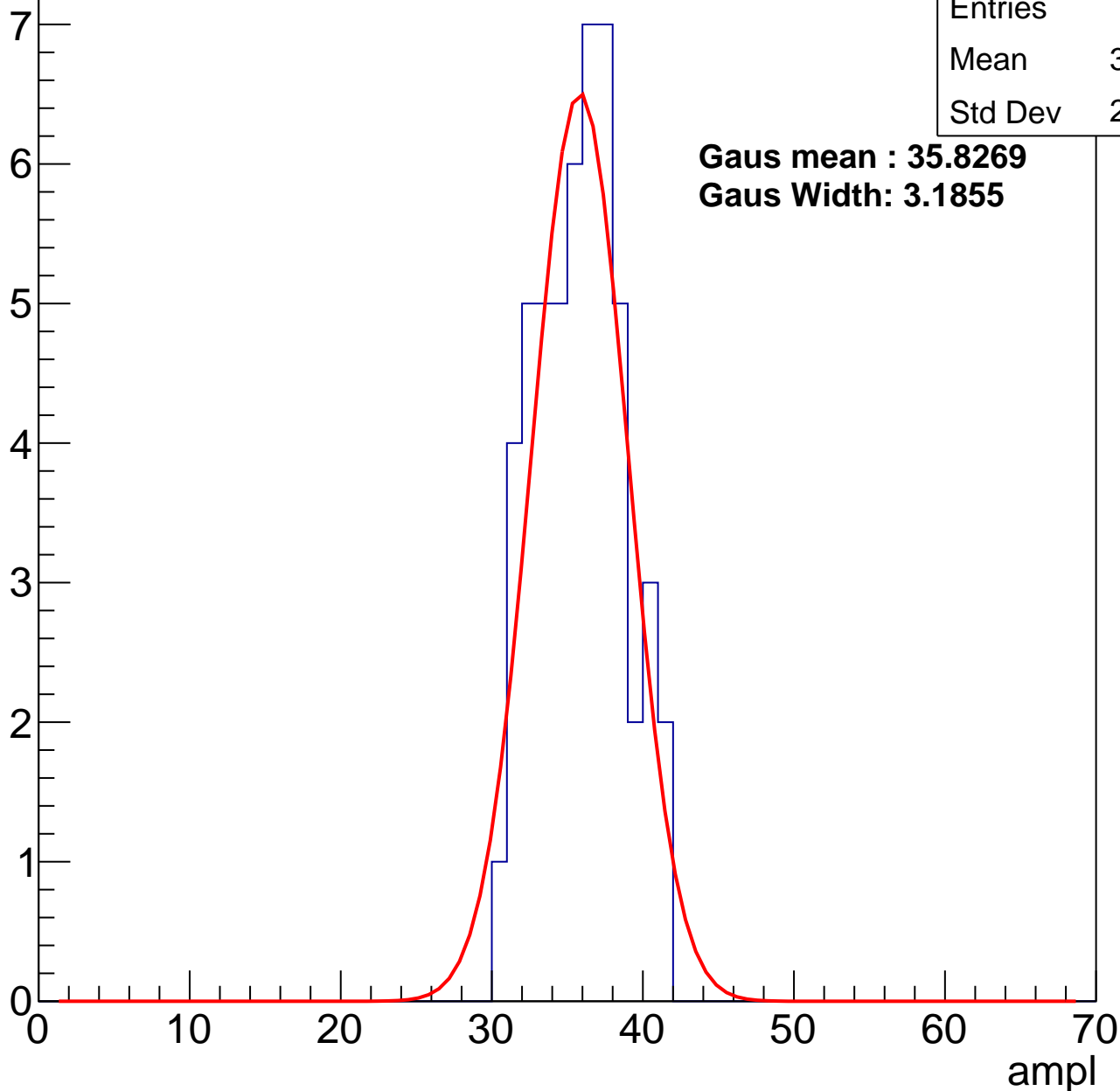
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	35.38
Std Dev	2.809

Gaus mean : 35.8269

Gaus Width: 3.1855



B0L001S, U14-ch102, adc2

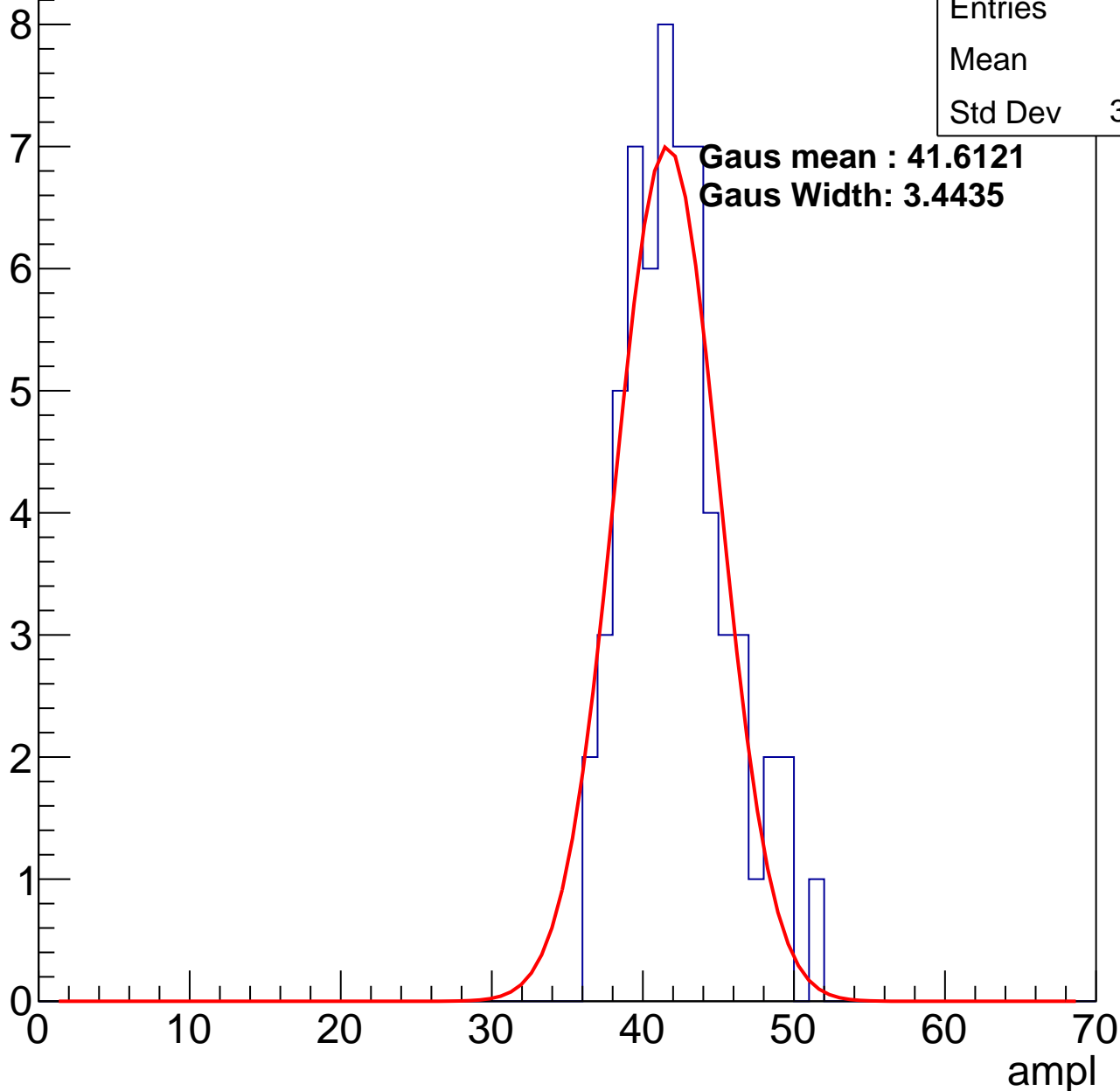
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	41.8
Std Dev	3.372

Gaus mean : 41.6121

Gaus Width: 3.4435

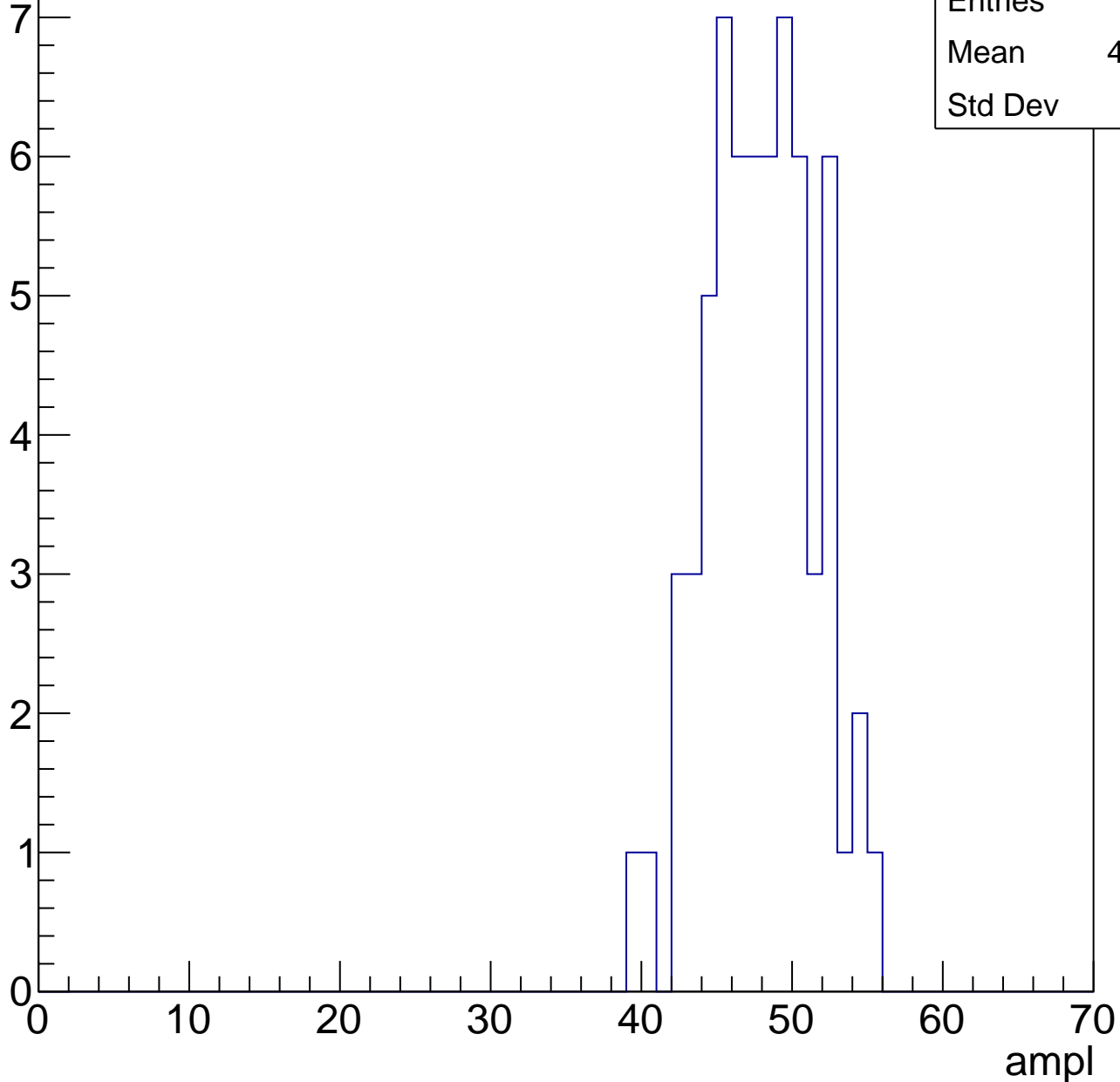


B0L001S, U14-ch102, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	47.48
Std Dev	3.5

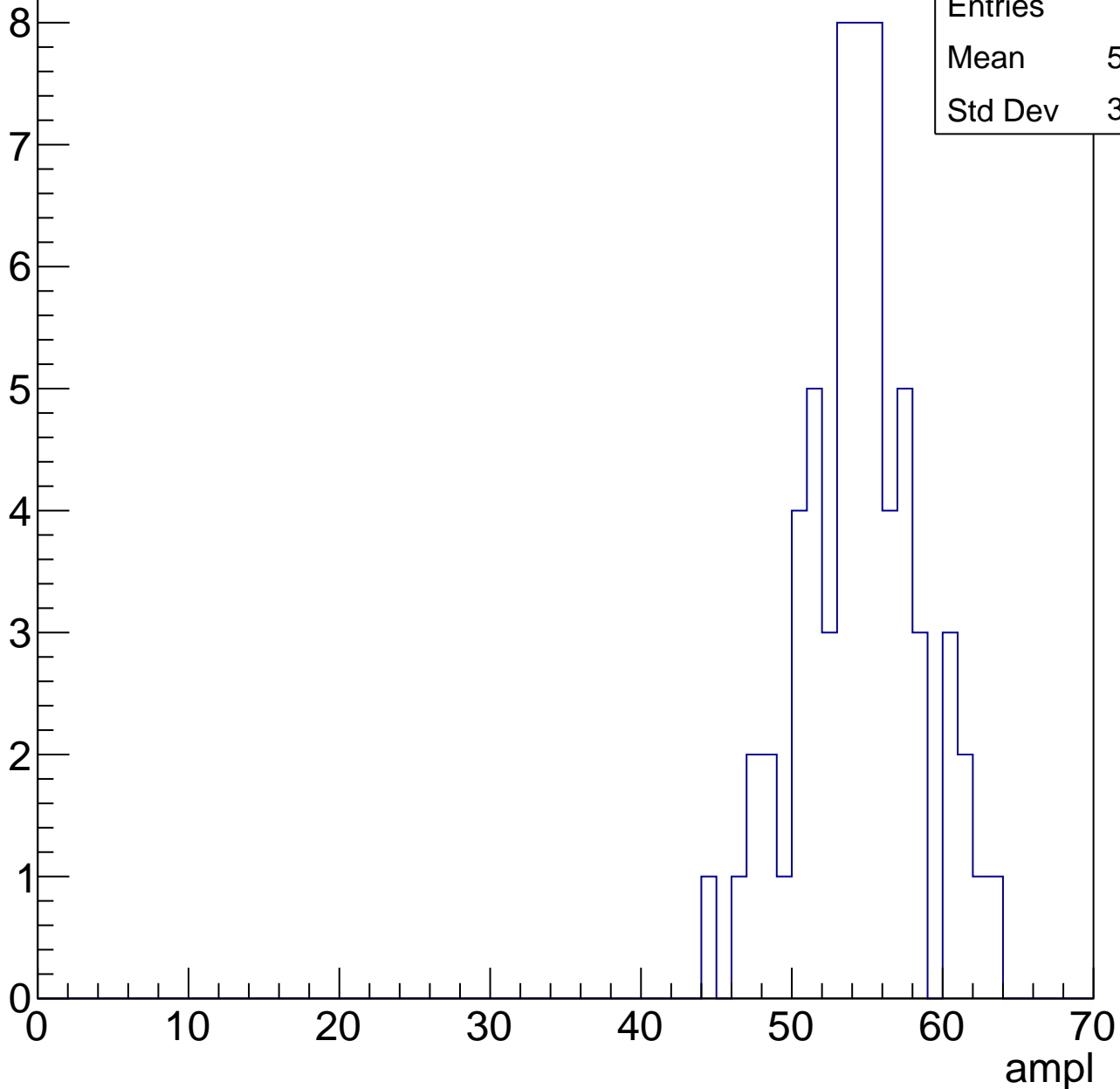


B0L001S, U14-ch102, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	53.97
Std Dev	3.927

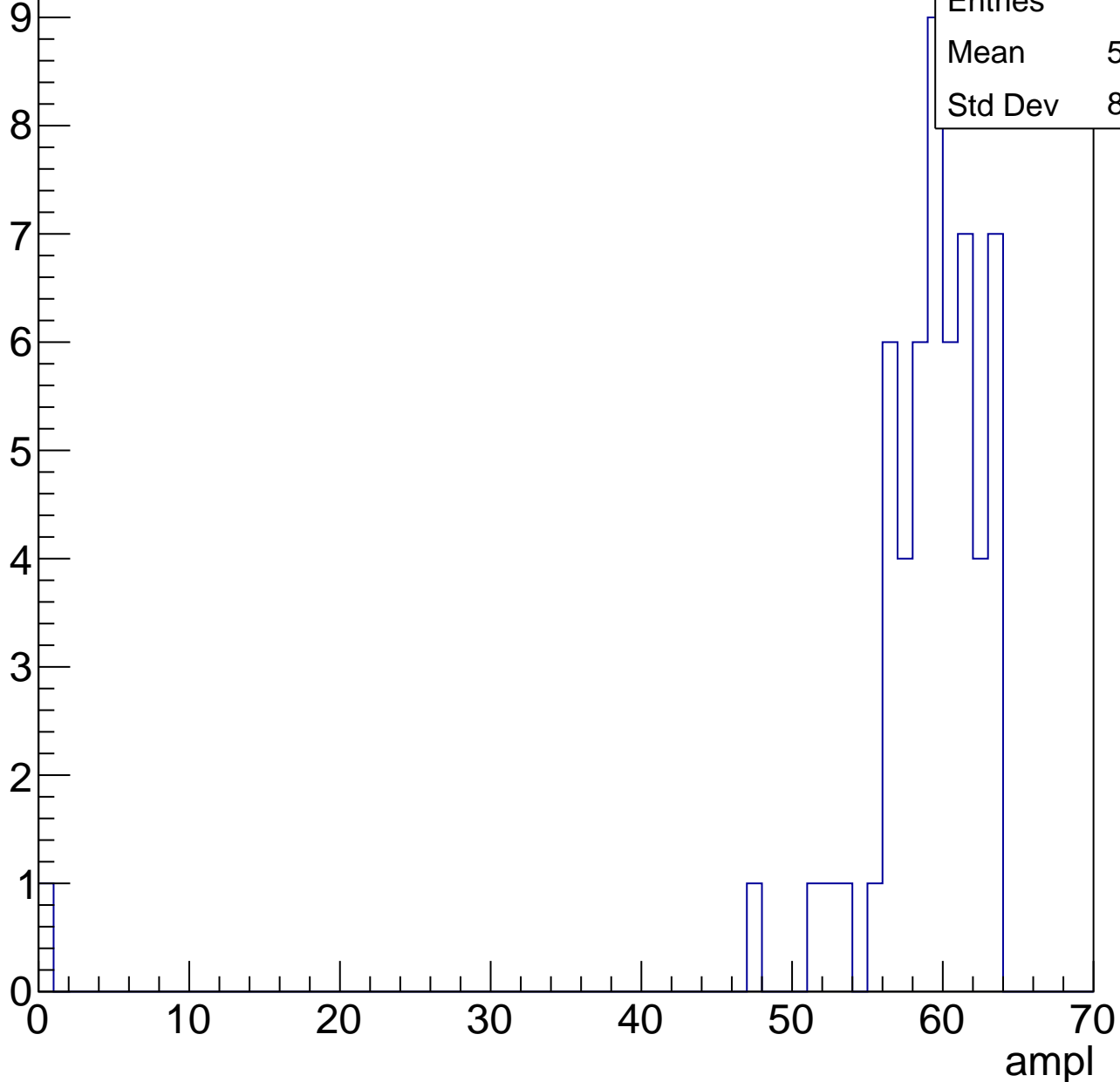


B0L001S, U14-ch102, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

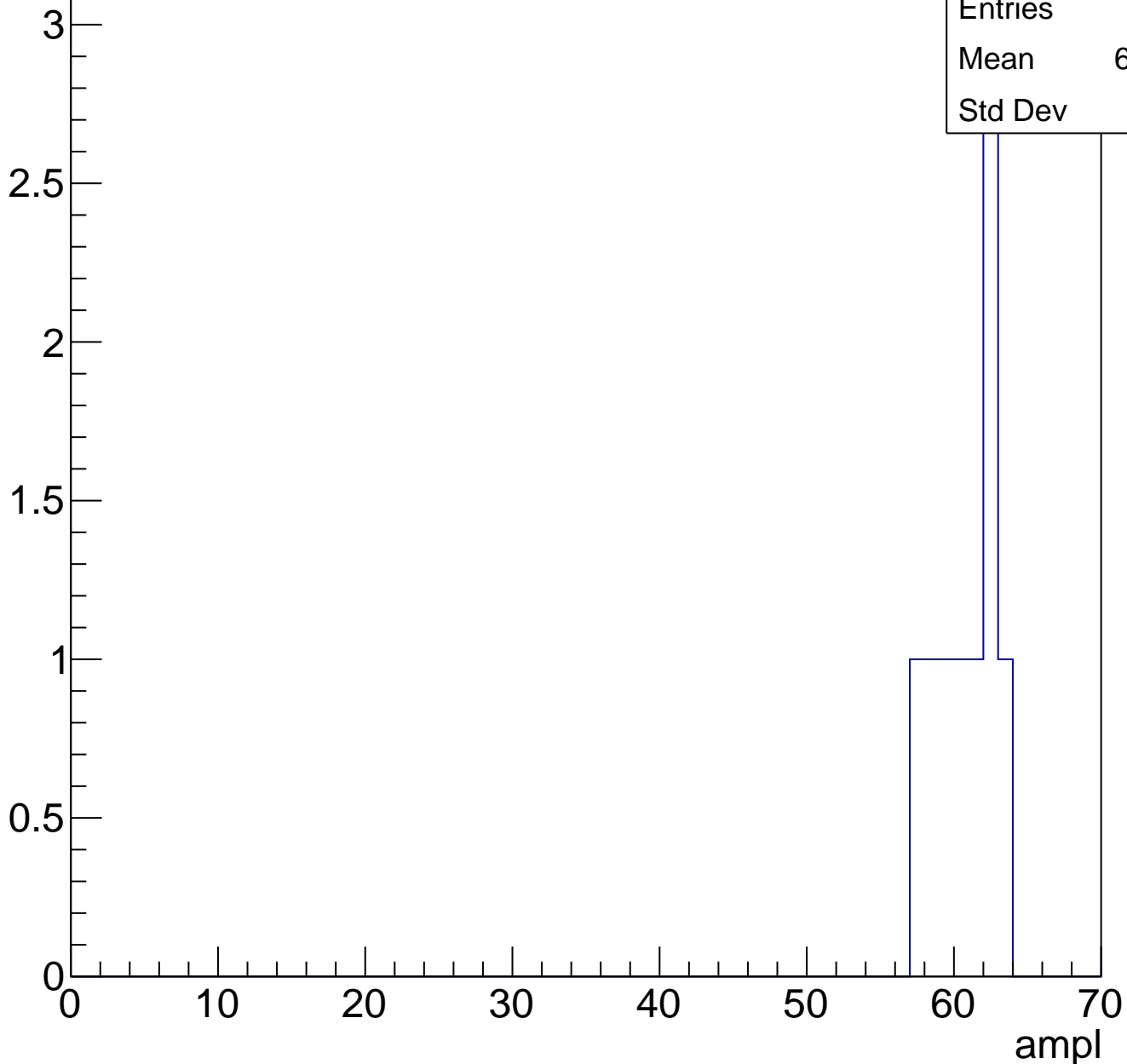
Entries	55
Mean	57.76
Std Dev	8.489



B0L001S, U14-ch102, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch102, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch103, adc0

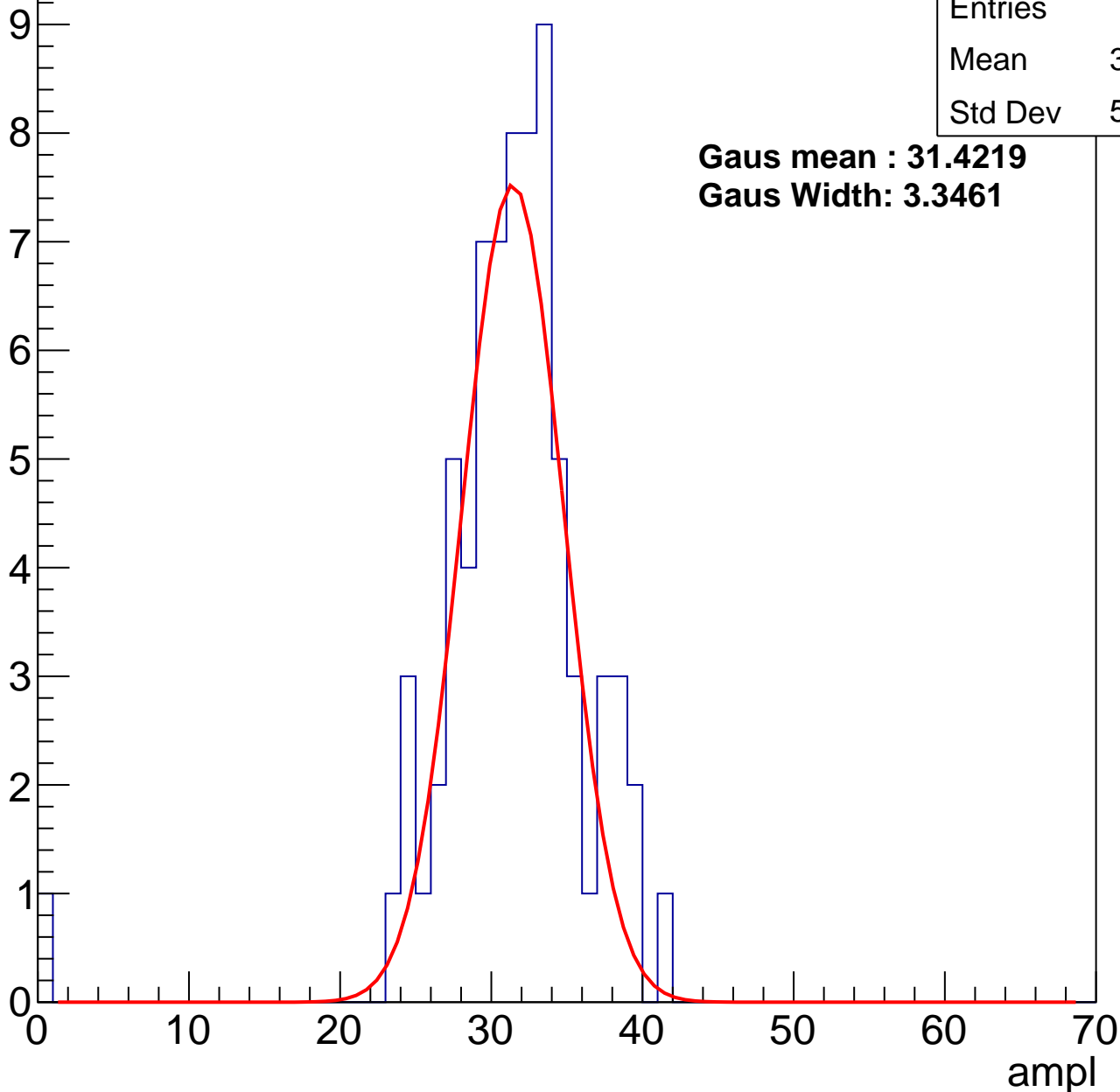
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	30.92
Std Dev	5.268

Gaus mean : 31.4219

Gaus Width: 3.3461



B0L001S, U14-ch103, adc1

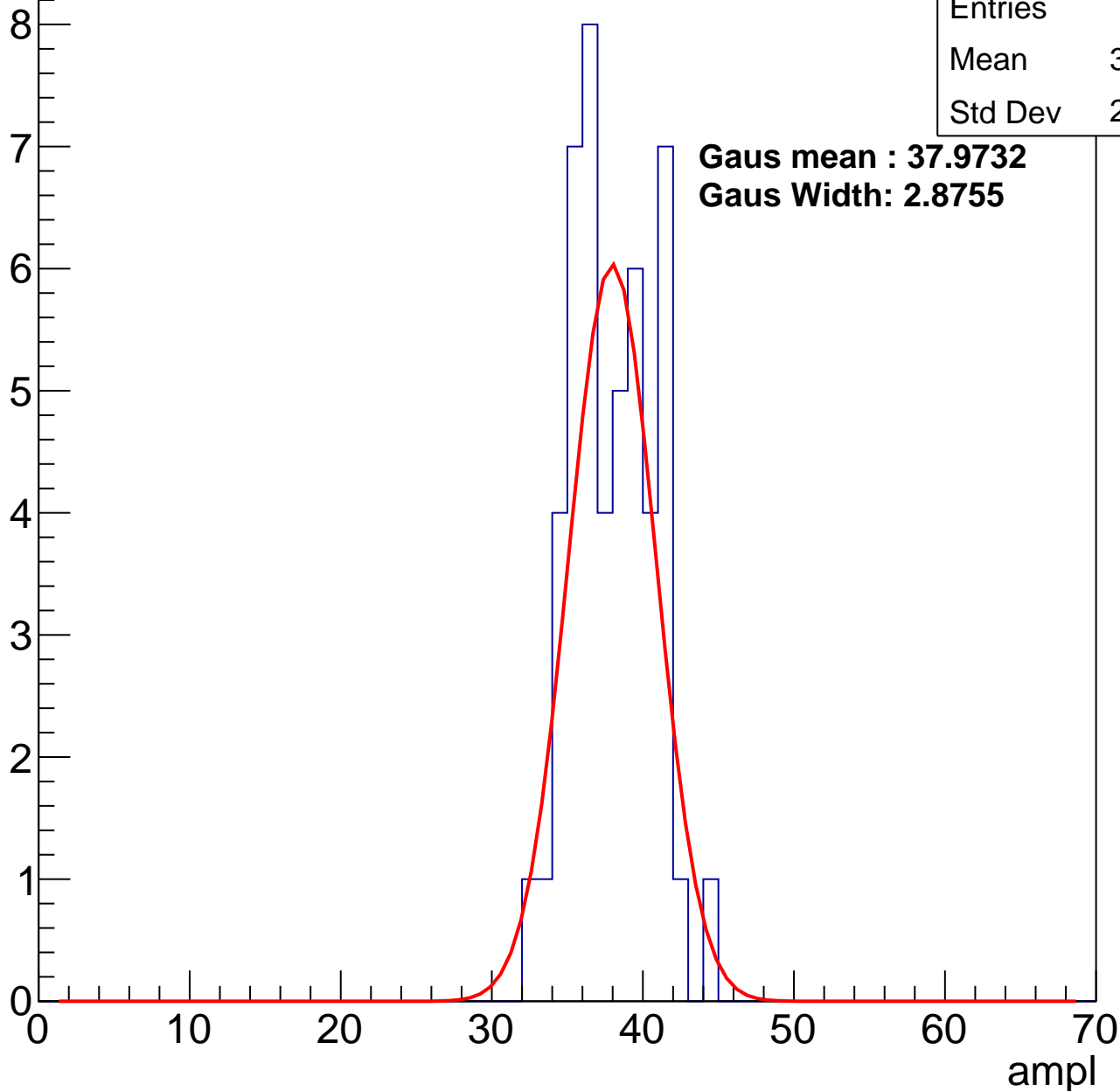
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	37.53
Std Dev	2.673

Gaus mean : 37.9732

Gaus Width: 2.8755



B0L001S, U14-ch103, adc2

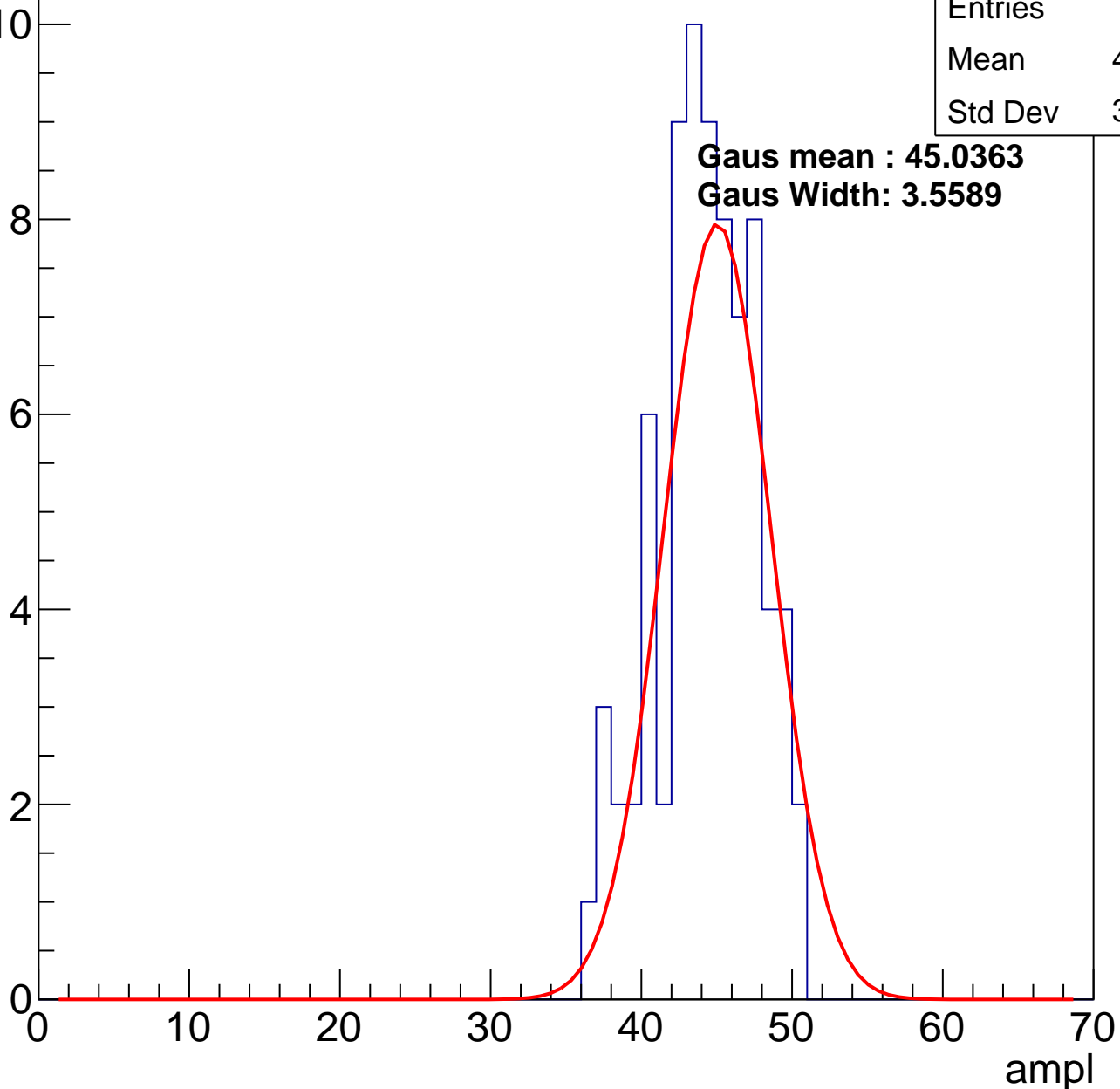
calib_packv5_042523_0143.root, FC#9, port A1

Entries	77
Mean	43.81
Std Dev	3.291

Gaus mean : 45.0363

Gaus Width: 3.5589

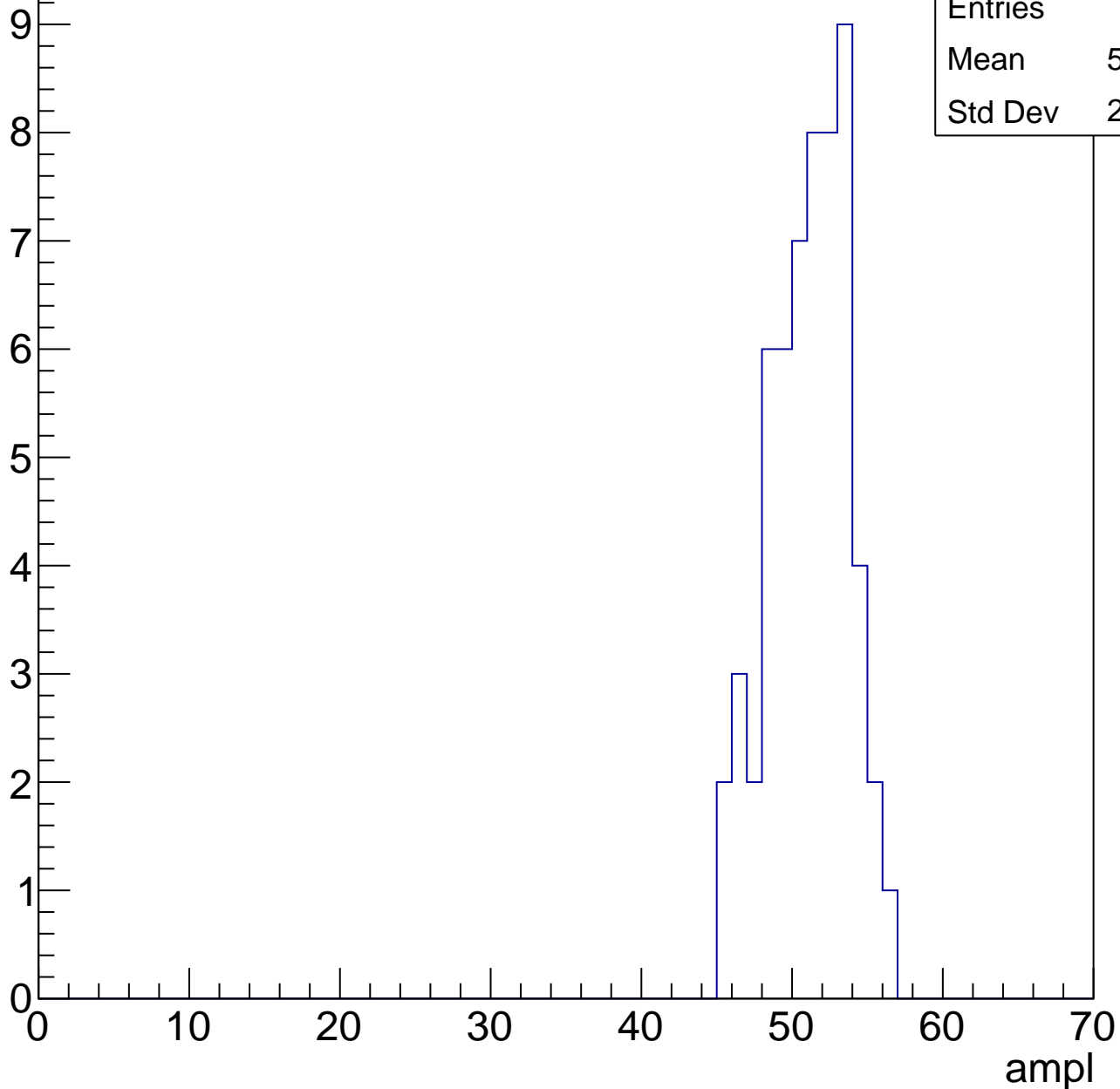
Entry



B0L001S, U14-ch103, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

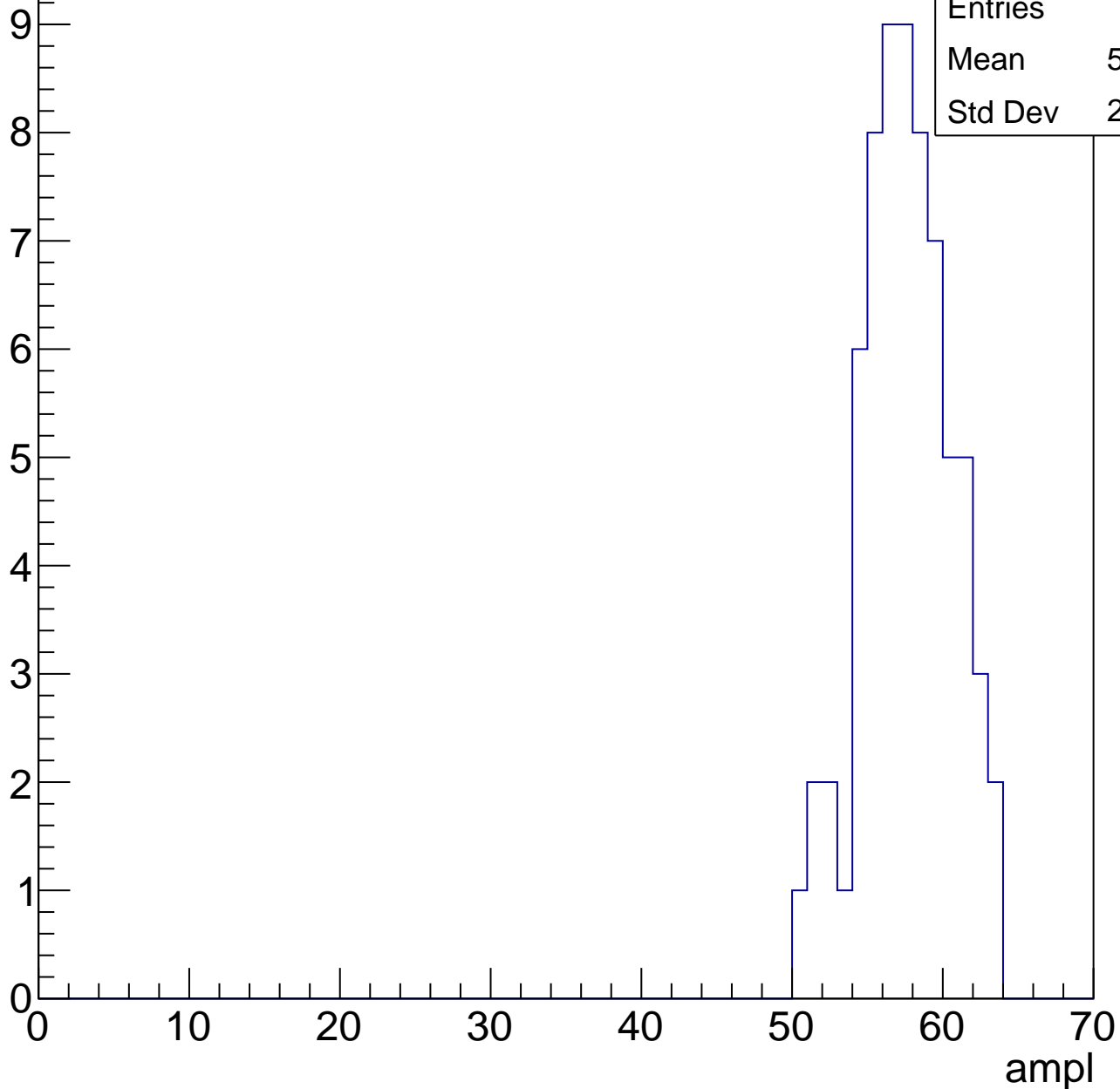


Entries	58
Mean	50.64
Std Dev	2.604

B0L001S, U14-ch103, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

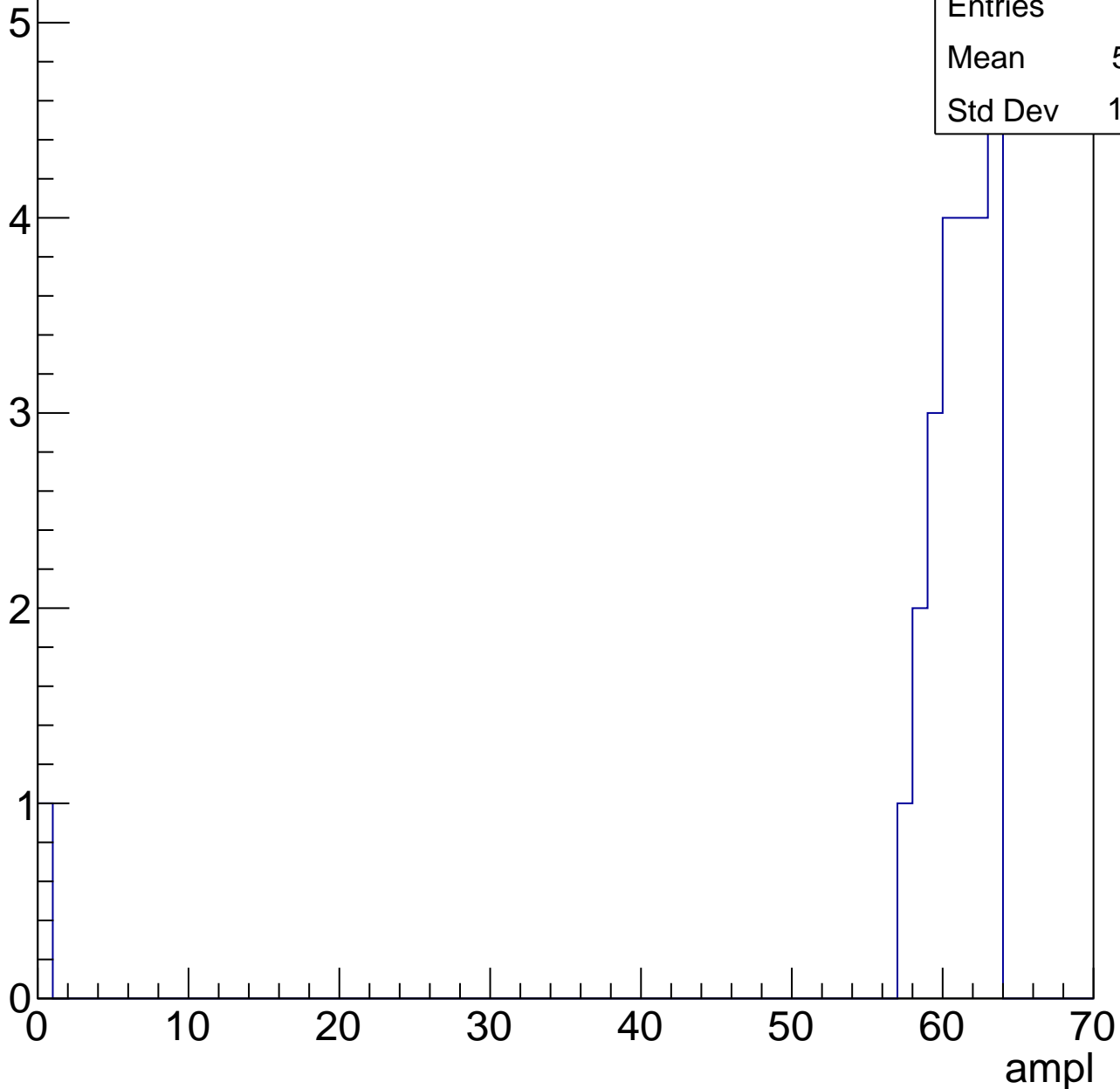


B0L001S, U14-ch103, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

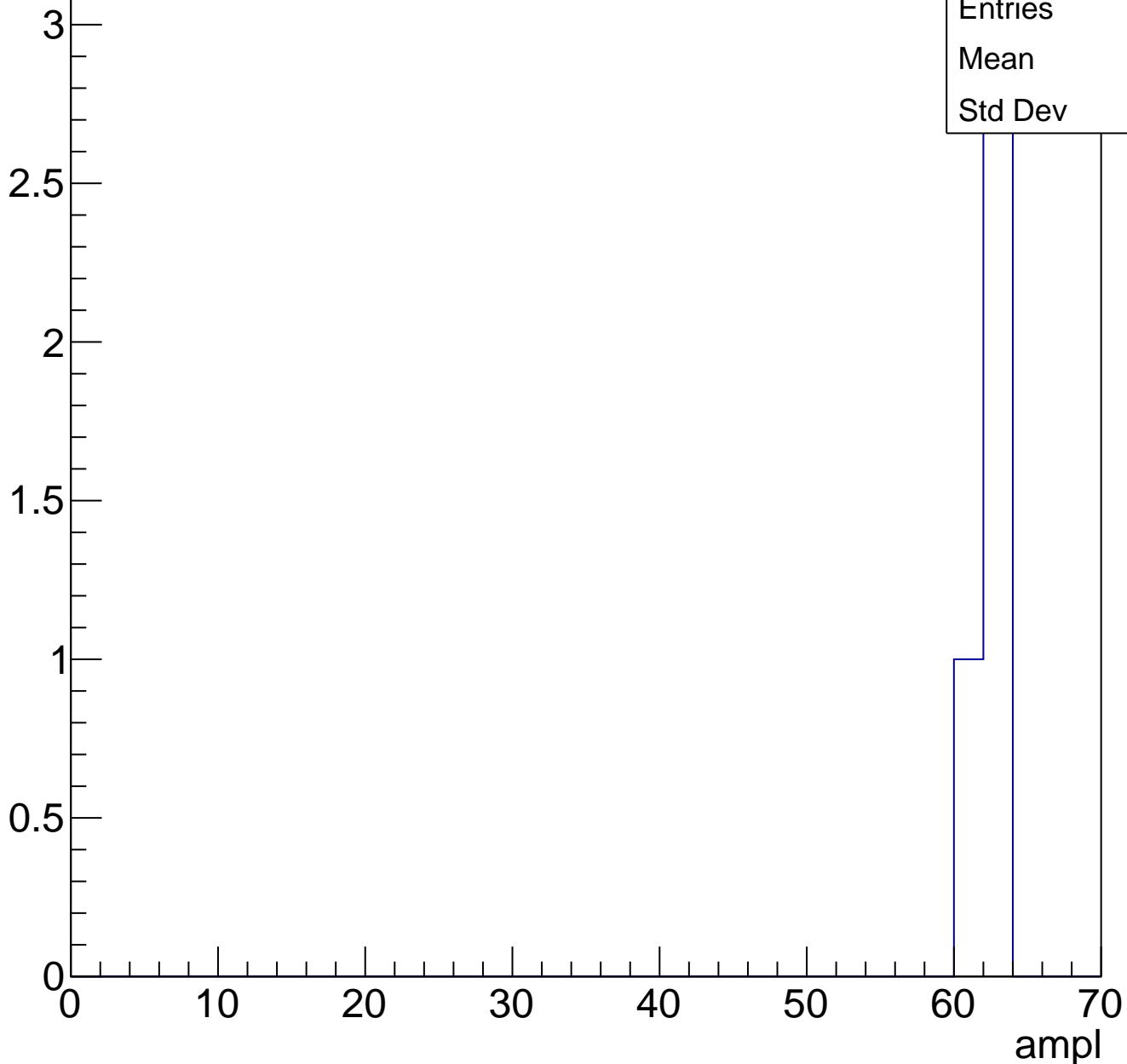
Entries	24
Mean	58.21
Std Dev	12.26



B0L001S, U14-ch103, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch103, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch104, adc0

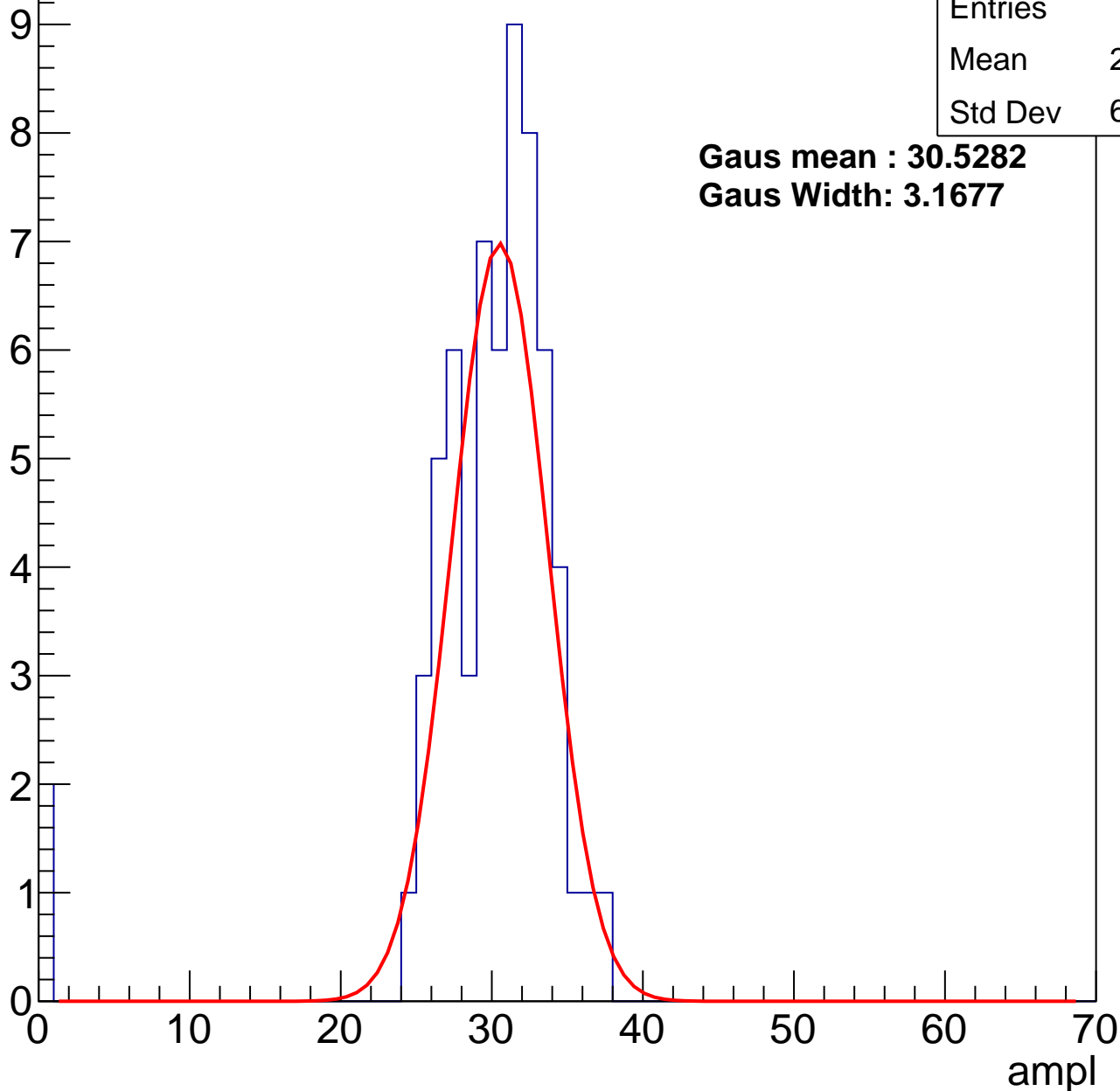
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.13
Std Dev	6.022

Gaus mean : 30.5282

Gaus Width: 3.1677



B0L001S, U14-ch104, adc1

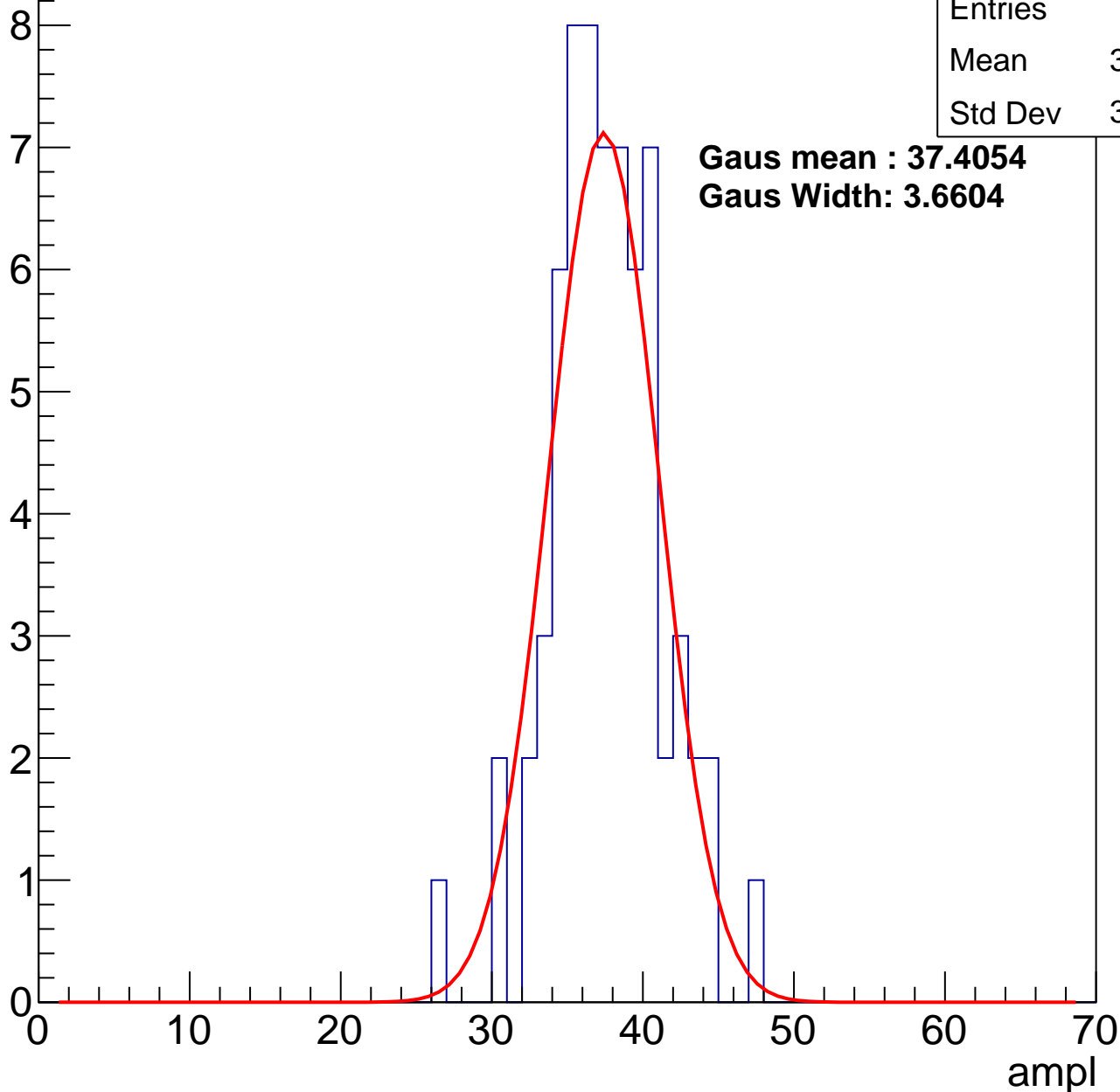
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.15
Std Dev	3.625

Gaus mean : 37.4054

Gaus Width: 3.6604



B0L001S, U14-ch104, adc2

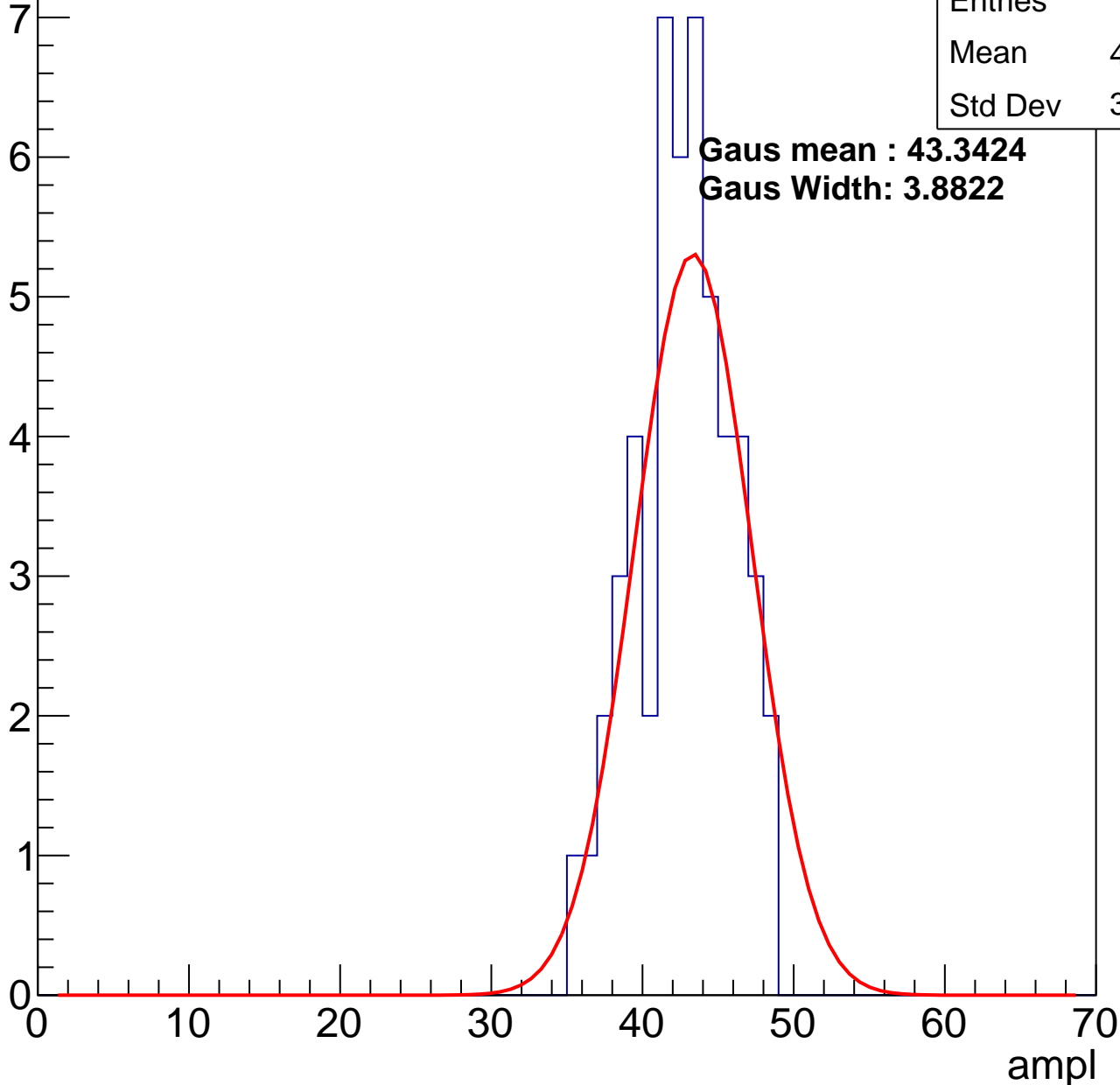
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	42.27
Std Dev	3.144

Gaus mean : 43.3424

Gaus Width: 3.8822

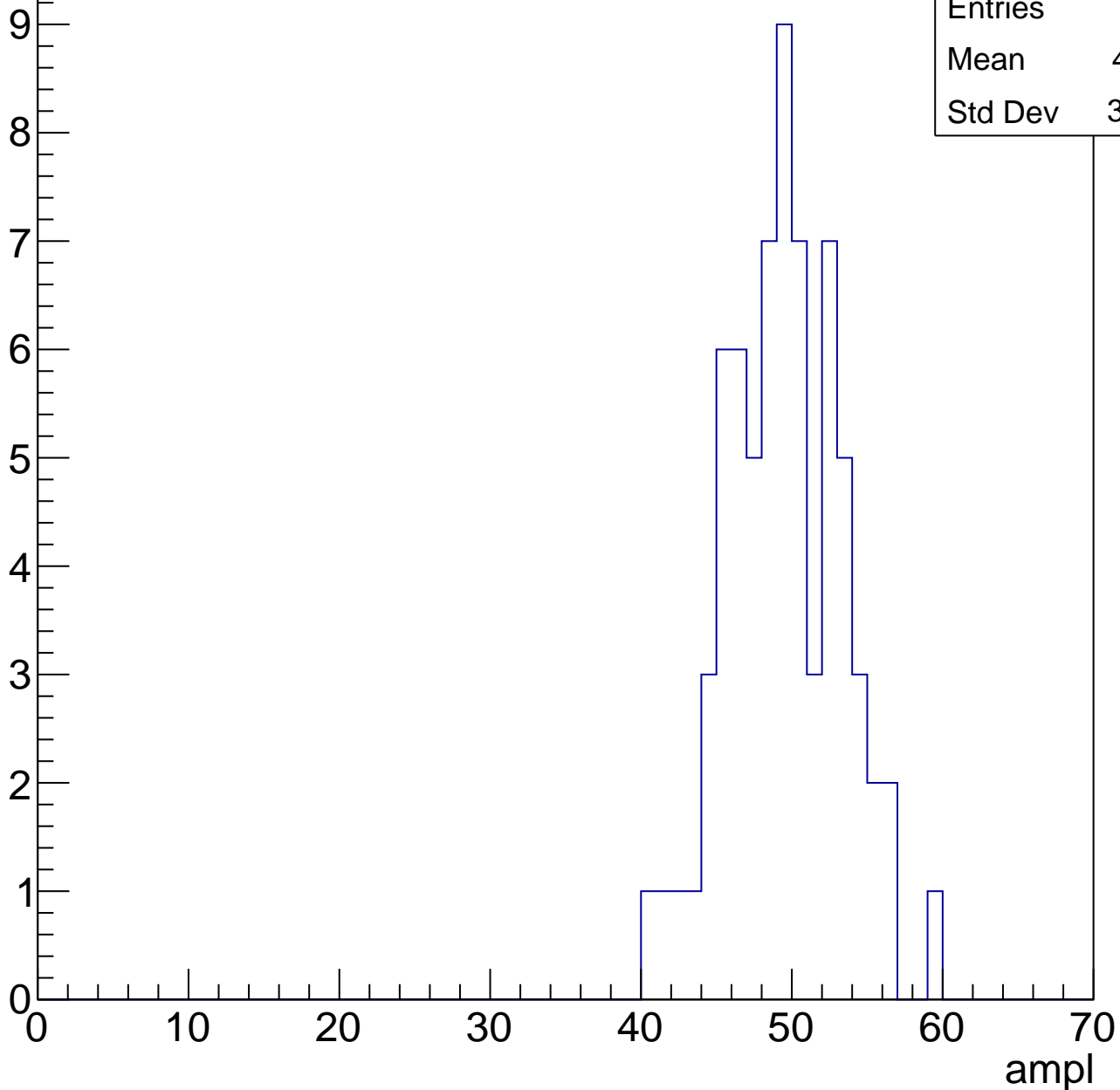


B0L001S, U14-ch104, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	49.01
Std Dev	3.759

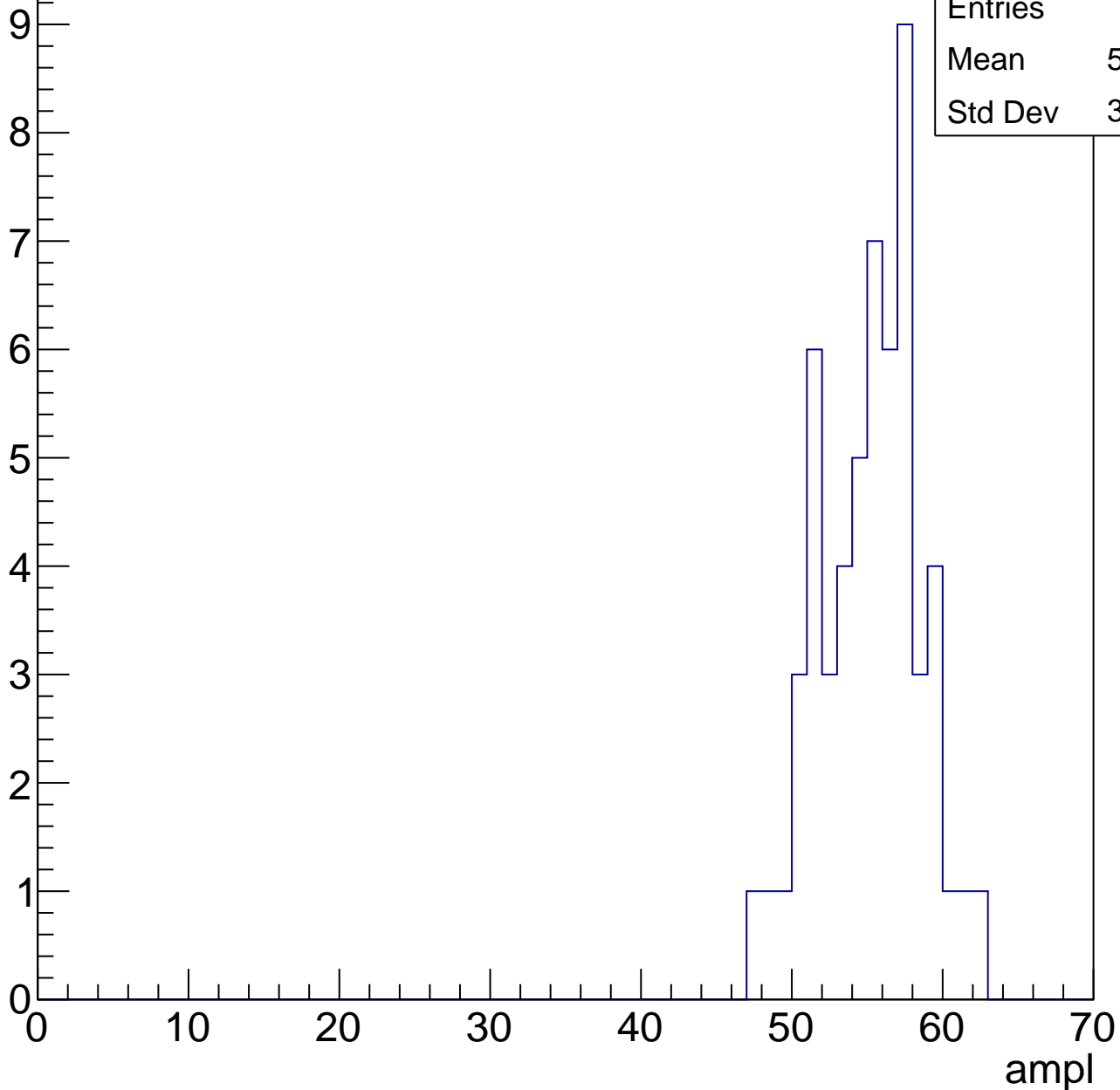


B0L001S, U14-ch104, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	54.73
Std Dev	3.292

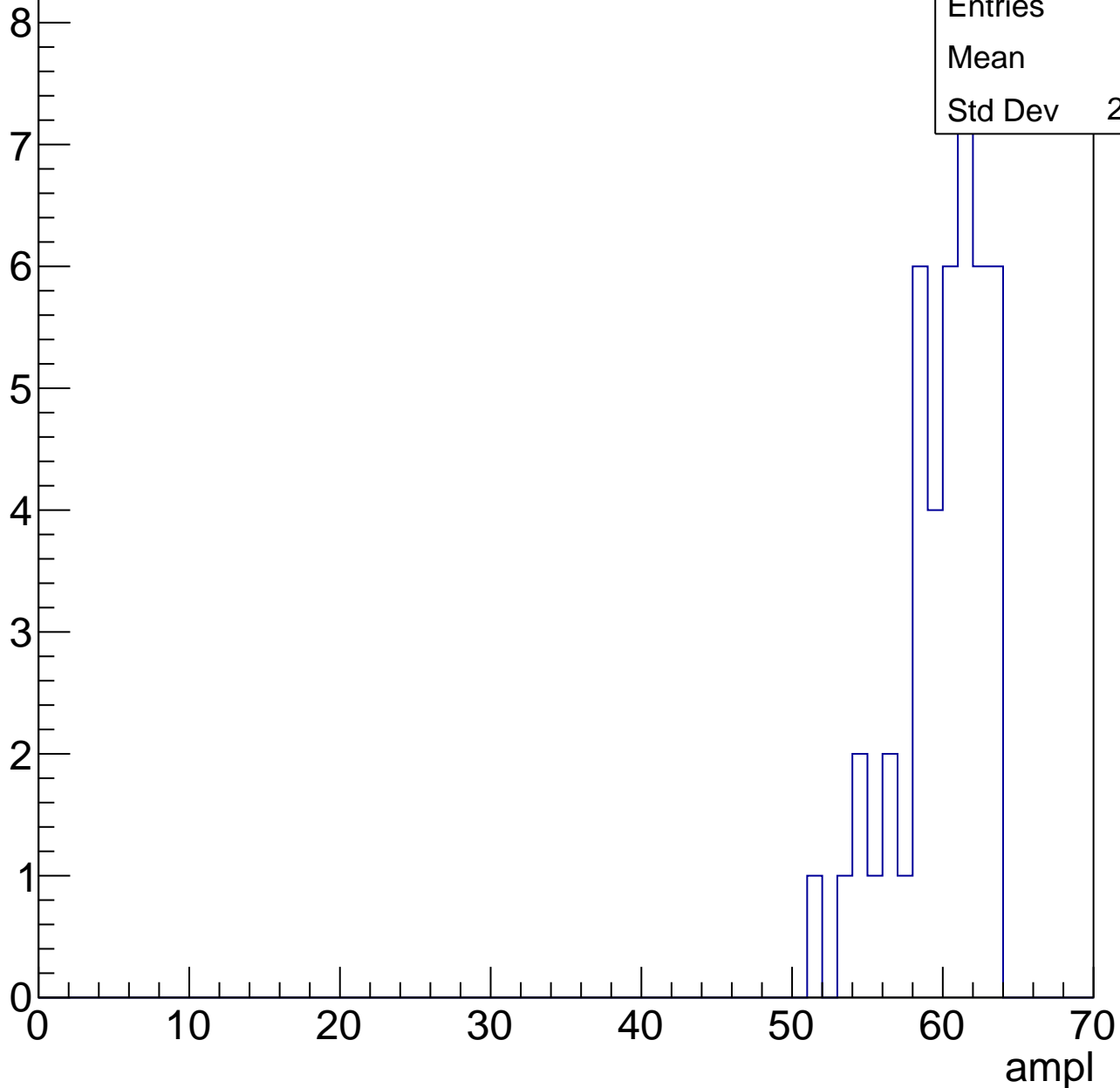


B0L001S, U14-ch104, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

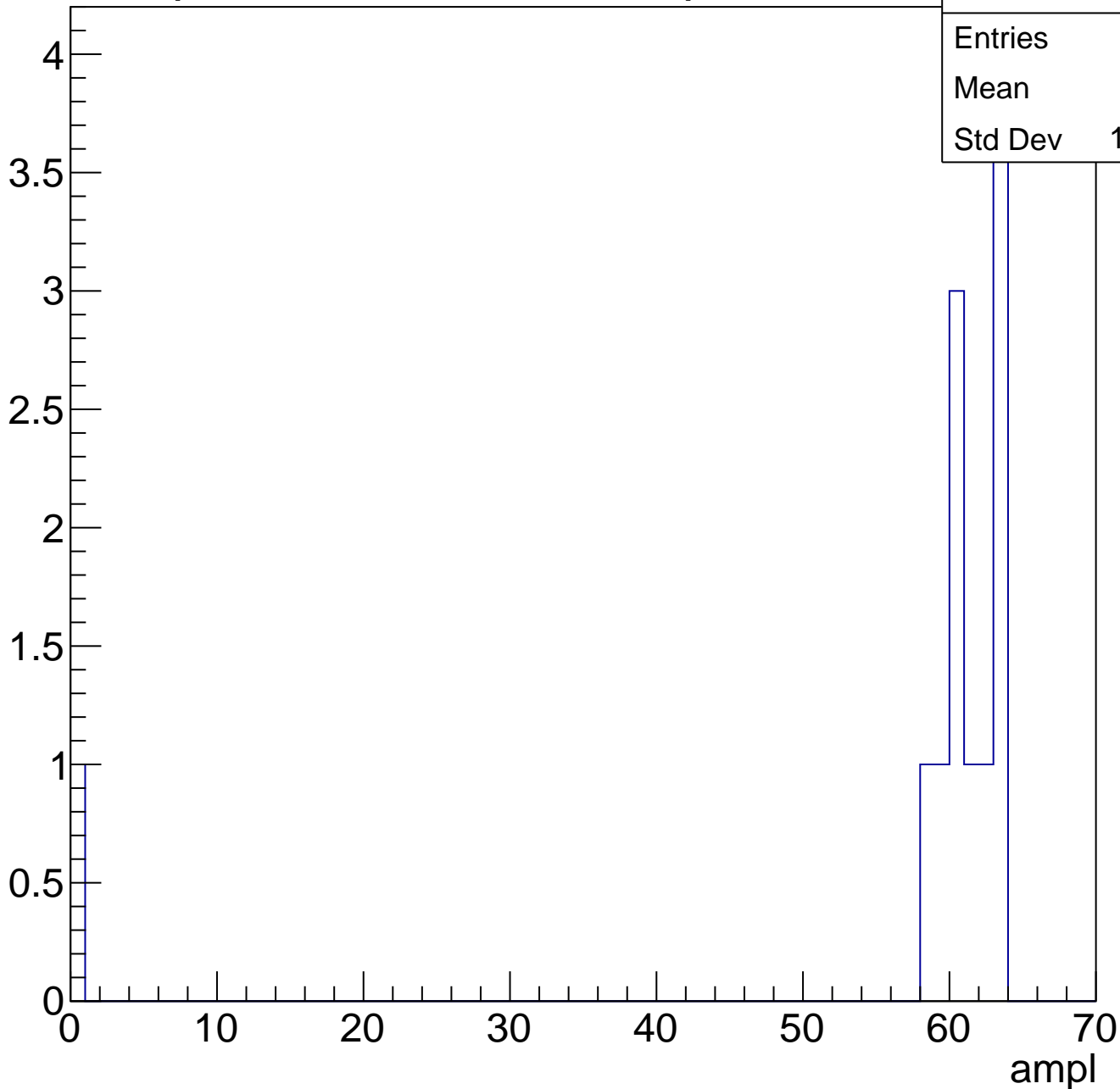
Entries	44
Mean	59.5
Std Dev	2.904



B0L001S, U14-ch104, adc6

calib_packv5_042523_0143.root, FC#9, port A1

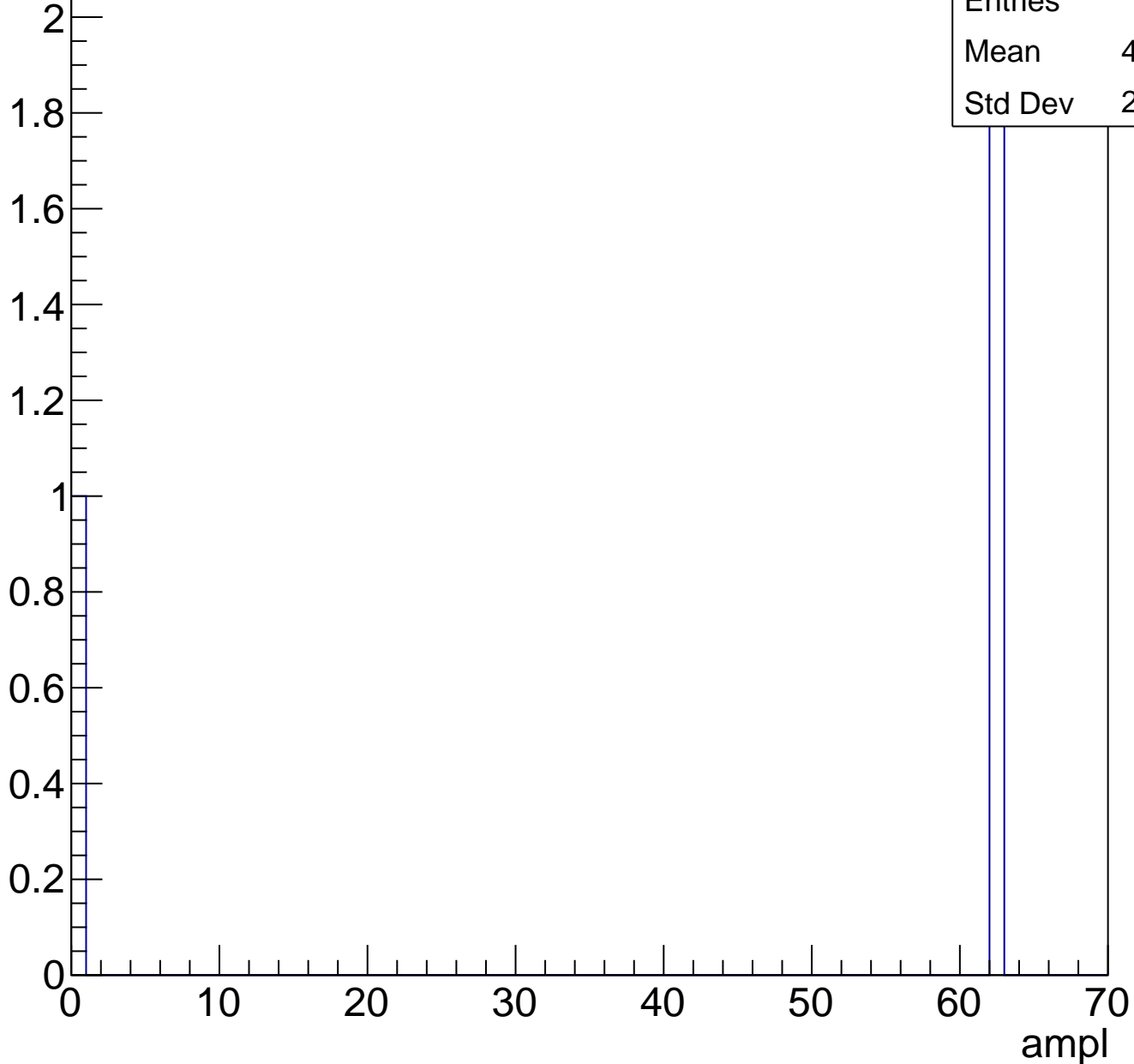
Entry



B0L001S, U14-ch104, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch105, adc0

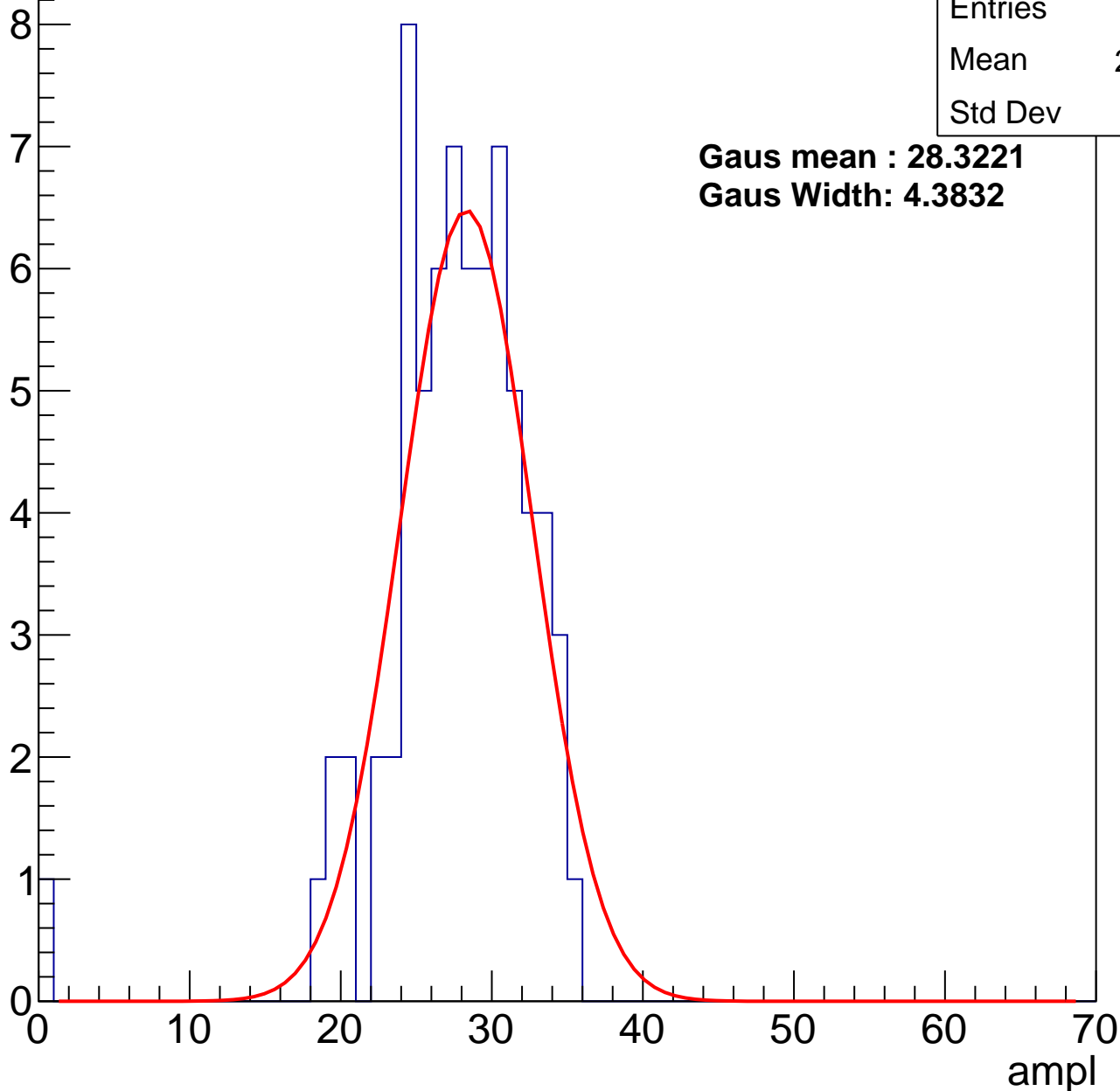
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	27.11
Std Dev	5.06

Gaus mean : 28.3221

Gaus Width: 4.3832



B0L001S, U14-ch105, adc1

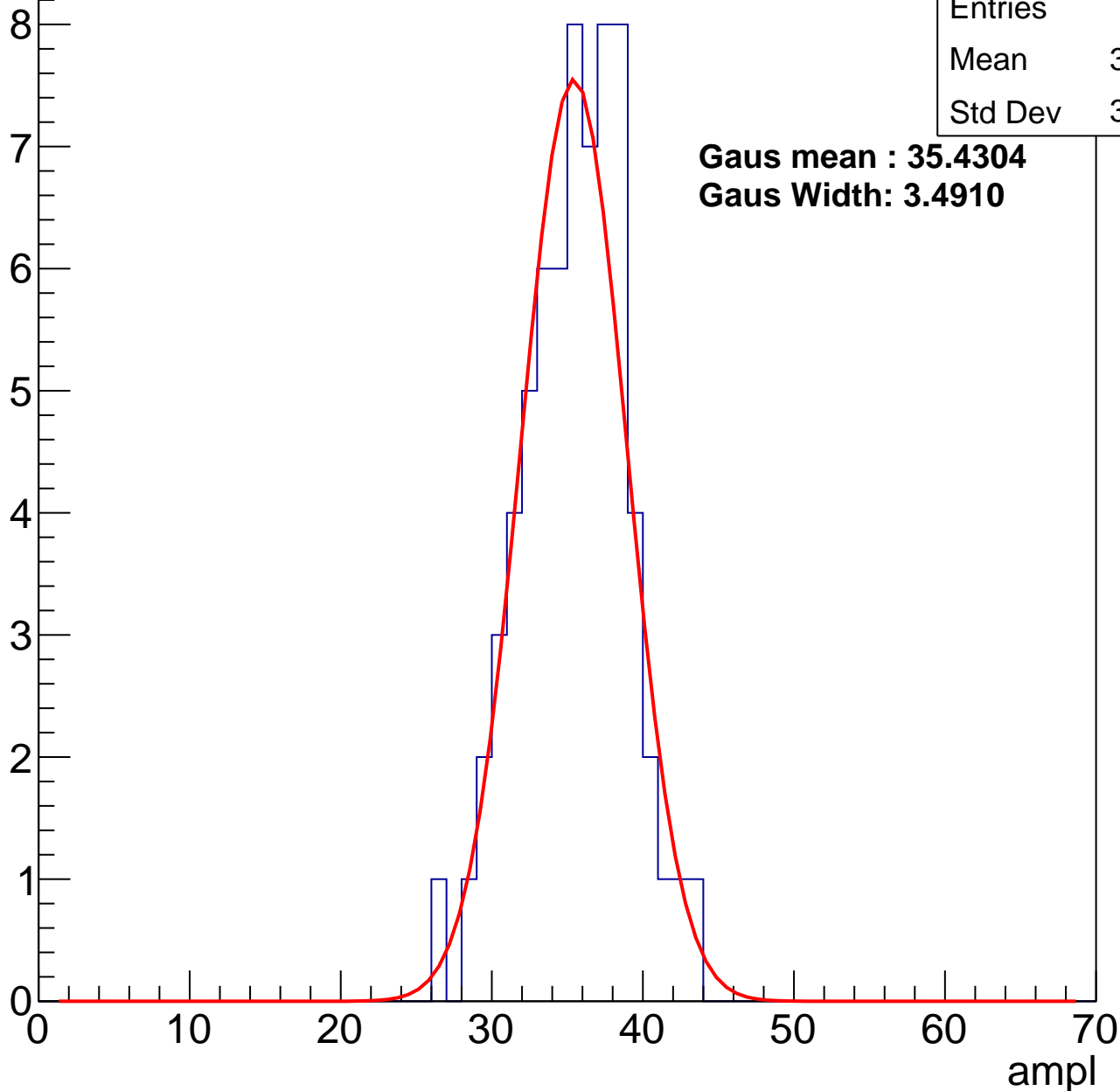
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	35.03
Std Dev	3.408

Gaus mean : 35.4304

Gaus Width: 3.4910



B0L001S, U14-ch105, adc2

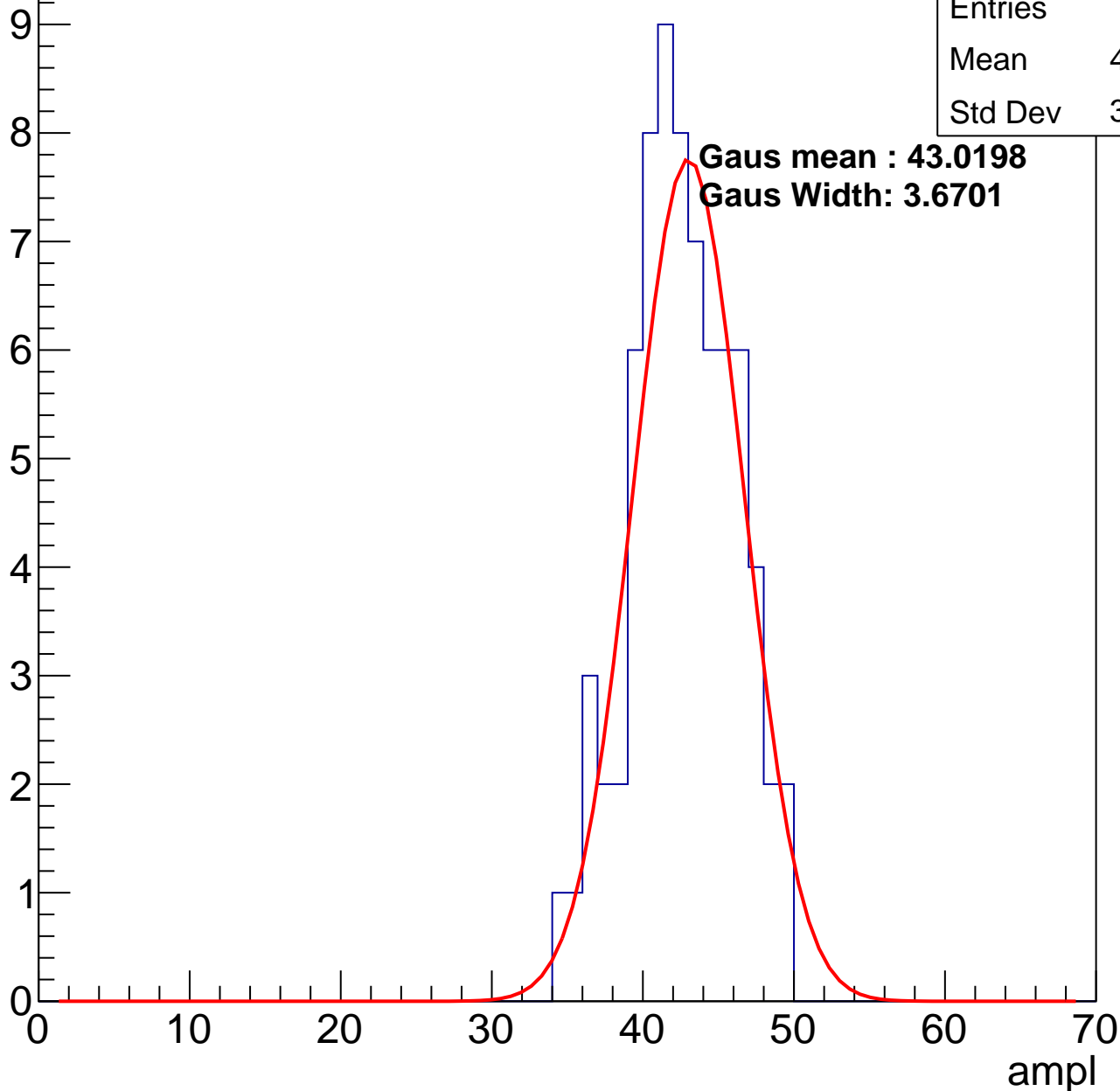
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	42.18
Std Dev	3.406

Gaus mean : 43.0198

Gaus Width: 3.6701

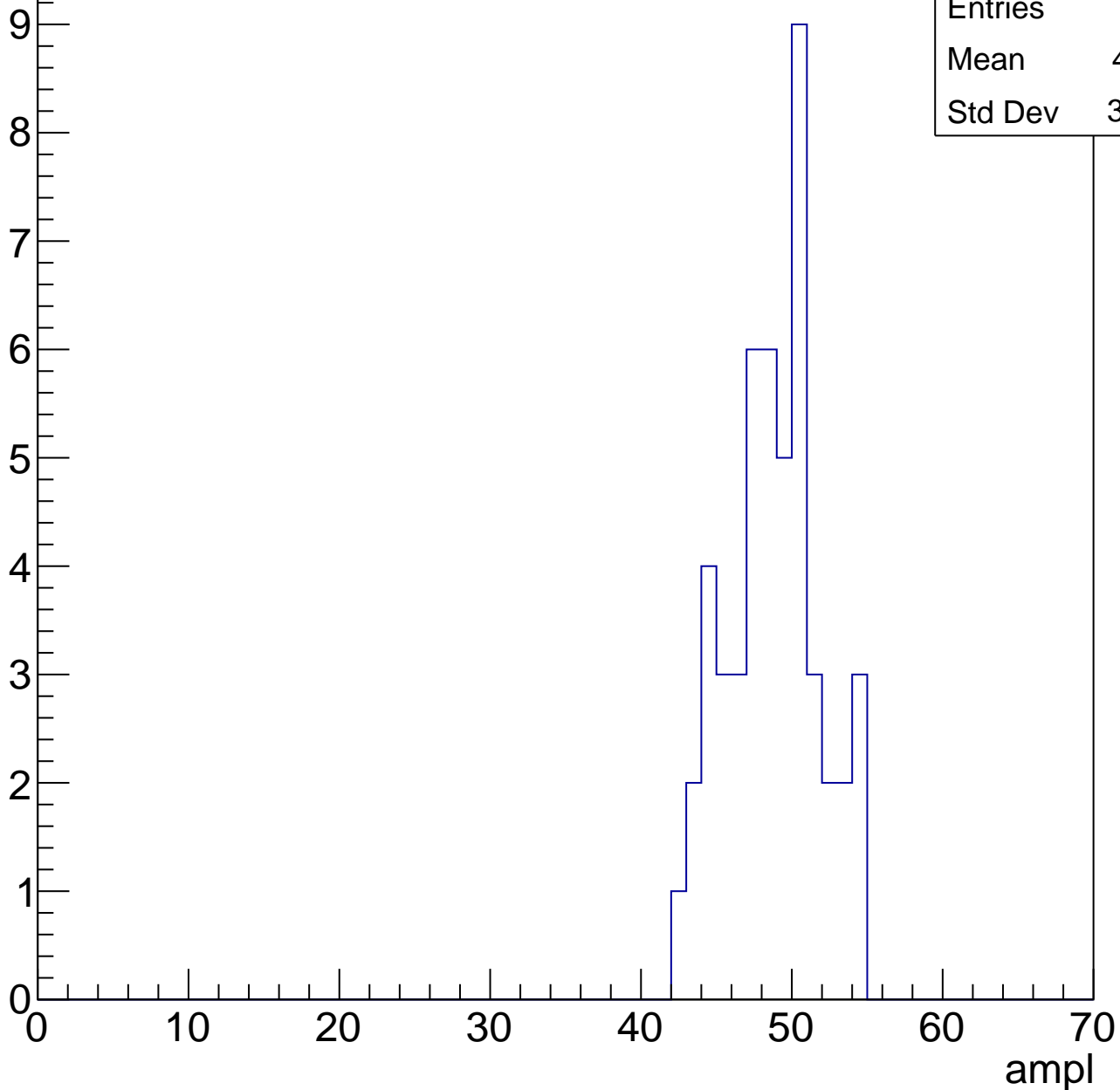


B0L001S, U14-ch105, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	48.31
Std Dev	3.025

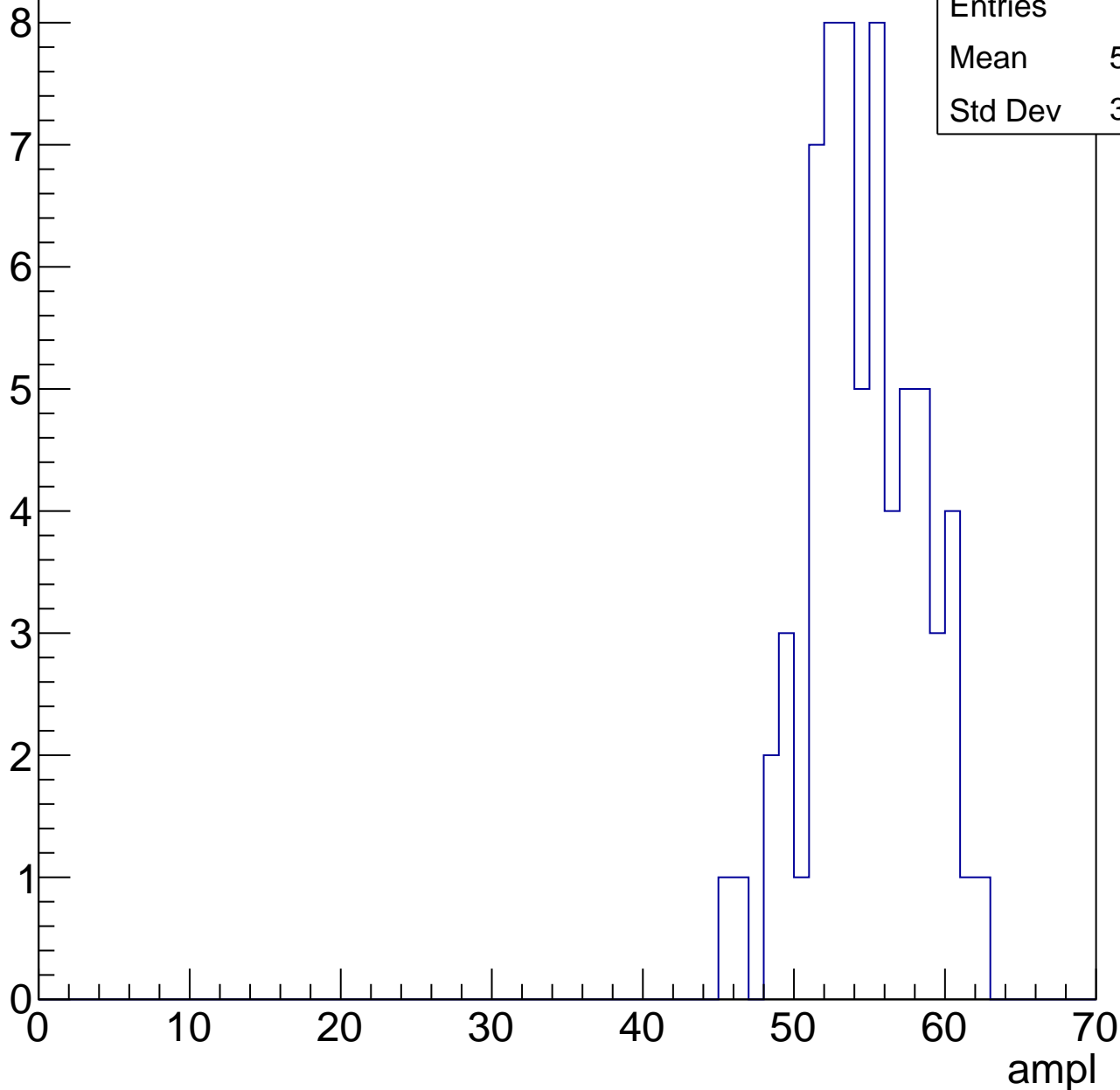


B0L001S, U14-ch105, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

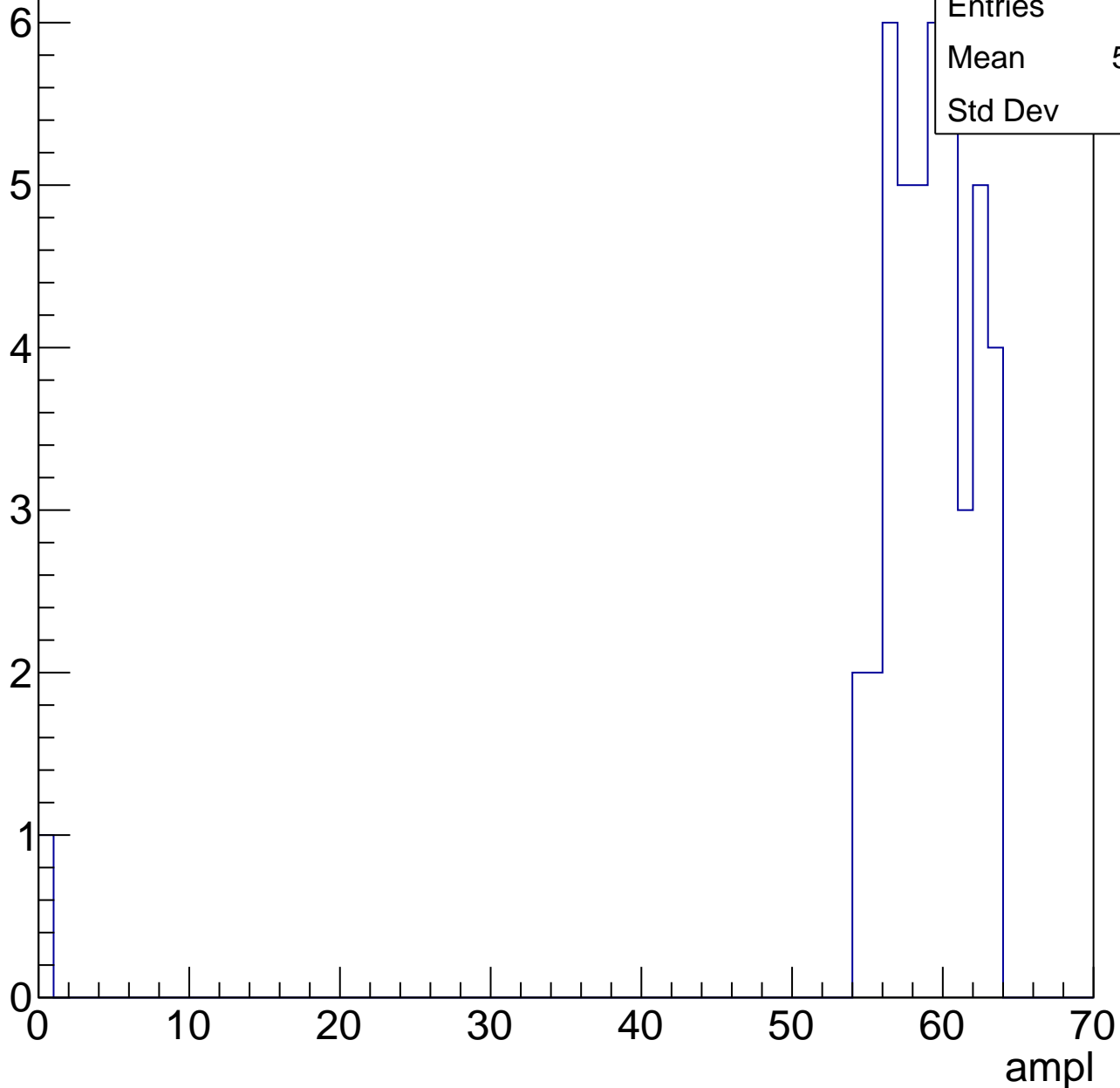
Entries	67
Mean	54.18
Std Dev	3.644



B0L001S, U14-ch105, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

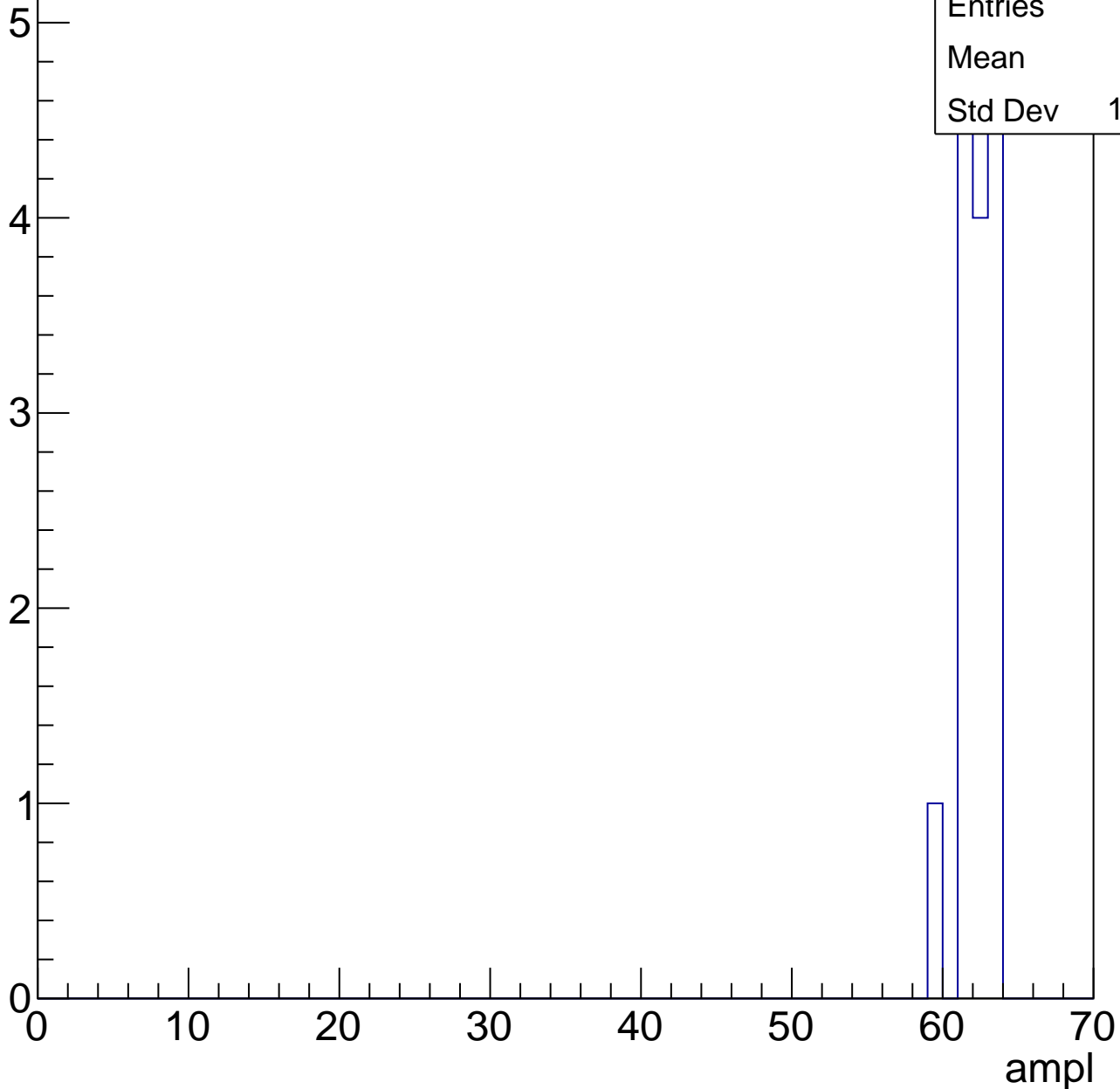


B0L001S, U14-ch105, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

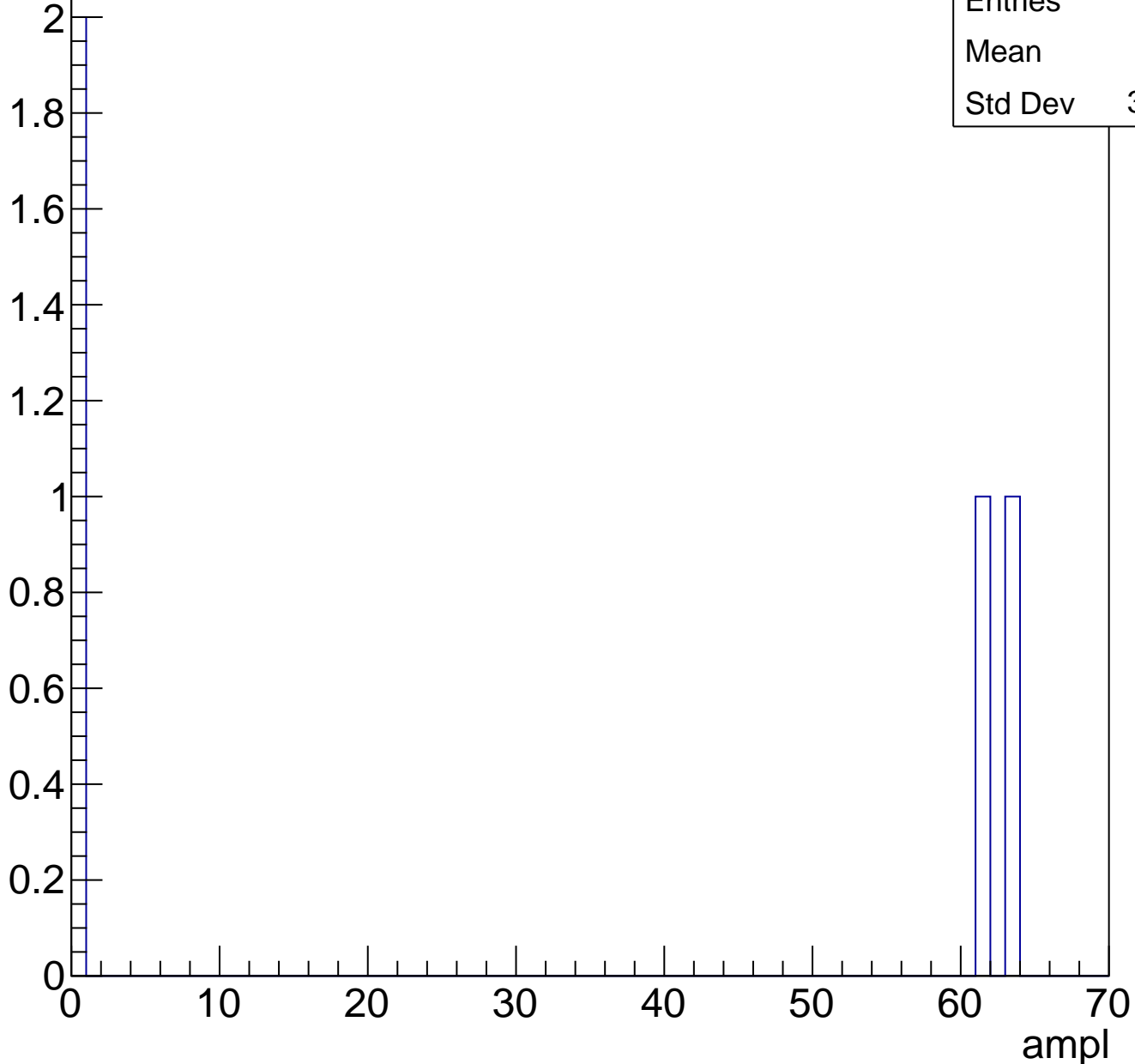
Entries	15
Mean	61.8
Std Dev	1.108



B0L001S, U14-ch105, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	4
Mean	31
Std Dev	31.01

B0L001S, U14-ch106, adc0

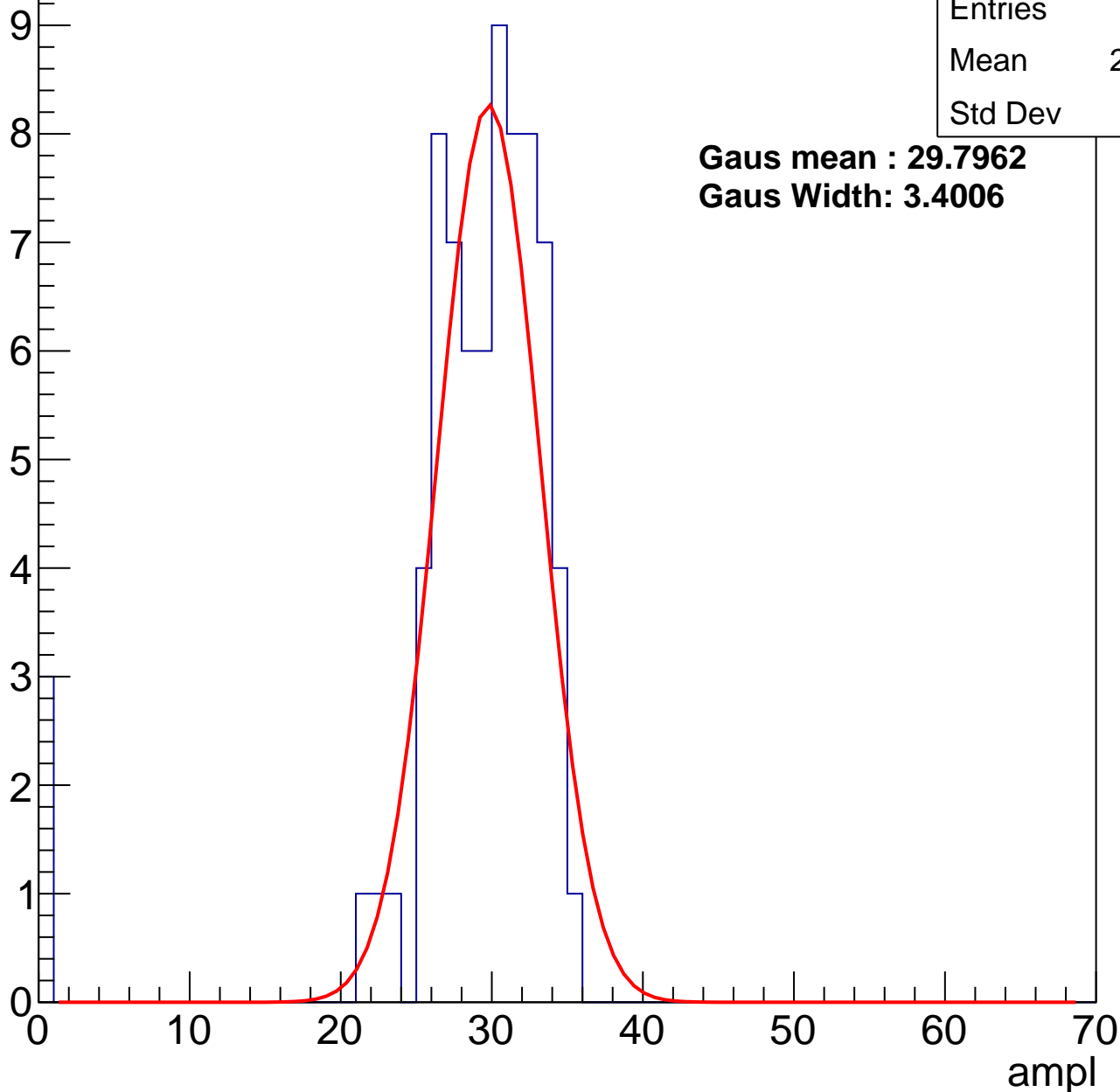
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	28.12
Std Dev	6.52

Gaus mean : 29.7962

Gaus Width: 3.4006



B0L001S, U14-ch106, adc1

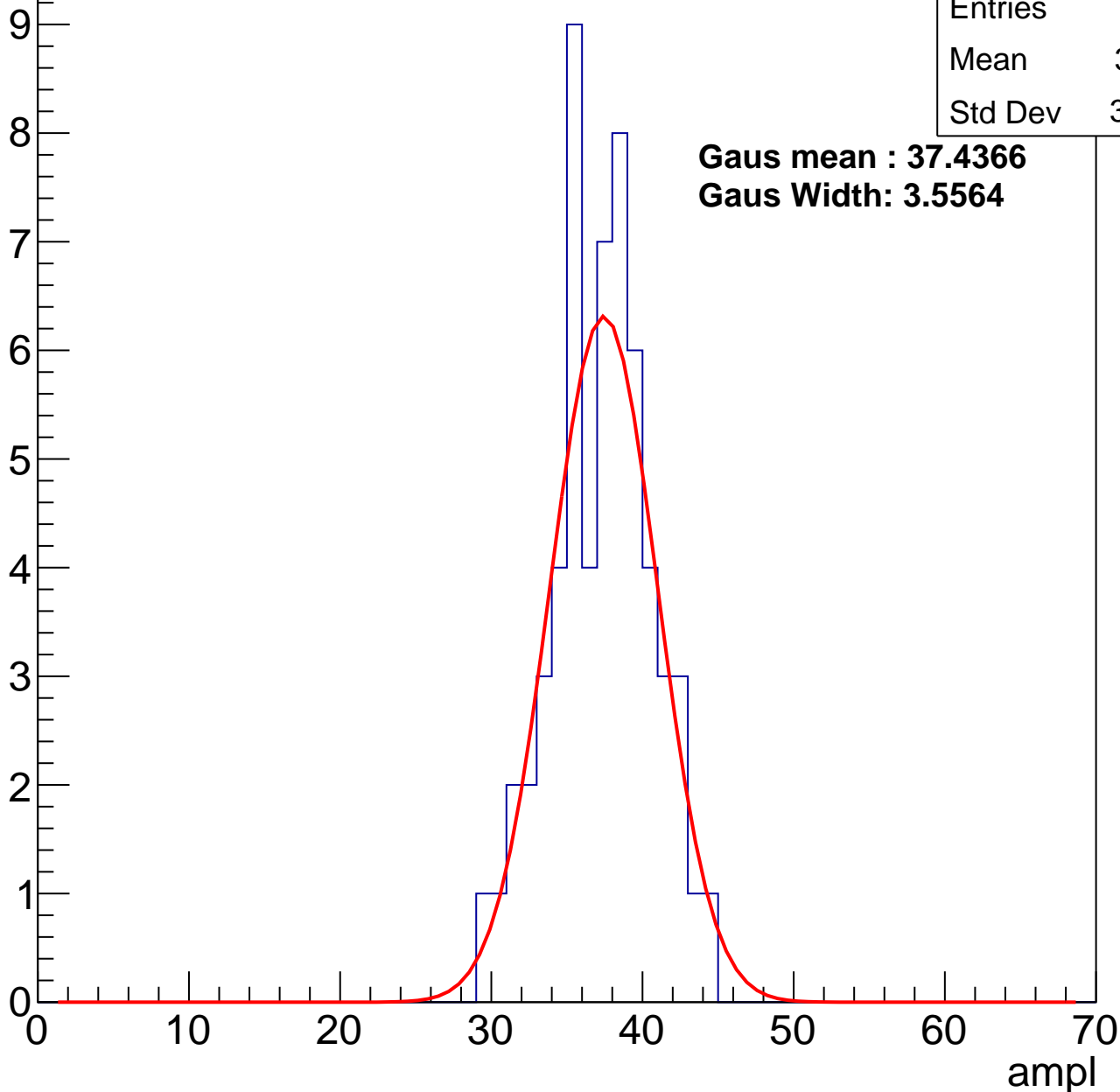
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	36.81
Std Dev	3.275

Gaus mean : 37.4366

Gaus Width: 3.5564



B0L001S, U14-ch106, adc2

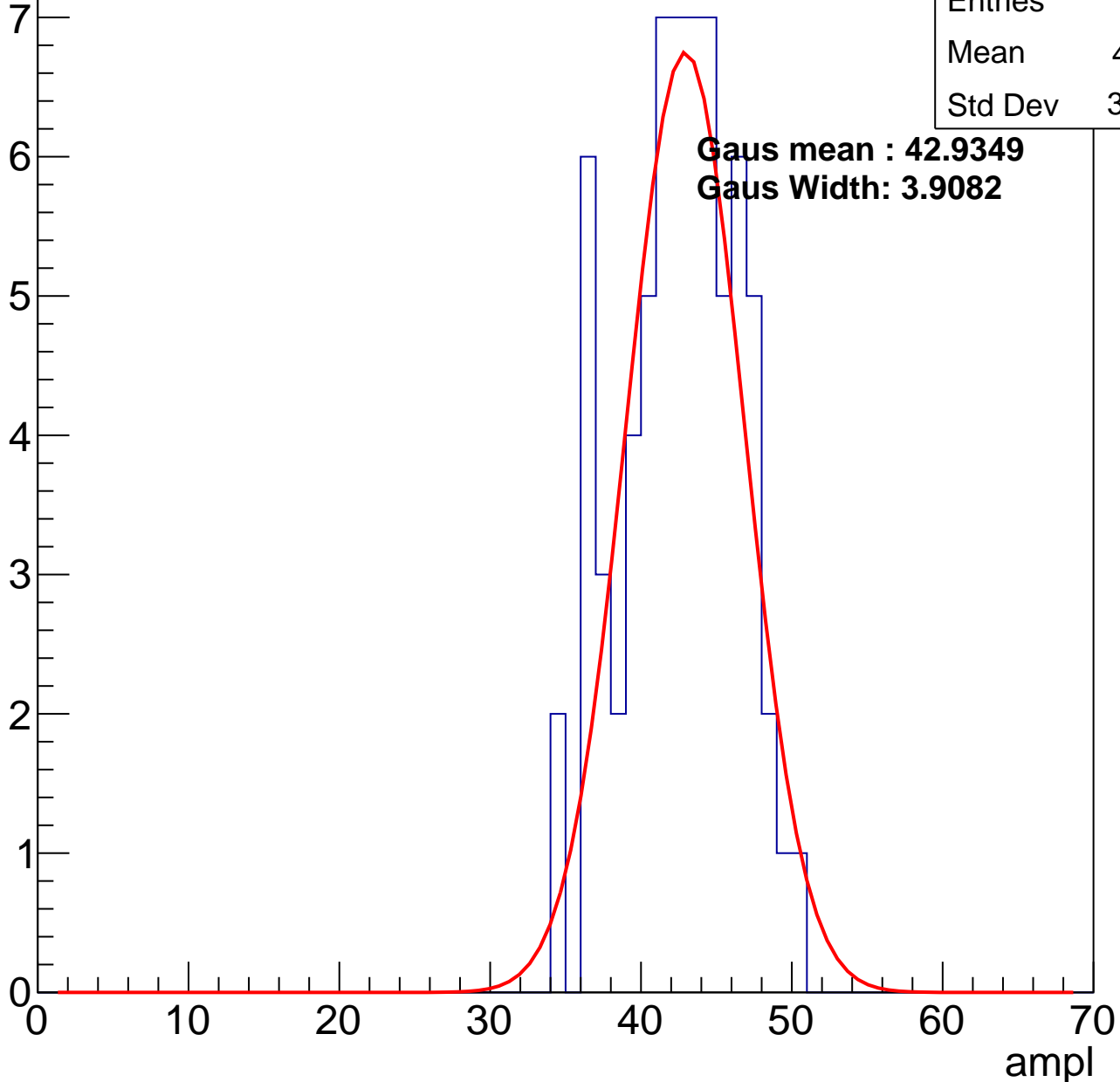
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	42.11
Std Dev	3.778

Gaus mean : 42.9349

Gaus Width: 3.9082

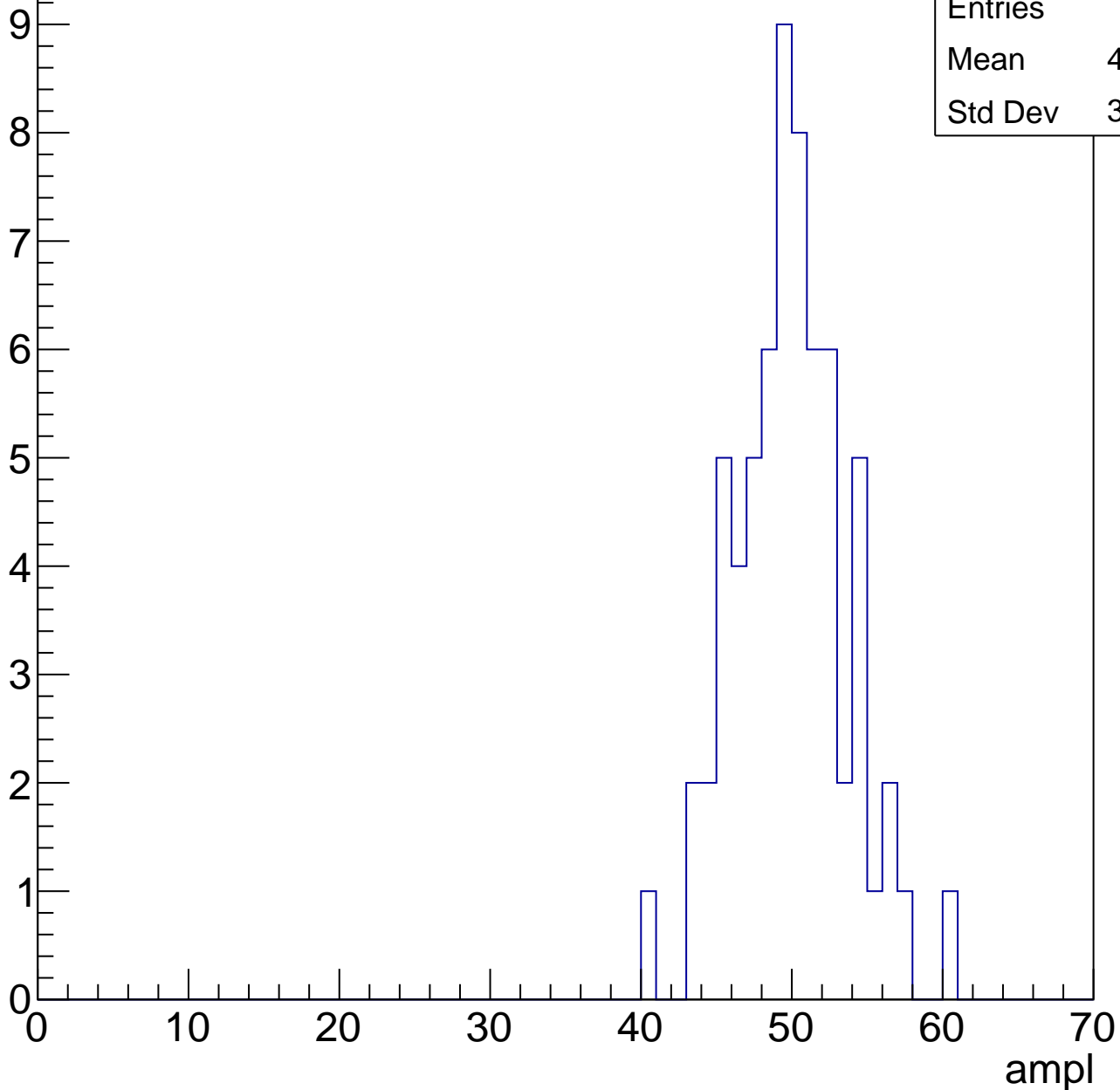


B0L001S, U14-ch106, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	49.47
Std Dev	3.677

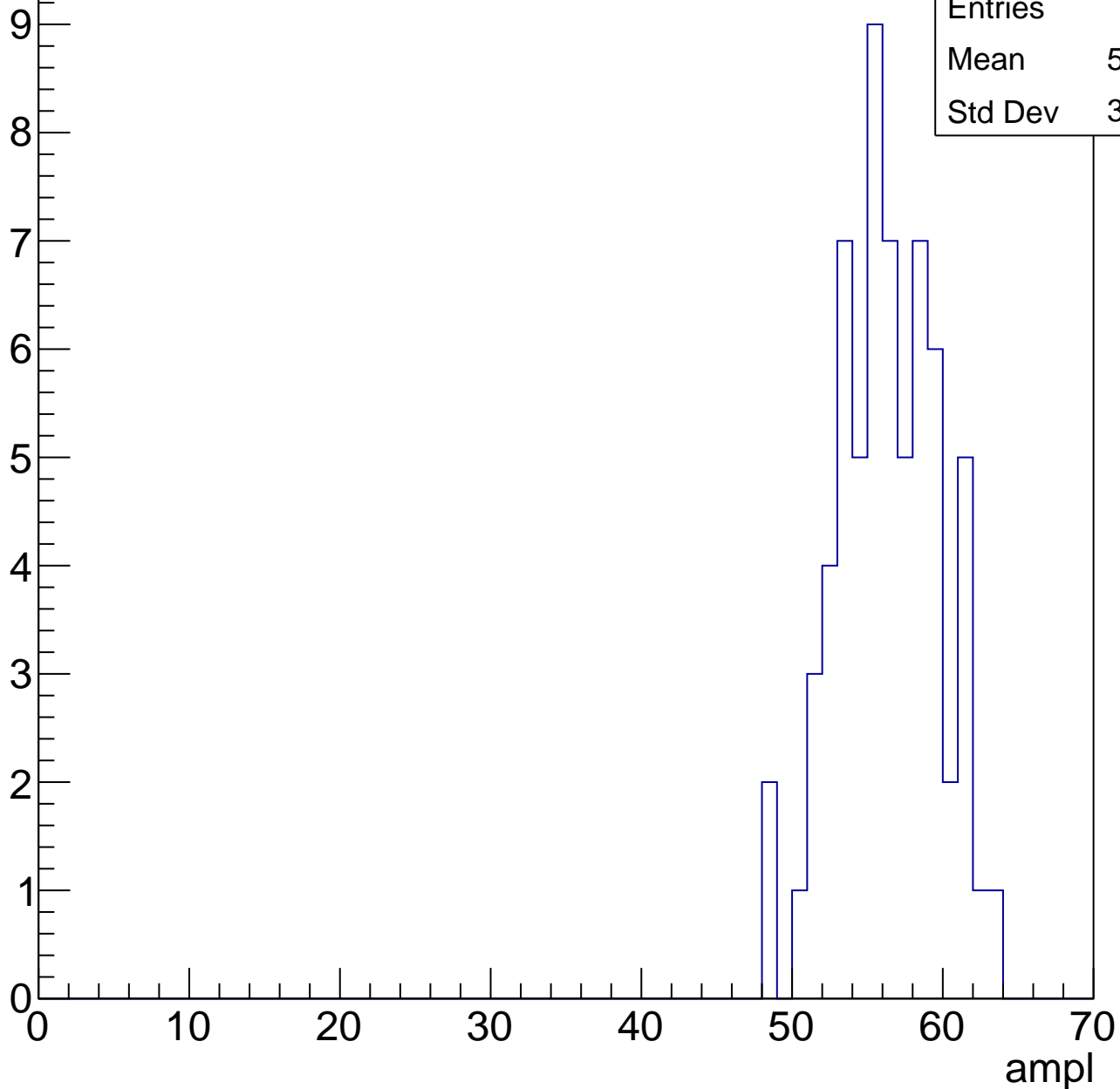


B0L001S, U14-ch106, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	55.85
Std Dev	3.334

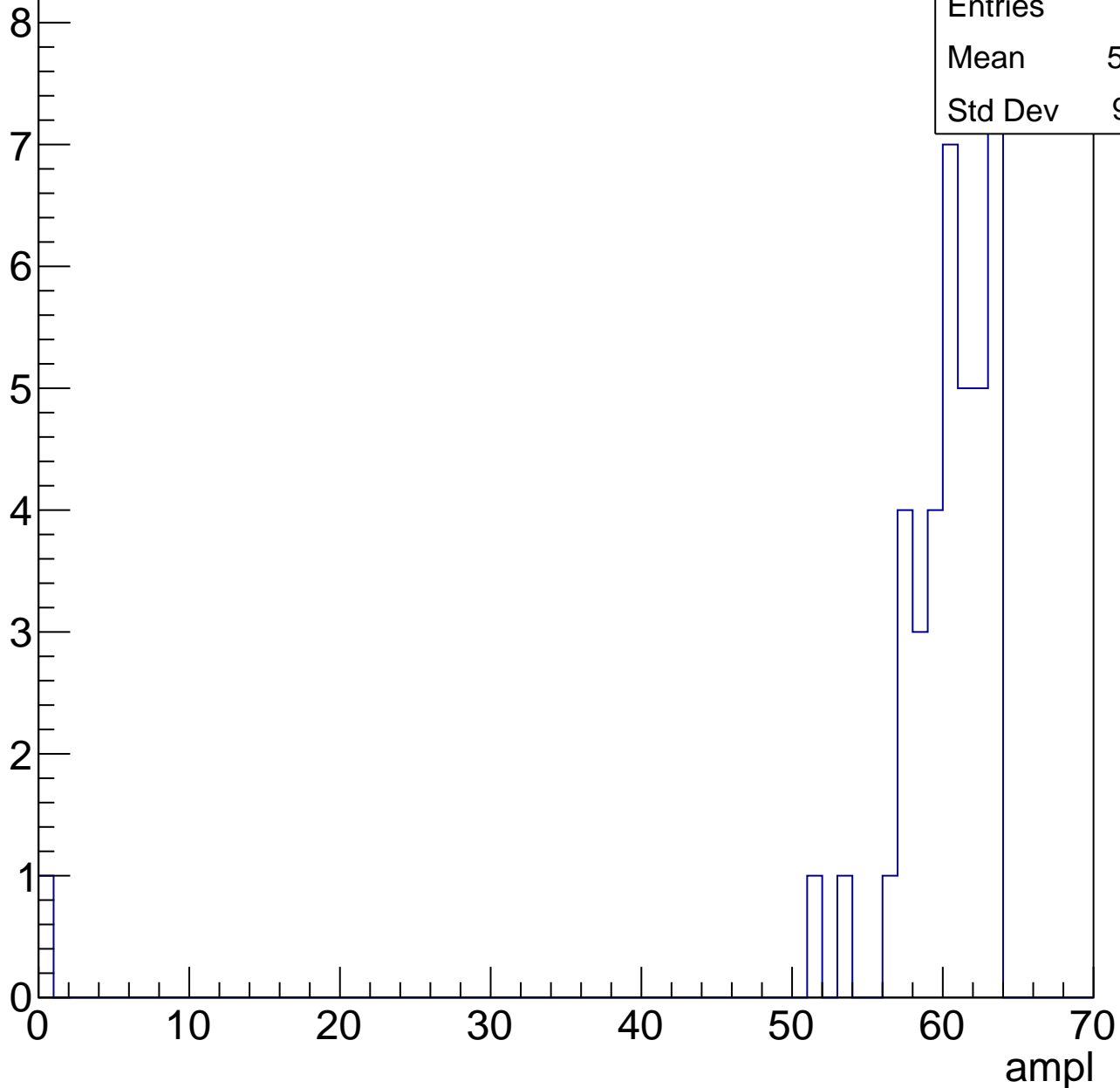


B0L001S, U14-ch106, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

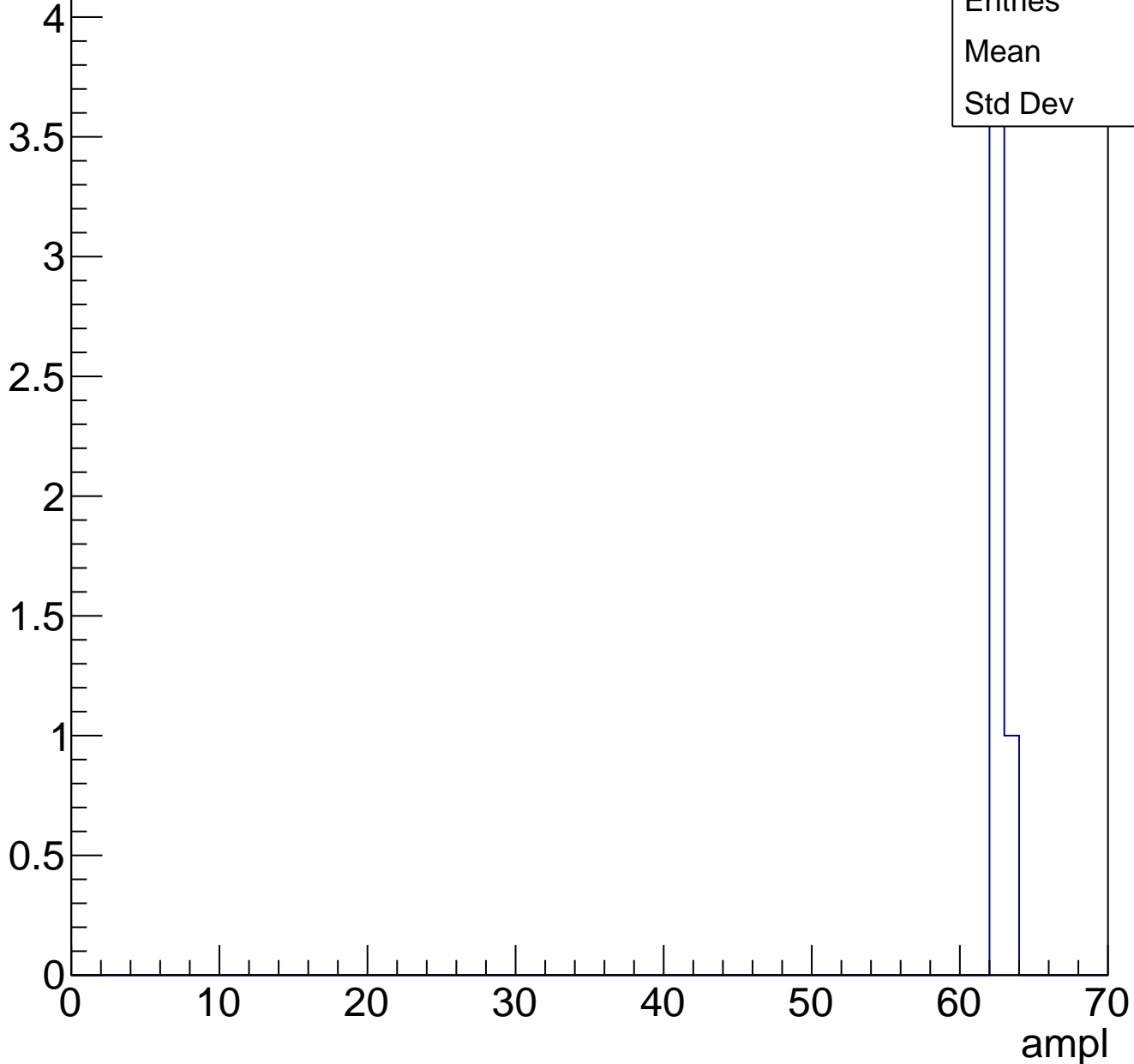
Entries	40
Mean	58.42
Std Dev	9.741



B0L001S, U14-ch106, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch106, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch107, adc0

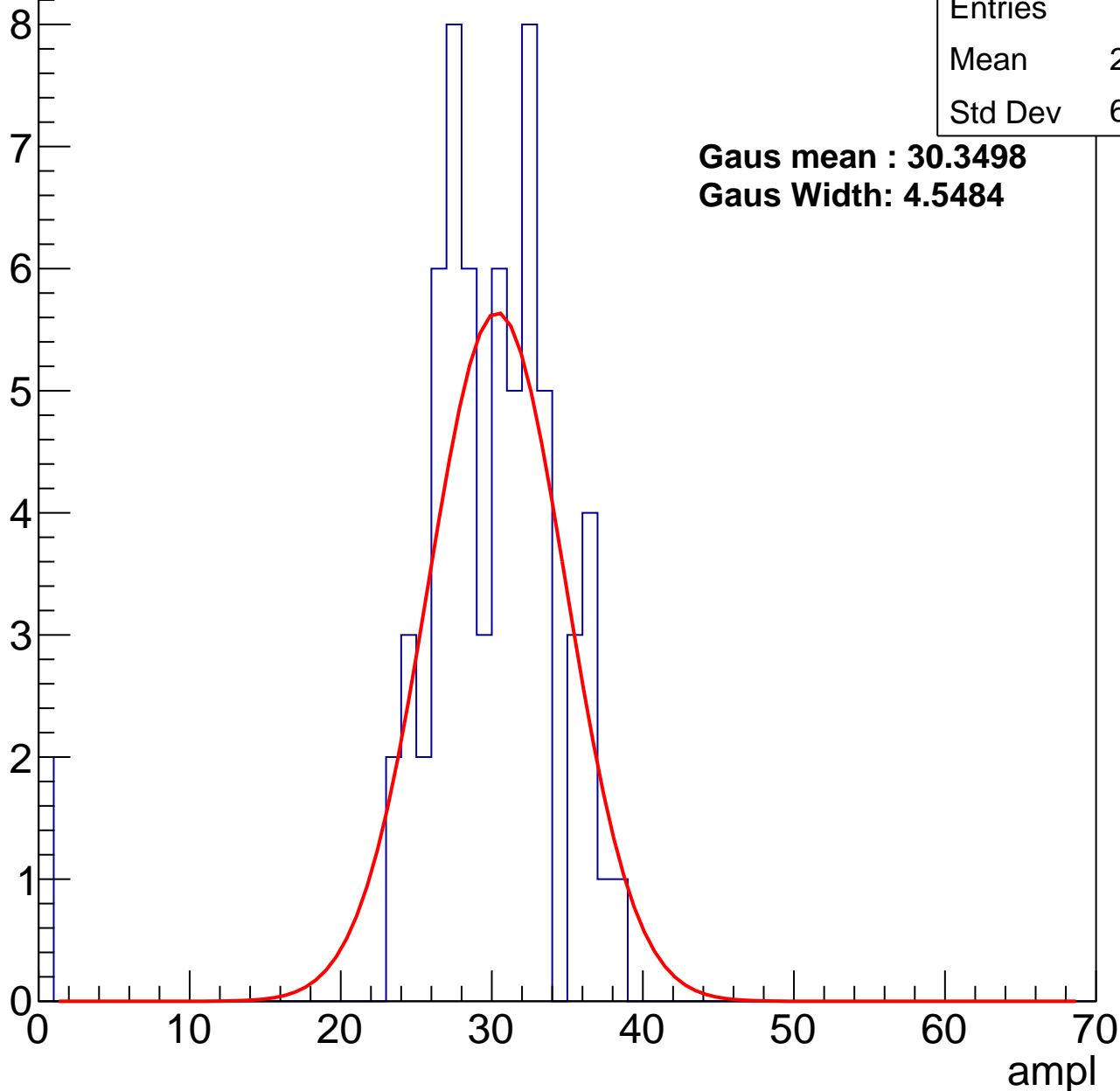
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	28.85
Std Dev	6.298

Gaus mean : 30.3498

Gaus Width: 4.5484



B0L001S, U14-ch107, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	74
Mean	36.39
Std Dev	4.361

Gaus mean : 37.5770

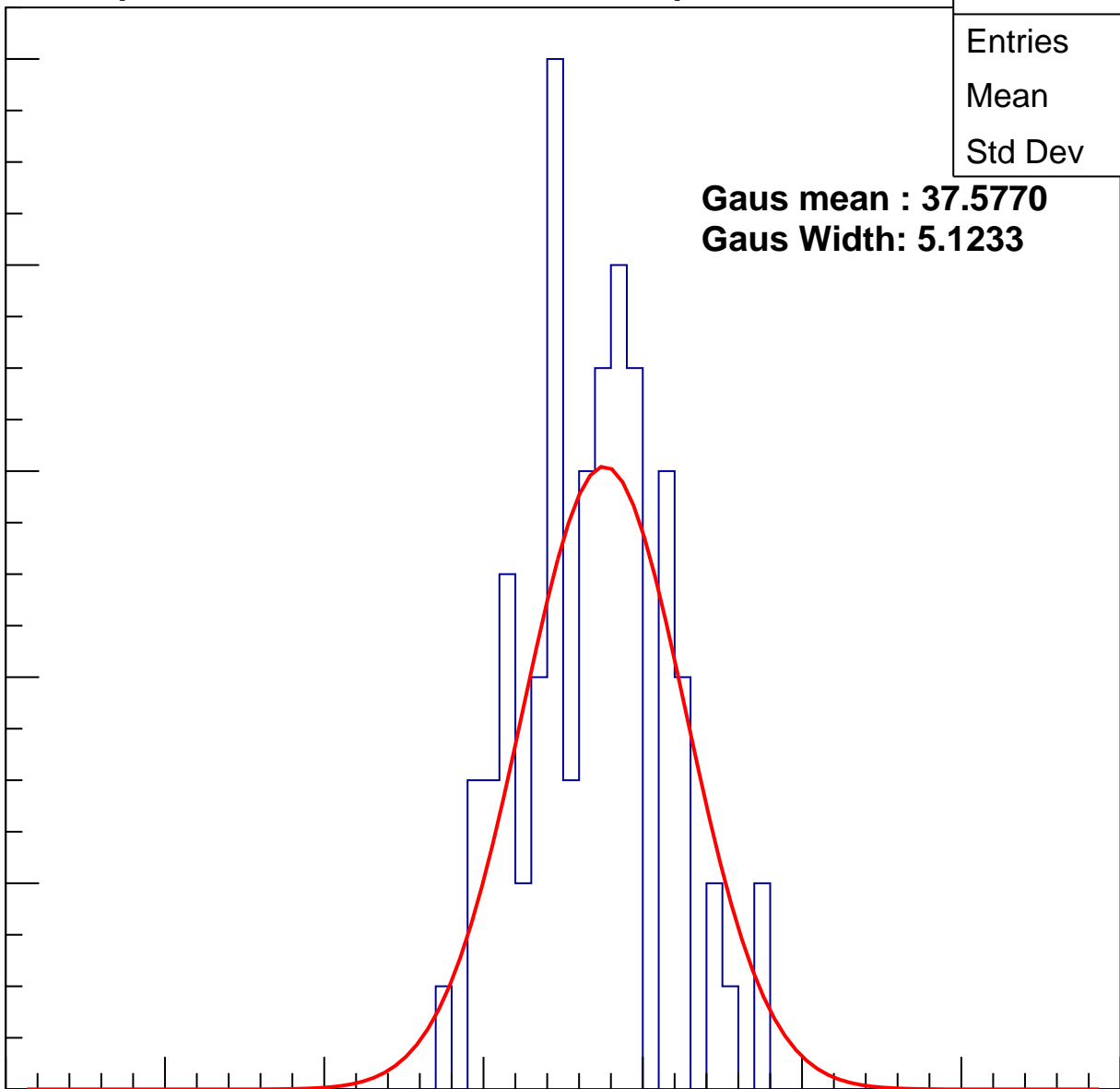
Gaus Width: 5.1233

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch107, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entries	65
Mean	42.97
Std Dev	3.953

Gaus mean : 44.1102

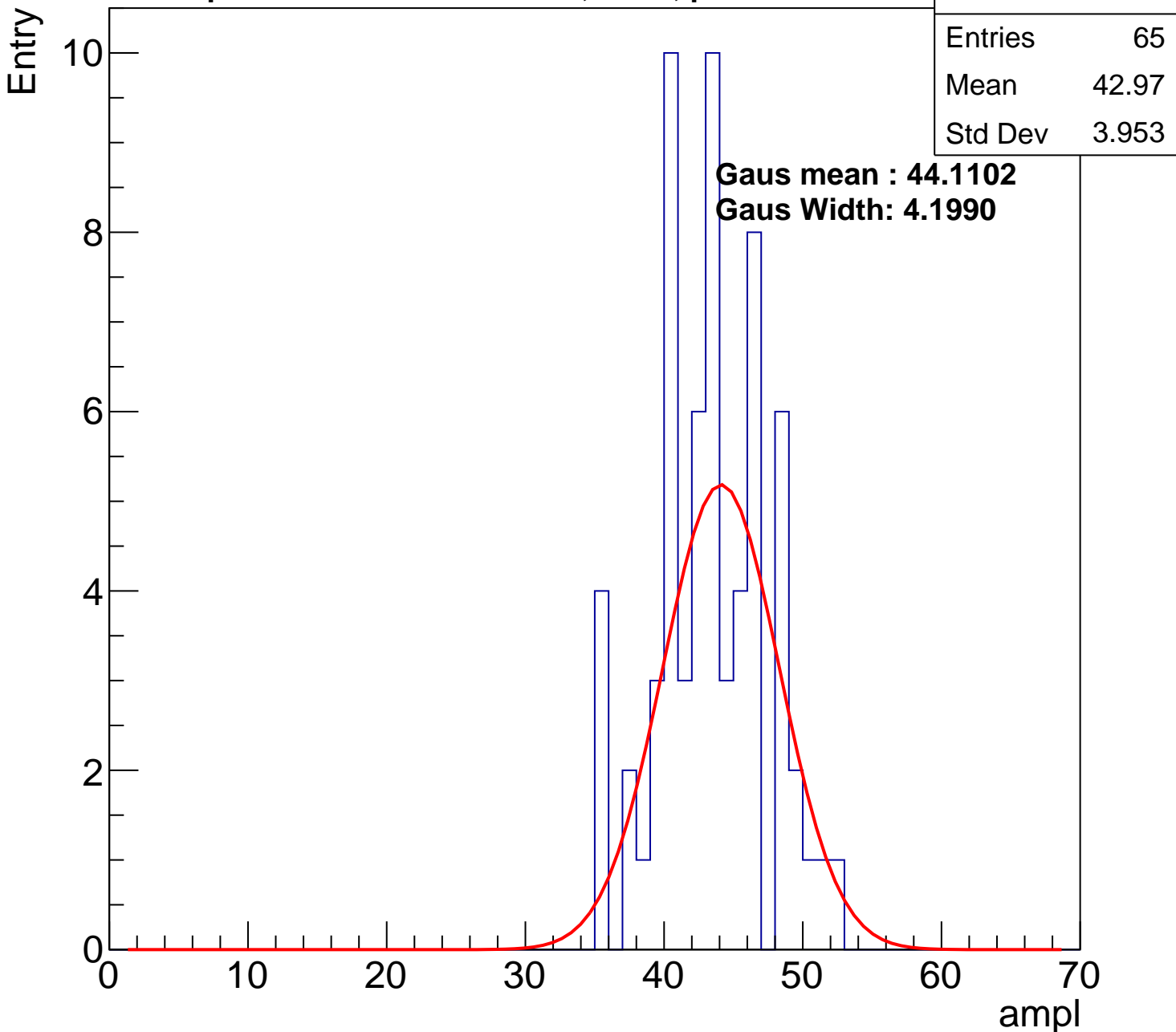
Gaus Width: 4.1990

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

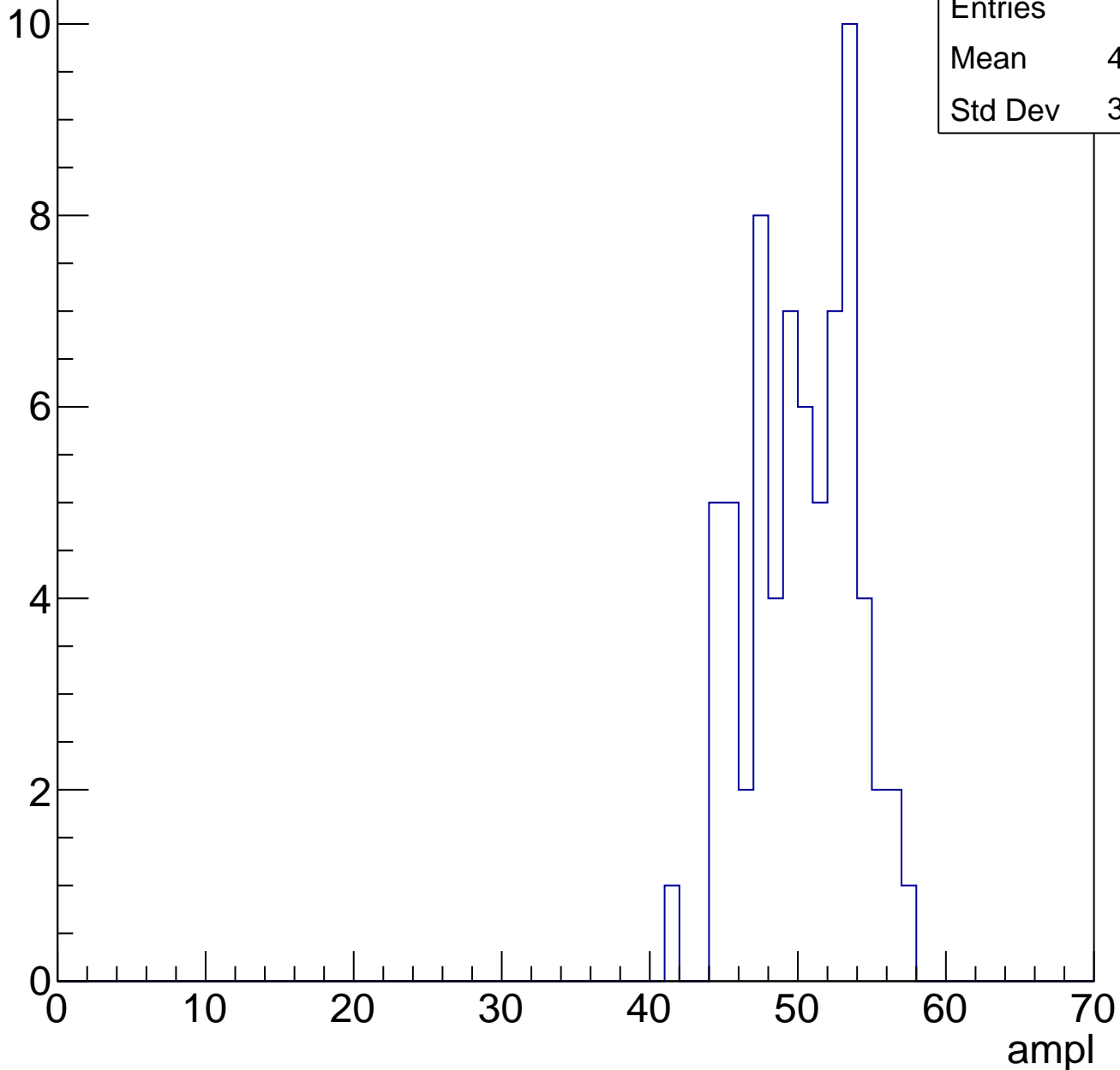


B0L001S, U14-ch107, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	69
Mean	49.75
Std Dev	3.544

Entry

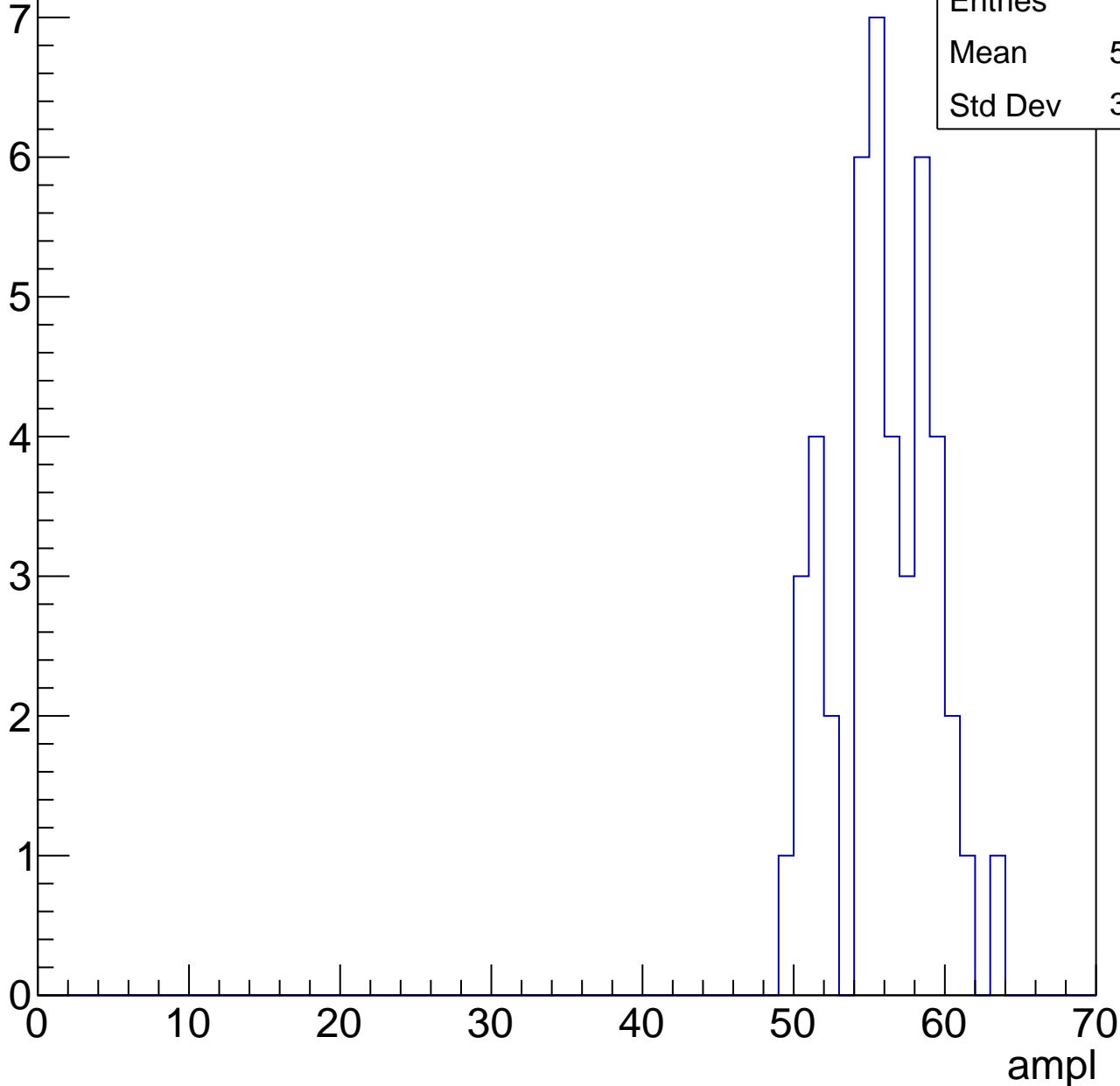


B0L001S, U14-ch107, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	55.43
Std Dev	3.285

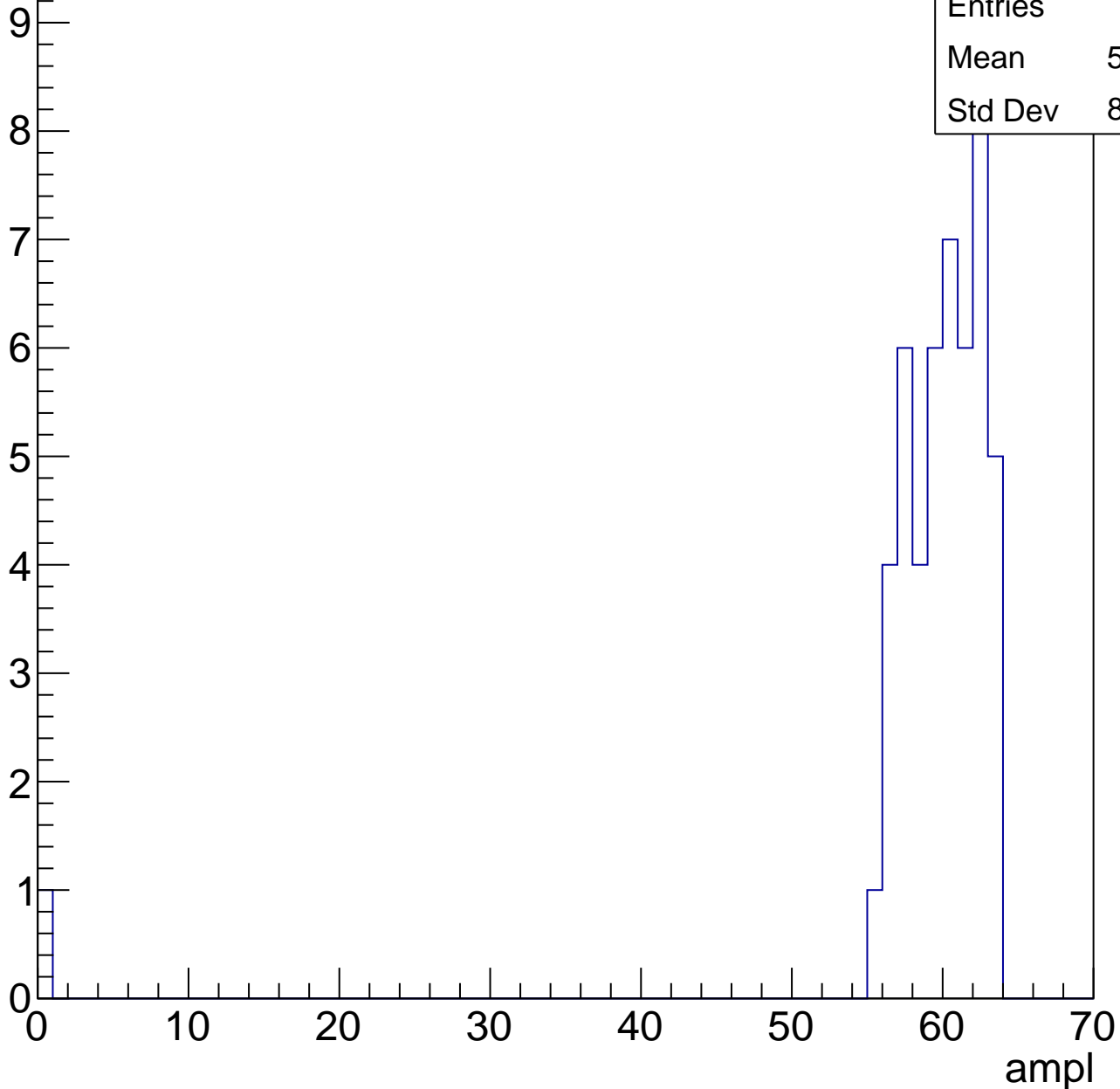


B0L001S, U14-ch107, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

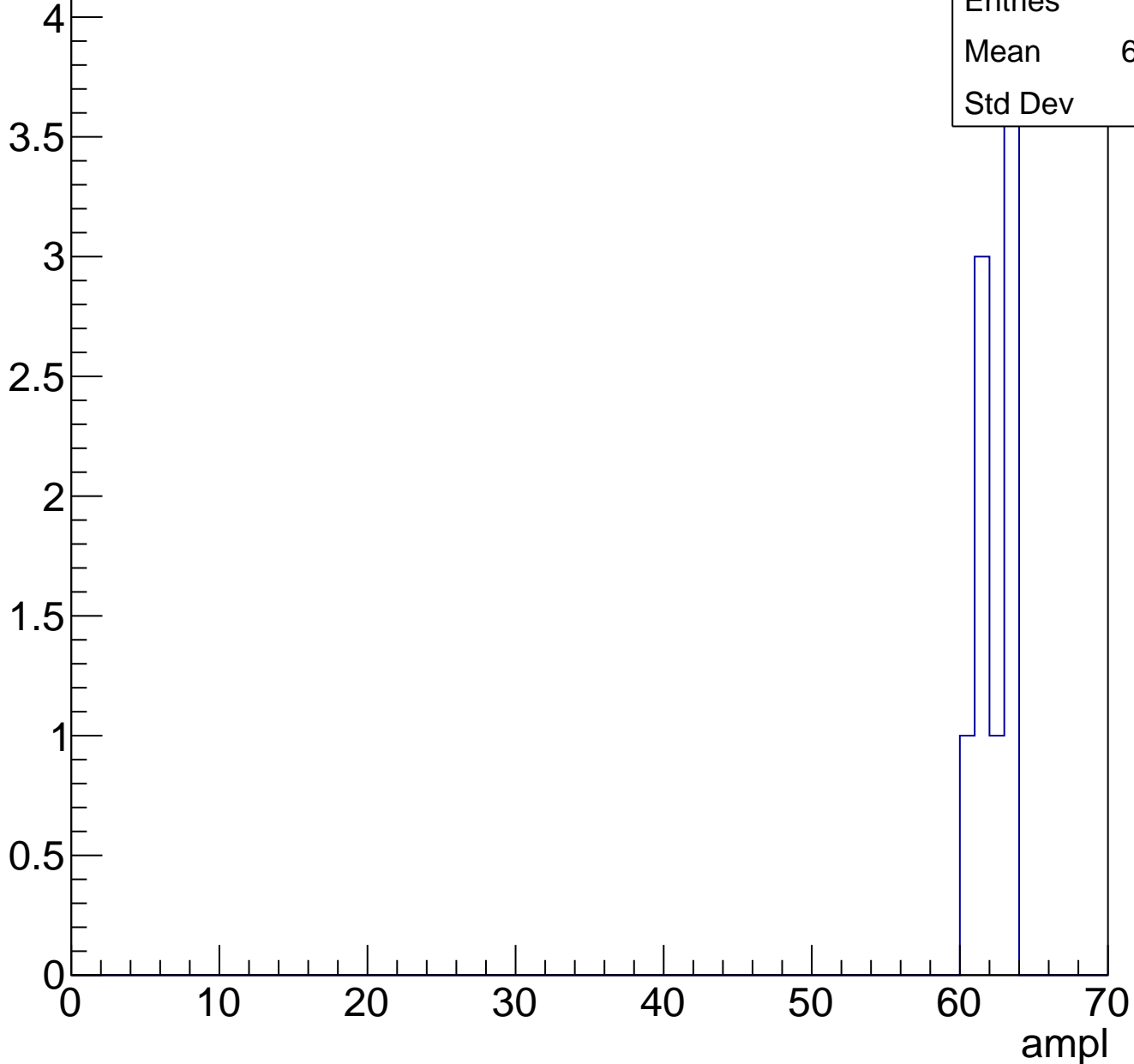
Entries	49
Mean	58.49
Std Dev	8.737



B0L001S, U14-ch107, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch107, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	0
Std Dev	0

B0L001S, U14-ch108, adc0

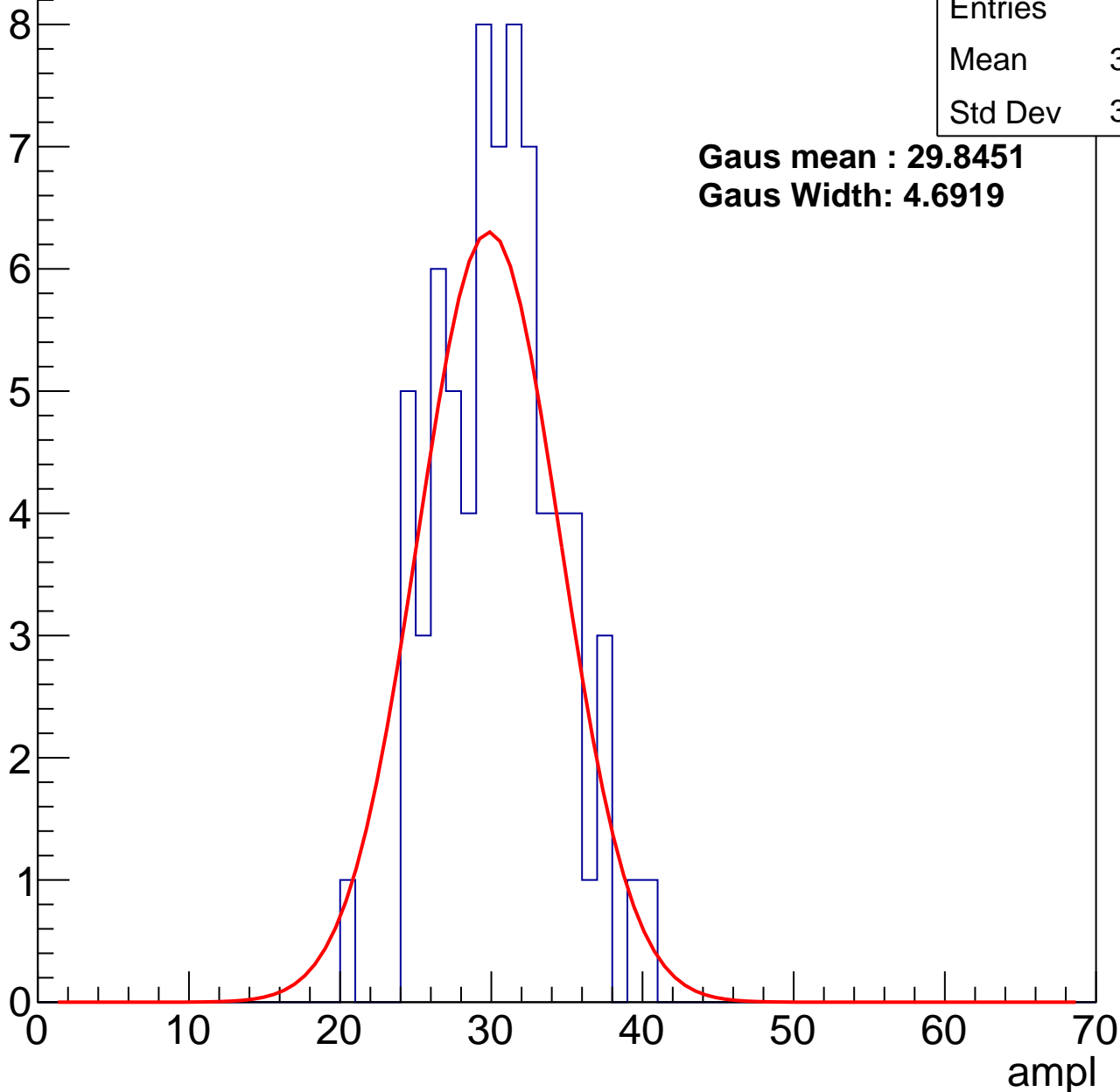
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	30.08
Std Dev	3.943

Gaus mean : 29.8451

Gaus Width: 4.6919



B0L001S, U14-ch108, adc1

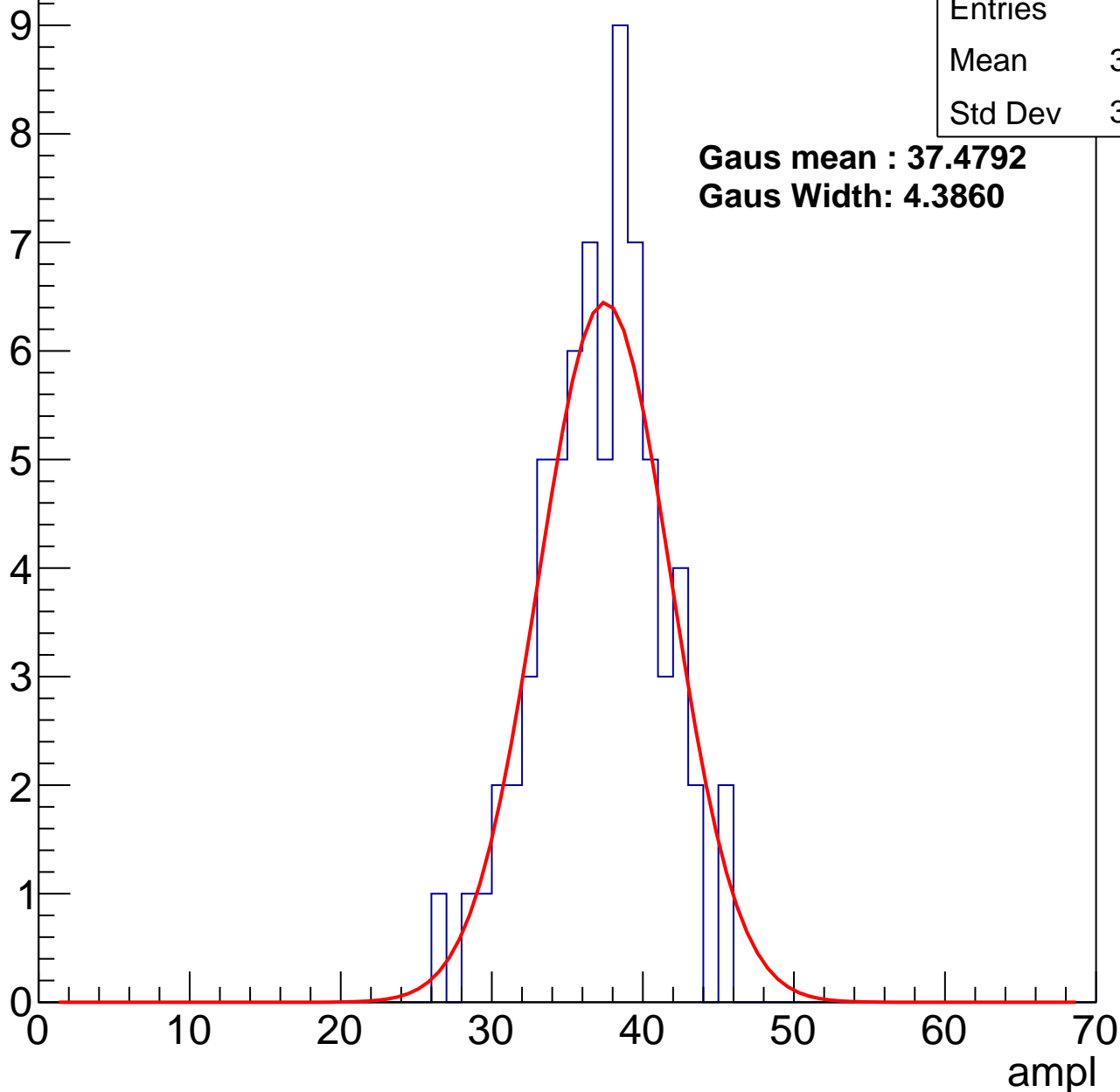
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	36.64
Std Dev	3.943

Gaus mean : 37.4792

Gaus Width: 4.3860

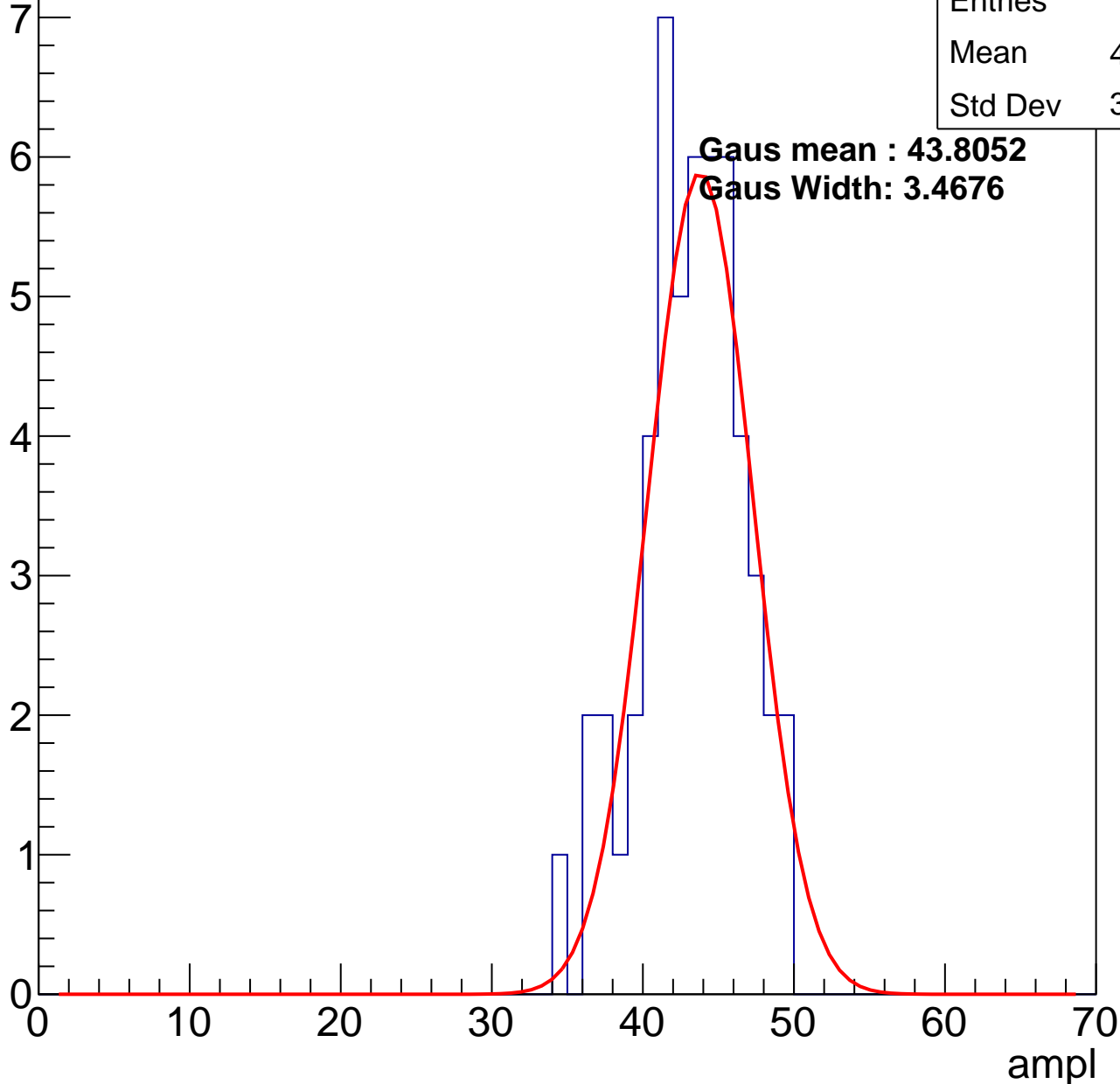


B0L001S, U14-ch108, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	42.72
Std Dev	3.389

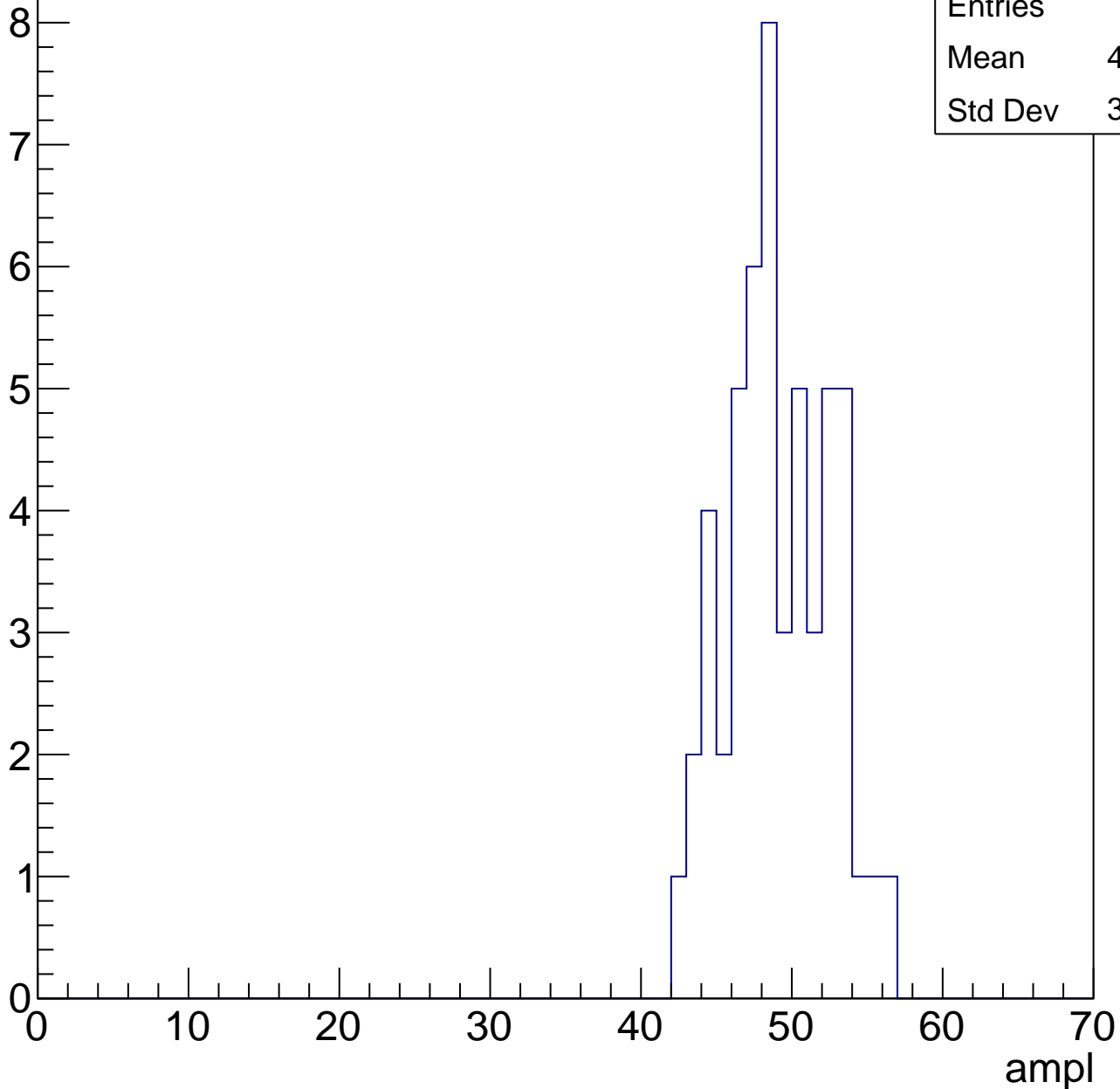


B0L001S, U14-ch108, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	48.65
Std Dev	3.328

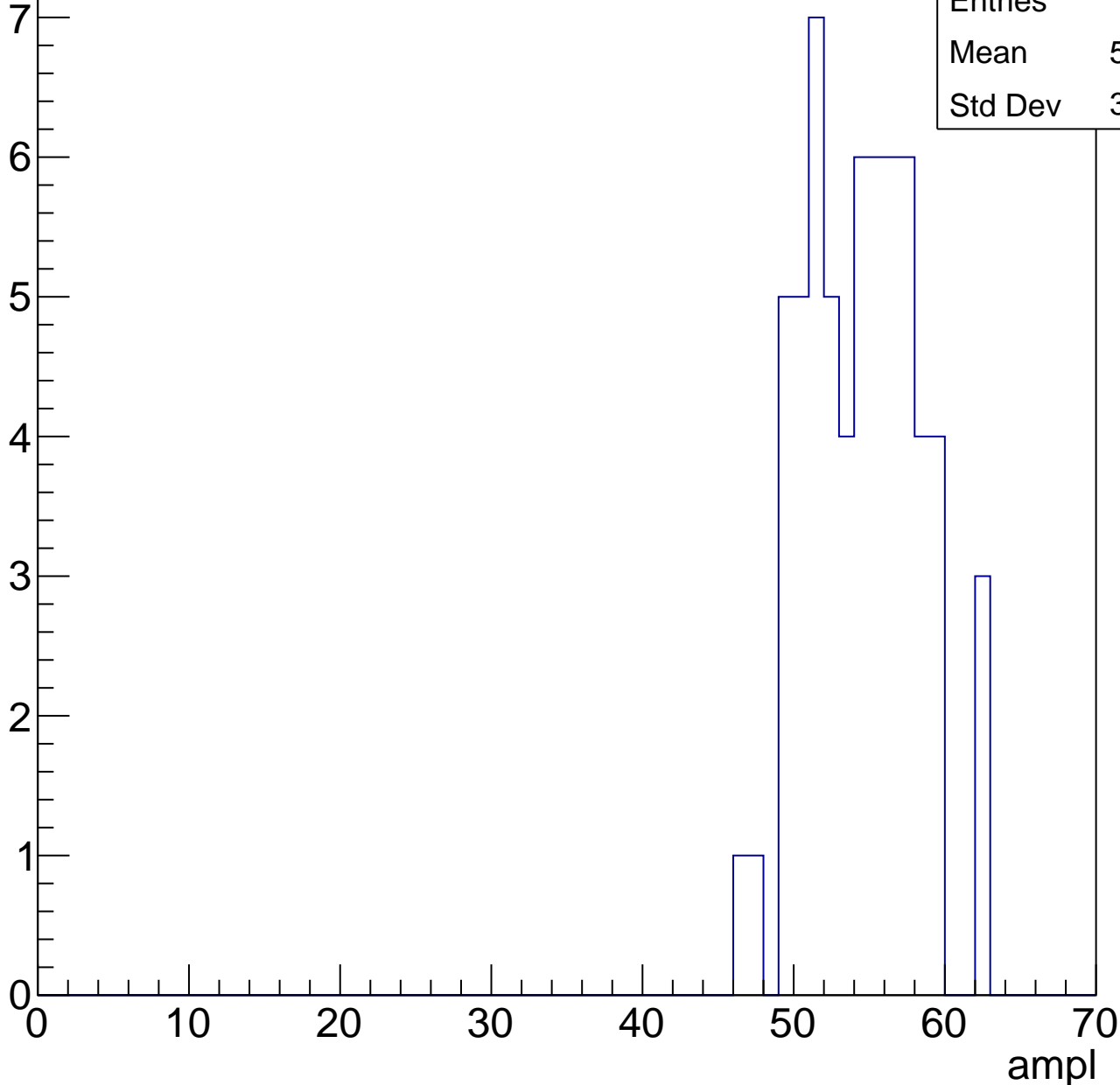


B0L001S, U14-ch108, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	54.02
Std Dev	3.662

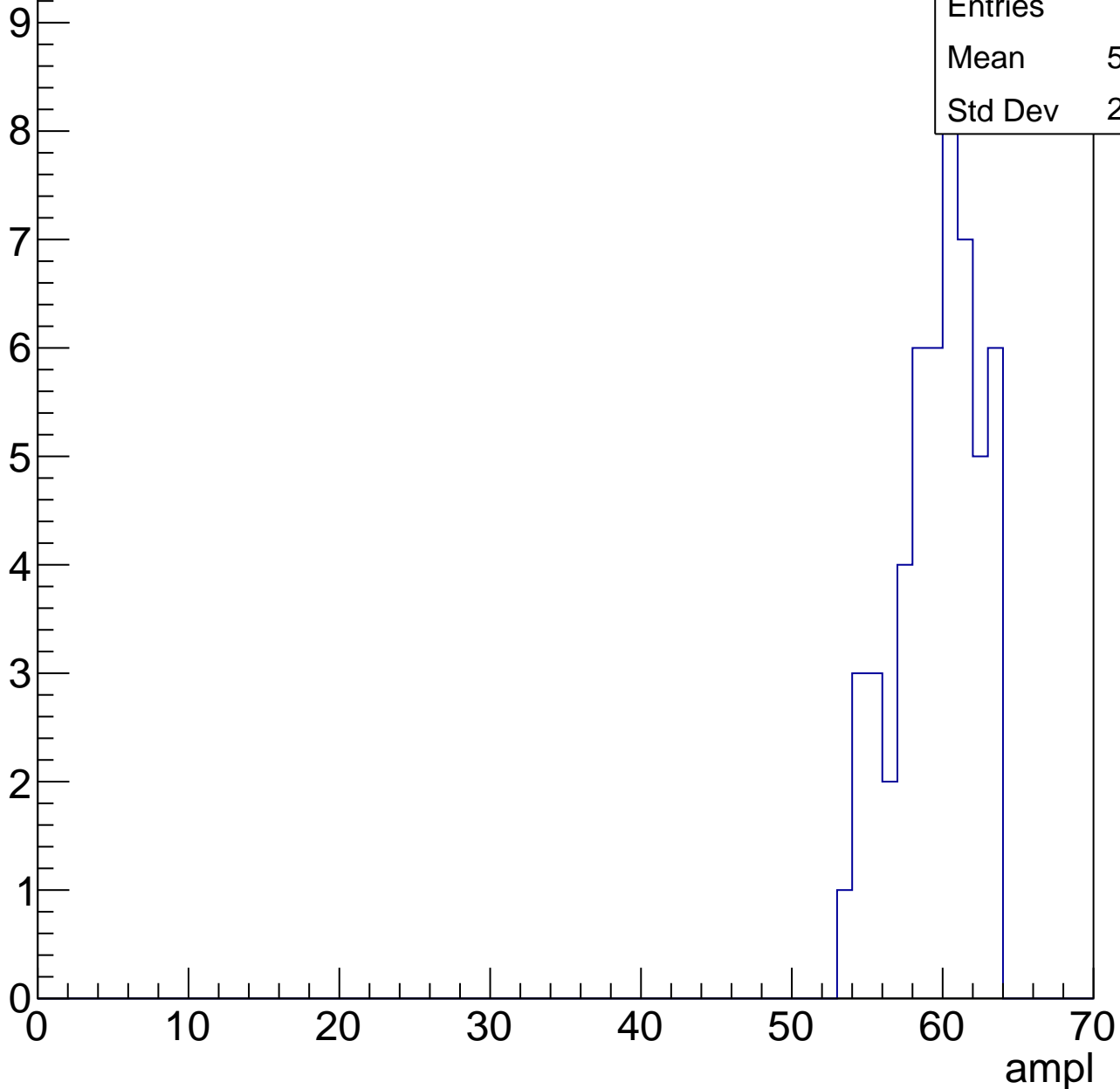


B0L001S, U14-ch108, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

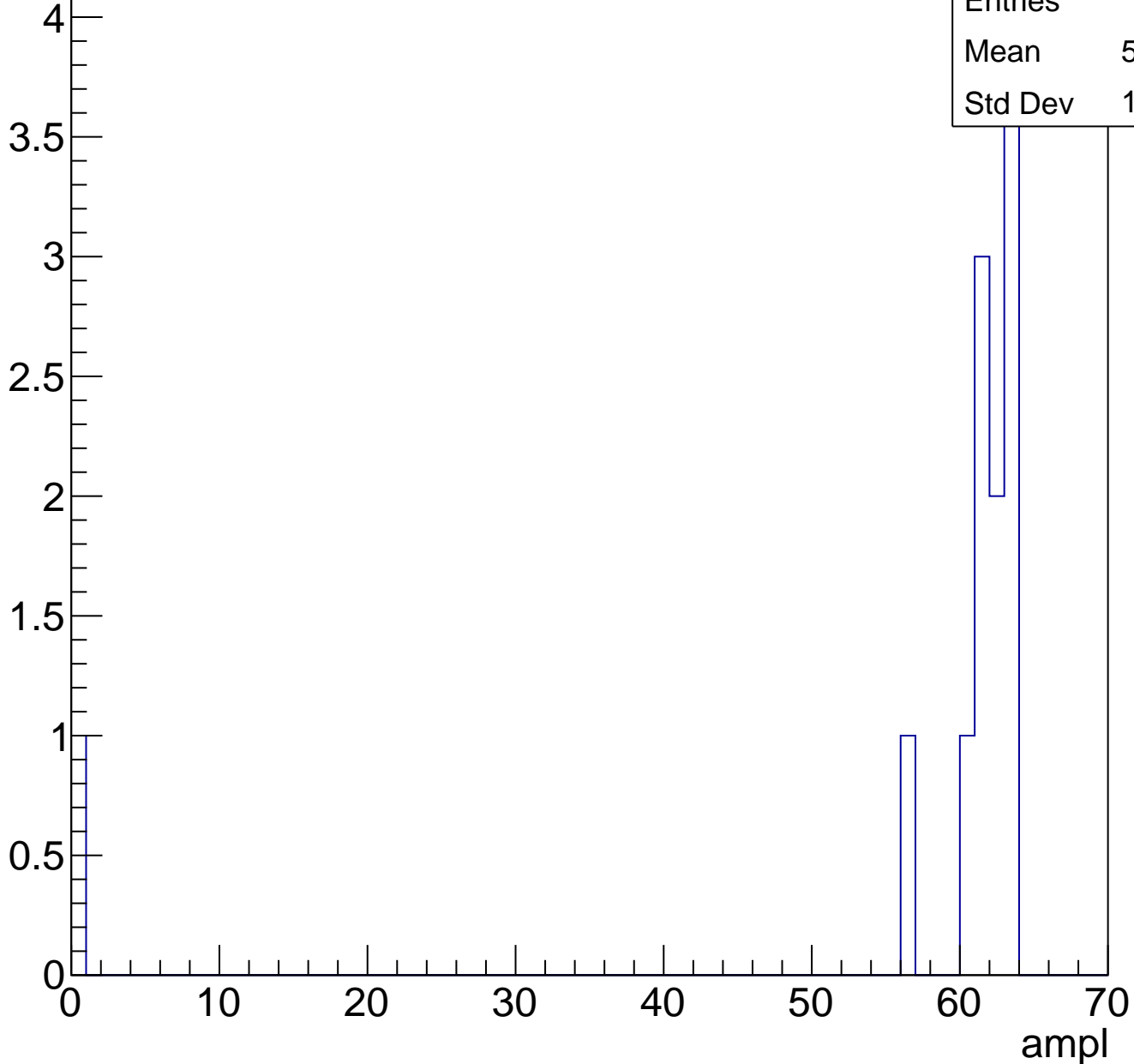
Entries	52
Mean	59.17
Std Dev	2.687



B0L001S, U14-ch108, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch108, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch109, adc0

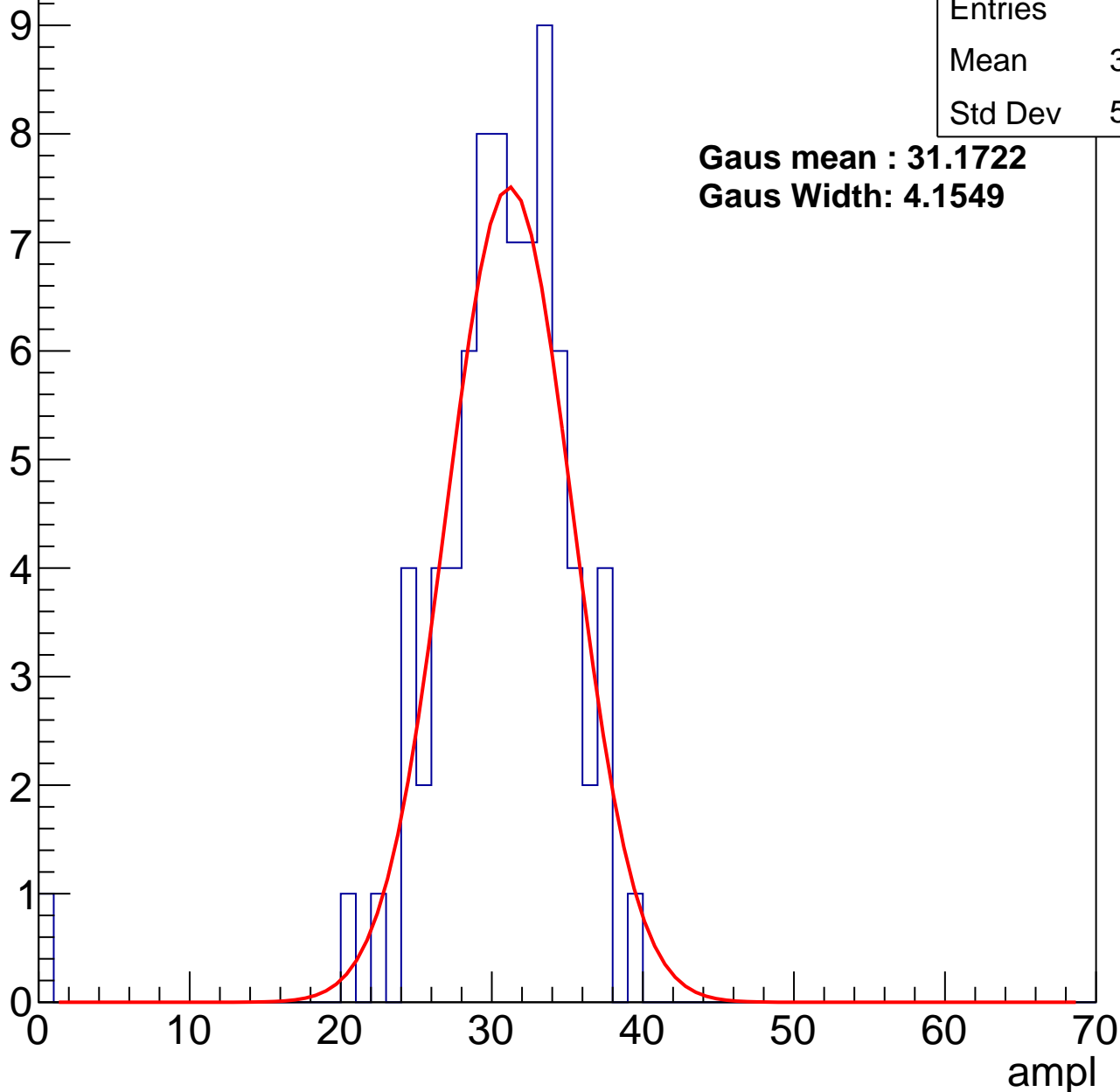
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	79
Mean	30.14
Std Dev	5.093

Gaus mean : 31.1722

Gaus Width: 4.1549



B0L001S, U14-ch109, adc1

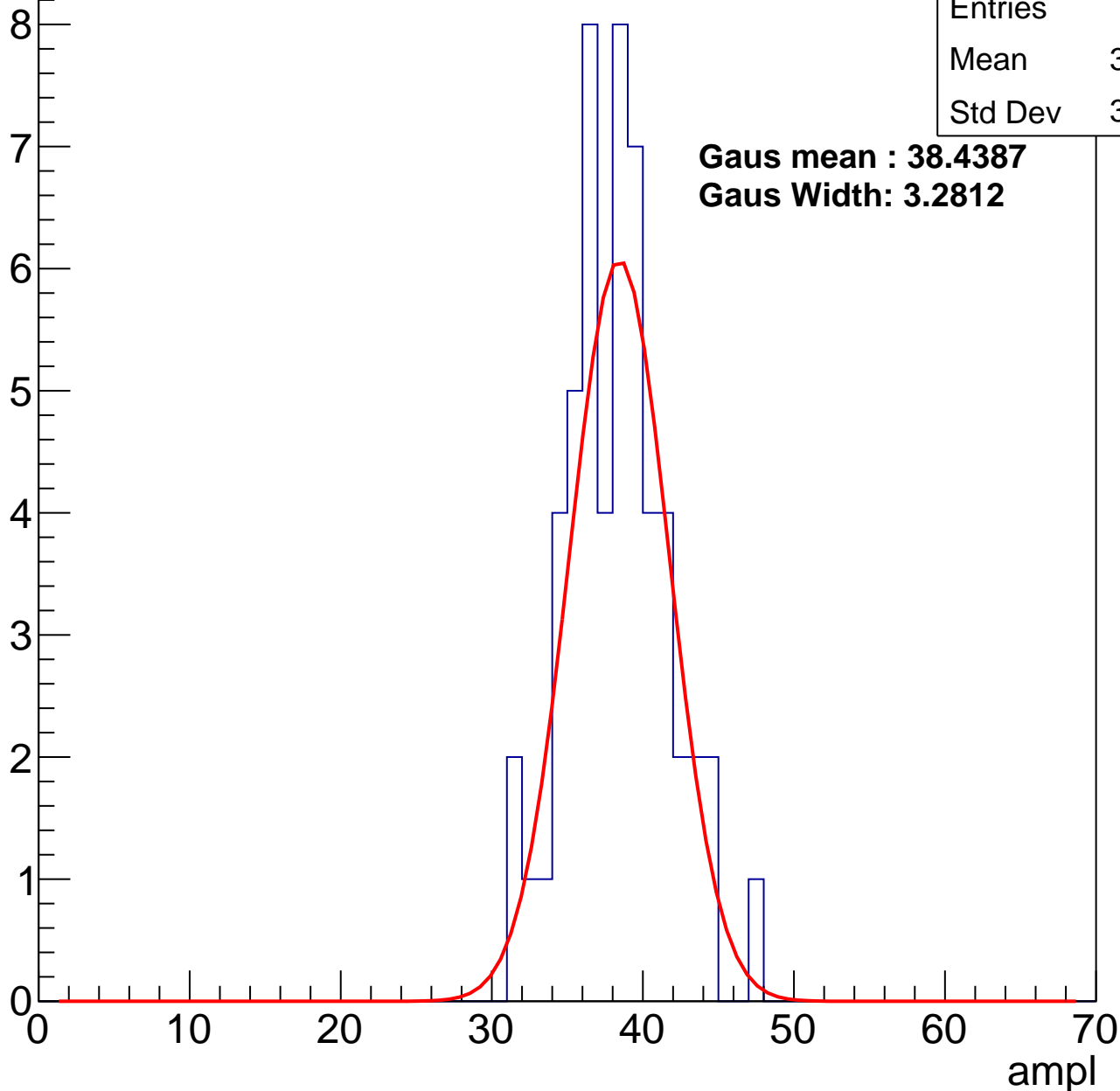
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	37.82
Std Dev	3.292

Gaus mean : 38.4387

Gaus Width: 3.2812



B0L001S, U14-ch109, adc2

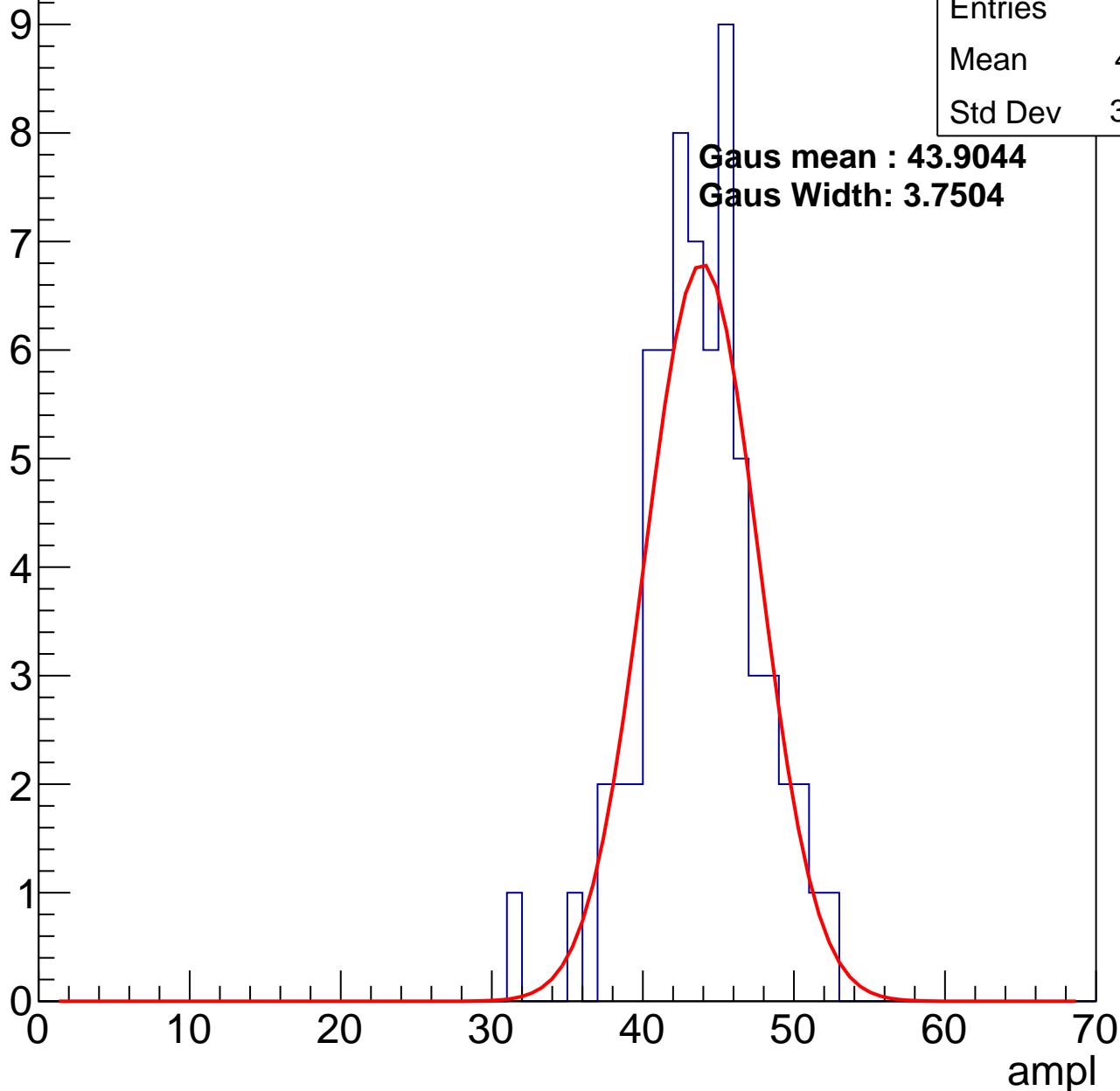
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	43.31
Std Dev	3.798

Gaus mean : 43.9044

Gaus Width: 3.7504

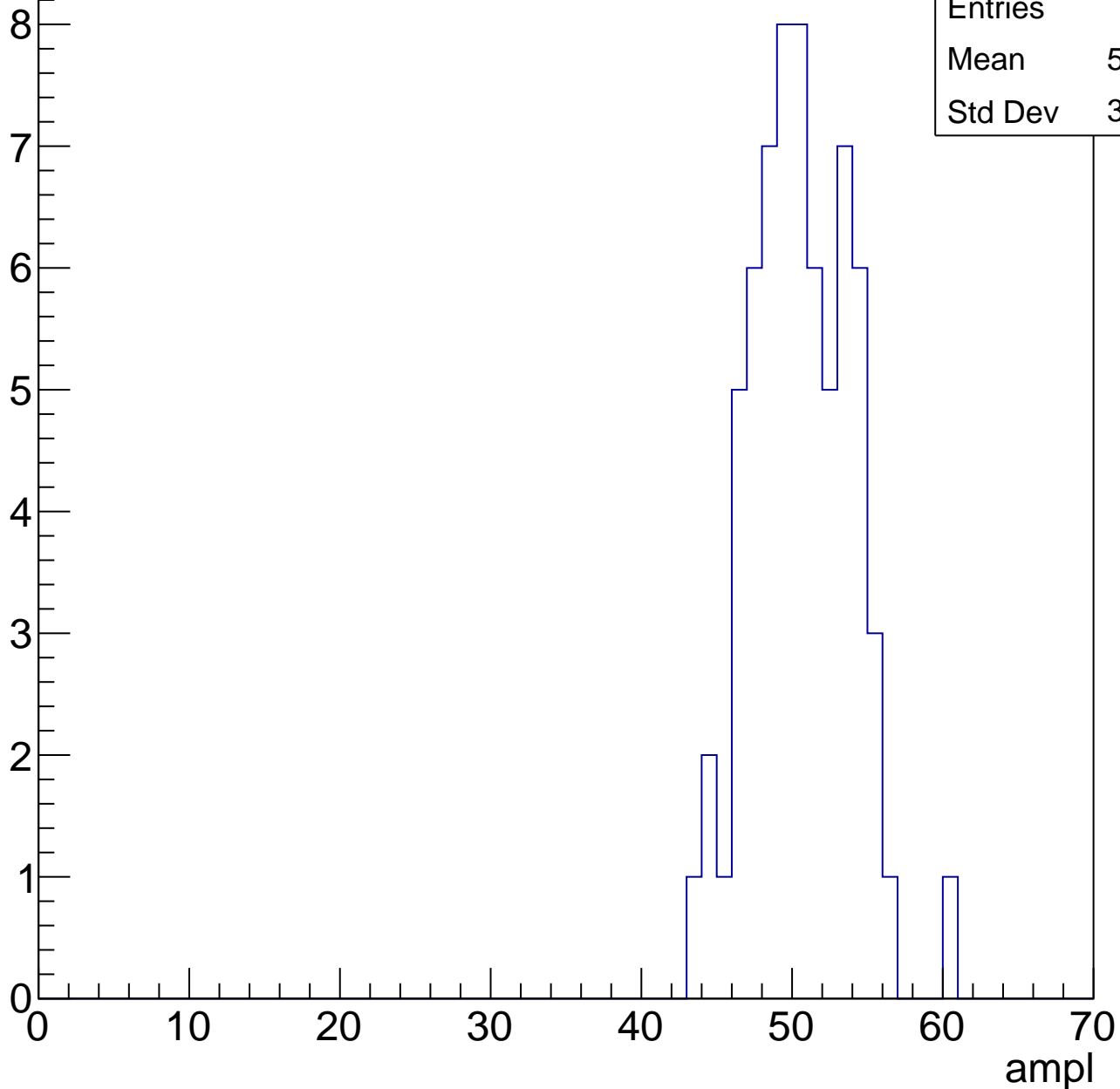


B0L001S, U14-ch109, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	50.12
Std Dev	3.258



B0L001S, U14-ch109, adc4

calib_packv5_042523_0143.root, FC#9, port A1

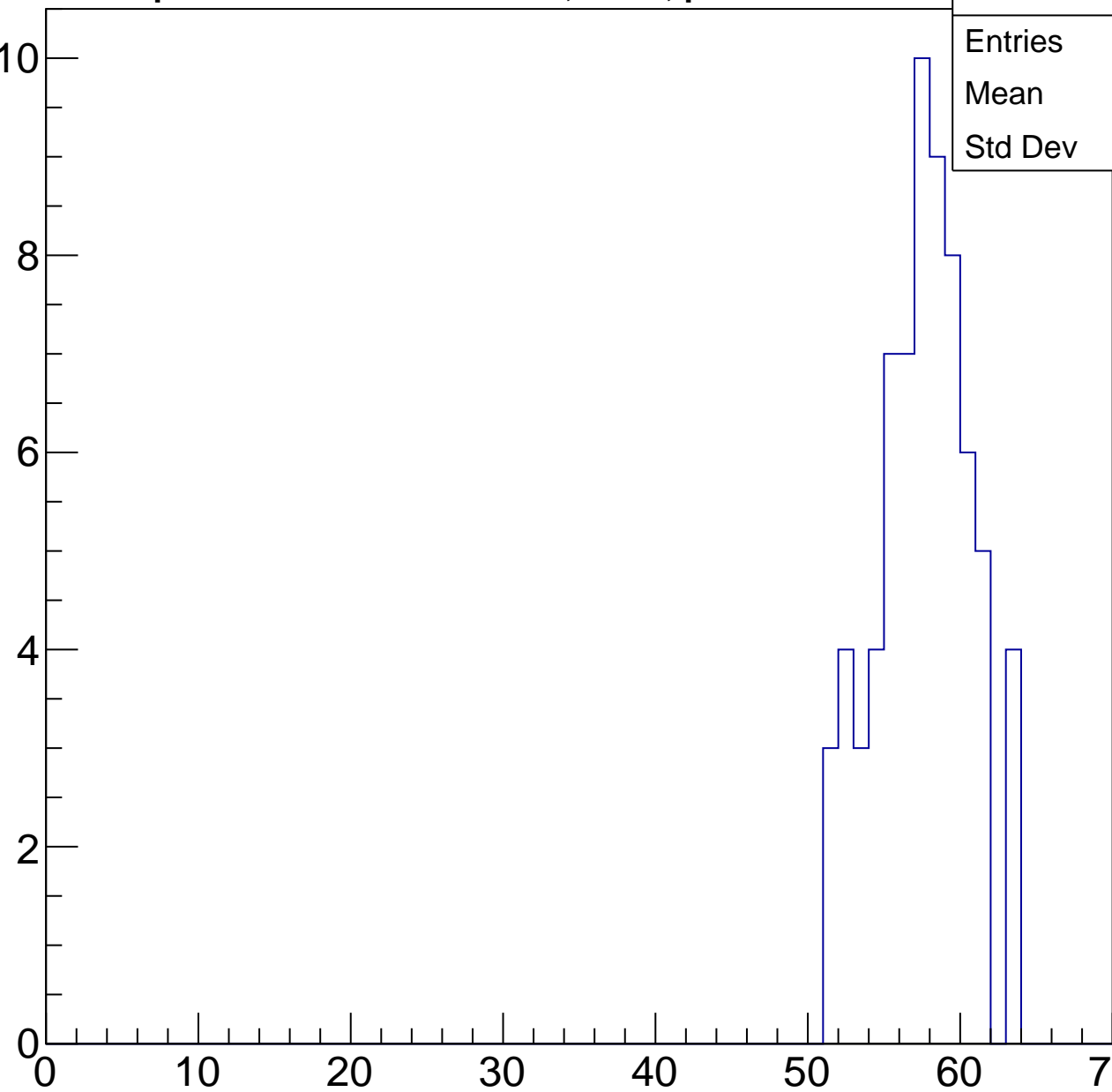
Entries	70
Mean	57.06
Std Dev	3.037

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

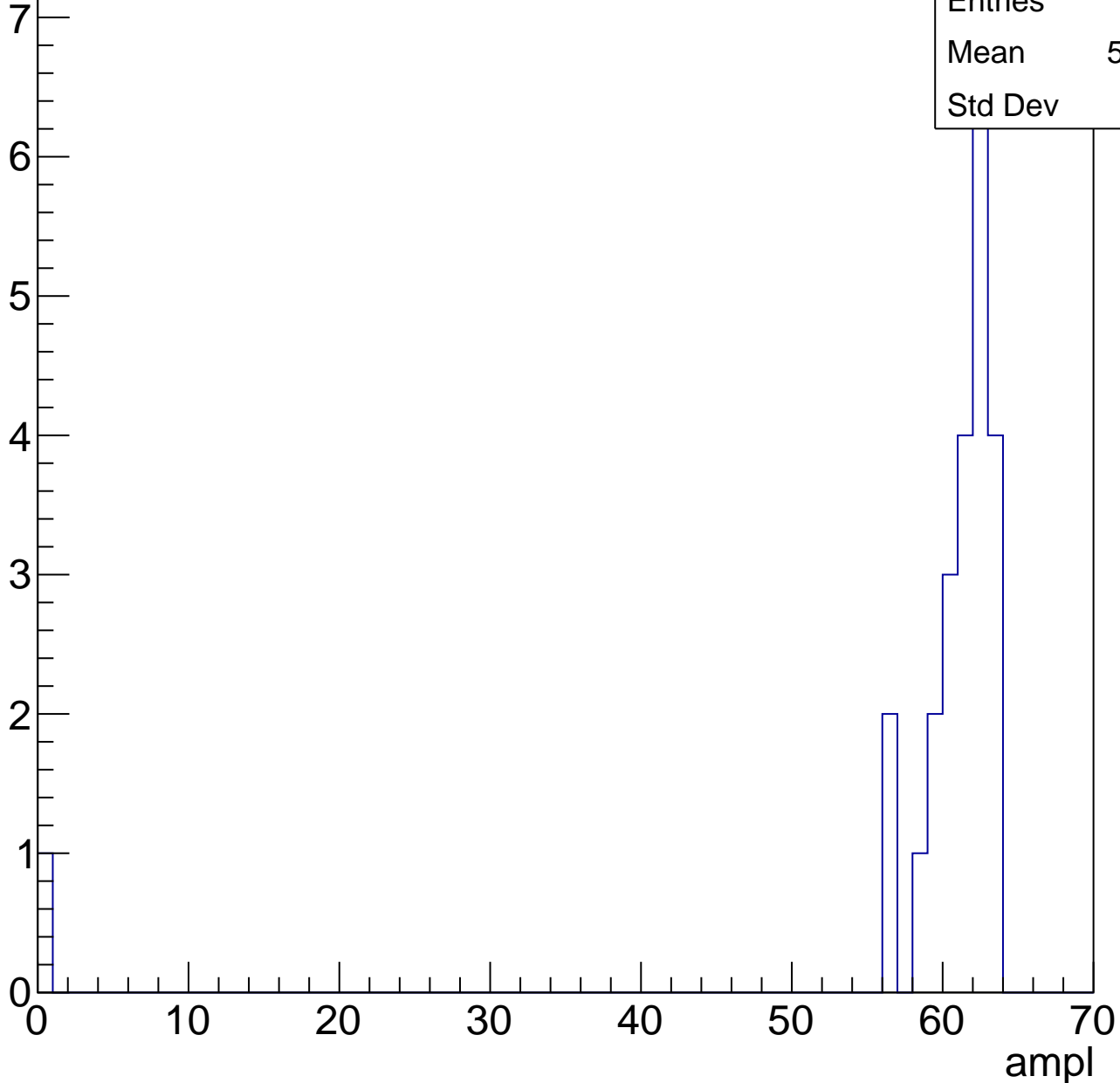


B0L001S, U14-ch109, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

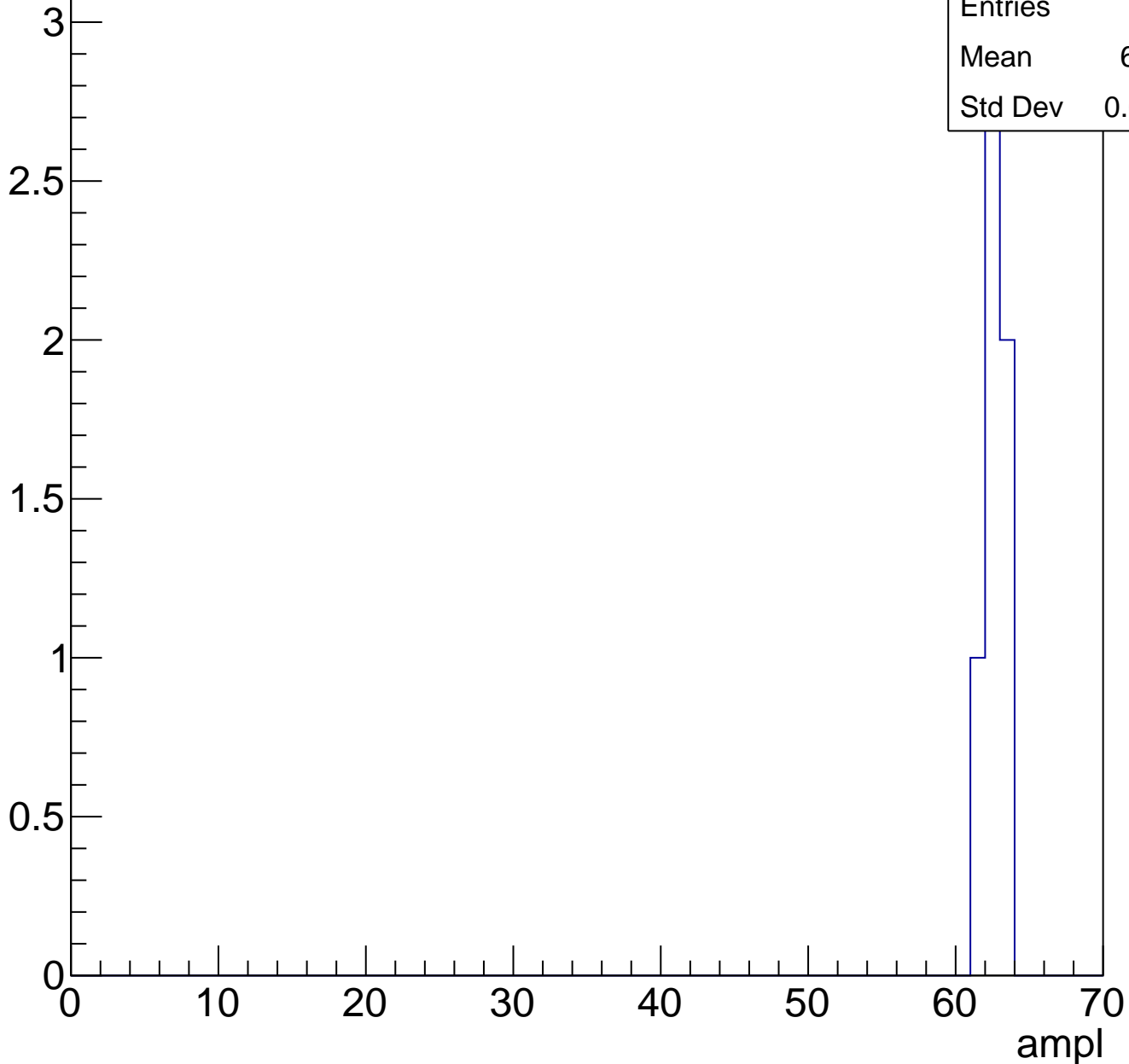
Entries	24
Mean	58.25
Std Dev	12.3



B0L001S, U14-ch109, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch109, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch110, adc0

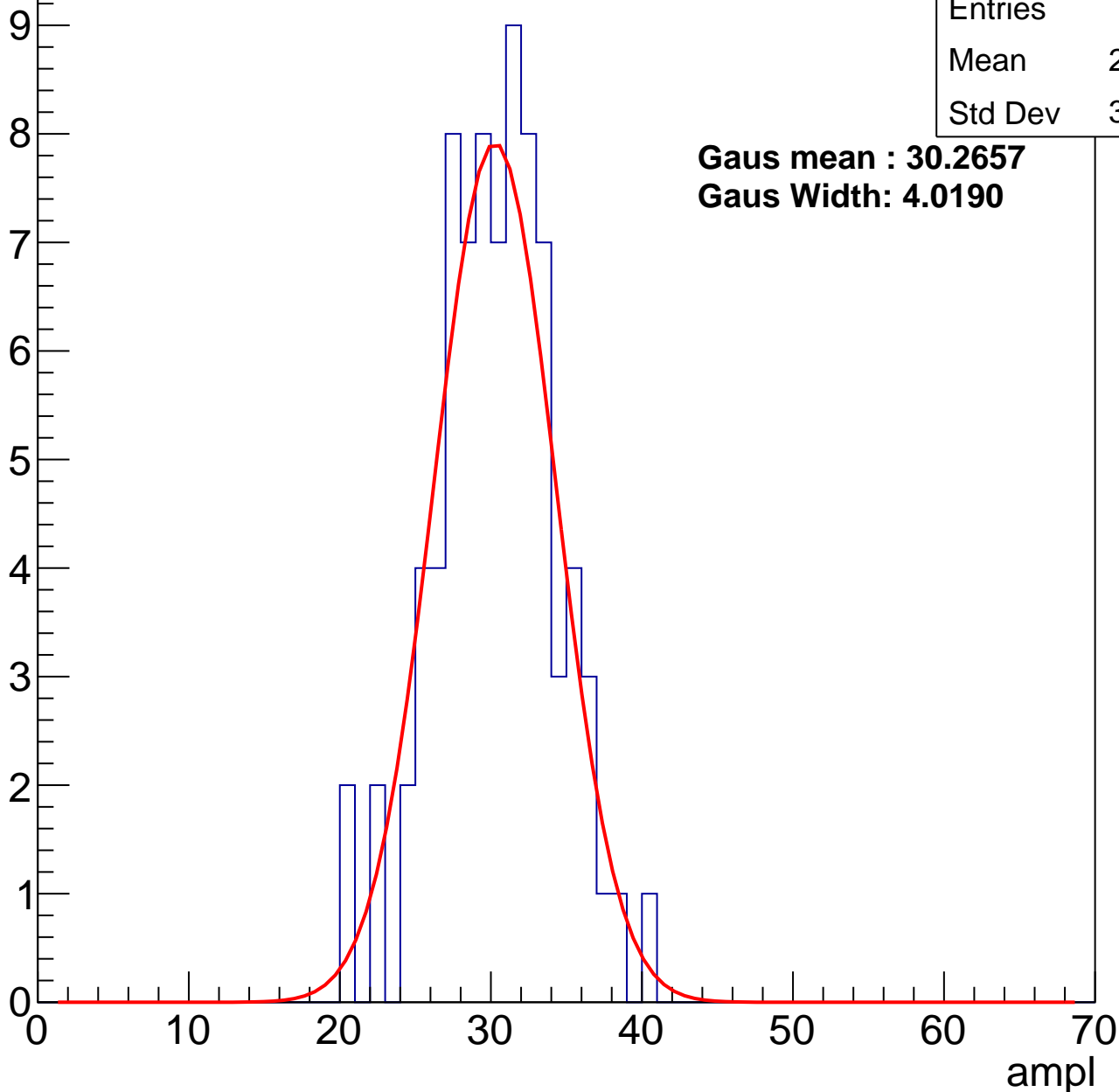
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	29.89
Std Dev	3.919

Gaus mean : 30.2657

Gaus Width: 4.0190



B0L001S, U14-ch110, adc1

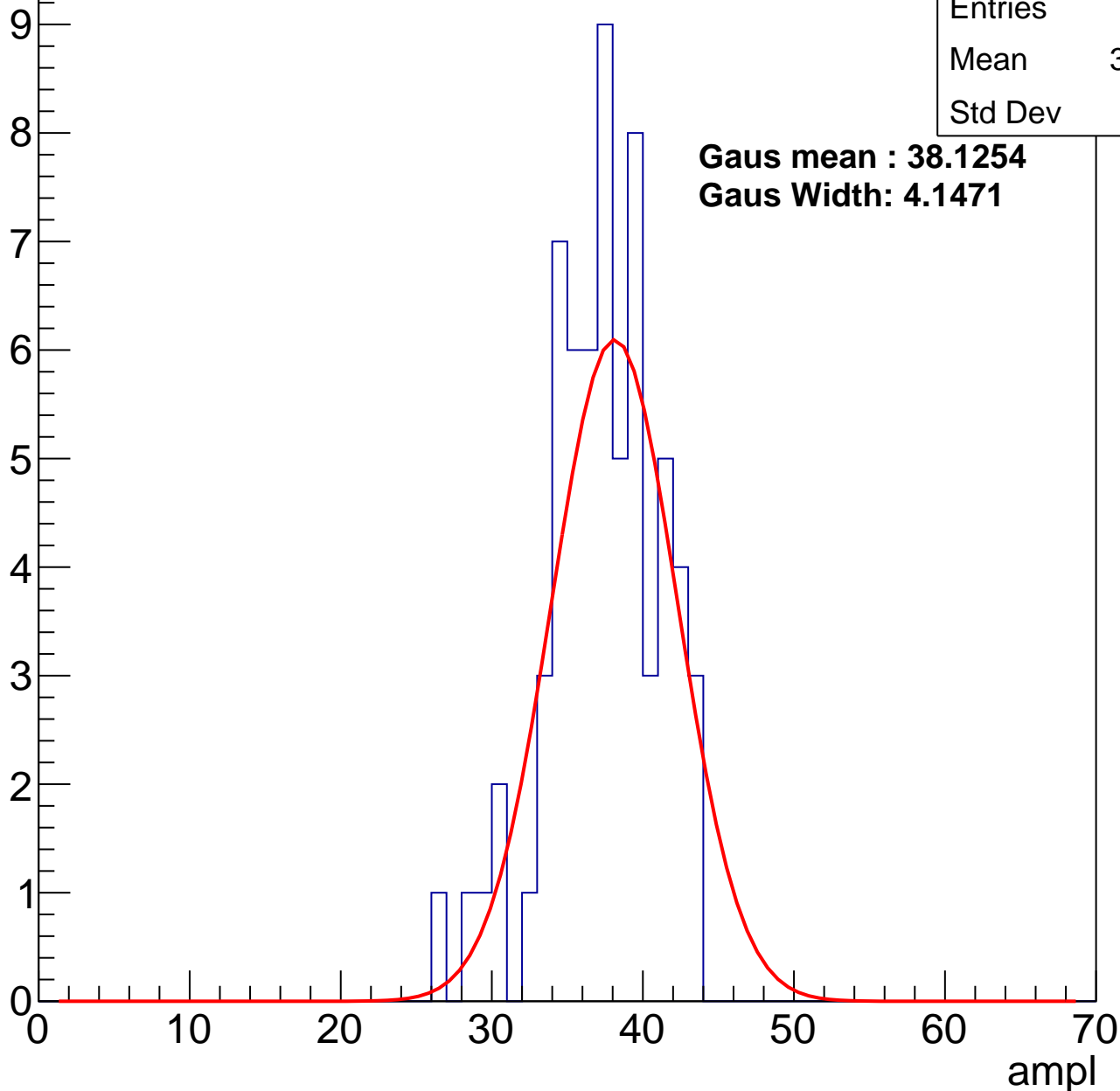
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	36.85
Std Dev	3.68

Gaus mean : 38.1254

Gaus Width: 4.1471



B0L001S, U14-ch110, adc2

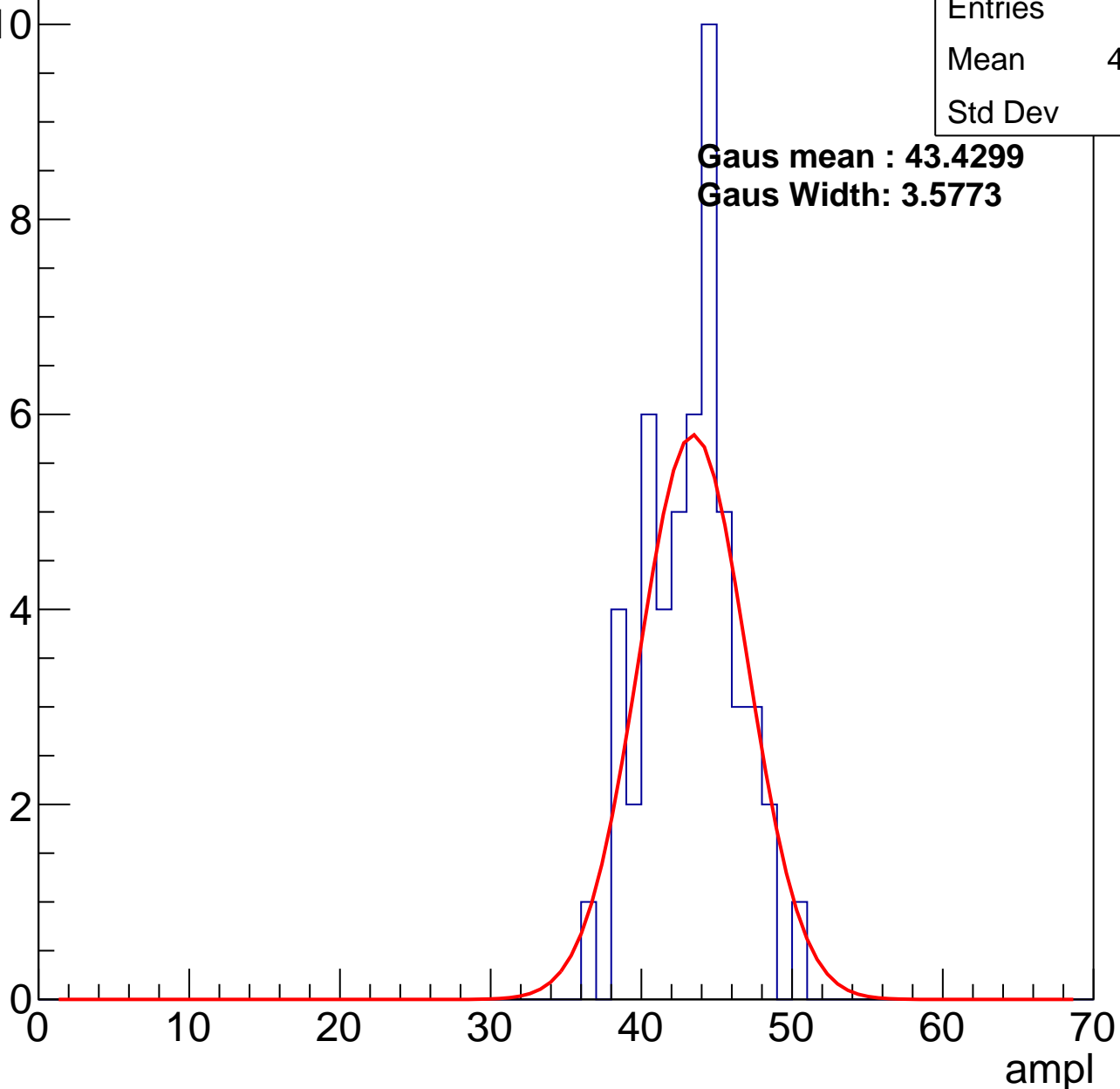
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	42.85
Std Dev	2.97

Gaus mean : 43.4299

Gaus Width: 3.5773

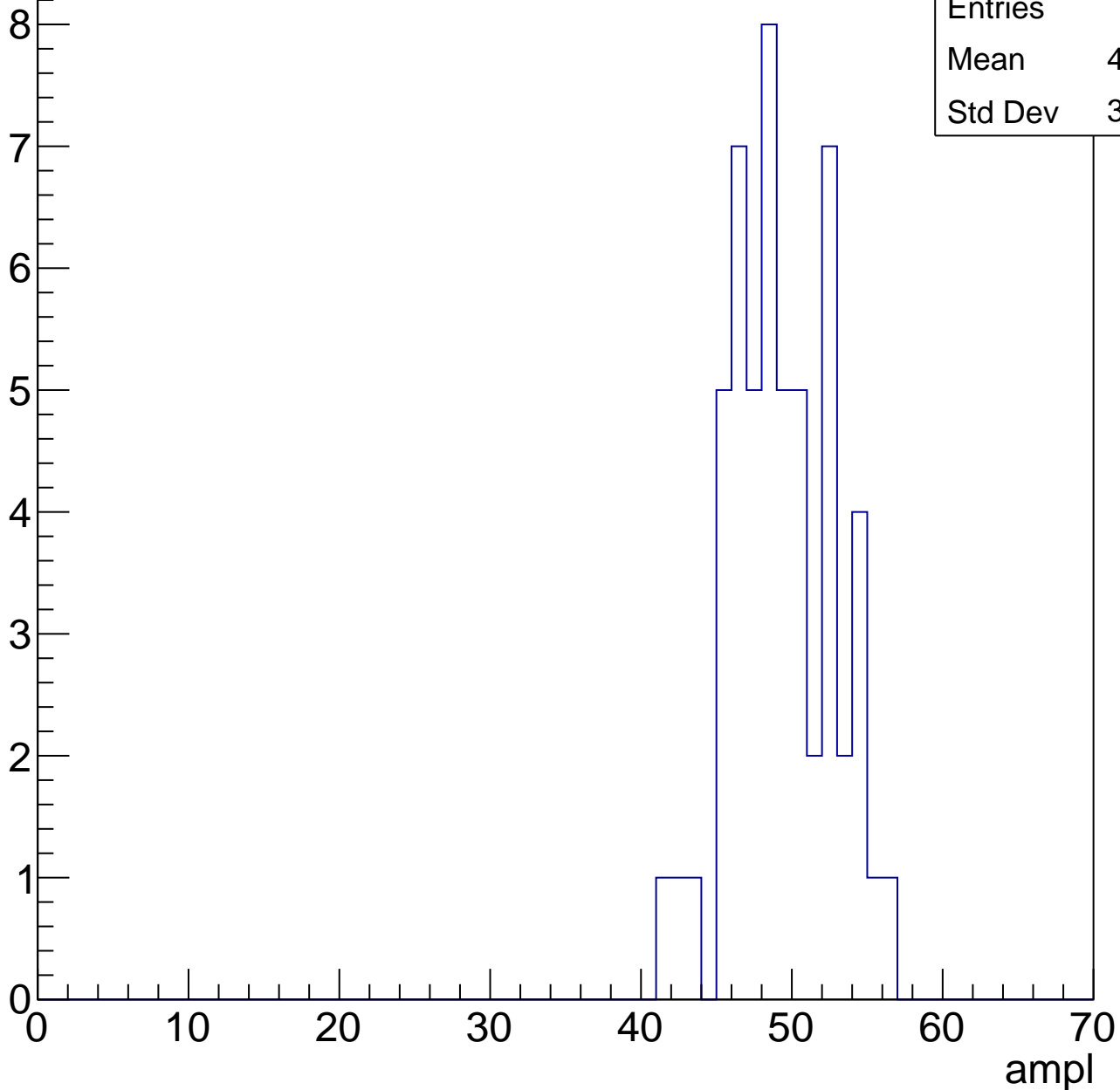


B0L001S, U14-ch110, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	48.84
Std Dev	3.334

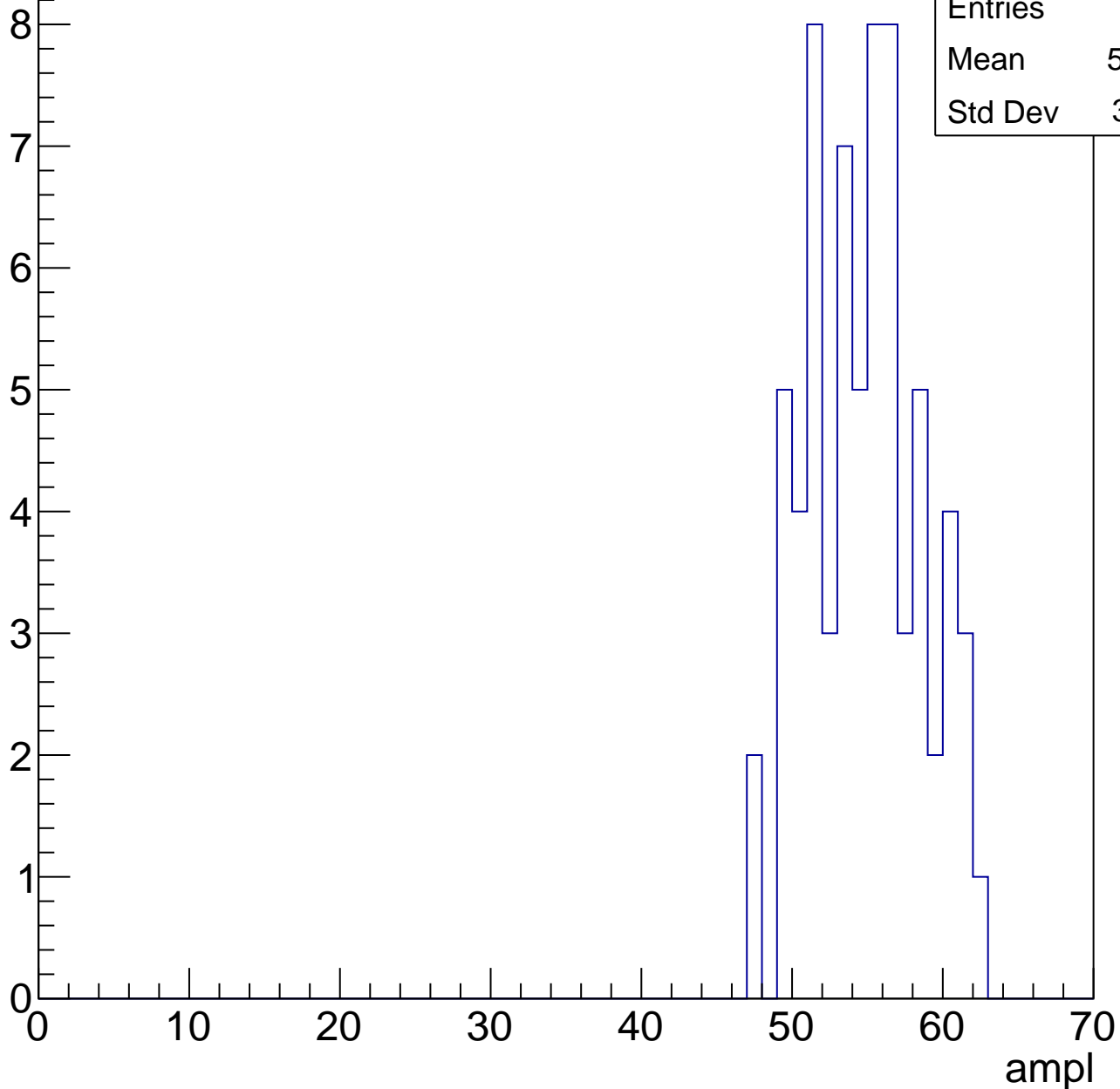


B0L001S, U14-ch110, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

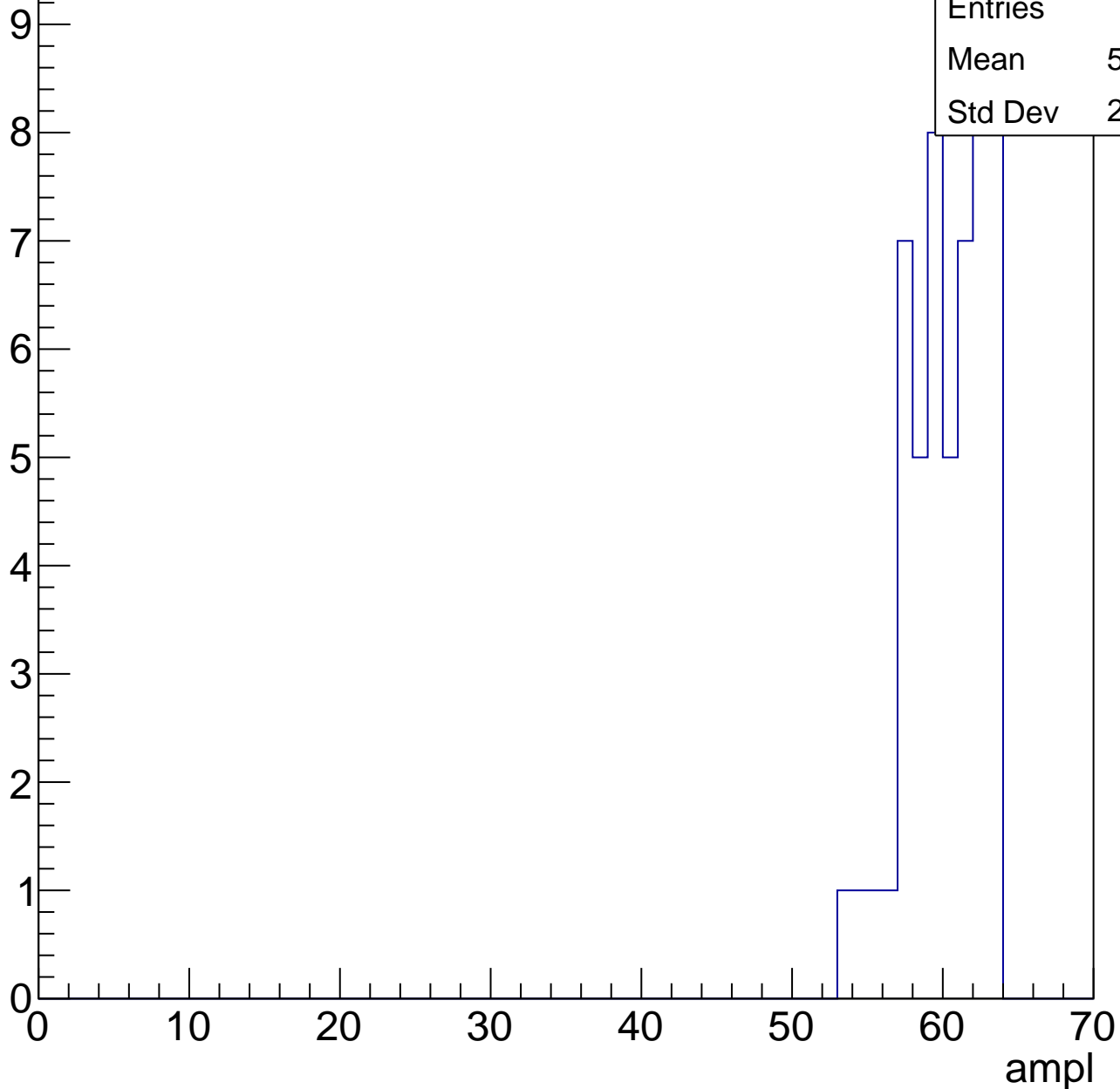
Entries	68
Mean	54.35
Std Dev	3.681



B0L001S, U14-ch110, adc5

calib_packv5_042523_0143.root, FC#9, port A1

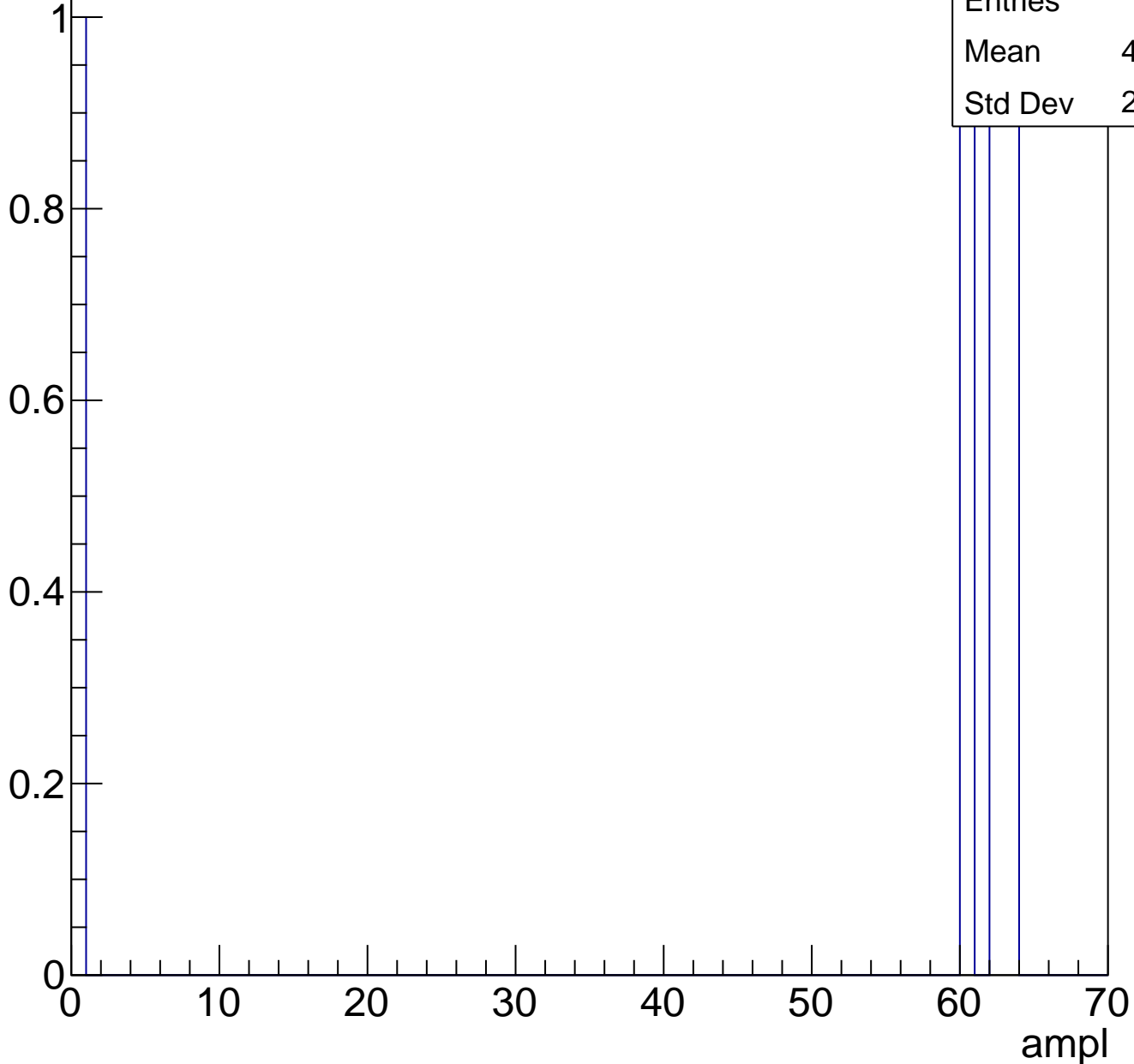
Entry



B0L001S, U14-ch110, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch110, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch111, adc0

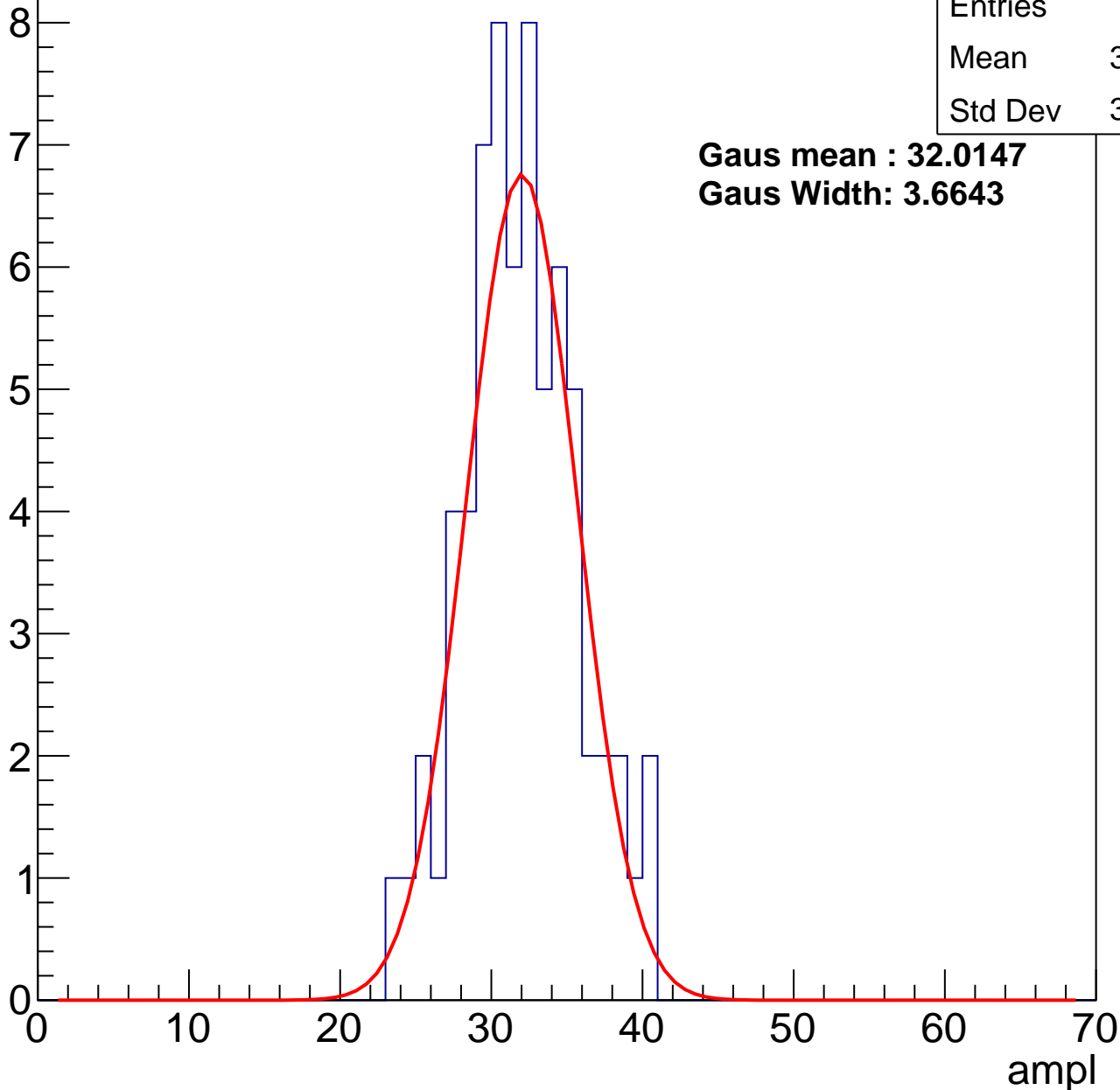
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	31.54
Std Dev	3.735

Gaus mean : 32.0147

Gaus Width: 3.6643



B0L001S, U14-ch111, adc1

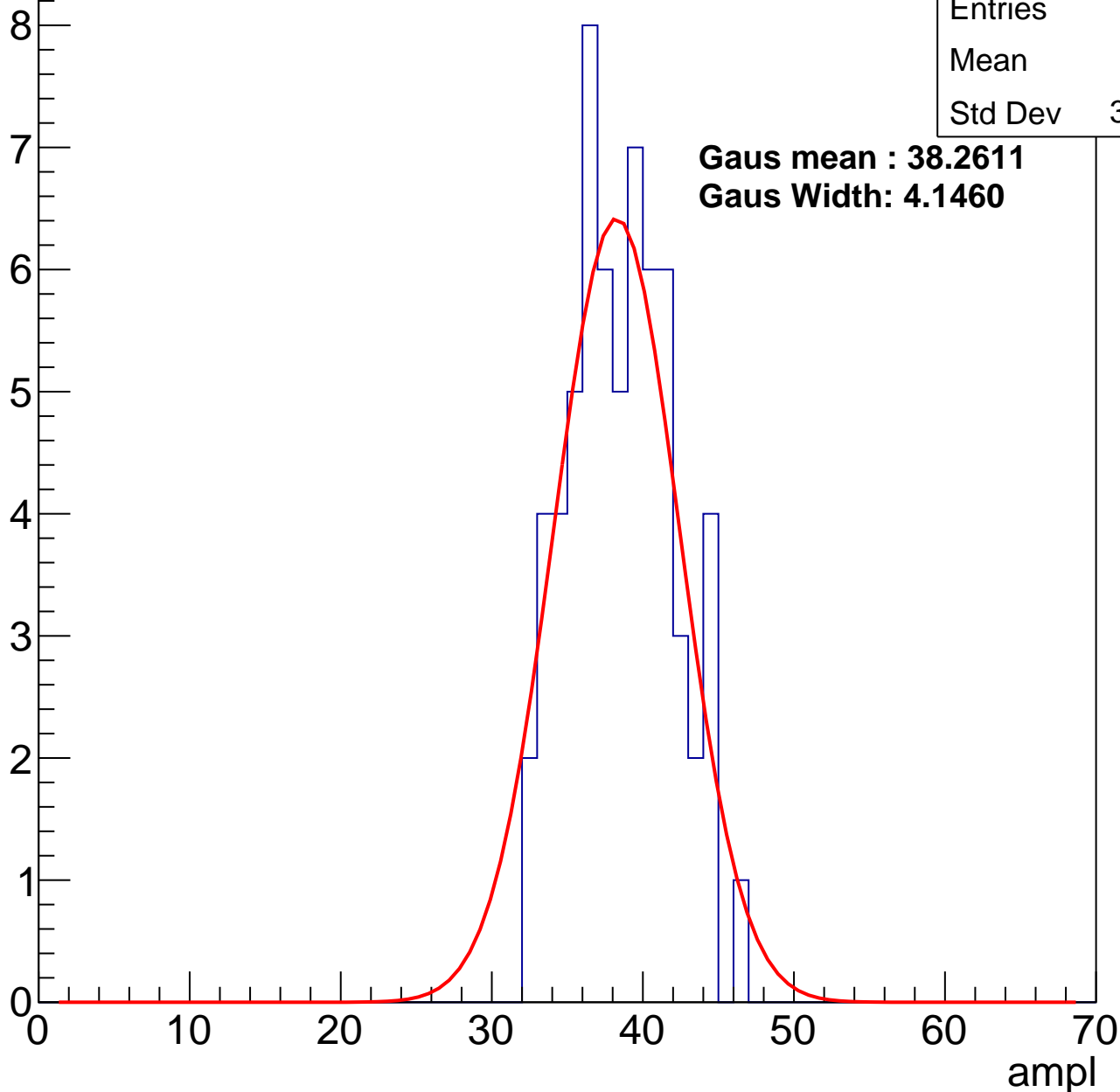
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	38.1
Std Dev	3.356

Gaus mean : 38.2611

Gaus Width: 4.1460



B0L001S, U14-ch111, adc2

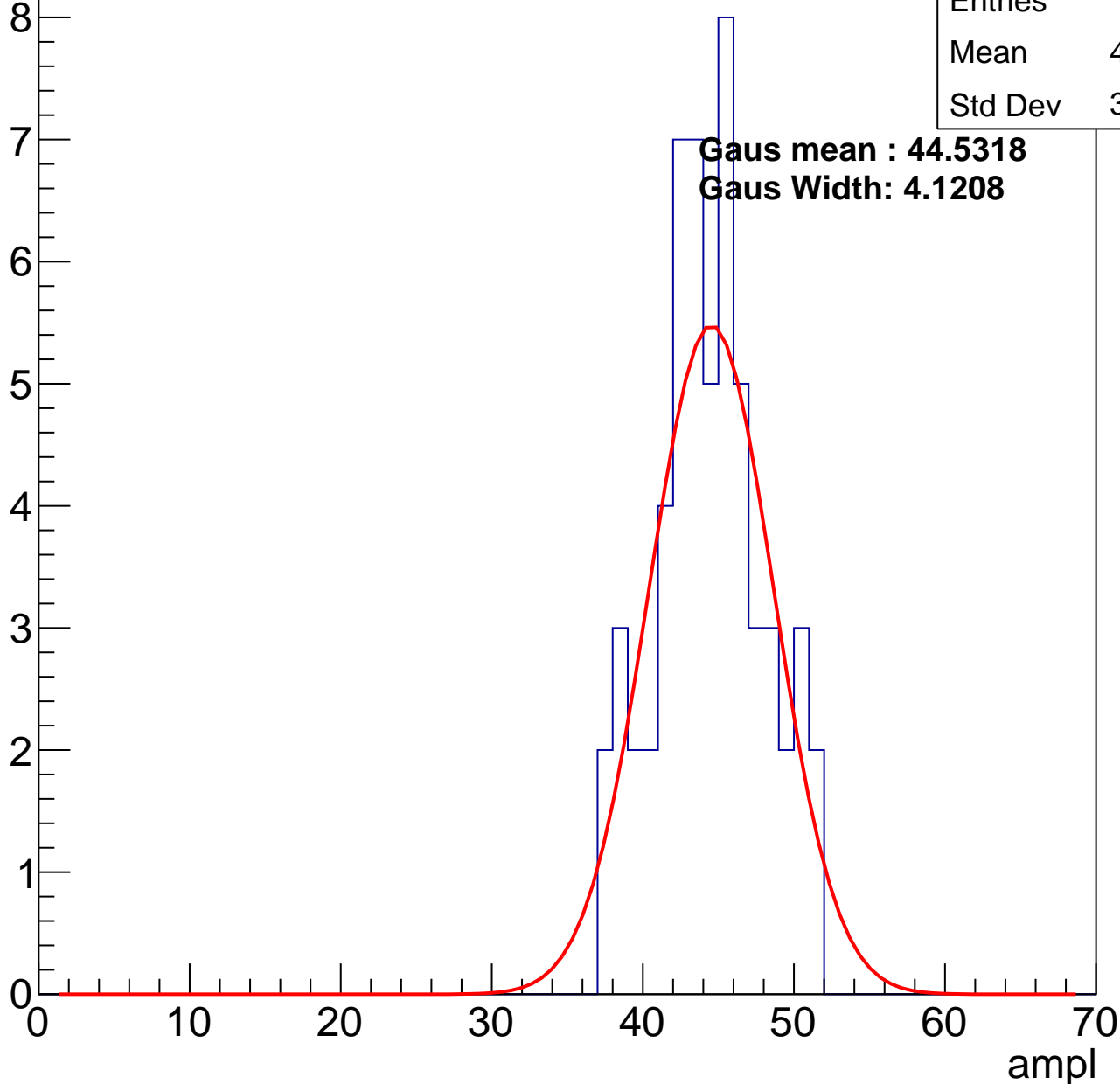
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.97
Std Dev	3.518

Gaus mean : 44.5318

Gaus Width: 4.1208

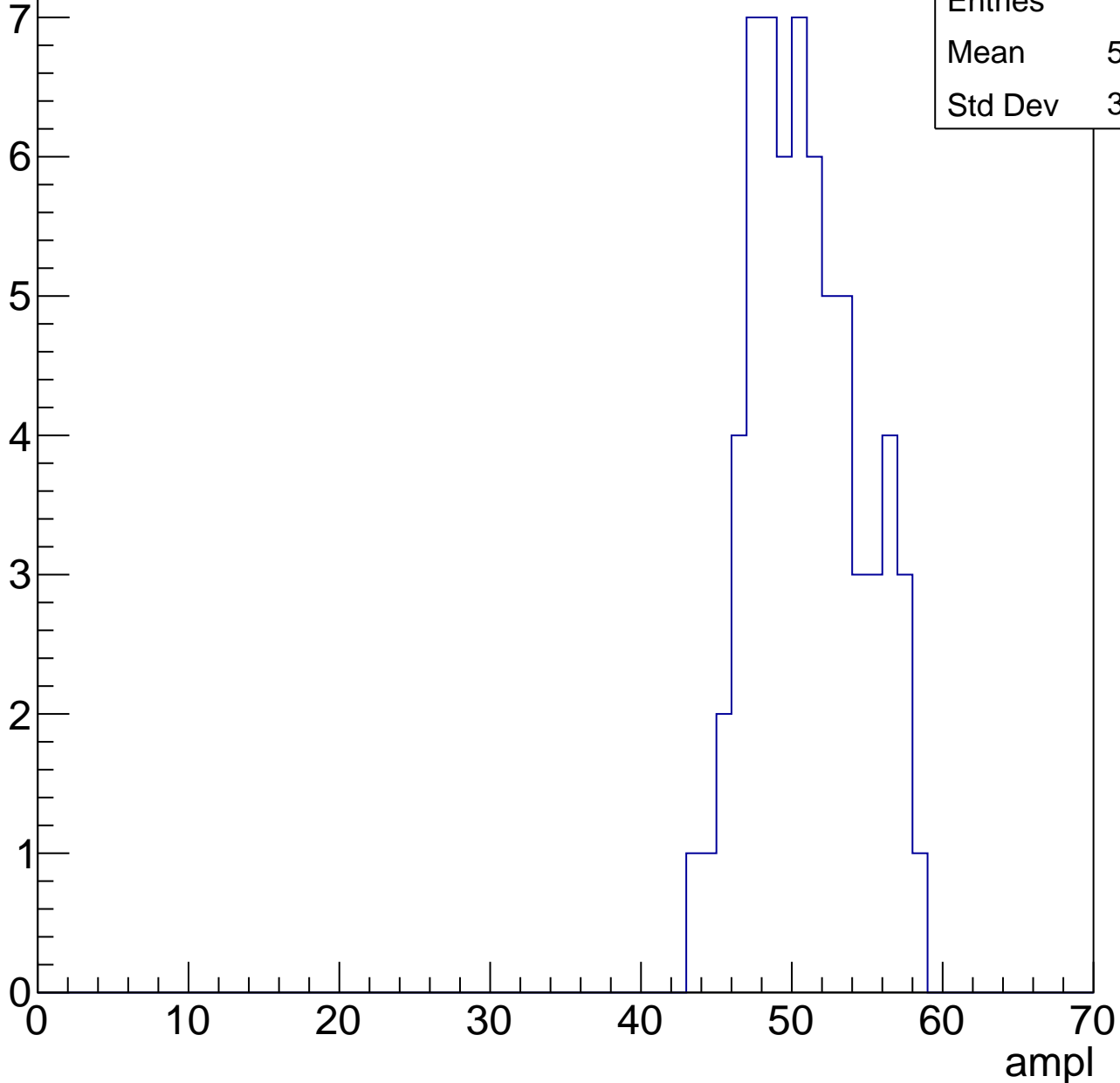


B0L001S, U14-ch111, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	50.48
Std Dev	3.574

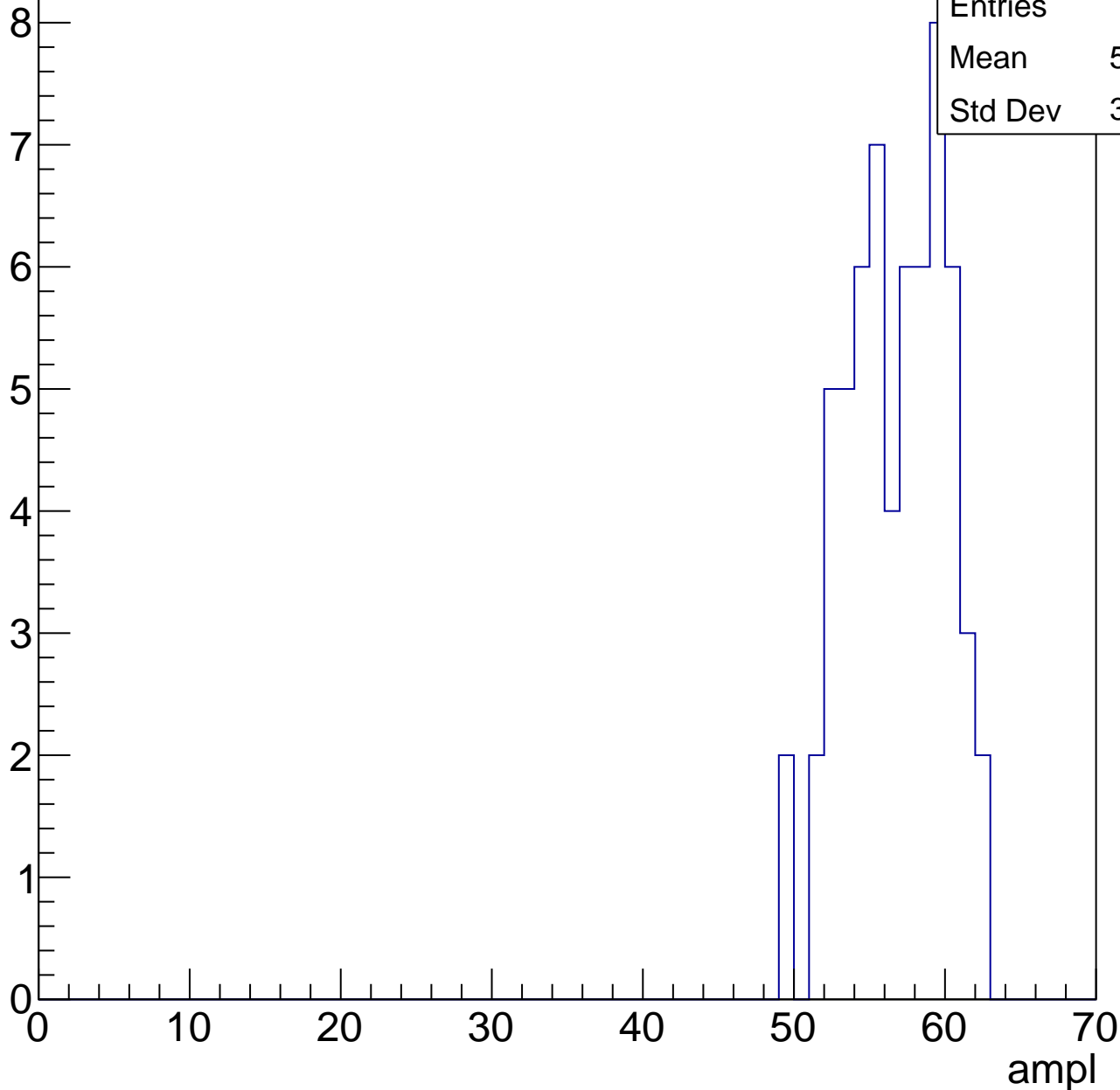


B0L001S, U14-ch111, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	56.24
Std Dev	3.226

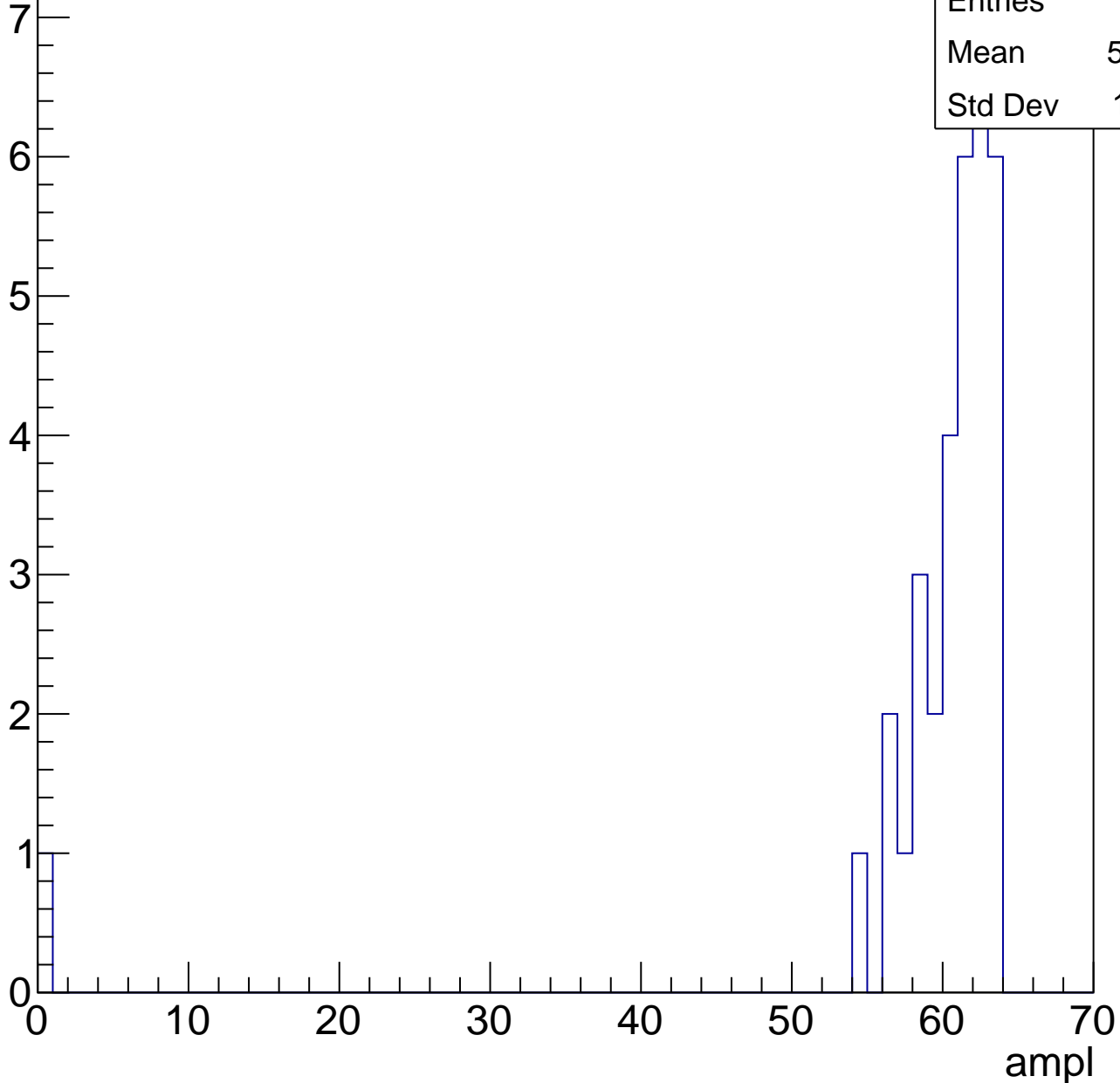


B0L001S, U14-ch111, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	33
Mean	58.58
Std Dev	10.61



B0L001S, U14-ch111, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch111, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch112, adc0

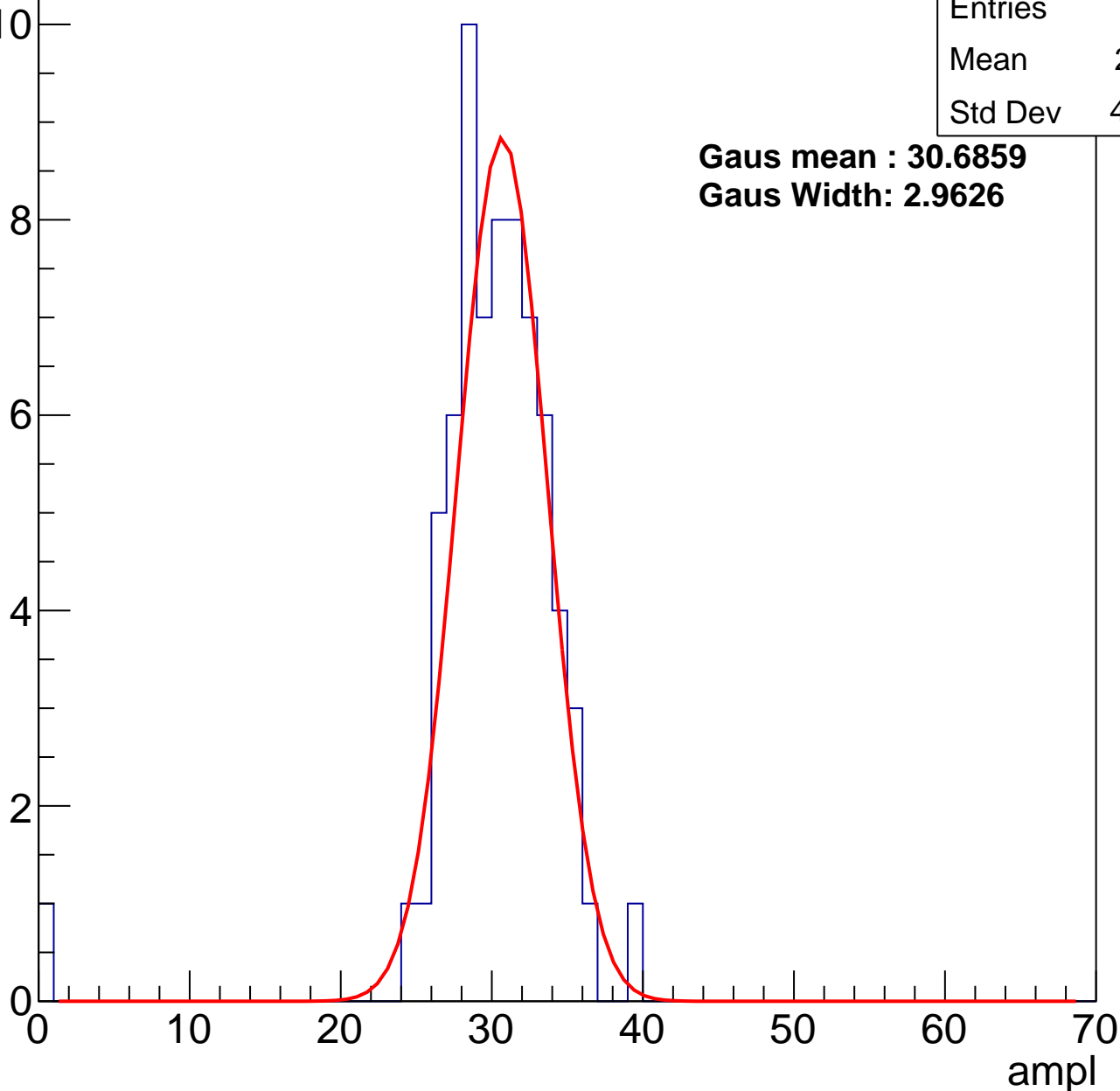
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	69
Mean	29.71
Std Dev	4.635

Gaus mean : 30.6859

Gaus Width: 2.9626



B0L001S, U14-ch112, adc1

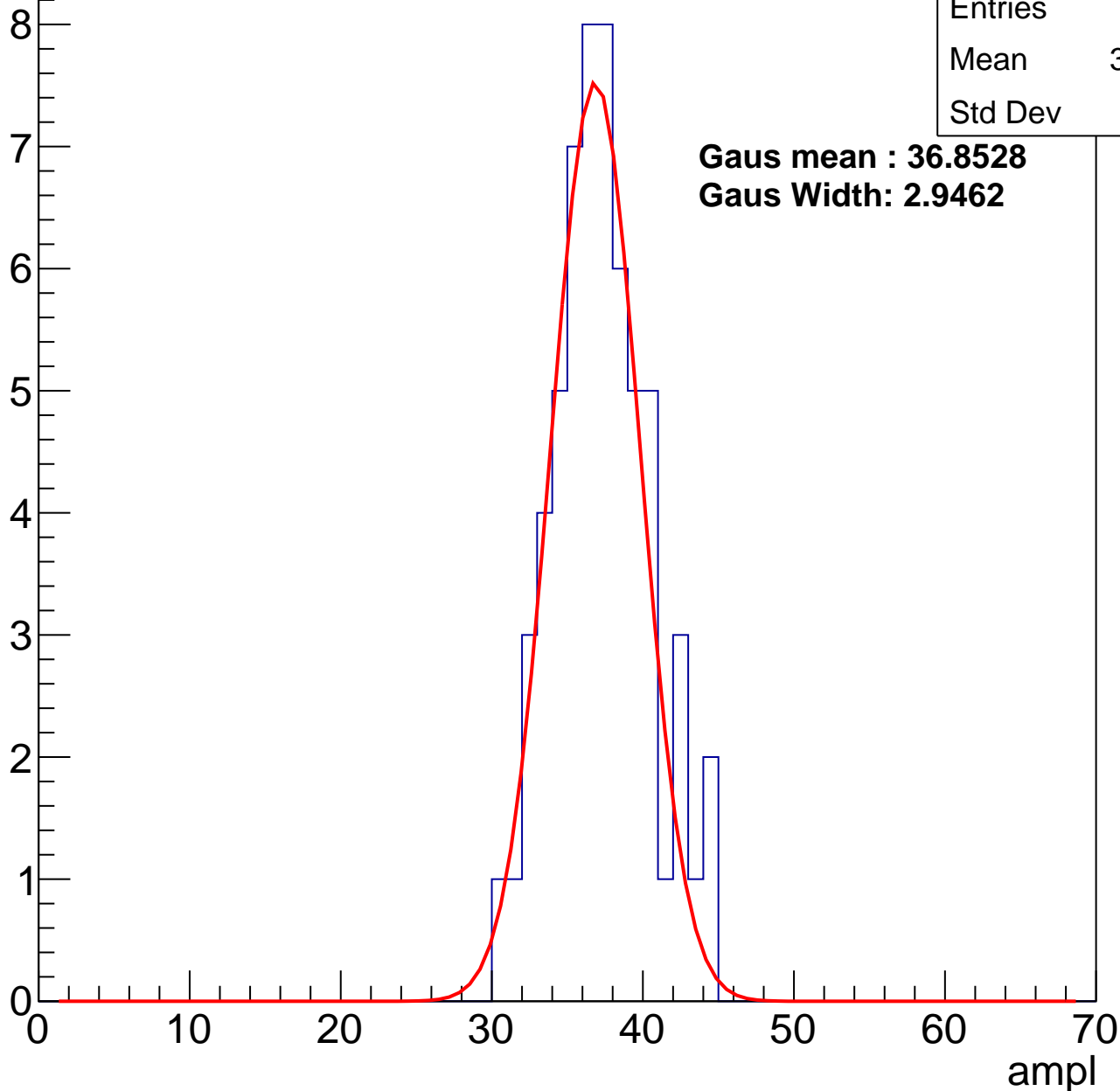
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	36.82
Std Dev	3.16

Gaus mean : 36.8528

Gaus Width: 2.9462



B0L001S, U14-ch112, adc2

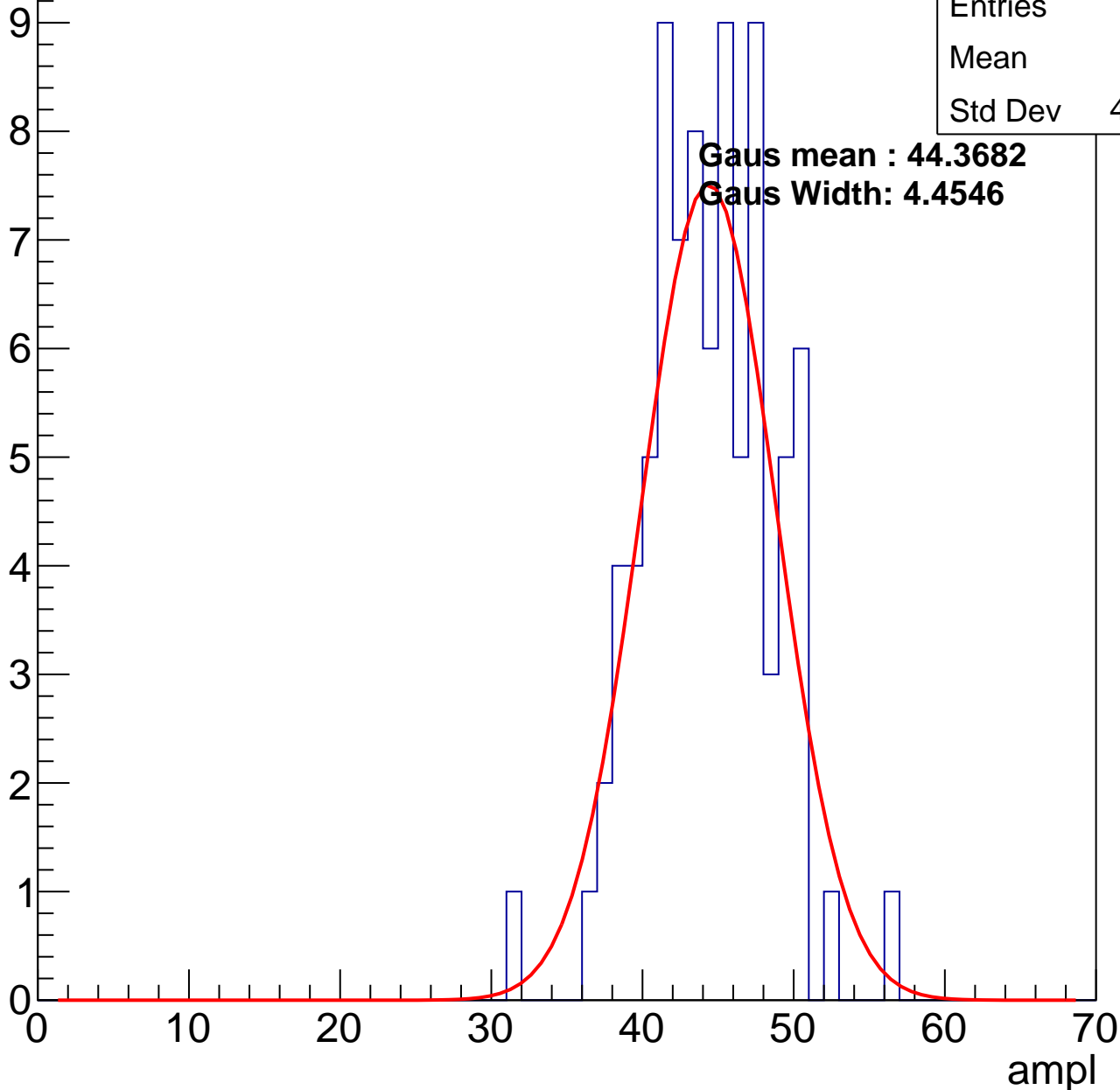
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	86
Mean	43.9
Std Dev	4.146

Gaus mean : 44.3682

Gaus Width: 4.4546

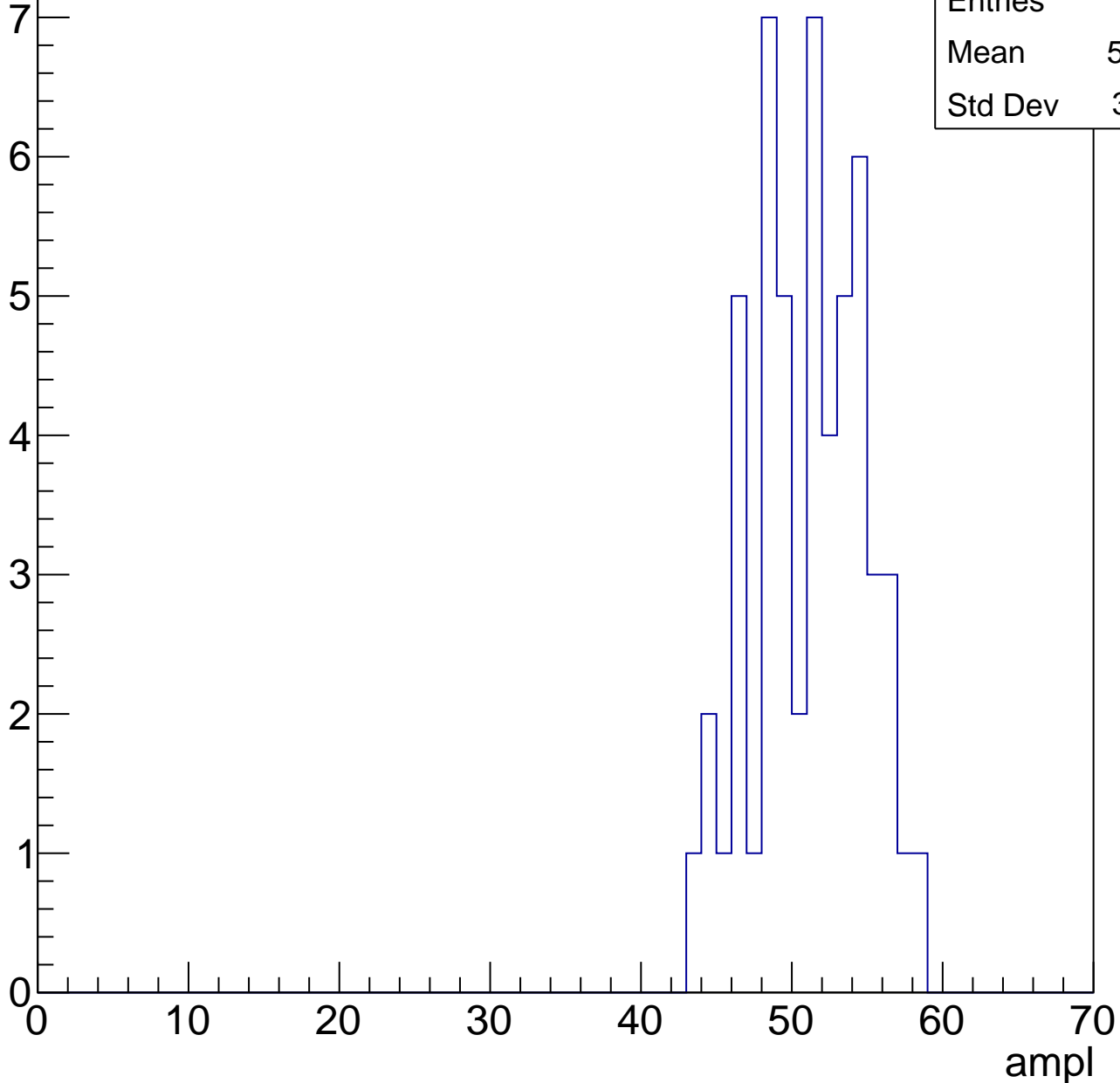


B0L001S, U14-ch112, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

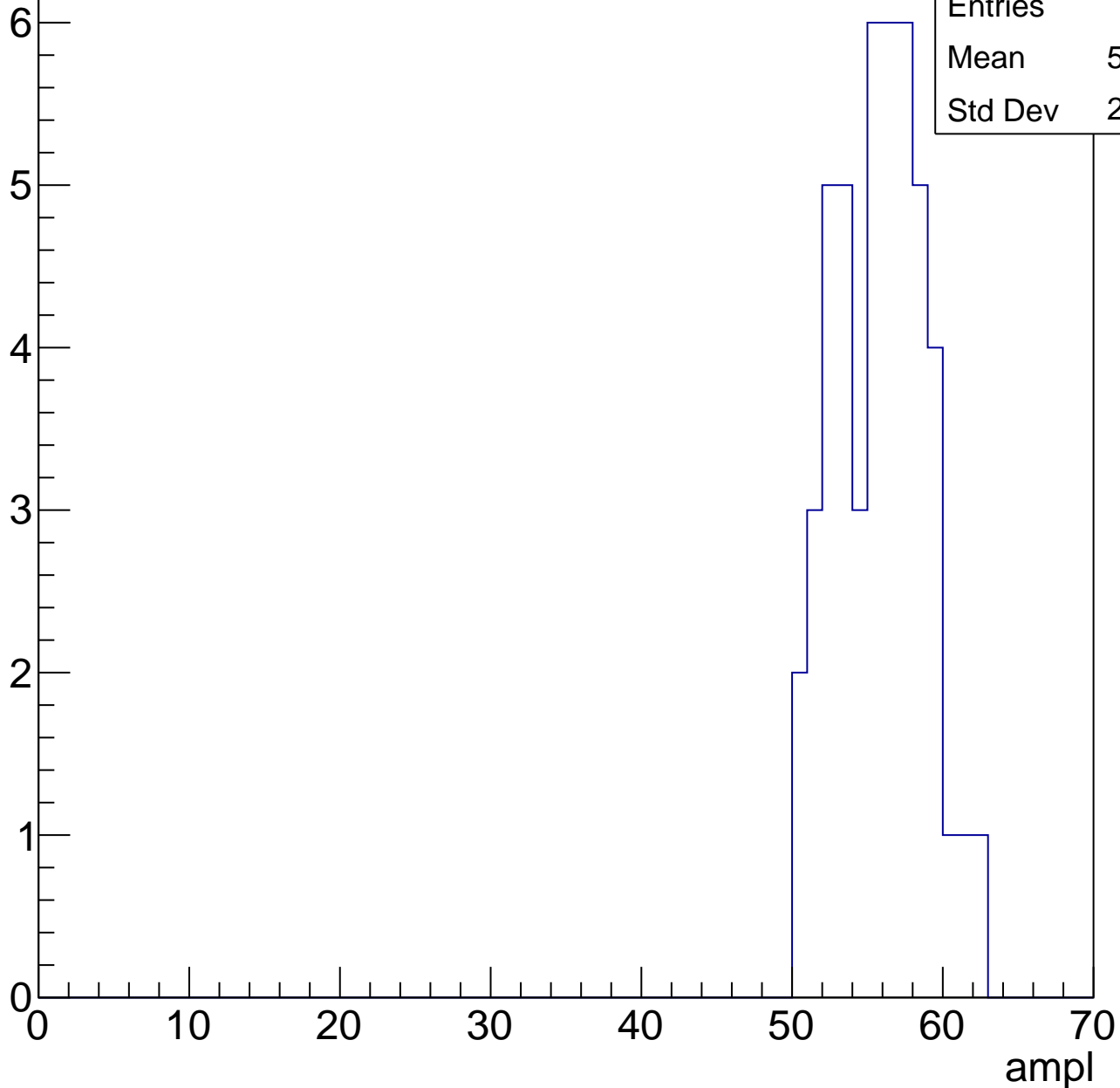
Entries	54
Mean	50.67
Std Dev	3.611



B0L001S, U14-ch112, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry



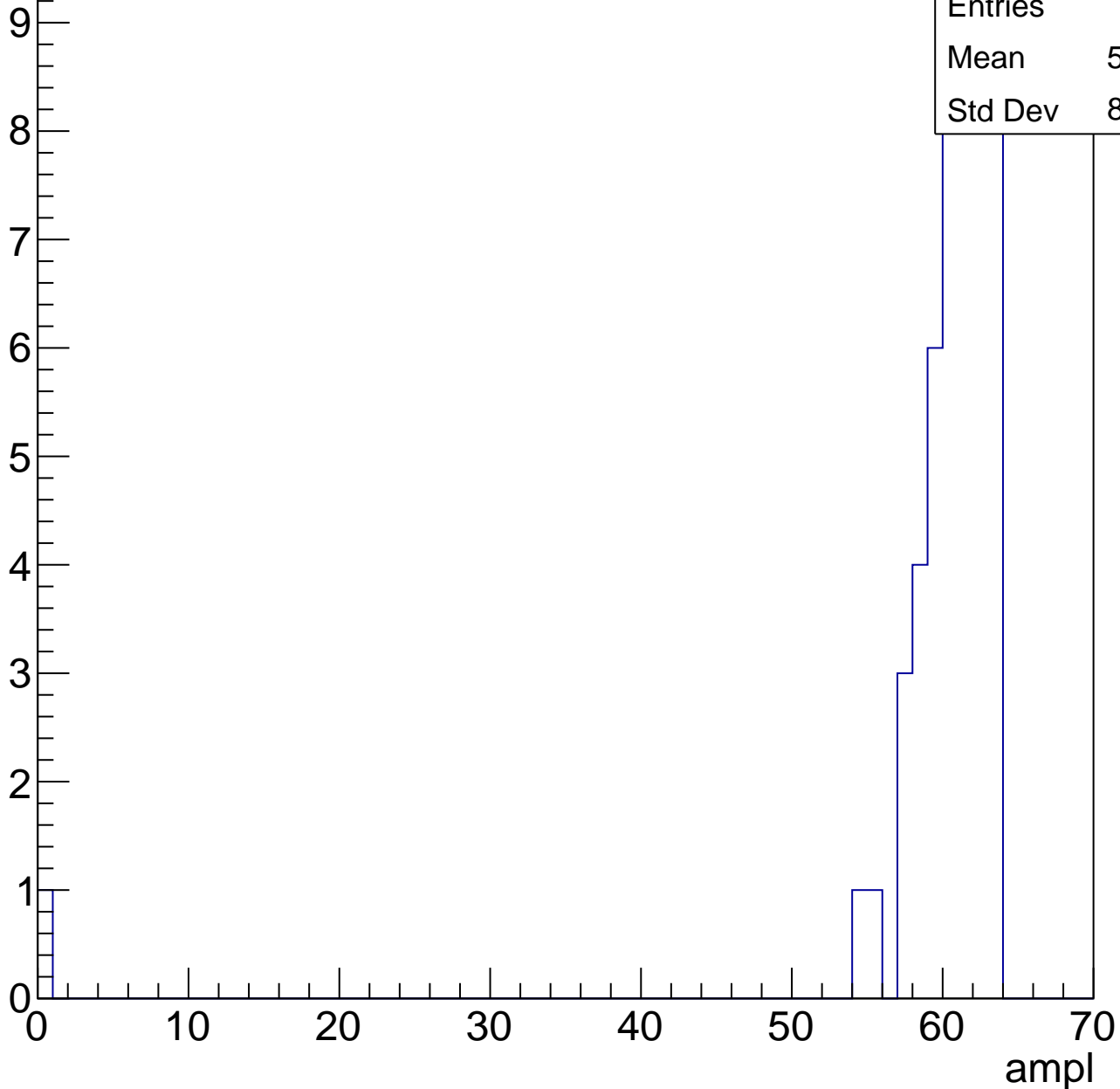
Entries	48
Mean	55.35
Std Dev	2.919

B0L001S, U14-ch112, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	51
Mean	59.22
Std Dev	8.637



B0L001S, U14-ch112, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch112, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	0
Std Dev	0

B0L001S, U14-ch113, adc0

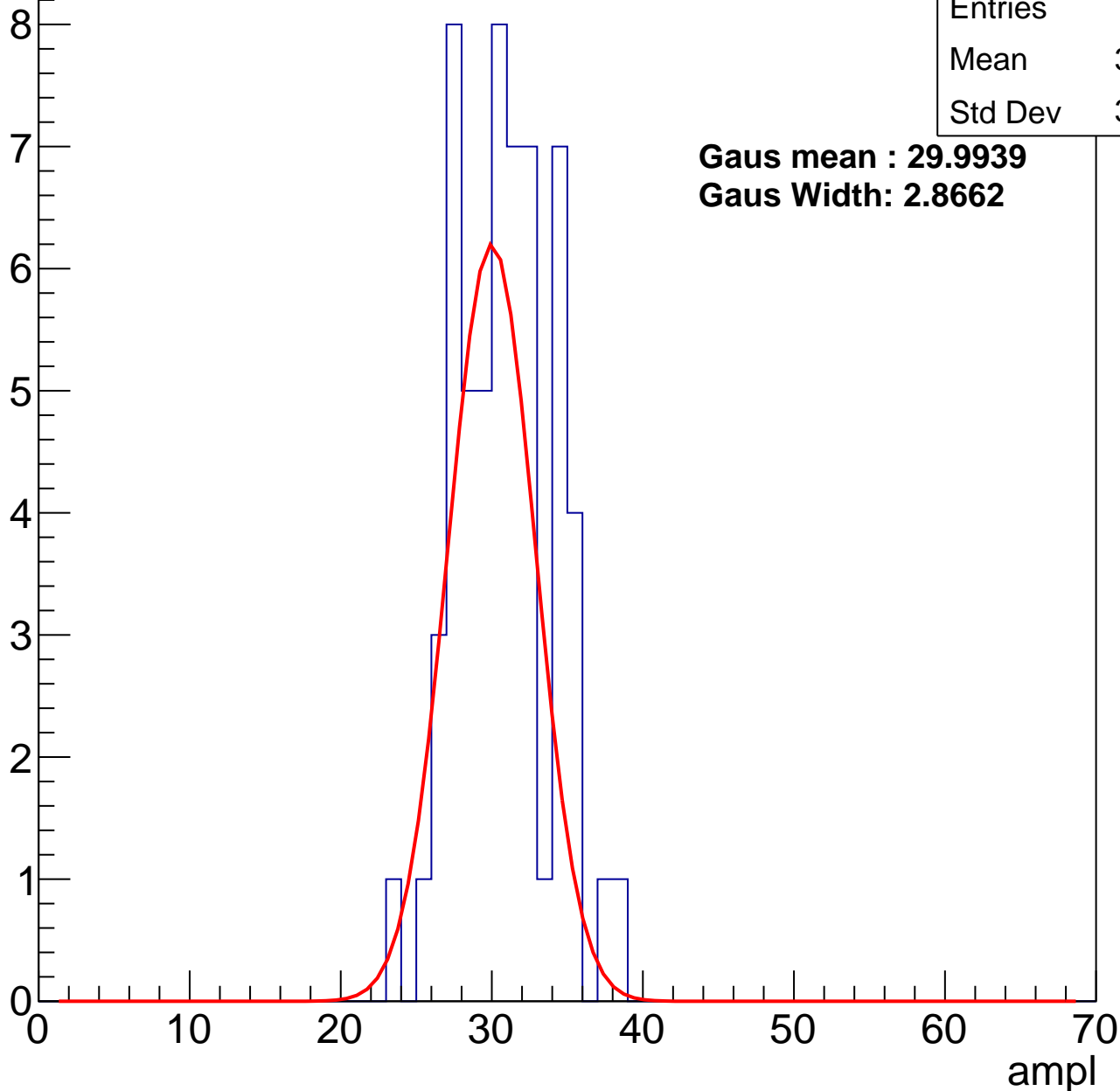
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	59
Mean	30.41
Std Dev	3.131

Gaus mean : 29.9939

Gaus Width: 2.8662



B0L001S, U14-ch113, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	71
Mean	37.15
Std Dev	3.475

Gaus mean : 37.5910

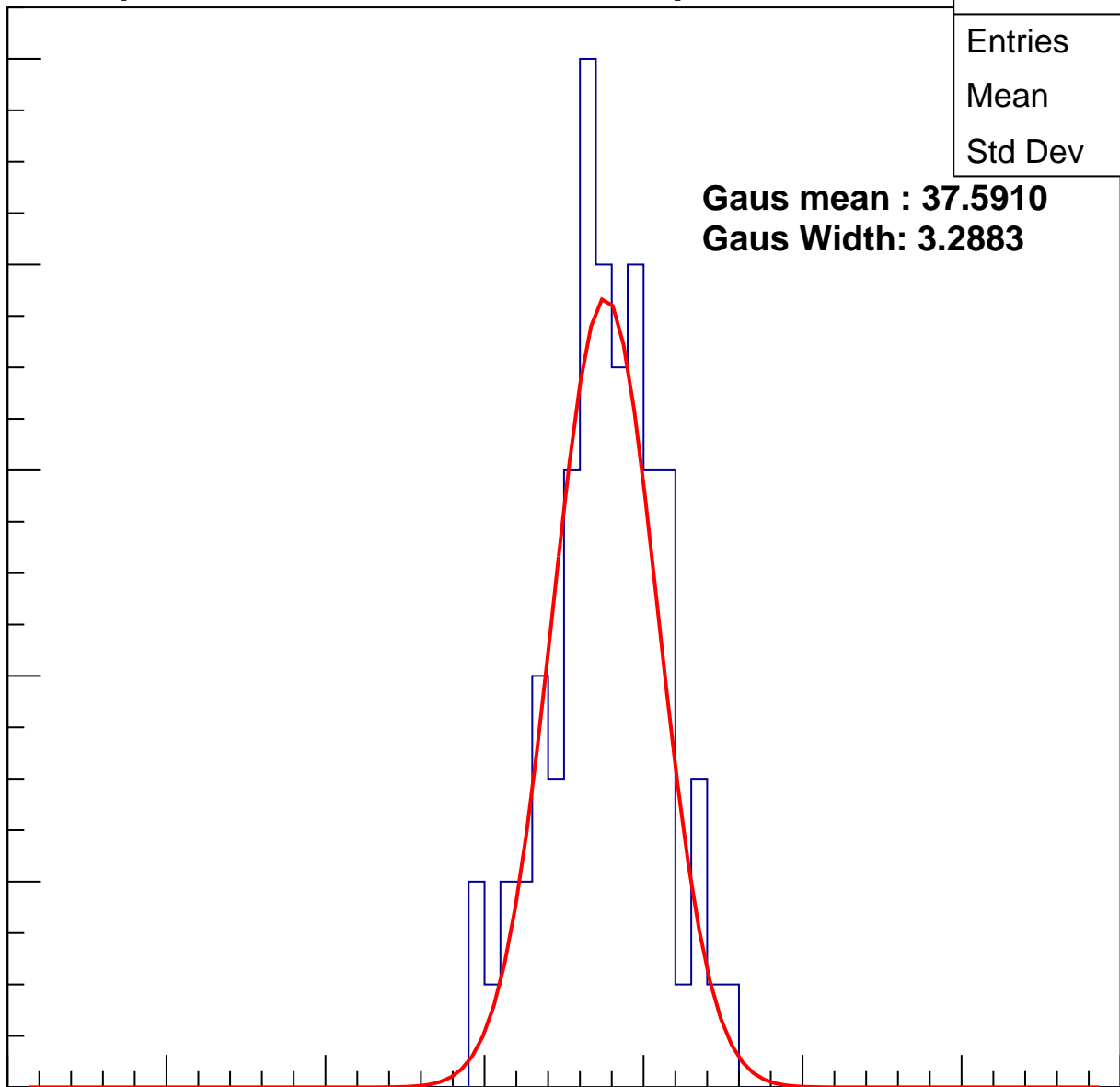
Gaus Width: 3.2883

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch113, adc2

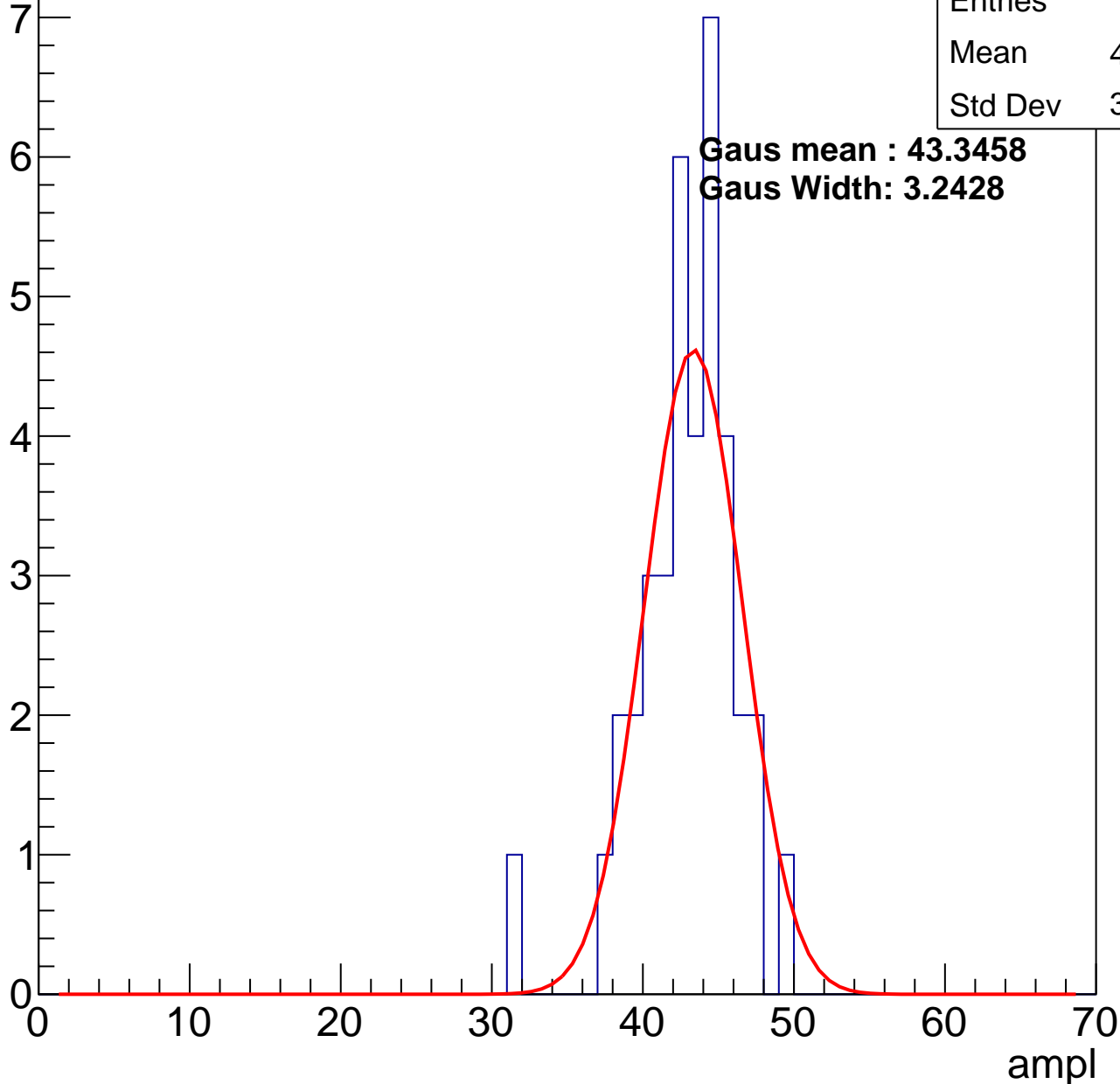
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	38
Mean	42.42
Std Dev	3.258

Gaus mean : 43.3458

Gaus Width: 3.2428

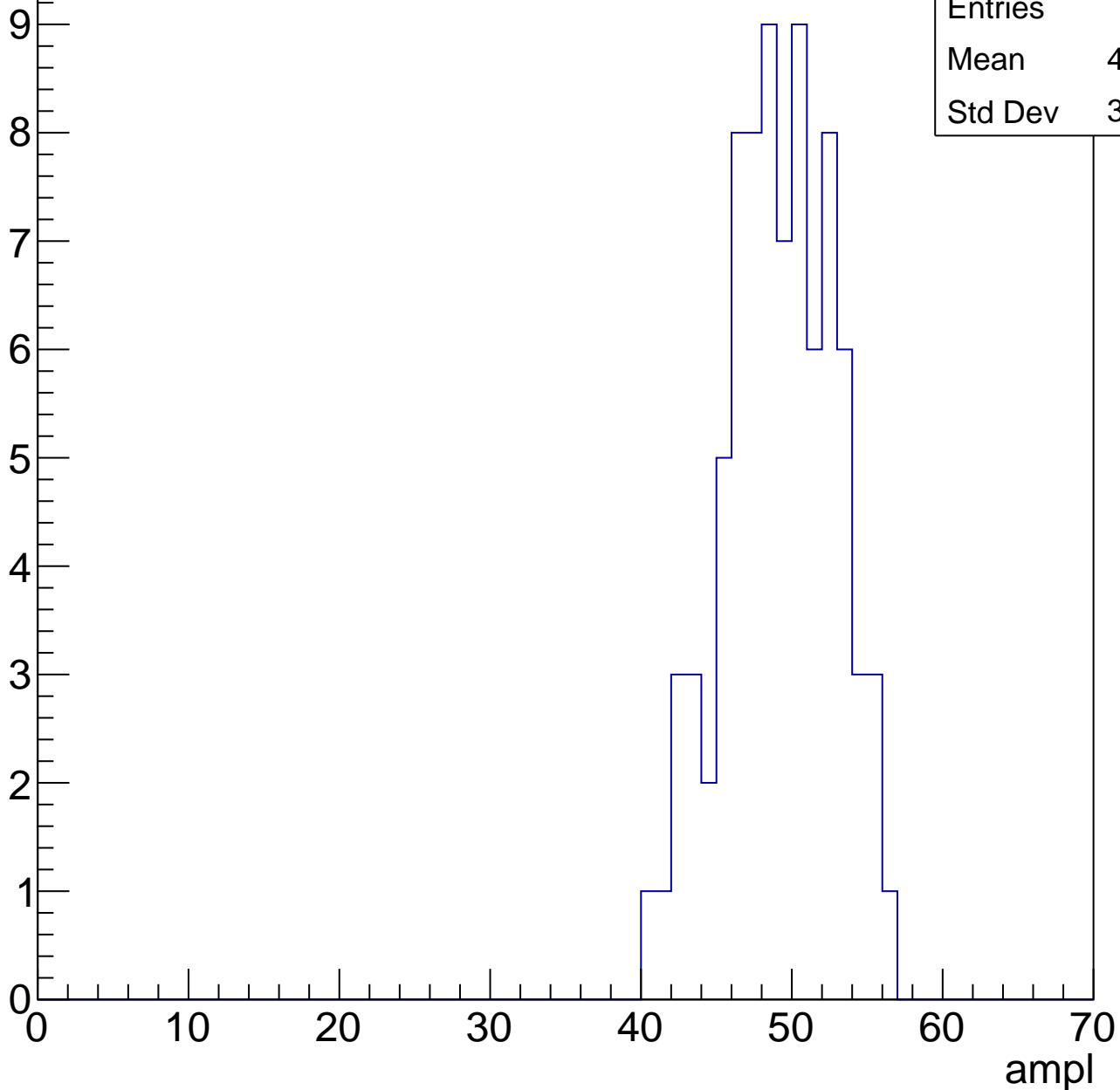


B0L001S, U14-ch113, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	83
Mean	48.69
Std Dev	3.587

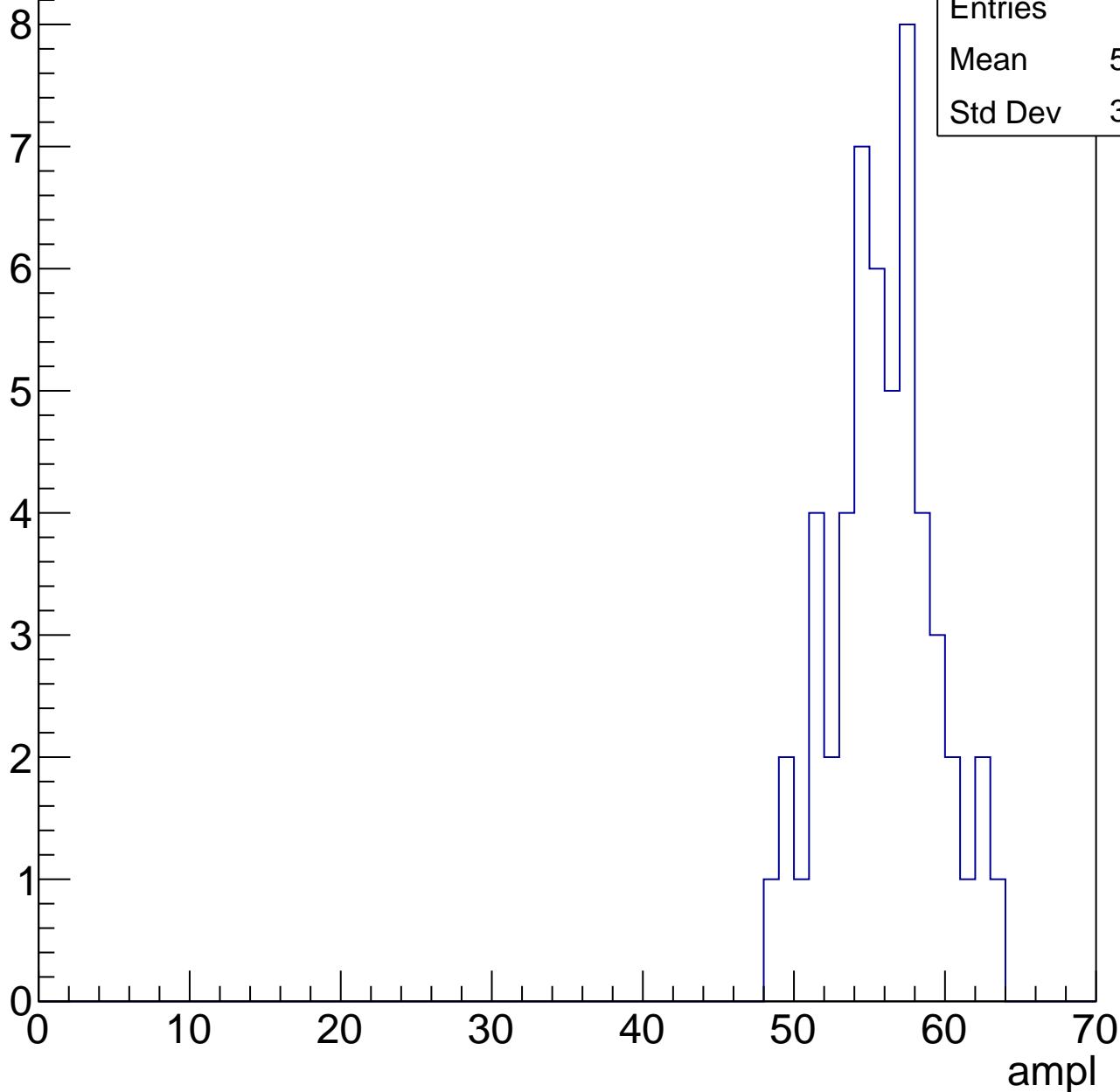


B0L001S, U14-ch113, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	55.42
Std Dev	3.395

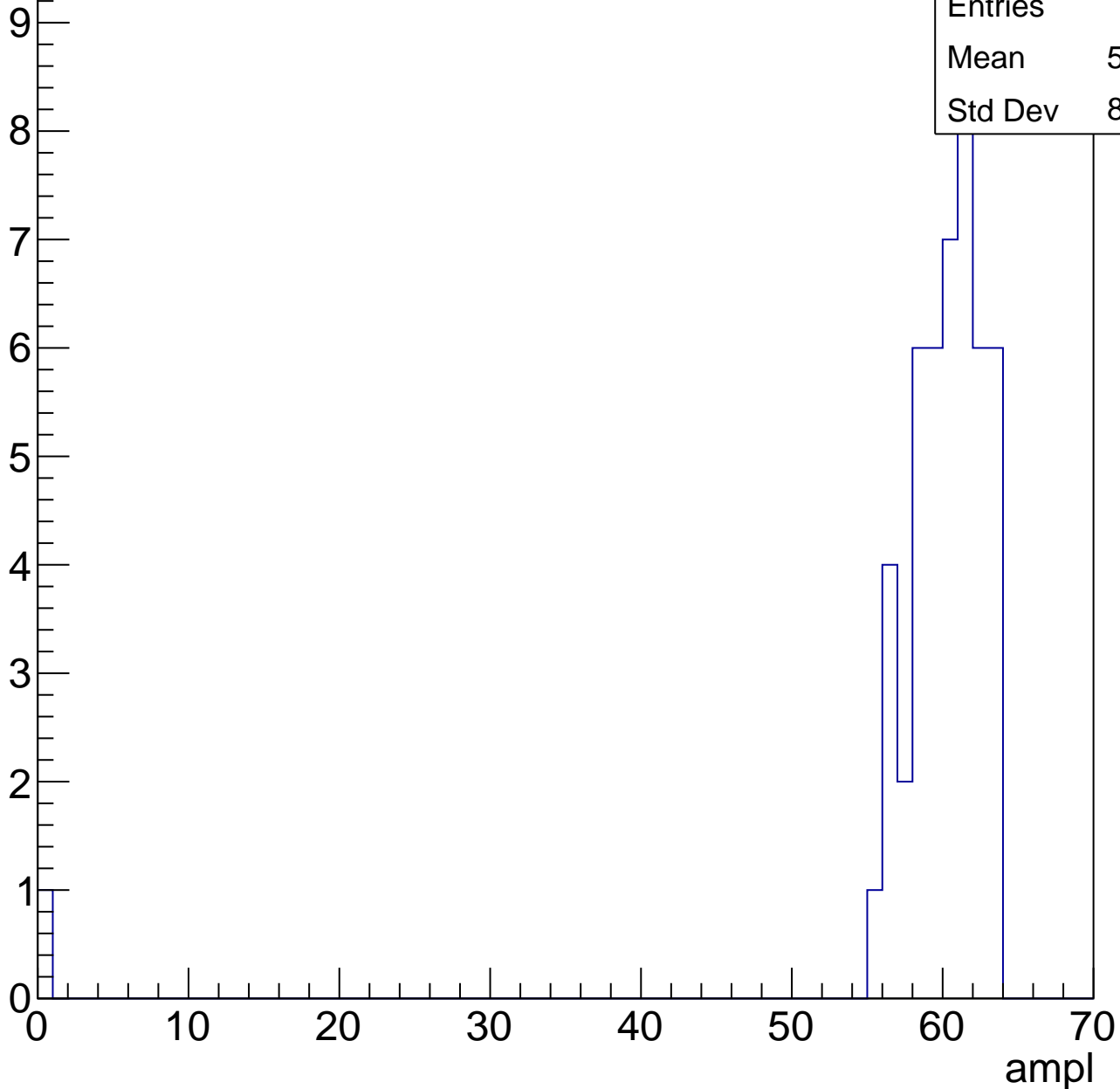


B0L001S, U14-ch113, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

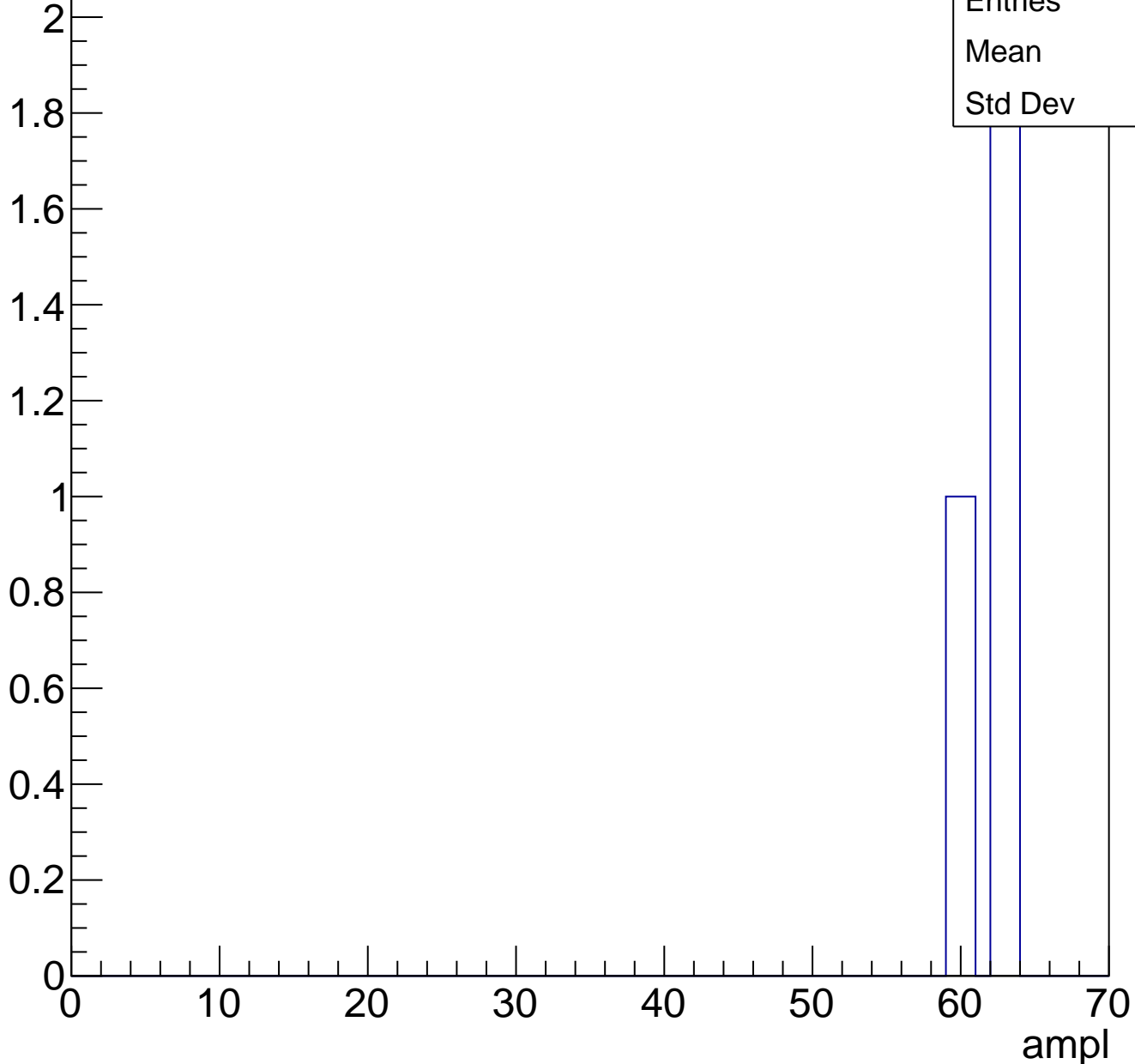
Entries	48
Mean	58.62
Std Dev	8.819



B0L001S, U14-ch113, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

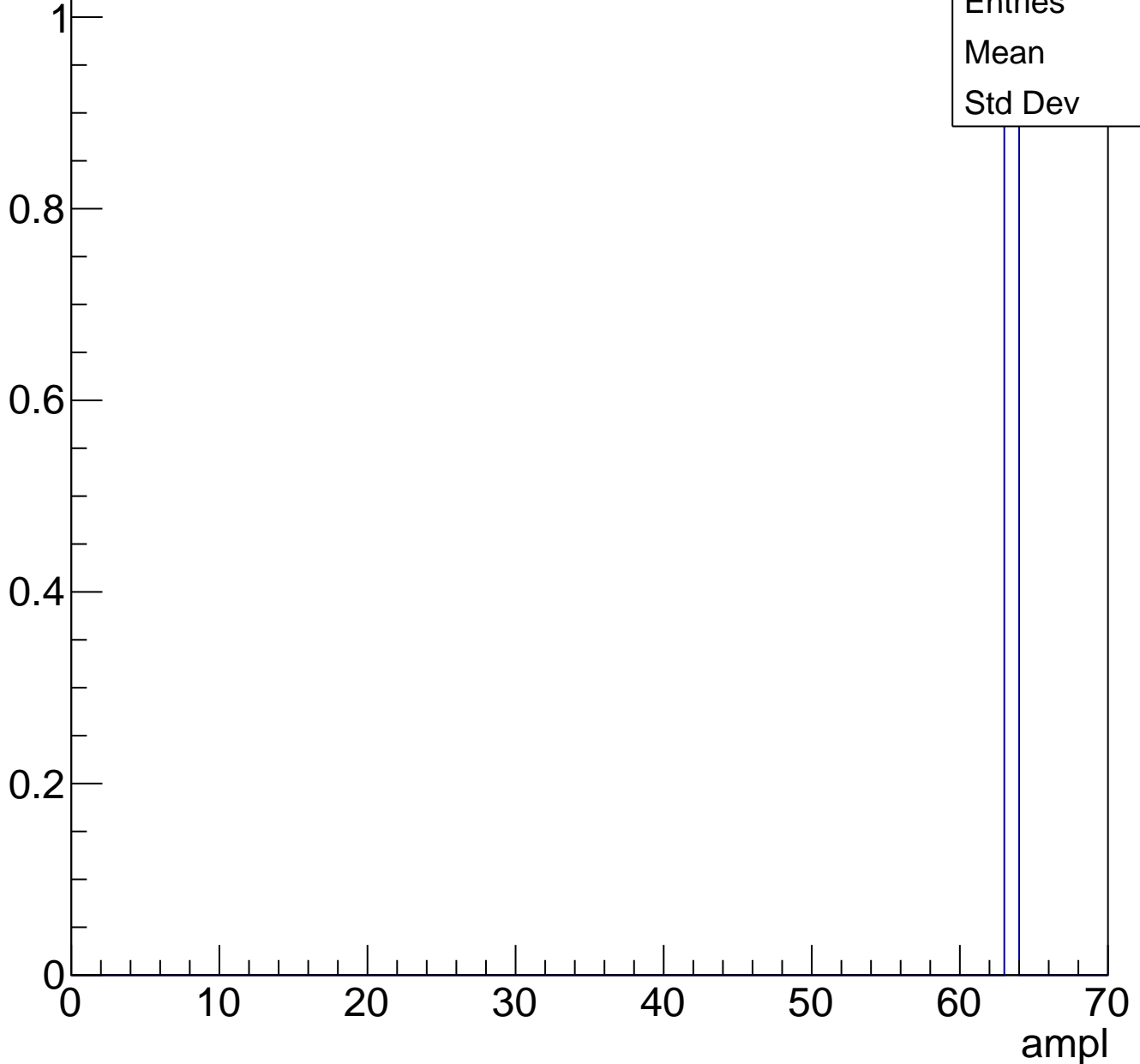


Entries	6
Mean	61.5
Std Dev	1.5

B0L001S, U14-ch113, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch114, adc0

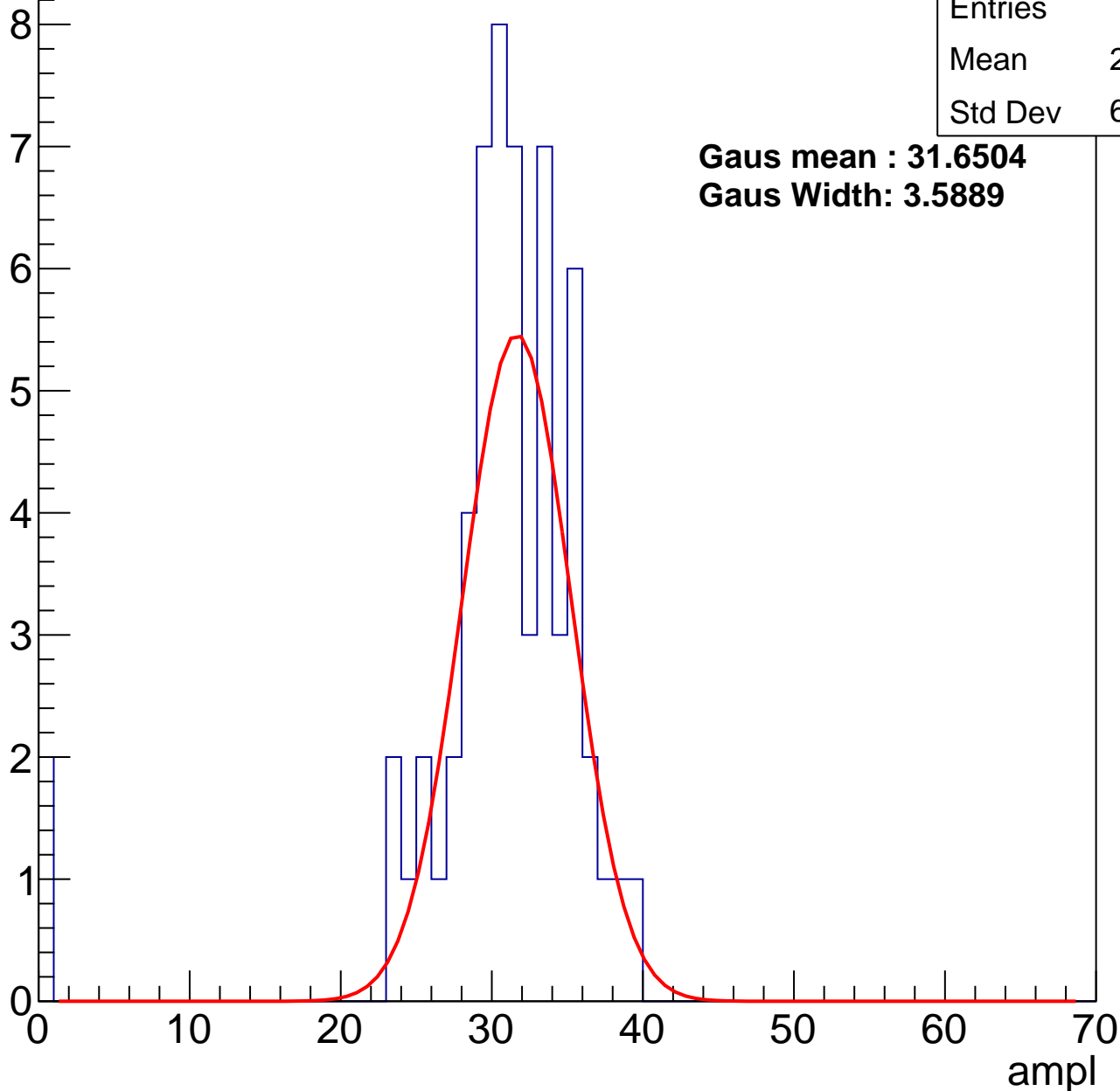
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	29.95
Std Dev	6.566

Gaus mean : 31.6504

Gaus Width: 3.5889



B0L001S, U14-ch114, adc1

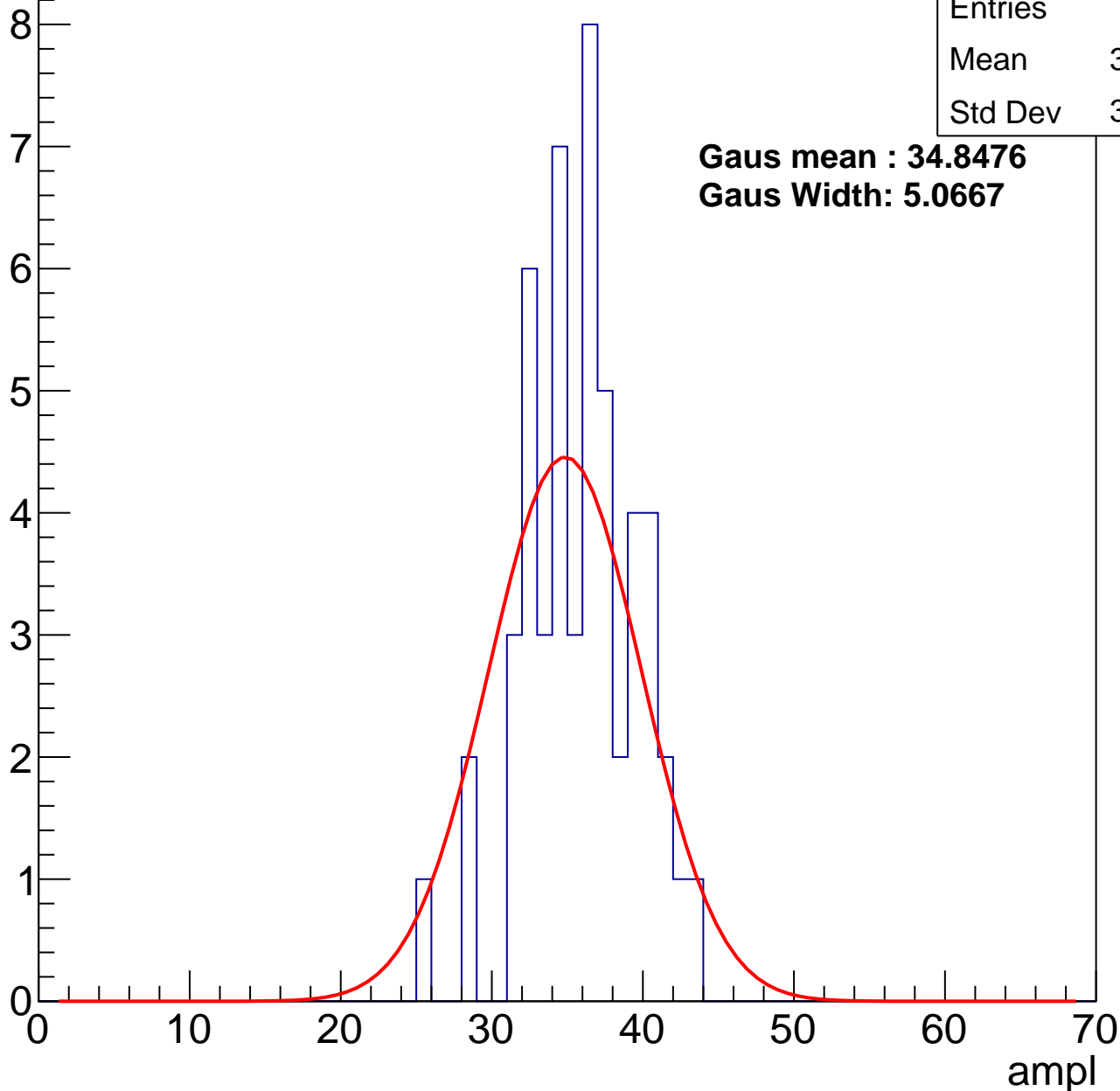
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	52
Mean	35.38
Std Dev	3.685

Gaus mean : 34.8476

Gaus Width: 5.0667



B0L001S, U14-ch114, adc2

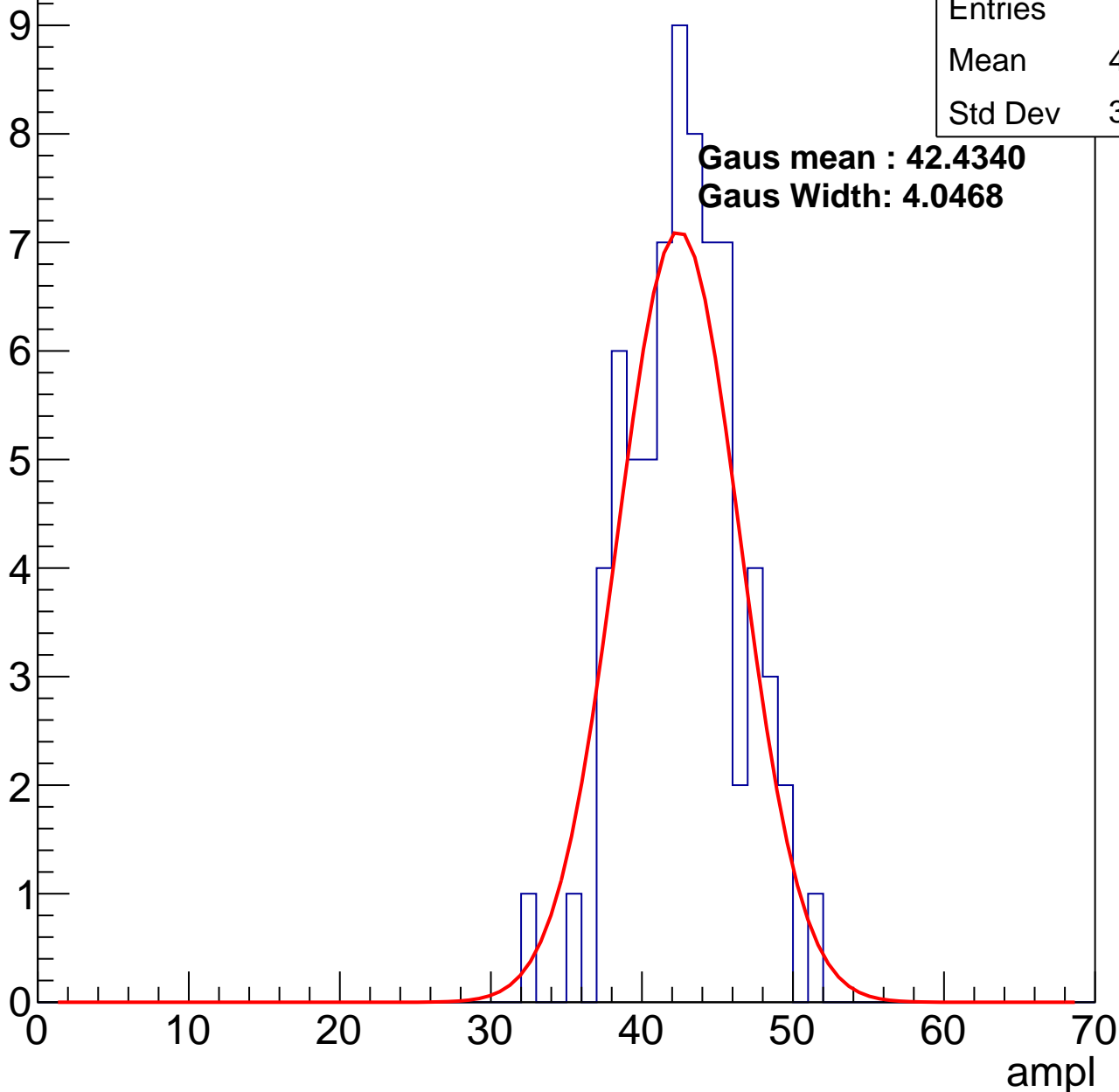
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	42.26
Std Dev	3.594

Gaus mean : 42.4340

Gaus Width: 4.0468

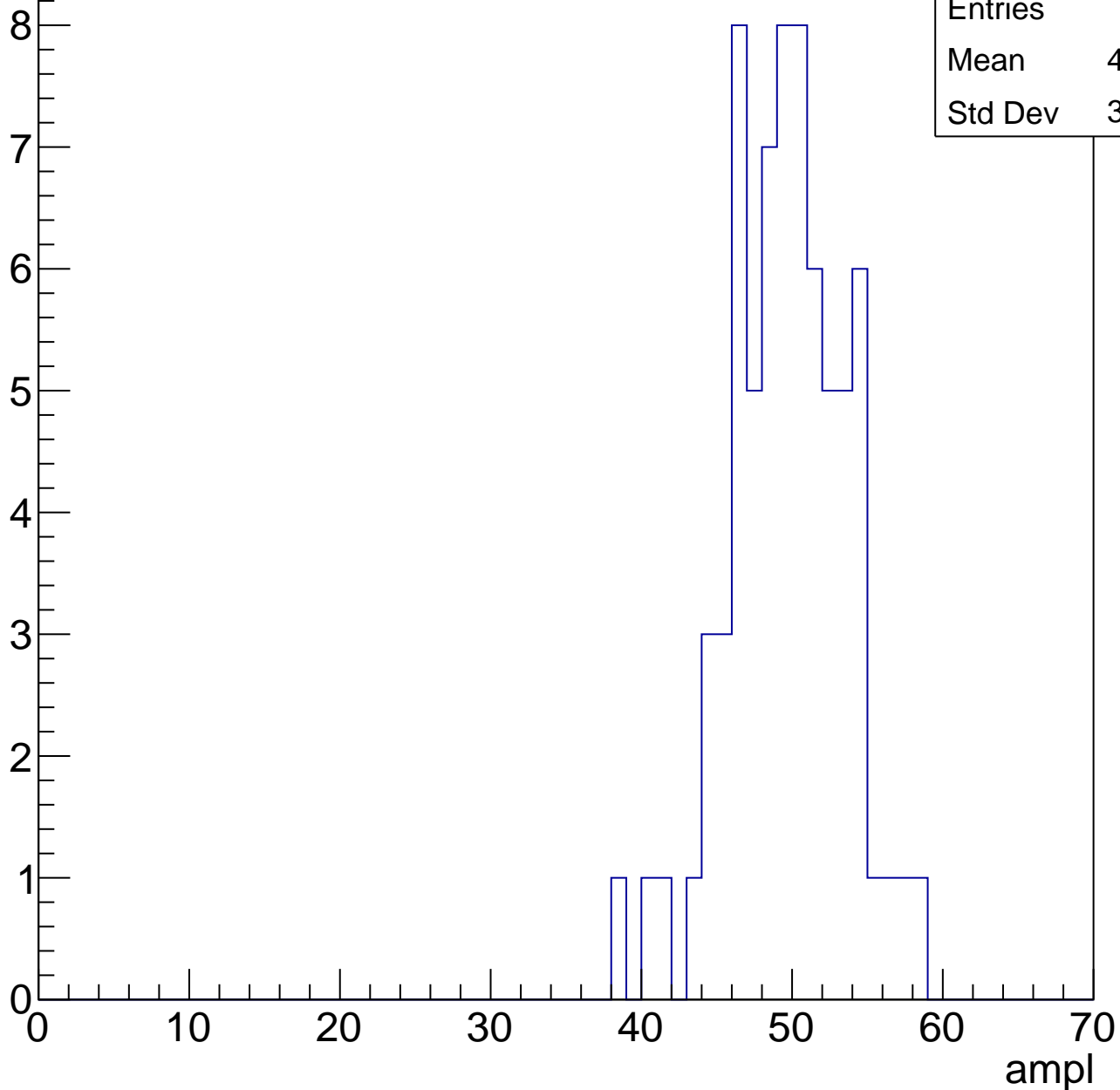


B0L001S, U14-ch114, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	49.18
Std Dev	3.835

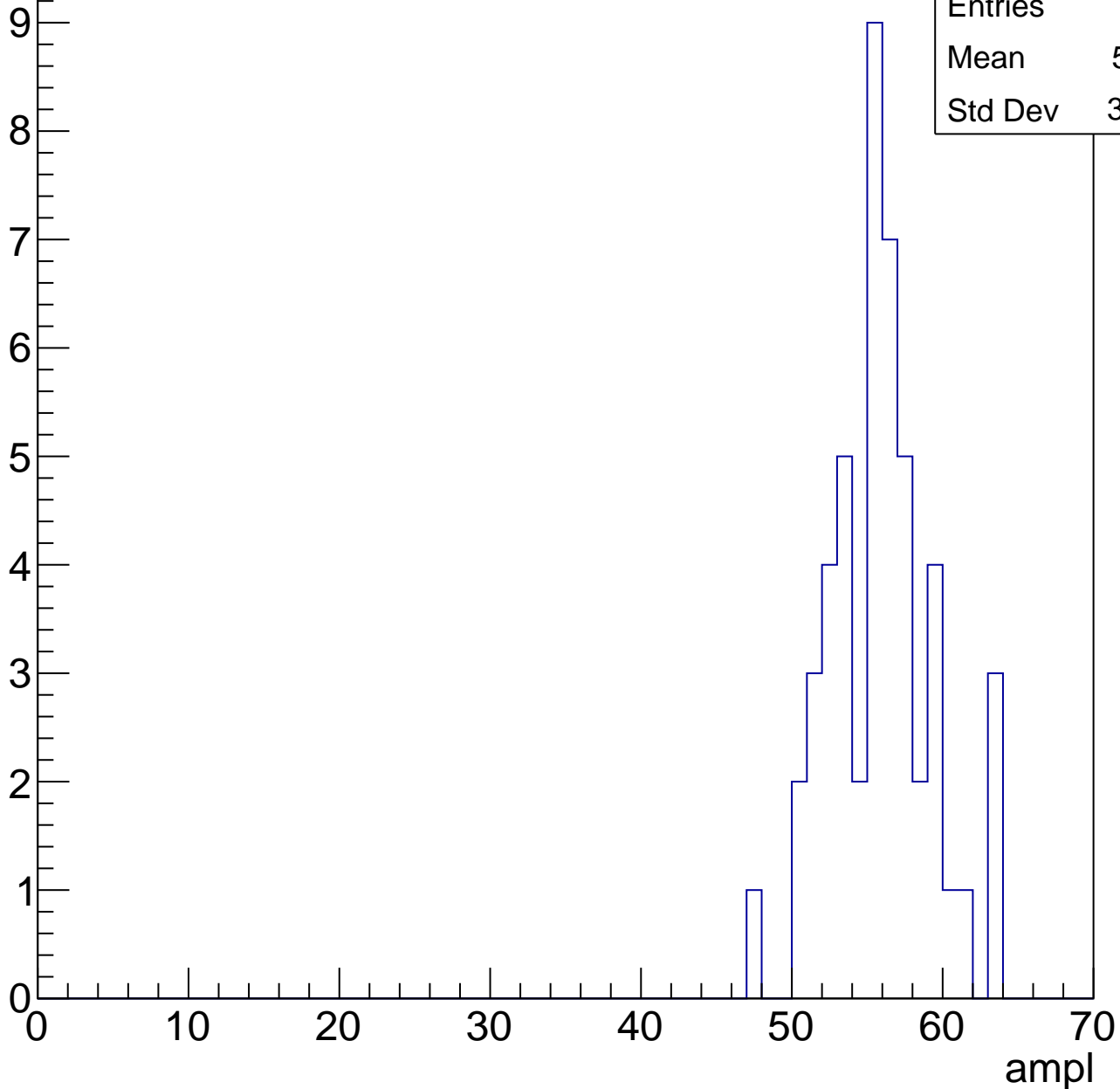


B0L001S, U14-ch114, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	55.41
Std Dev	3.422

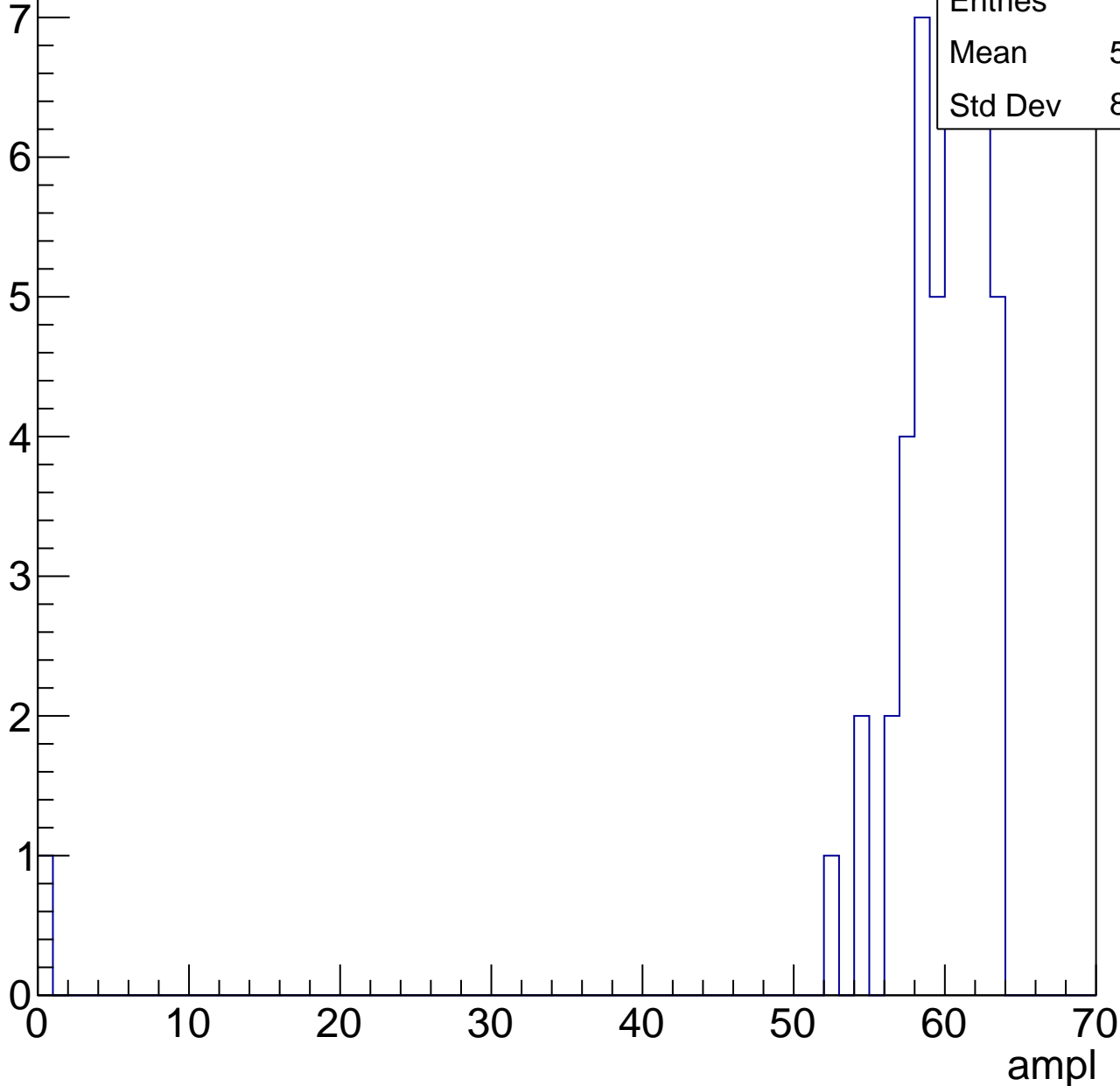


B0L001S, U14-ch114, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	48
Mean	58.27
Std Dev	8.866



B0L001S, U14-ch114, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

3

2.5

2

1.5

1

0.5

0

Entries

10

Mean

61.3

Std Dev

1.269

ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch114, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch115, adc0

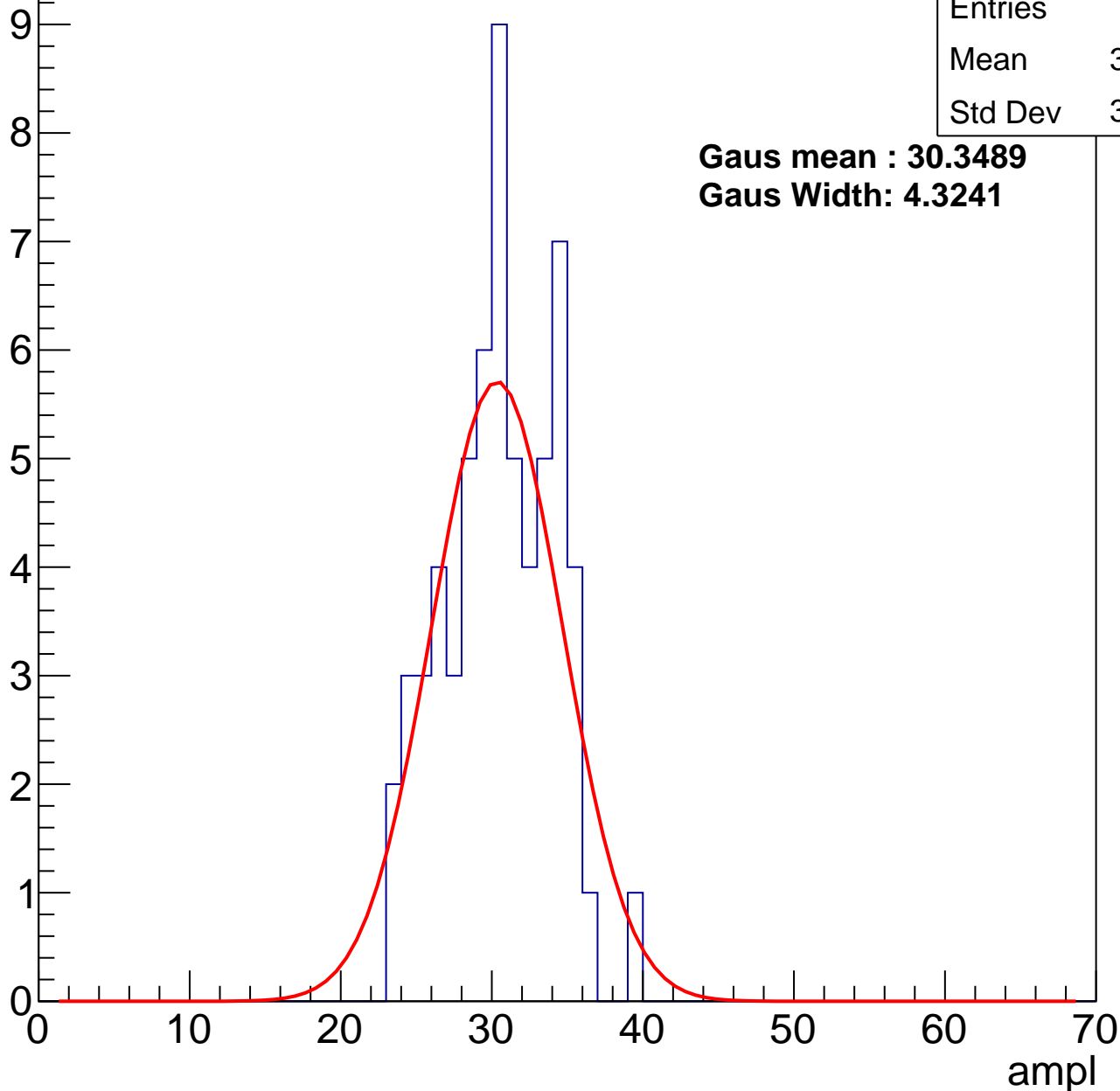
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.05
Std Dev	3.576

Gaus mean : 30.3489

Gaus Width: 4.3241



B0L001S, U14-ch115, adc1

calib_packv5_042523_0143.root, FC#9, port A1

Entries	72
Mean	36.54
Std Dev	3.253

Gaus mean : 37.7251

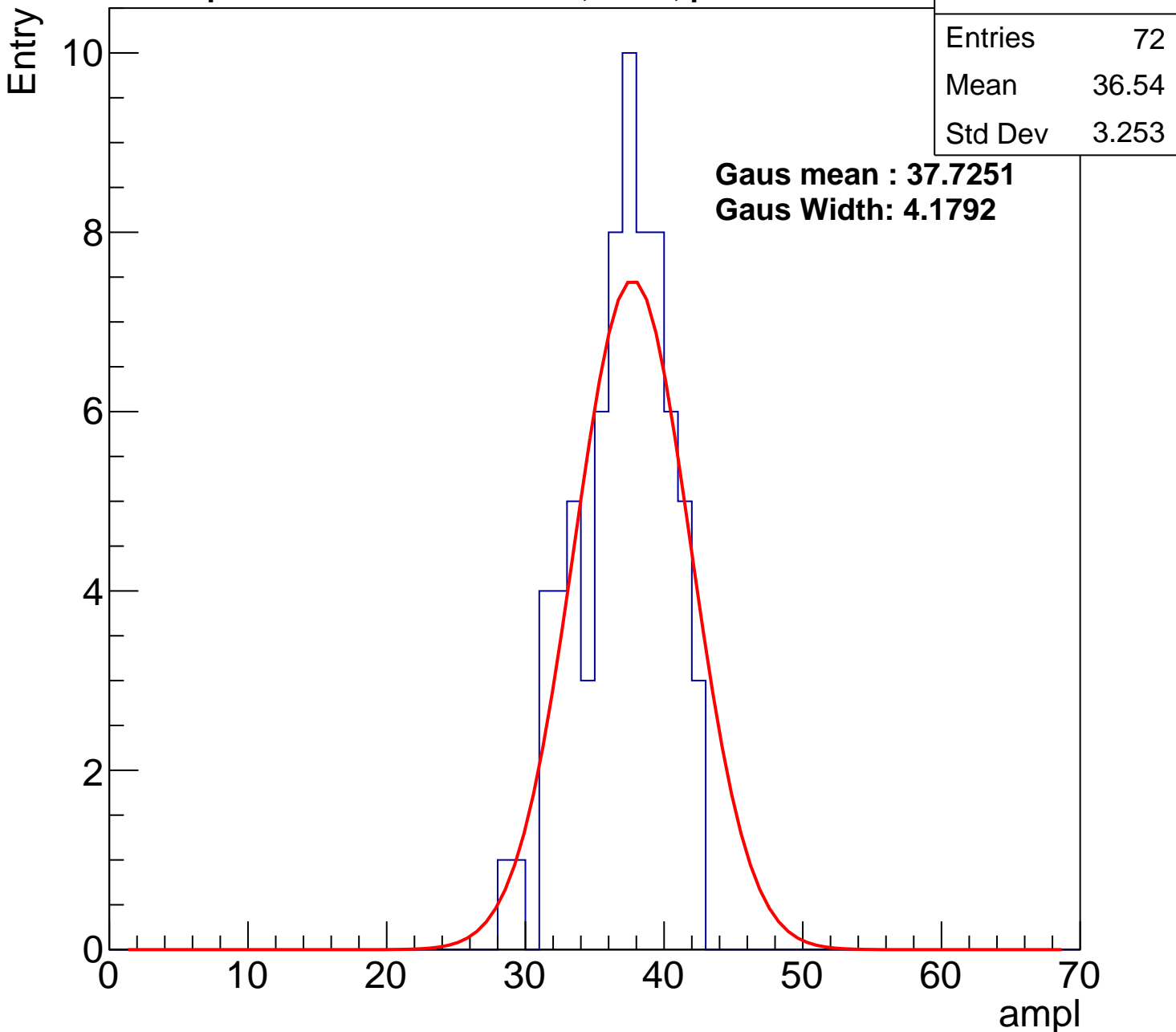
Gaus Width: 4.1792

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L001S, U14-ch115, adc2

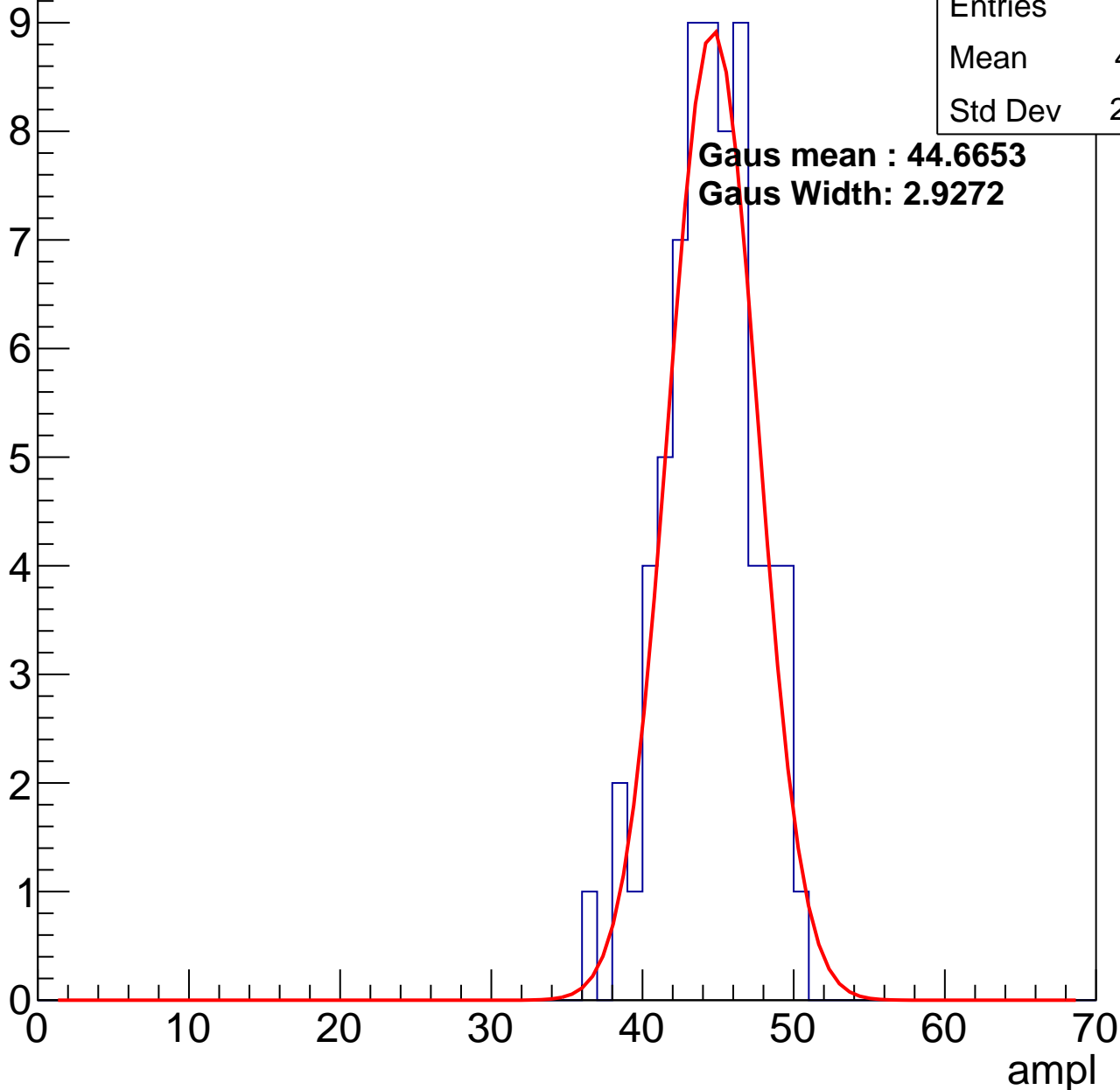
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.01
Std Dev	2.938

Gaus mean : 44.6653

Gaus Width: 2.9272

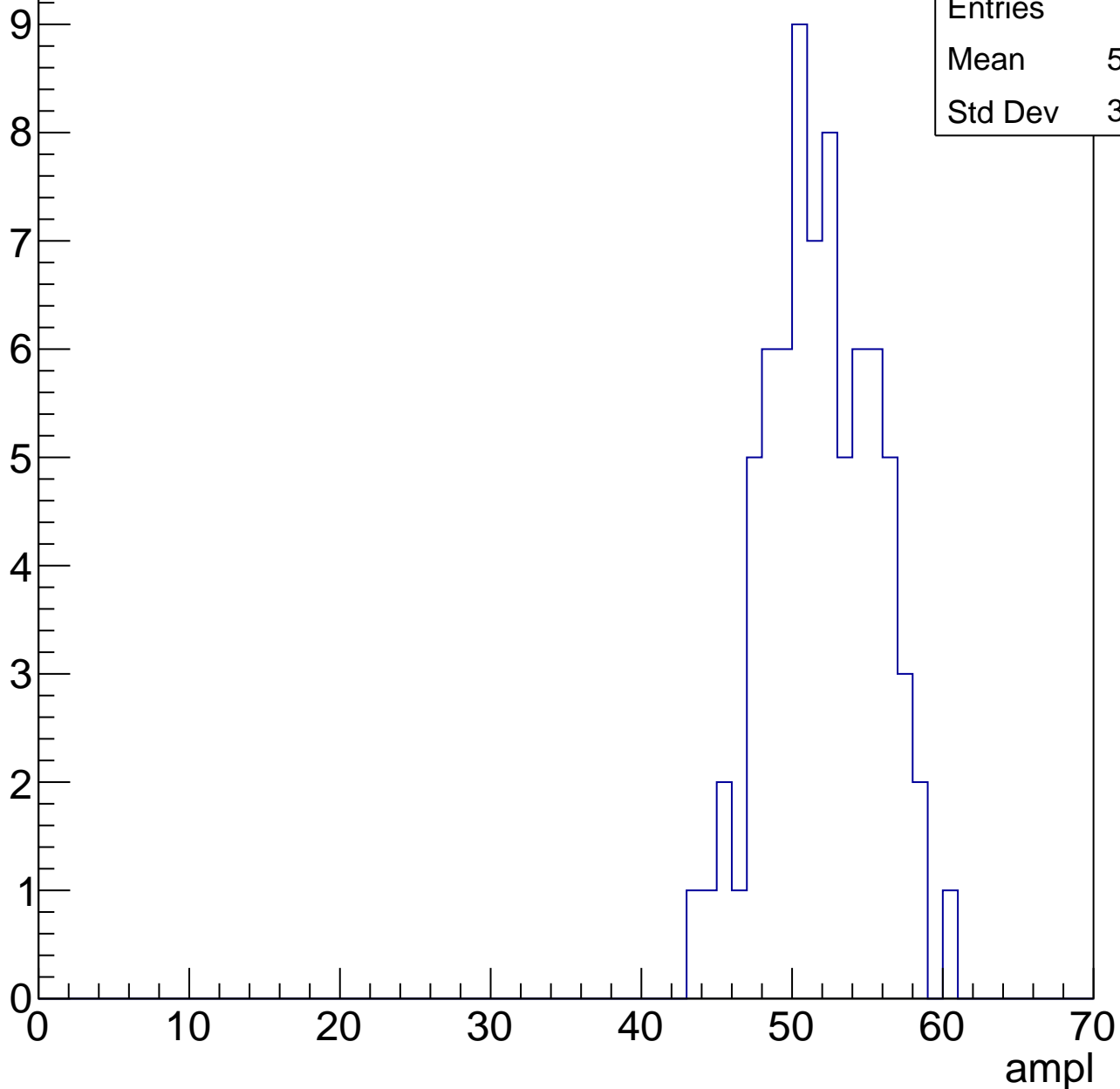


B0L001S, U14-ch115, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	51.47
Std Dev	3.584

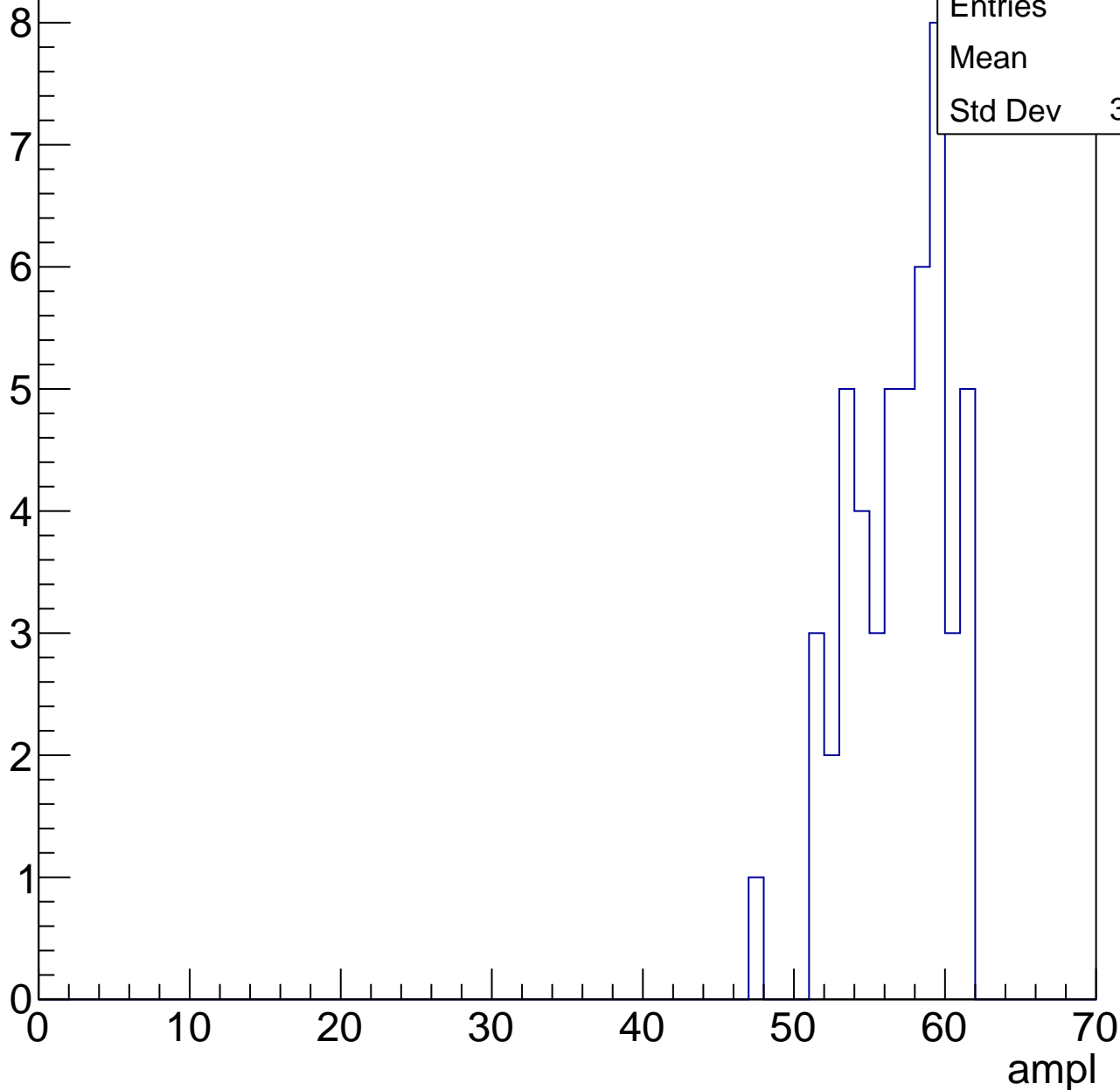


B0L001S, U14-ch115, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	56.4
Std Dev	3.219

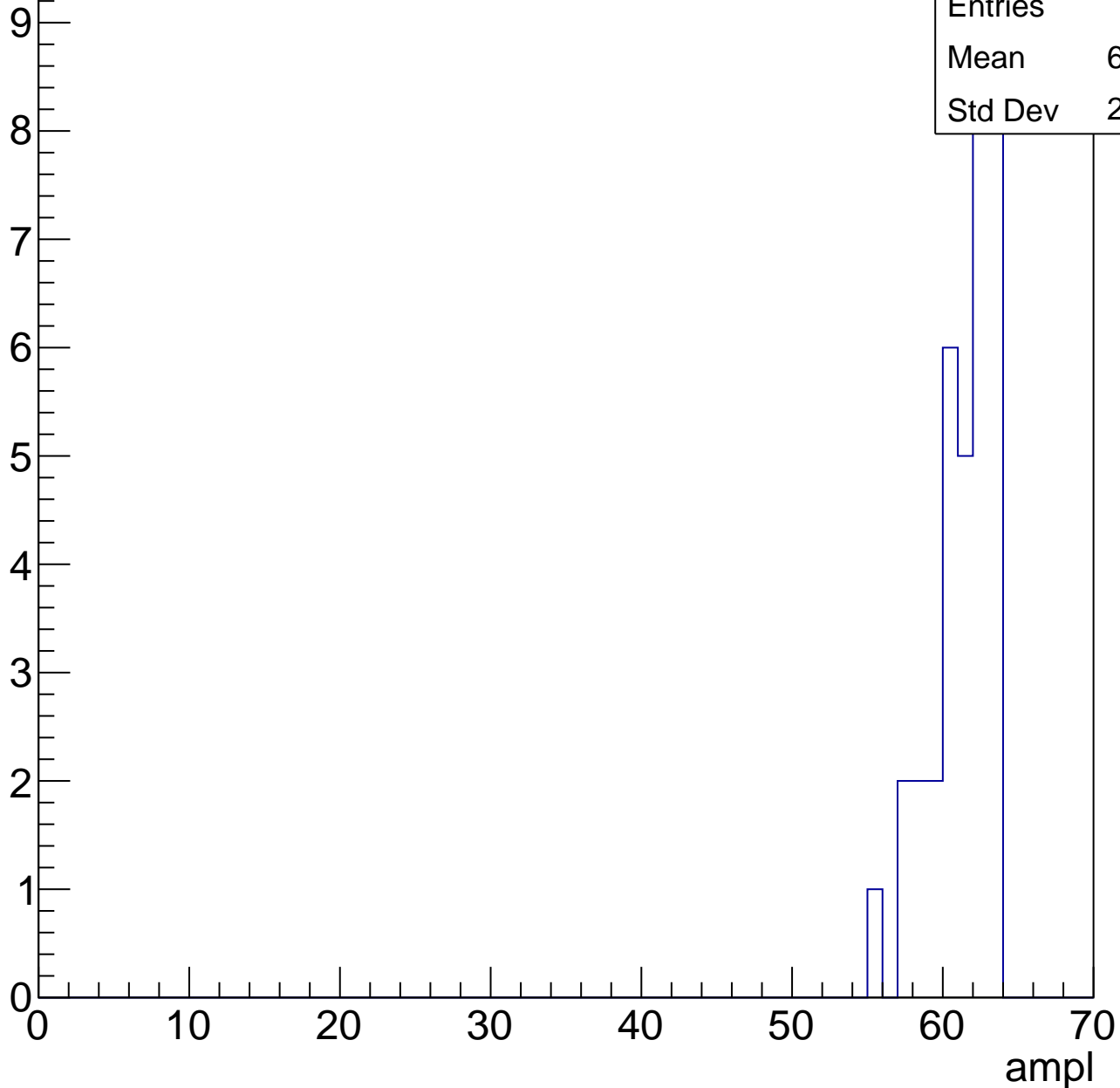


B0L001S, U14-ch115, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.86
Std Dev	2.002



B0L001S, U14-ch115, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch115, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

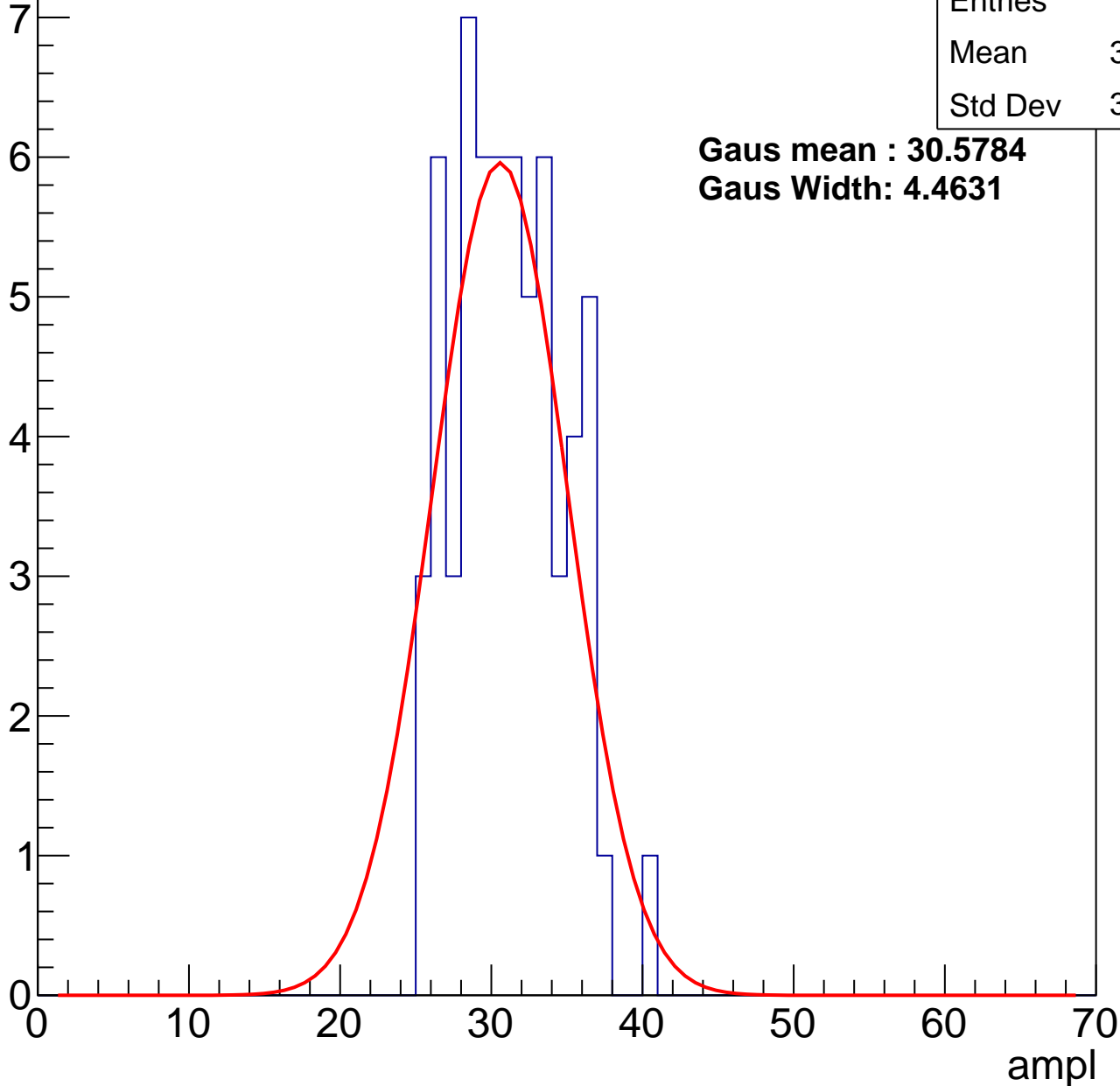
B0L001S, U14-ch116, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.73
Std Dev	3.493

Gaus mean : 30.5784
Gaus Width: 4.4631



B0L001S, U14-ch116, adc1

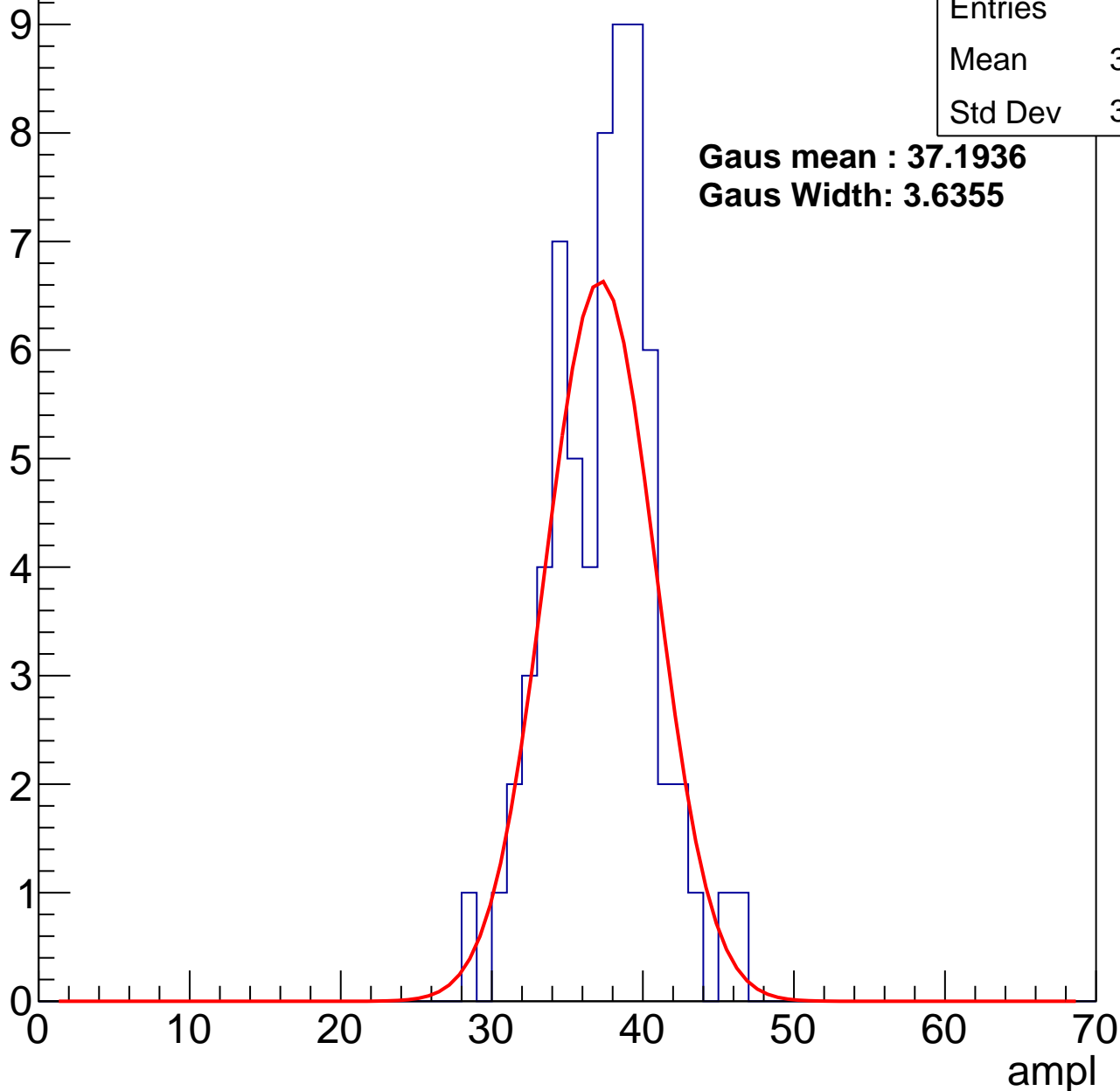
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	36.88
Std Dev	3.458

Gaus mean : 37.1936

Gaus Width: 3.6355



B0L001S, U14-ch116, adc2

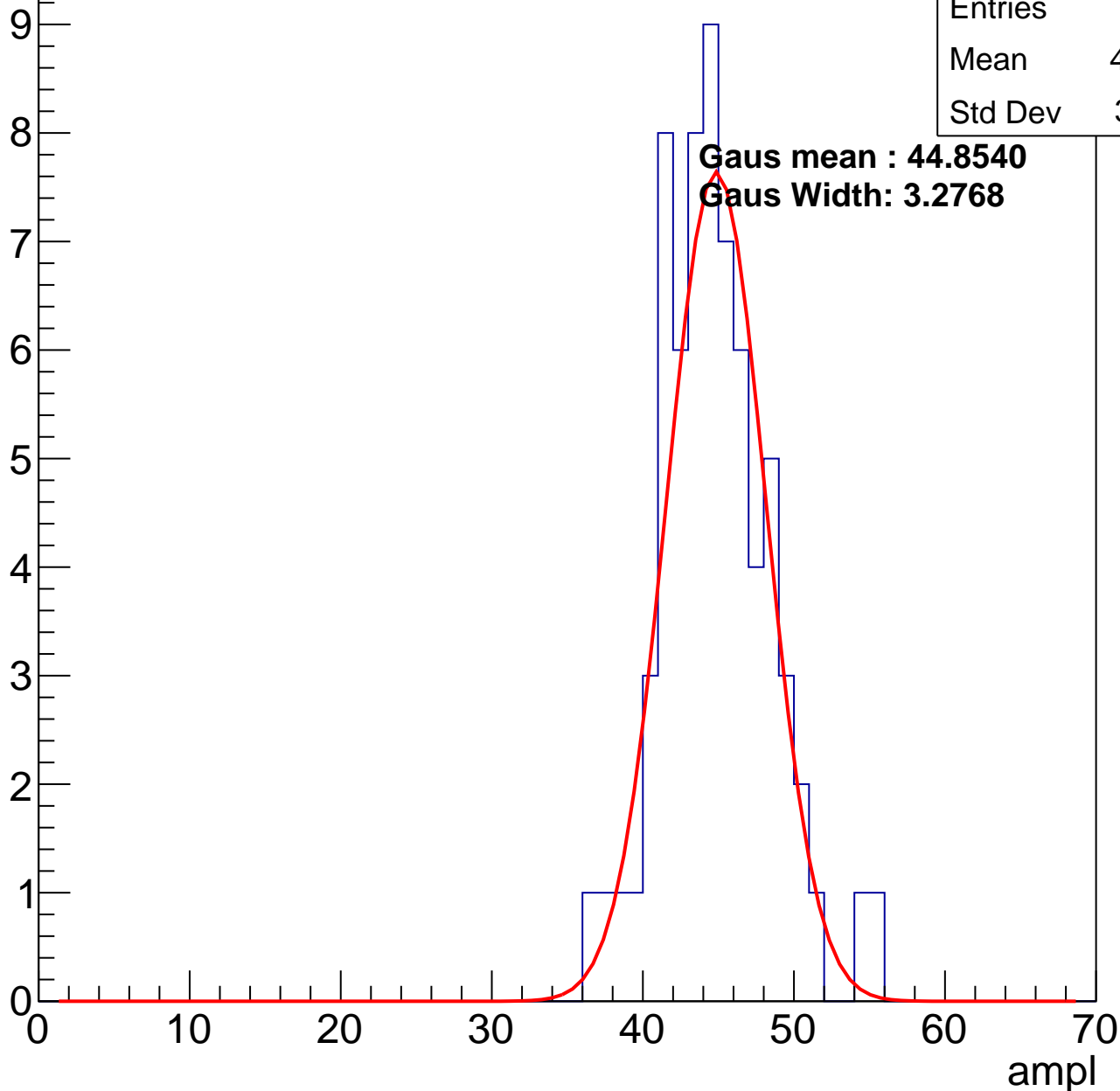
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	44.35
Std Dev	3.601

Gaus mean : 44.8540

Gaus Width: 3.2768

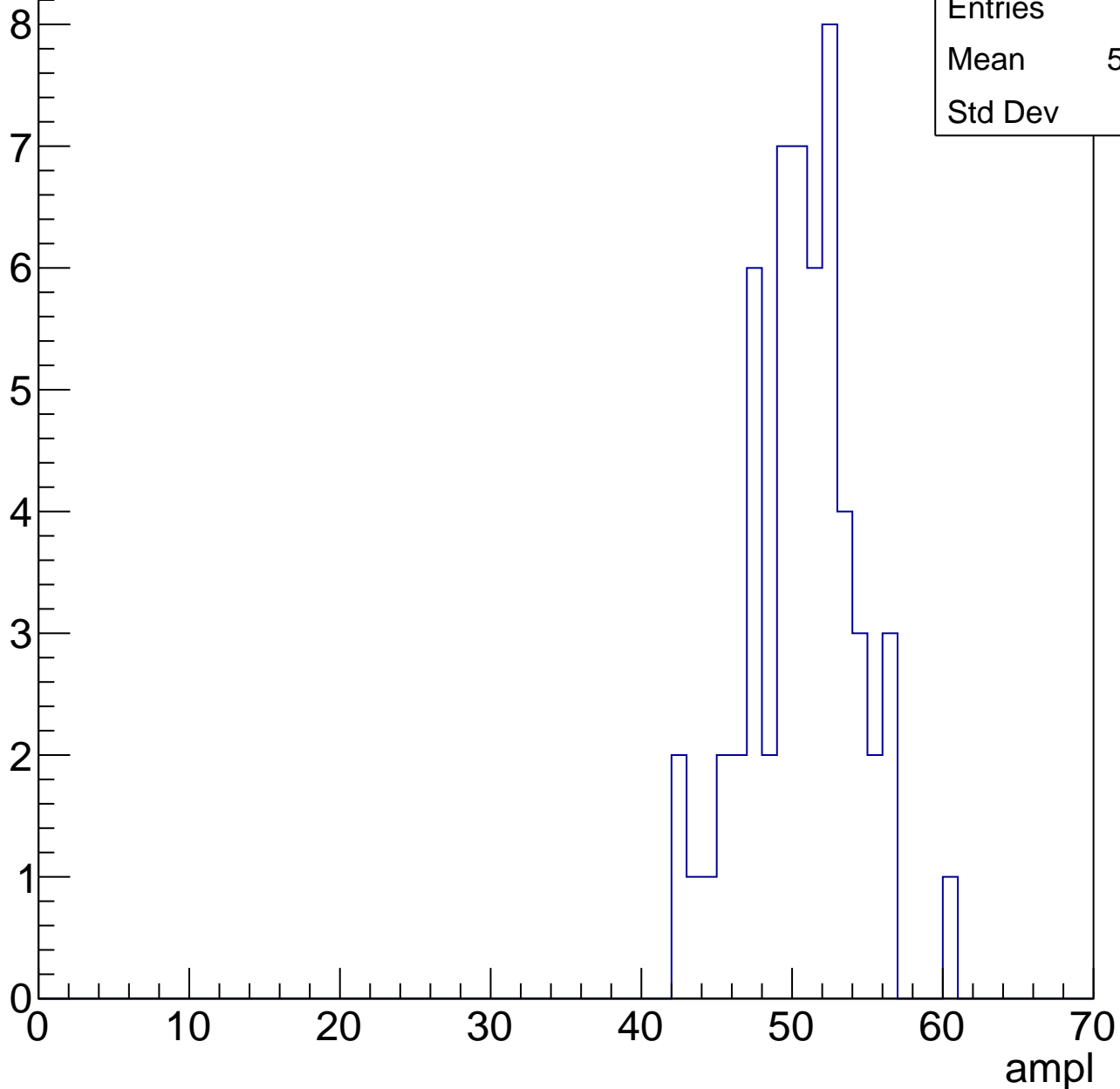


B0L001S, U14-ch116, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	50.14
Std Dev	3.61

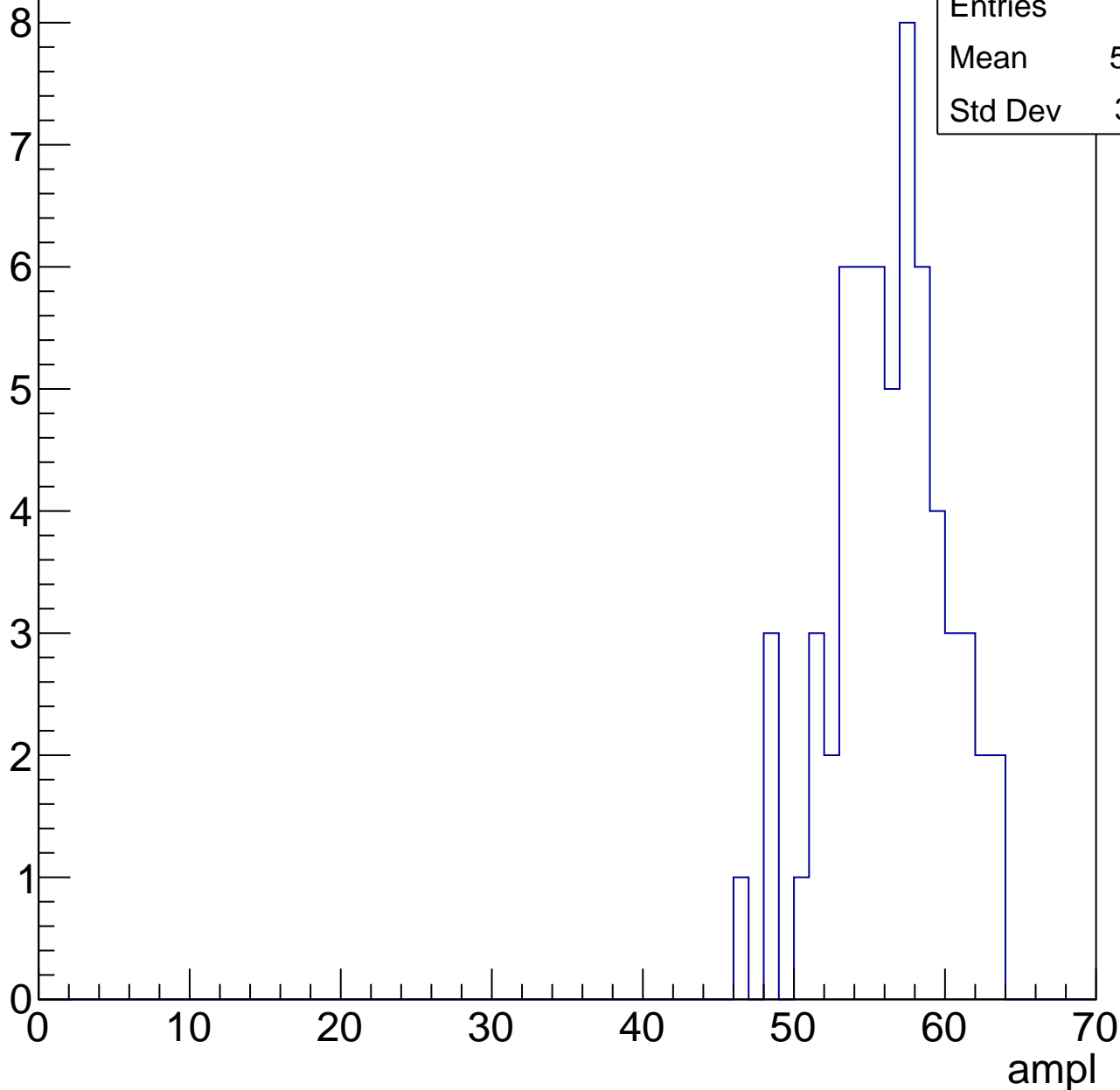


B0L001S, U14-ch116, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	55.77
Std Dev	3.791

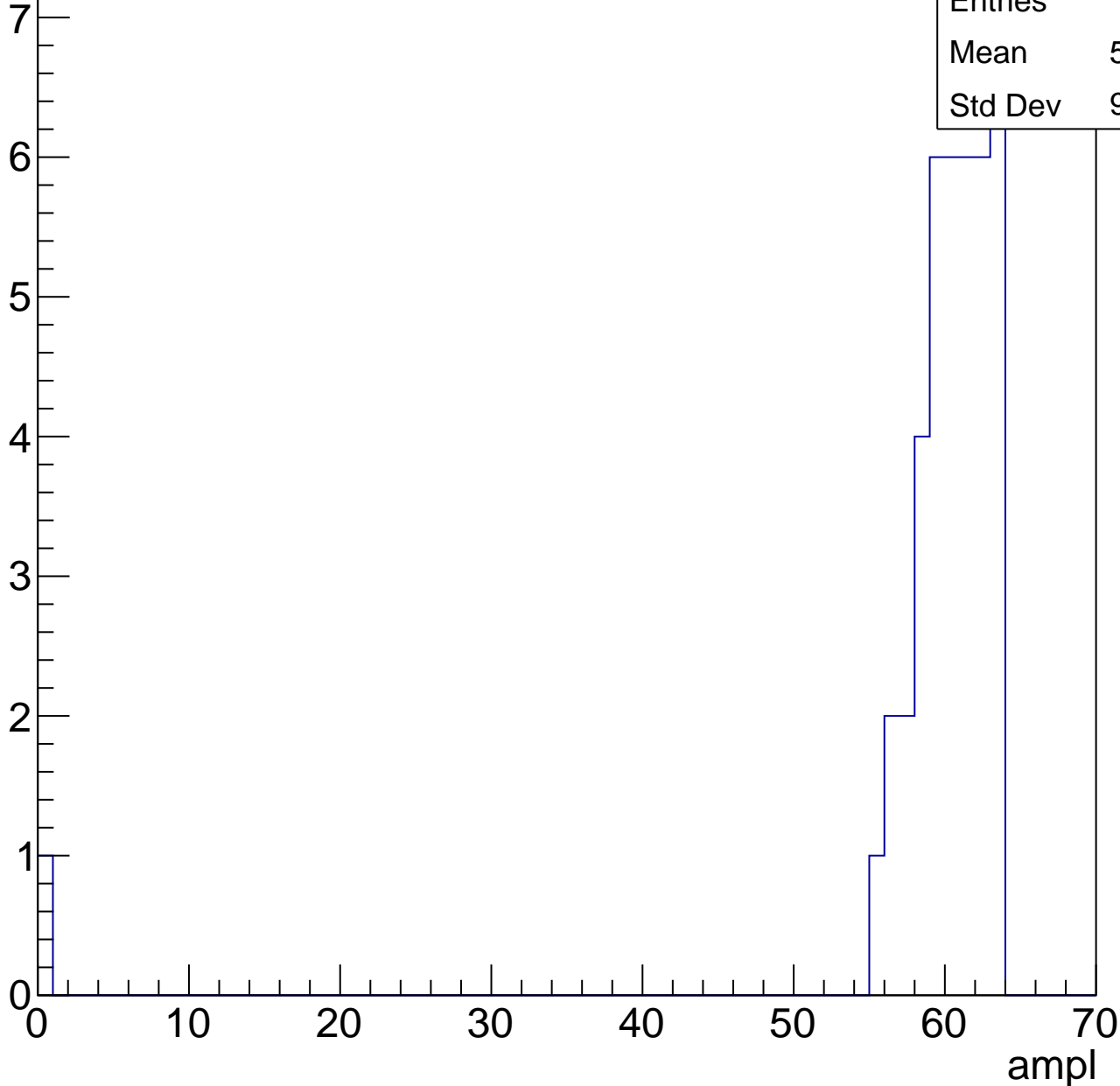


B0L001S, U14-ch116, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	41
Mean	58.68
Std Dev	9.524



B0L001S, U14-ch116, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B0L001S, U14-ch116, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch117, adc0

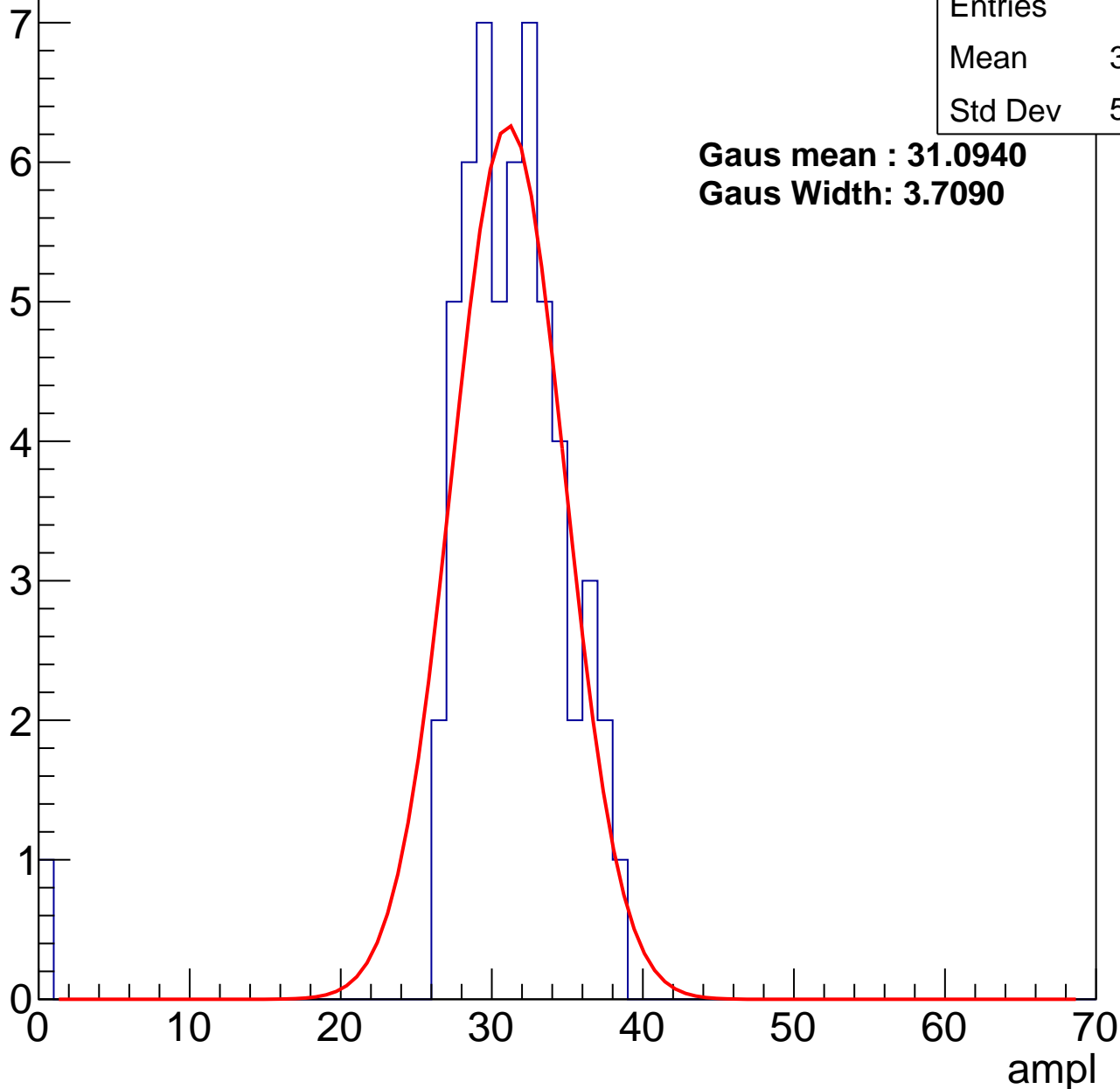
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	56
Mean	30.52
Std Dev	5.099

Gaus mean : 31.0940

Gaus Width: 3.7090



B0L001S, U14-ch117, adc1

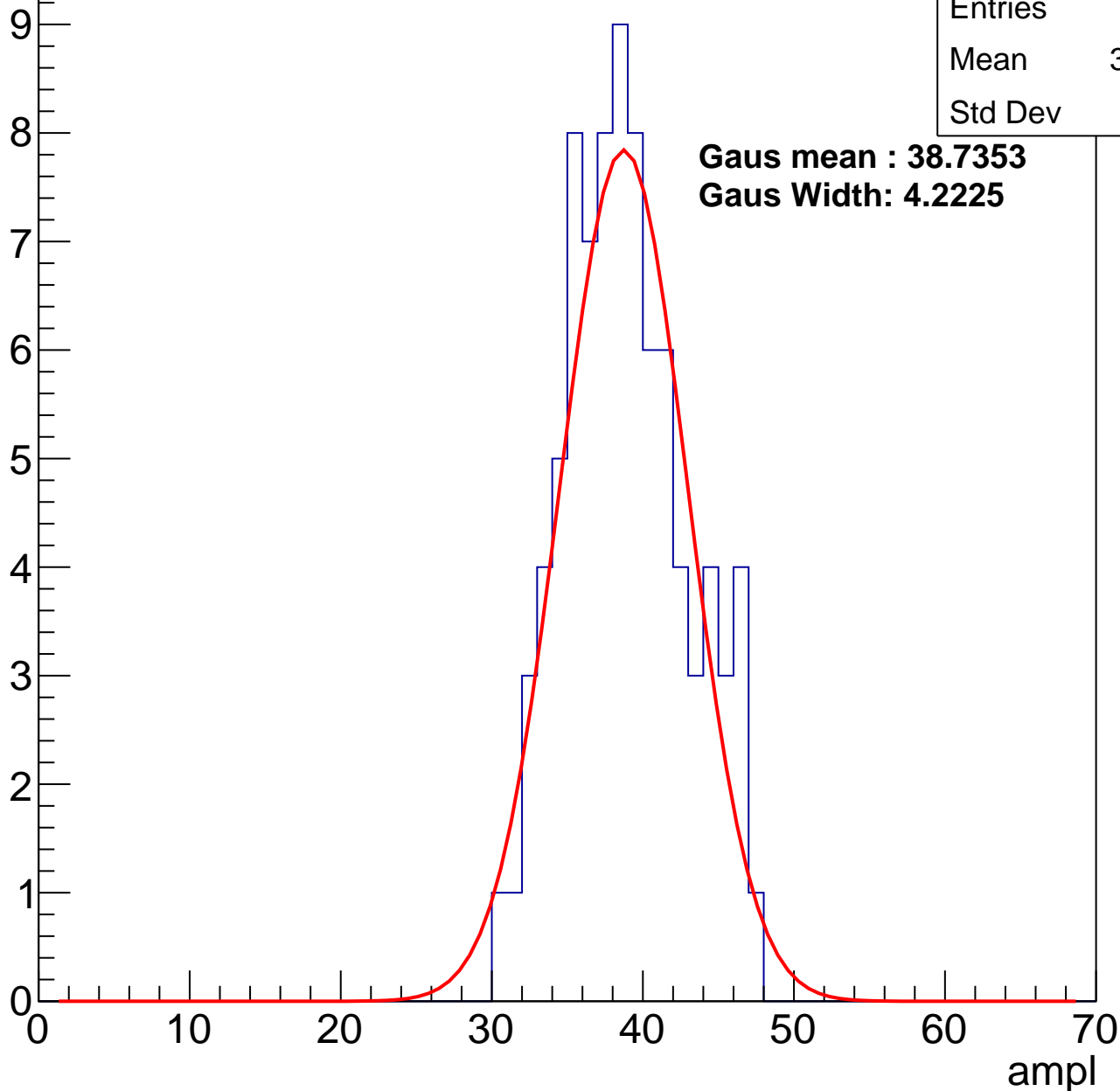
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	85
Mean	38.42
Std Dev	3.98

Gaus mean : 38.7353

Gaus Width: 4.2225



B0L001S, U14-ch117, adc2

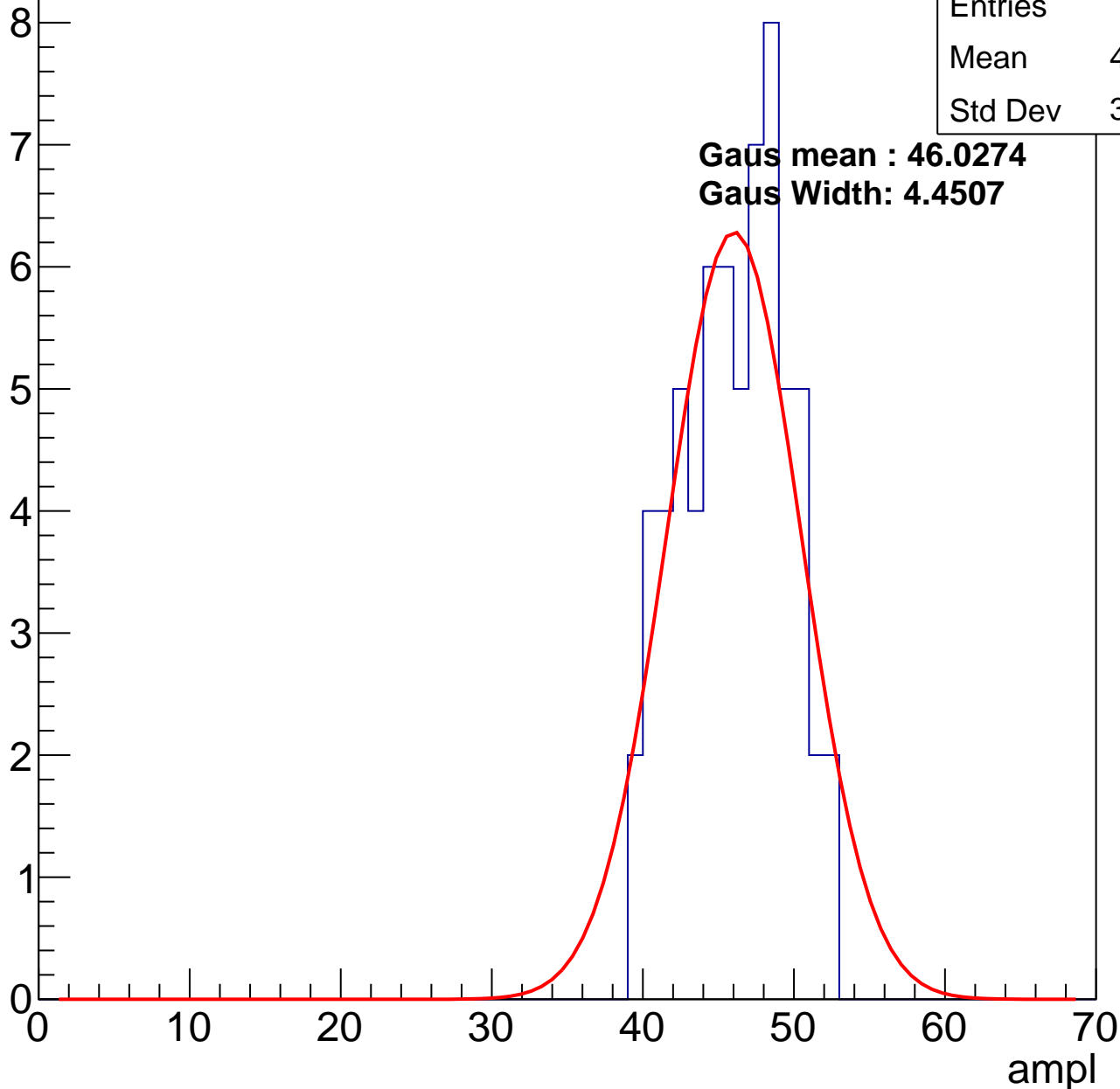
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	45.57
Std Dev	3.424

Gaus mean : 46.0274

Gaus Width: 4.4507

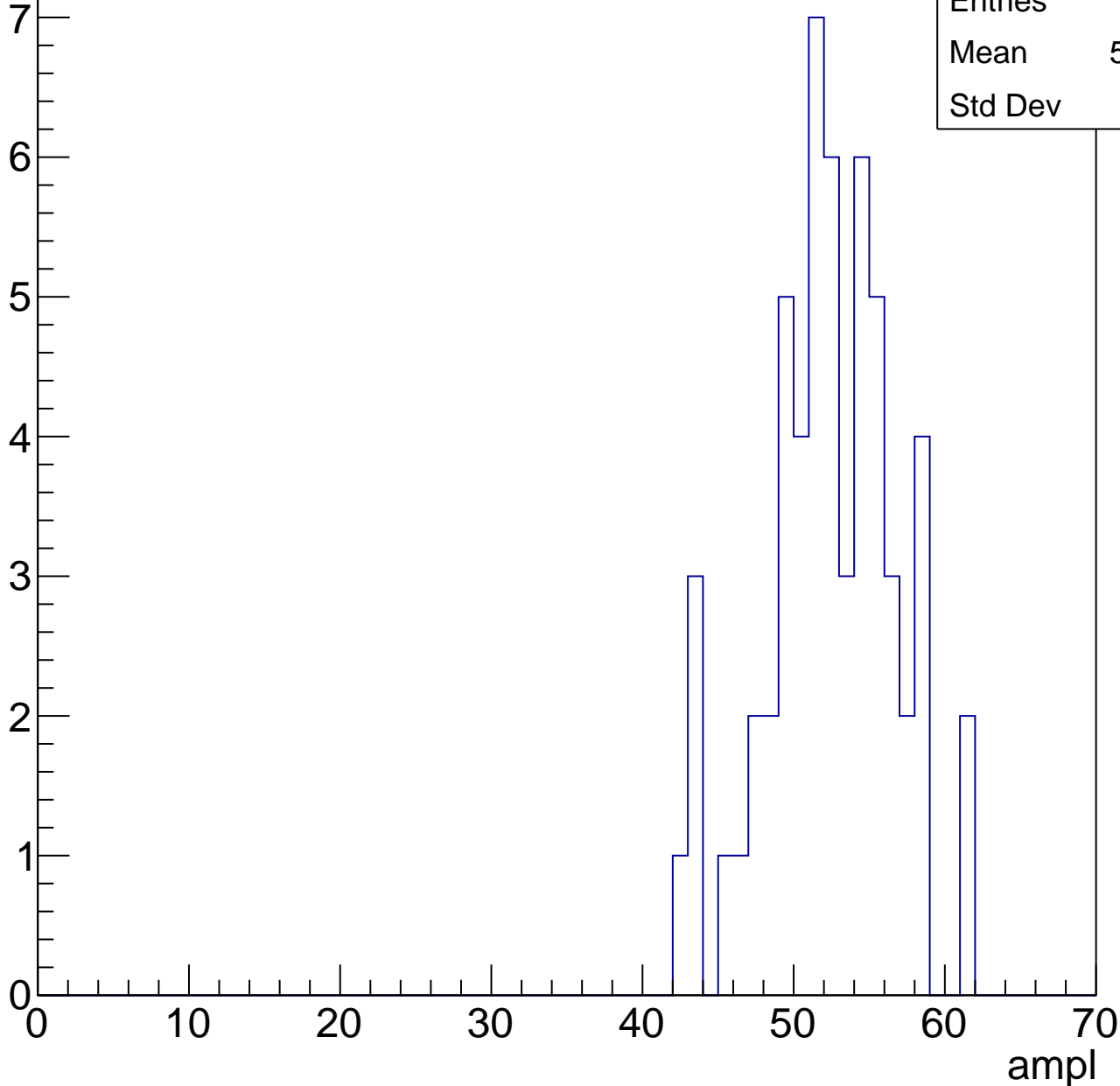


B0L001S, U14-ch117, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	51.93
Std Dev	4.3

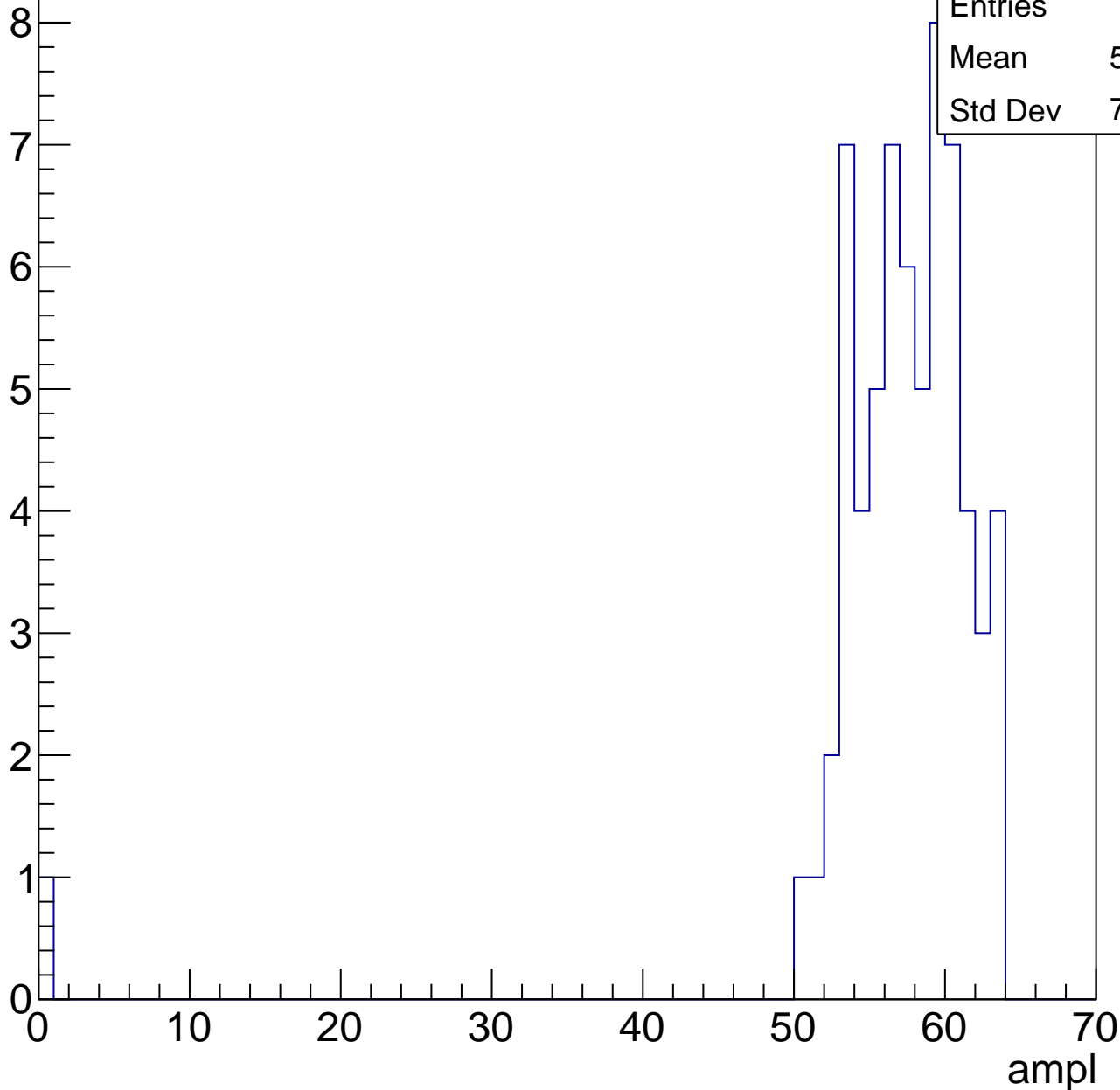


B0L001S, U14-ch117, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	56.38
Std Dev	7.763

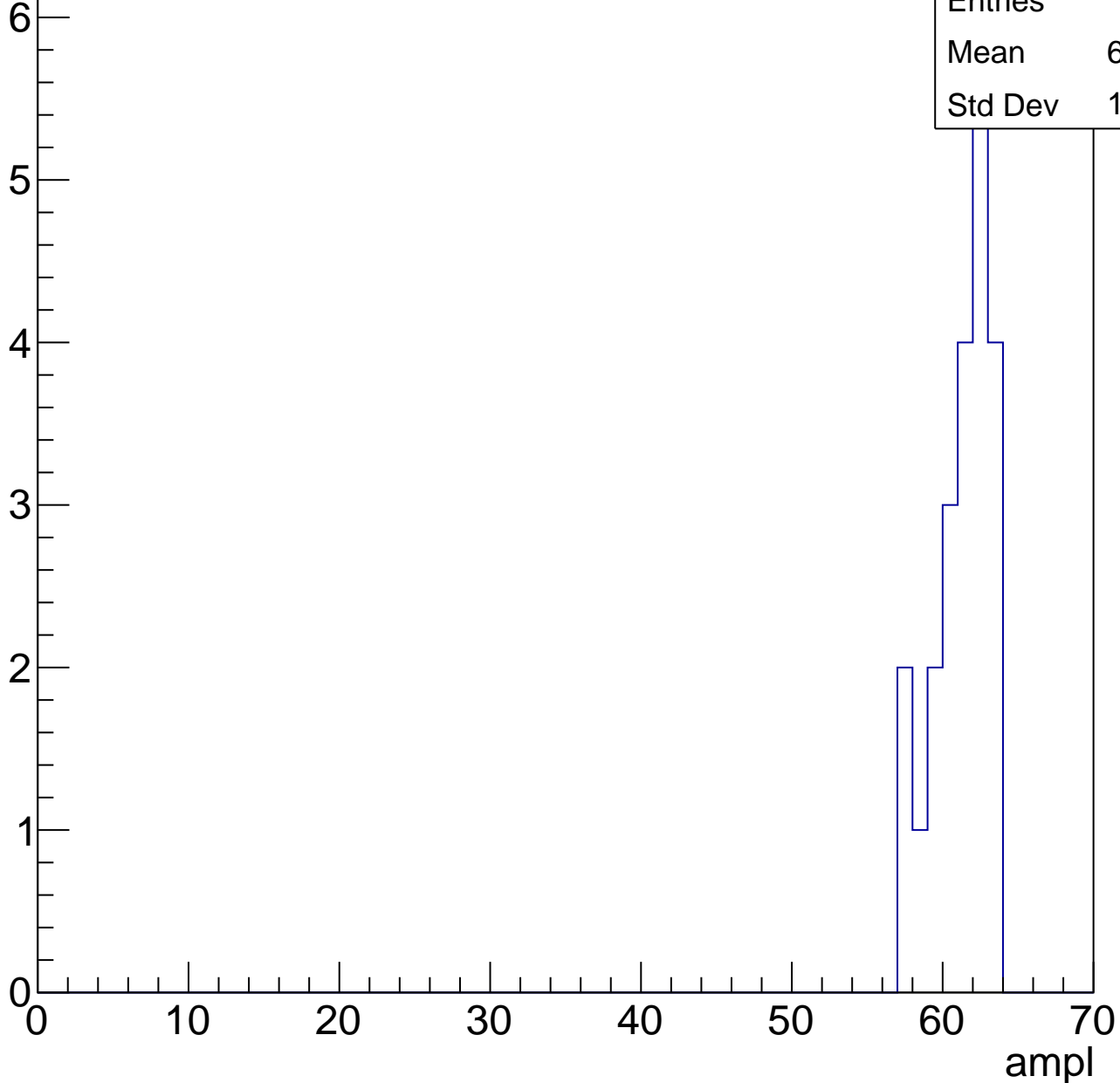


B0L001S, U14-ch117, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	22
Mean	60.82
Std Dev	1.825



B0L001S, U14-ch117, adc6

calib_packv5_042523_0143.root, FC#9, port A1

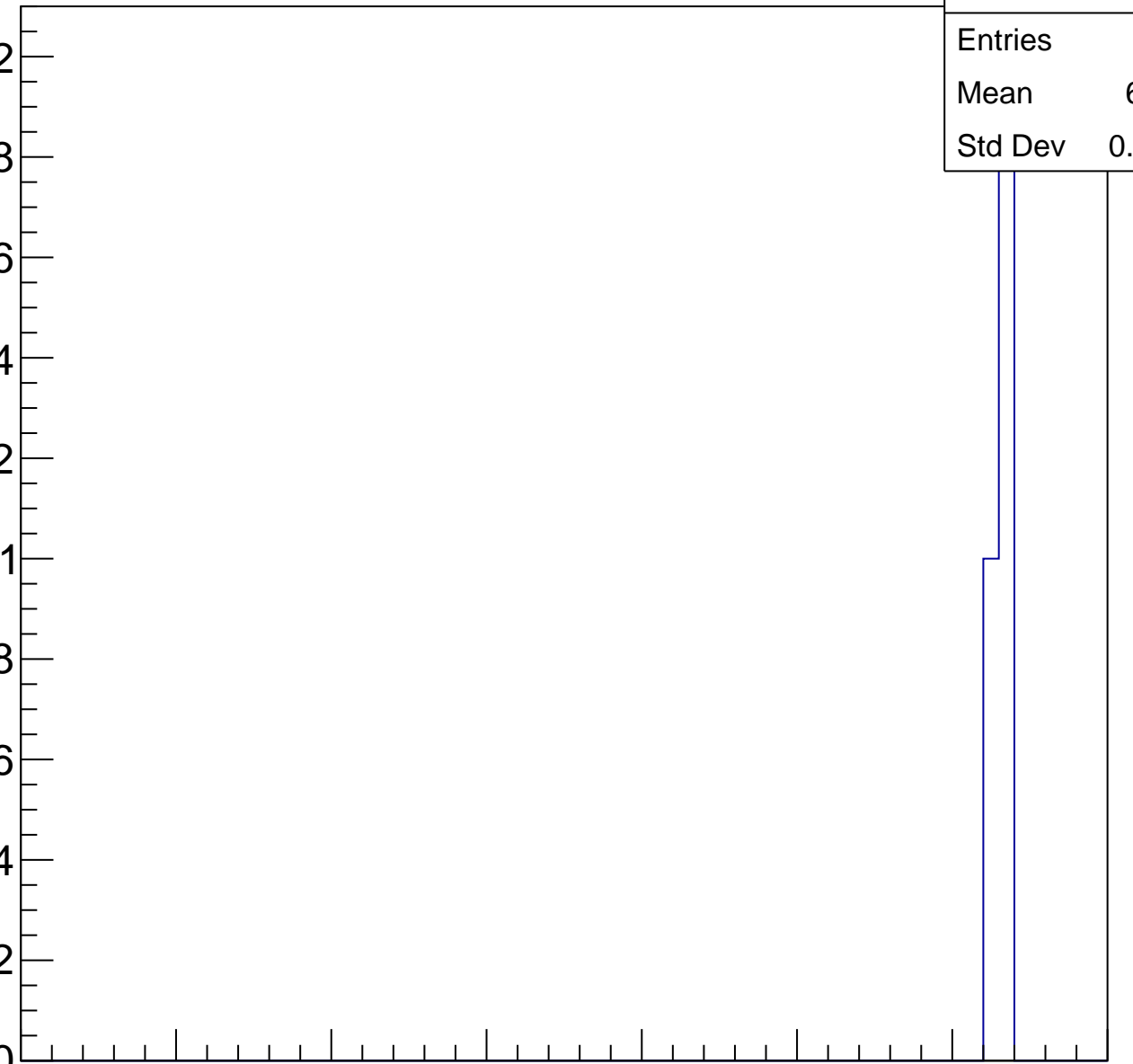
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

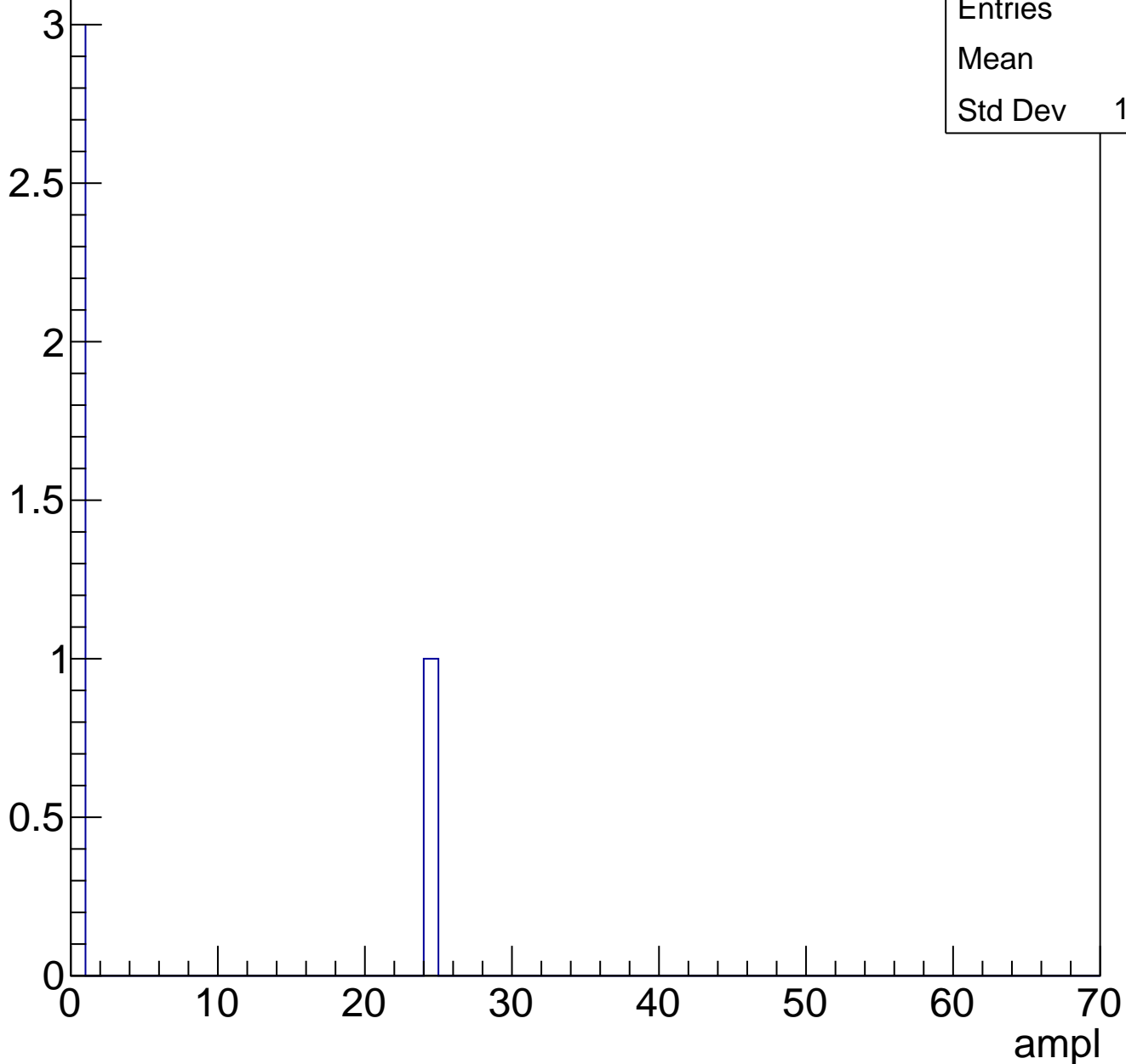
0 10 20 30 40 50 60 70



B0L001S, U14-ch117, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch118, adc0

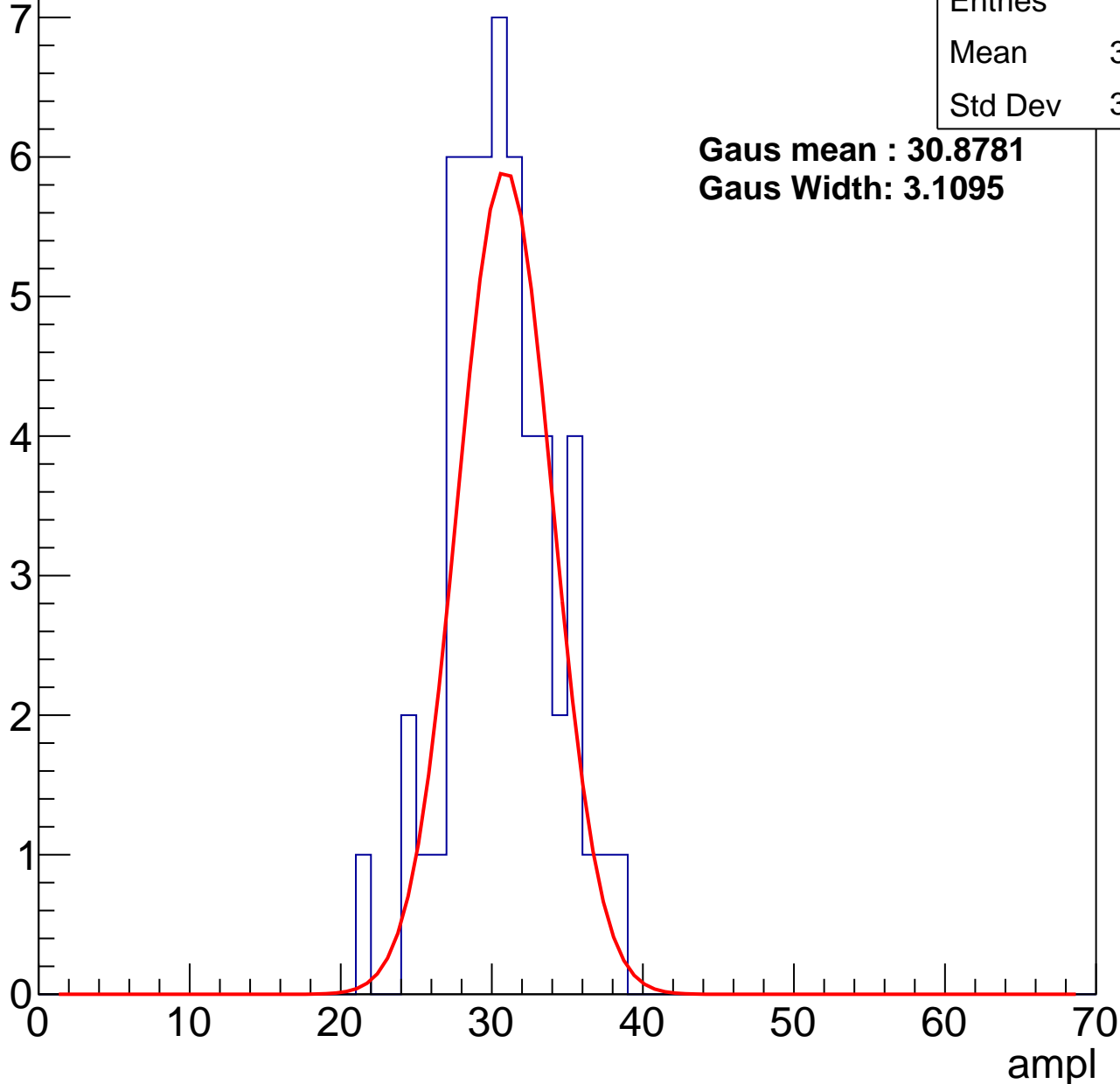
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	30.17
Std Dev	3.408

Gaus mean : 30.8781

Gaus Width: 3.1095



B0L001S, U14-ch118, adc1

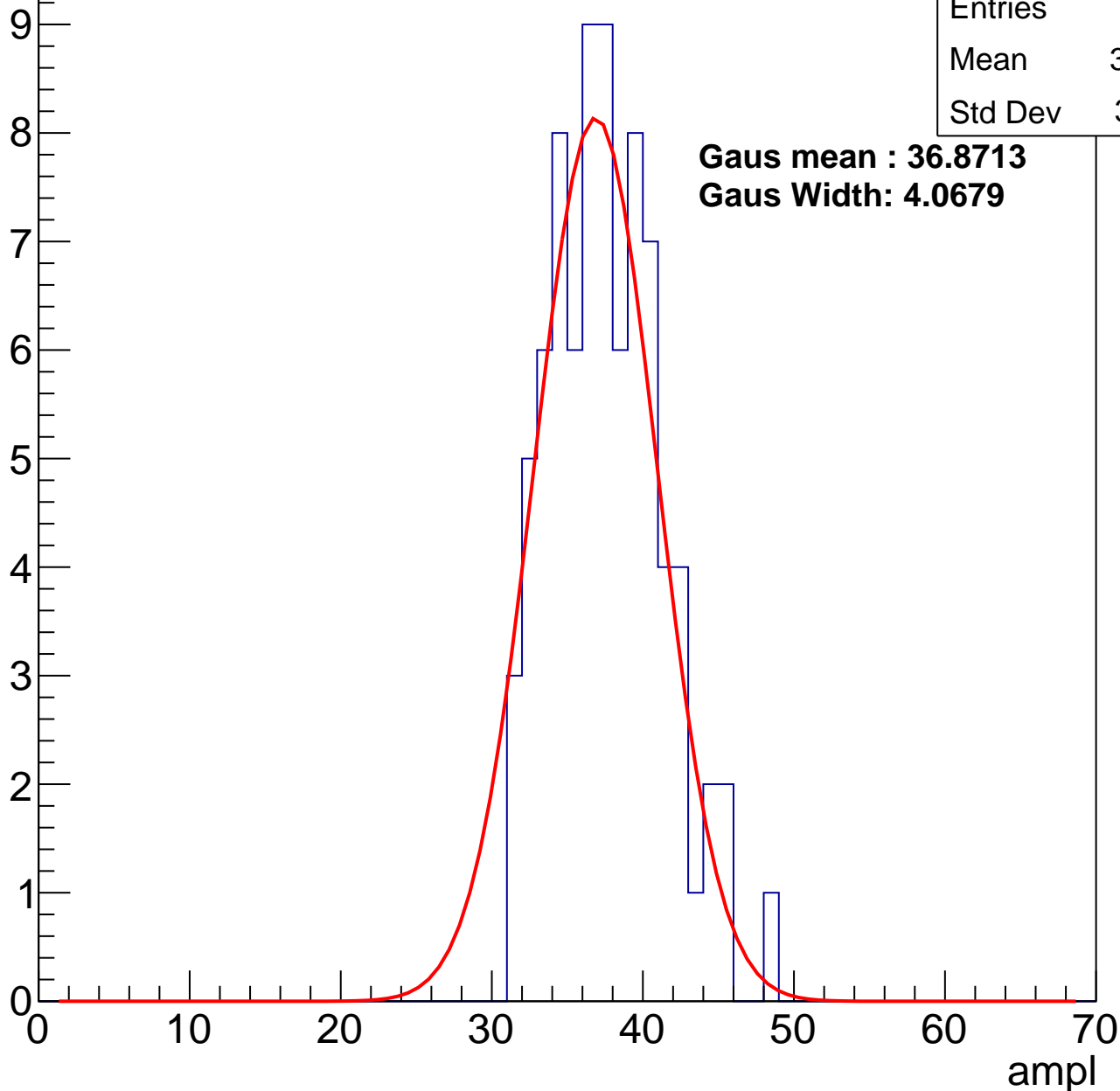
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	81
Mean	37.17
Std Dev	3.651

Gaus mean : 36.8713

Gaus Width: 4.0679



B0L001S, U14-ch118, adc2

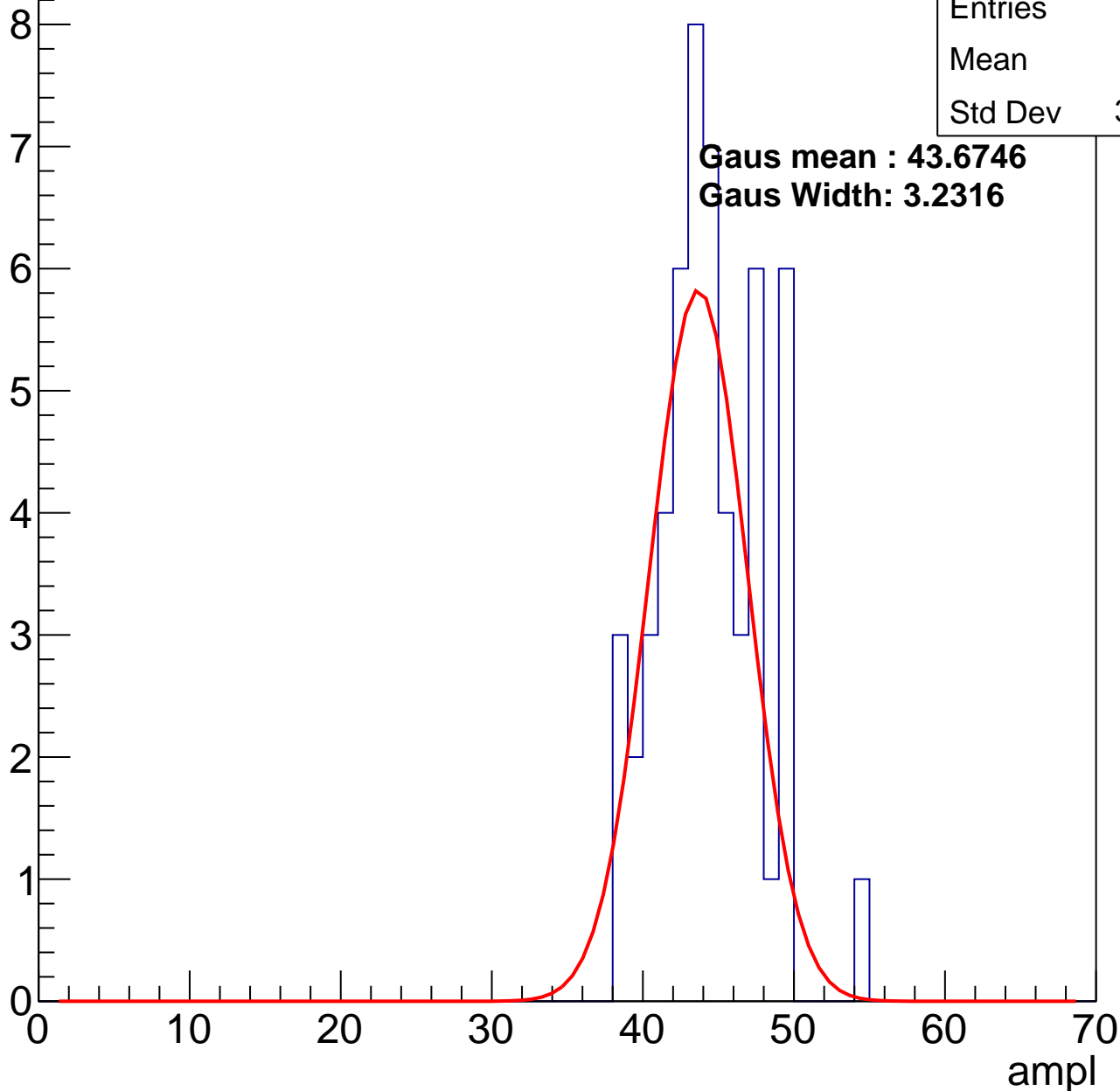
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	44
Std Dev	3.361

Gaus mean : 43.6746

Gaus Width: 3.2316

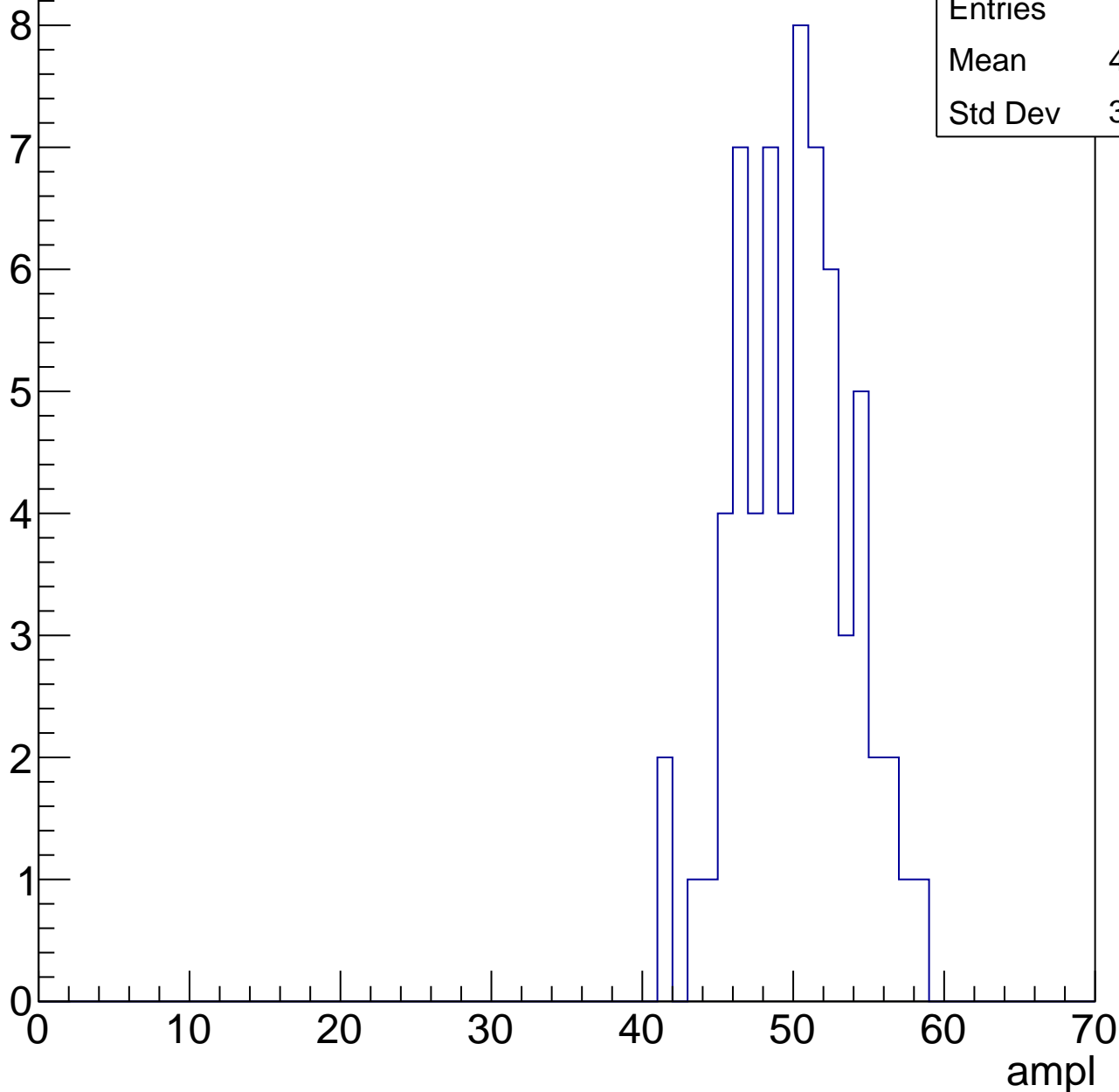


B0L001S, U14-ch118, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	65
Mean	49.63
Std Dev	3.703

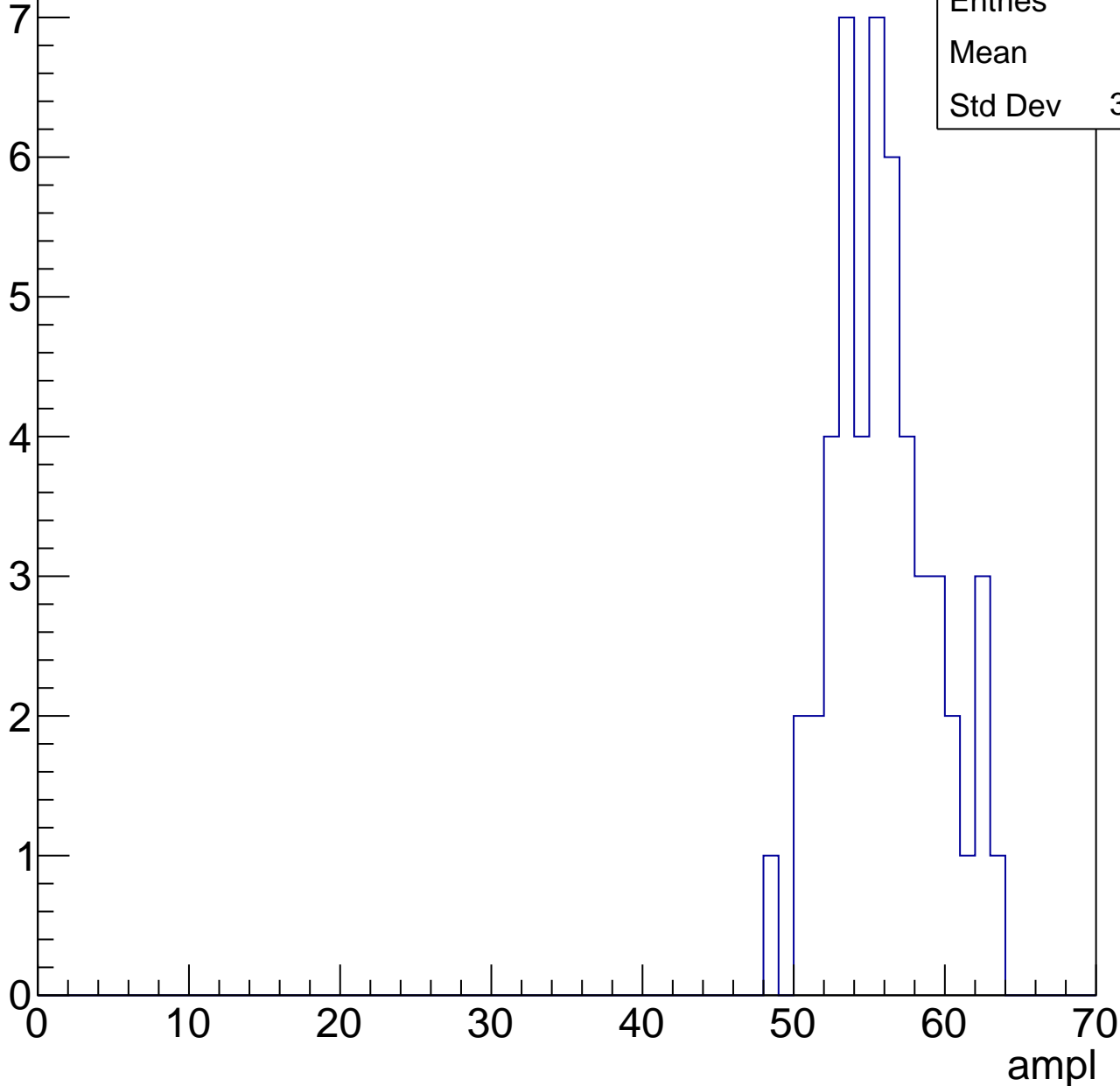


B0L001S, U14-ch118, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	50
Mean	55.5
Std Dev	3.407

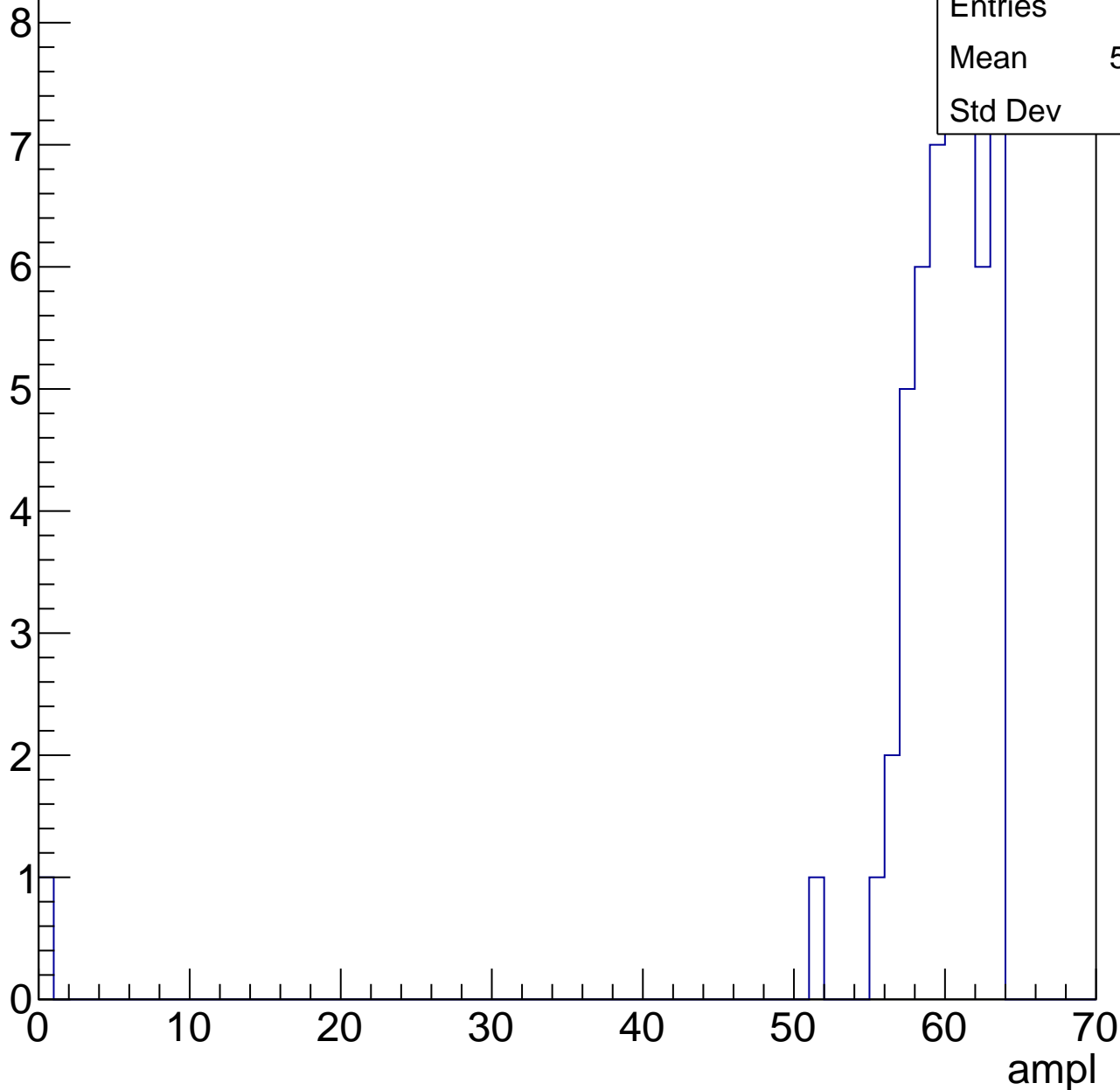


B0L001S, U14-ch118, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	58.64
Std Dev	8.49



B0L001S, U14-ch118, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch118, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch119, adc0

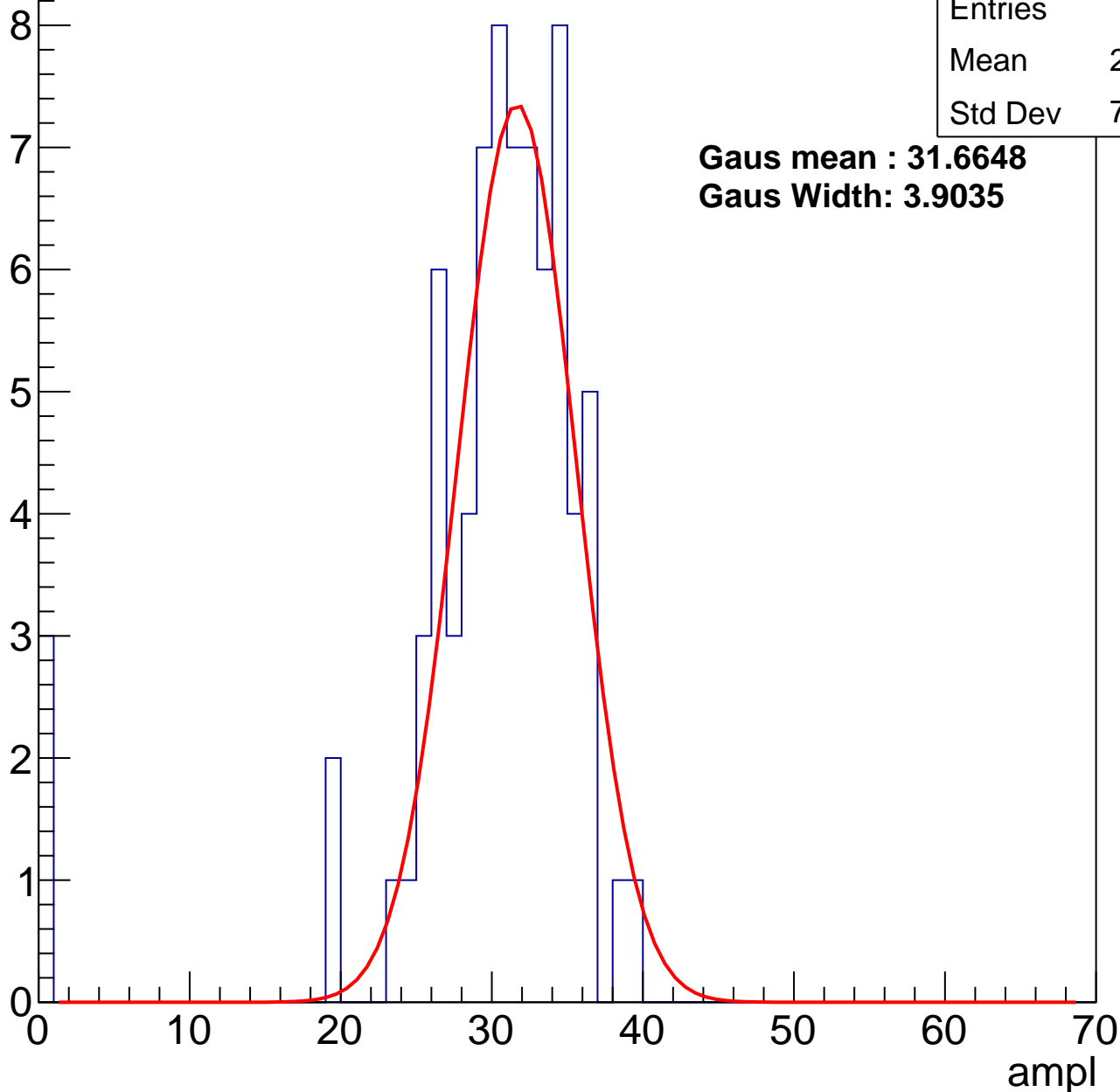
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	77
Mean	29.35
Std Dev	7.078

Gaus mean : 31.6648

Gaus Width: 3.9035



B0L001S, U14-ch119, adc1

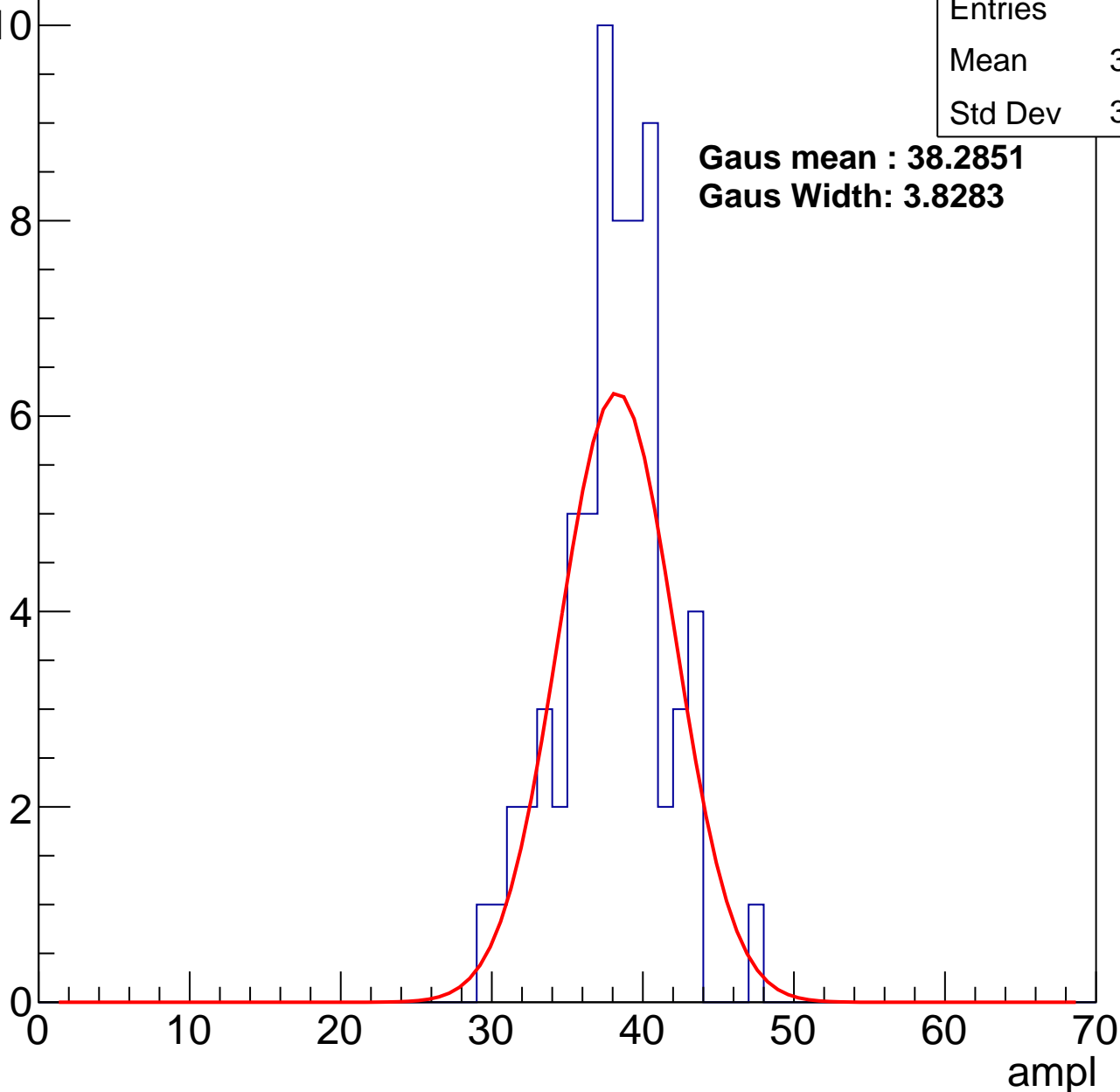
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	37.58
Std Dev	3.429

Gaus mean : 38.2851

Gaus Width: 3.8283



B0L001S, U14-ch119, adc2

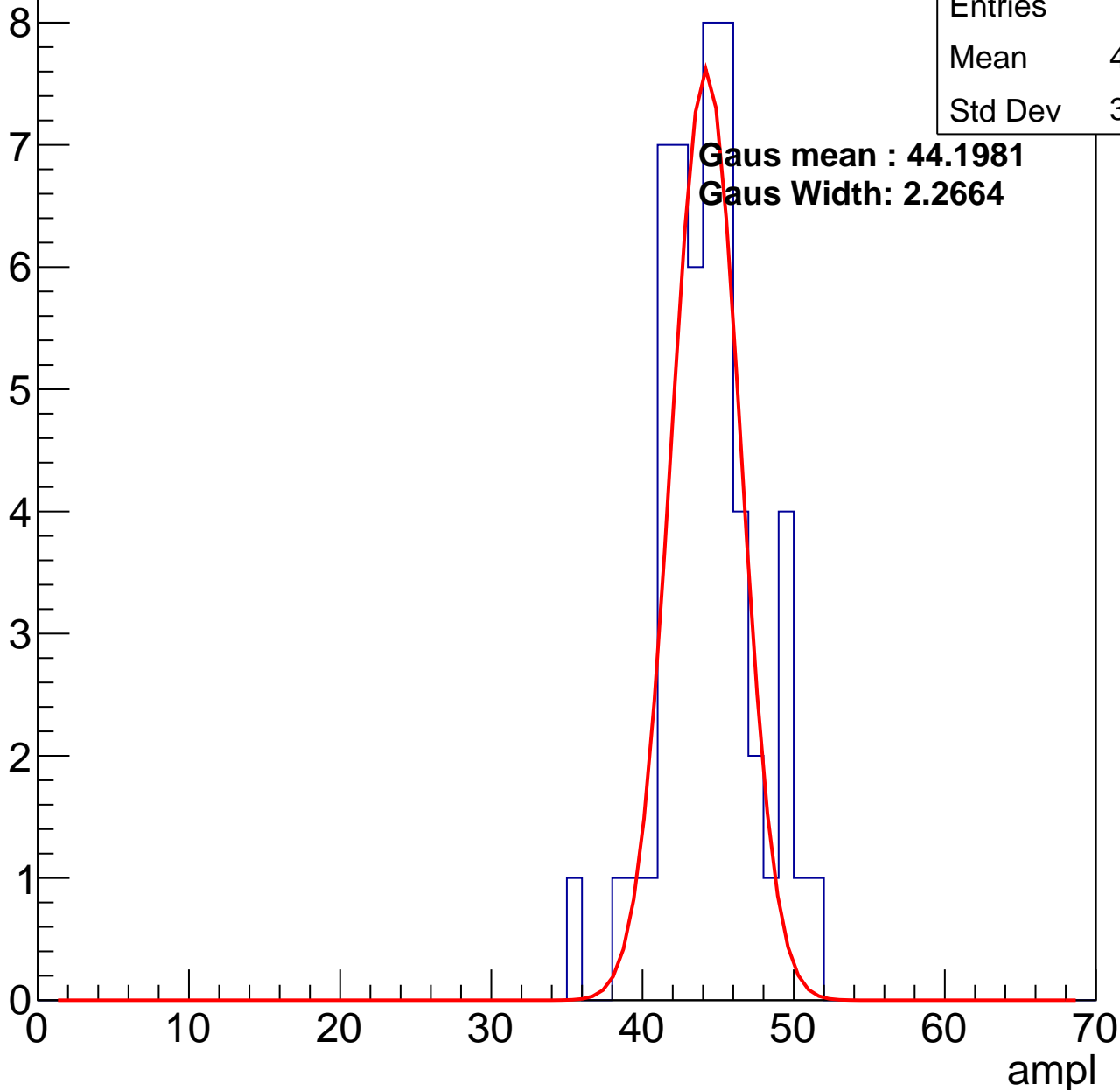
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	43.89
Std Dev	3.063

Gaus mean : 44.1981

Gaus Width: 2.2664

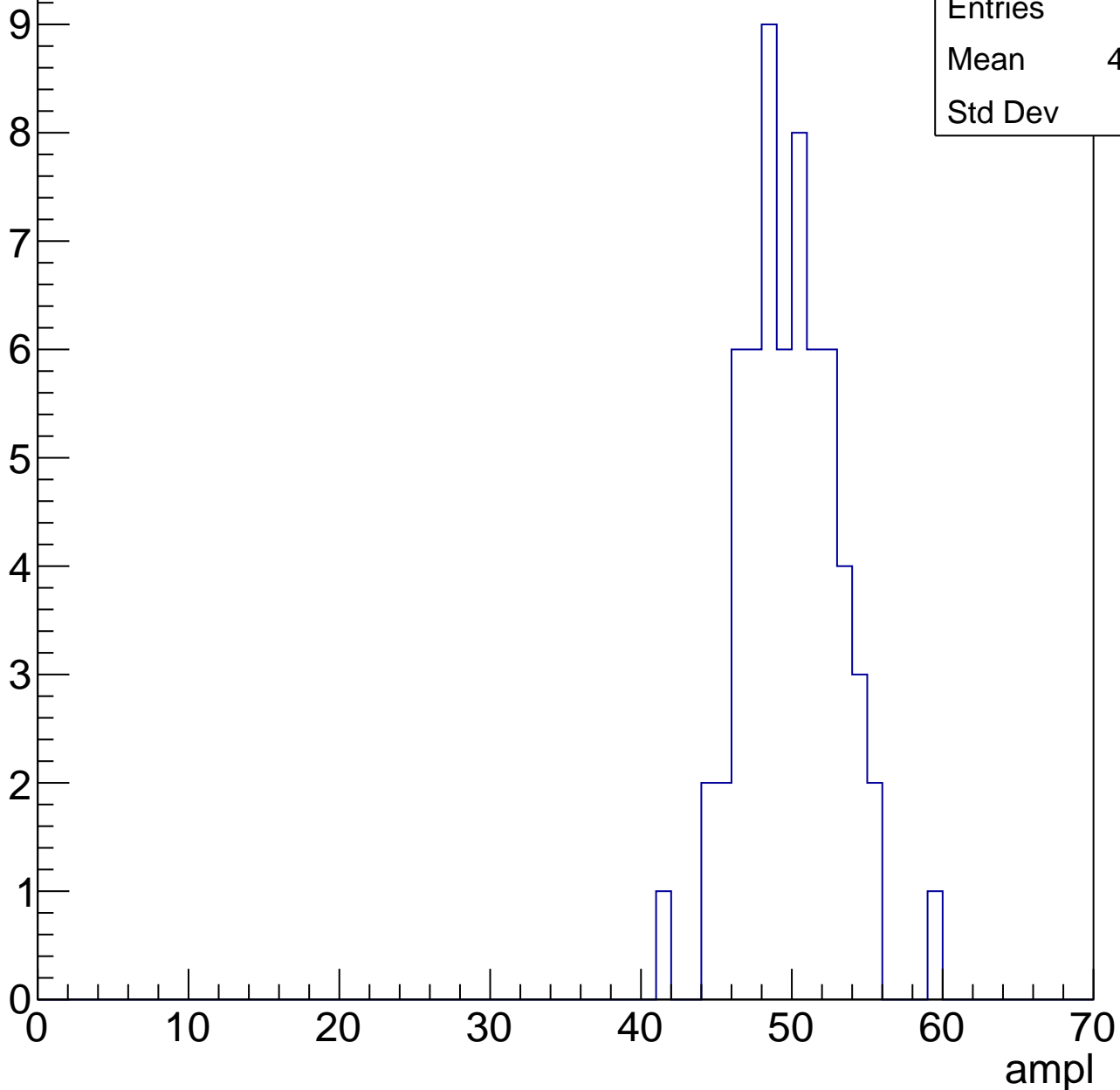


B0L001S, U14-ch119, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	49.42
Std Dev	3.16

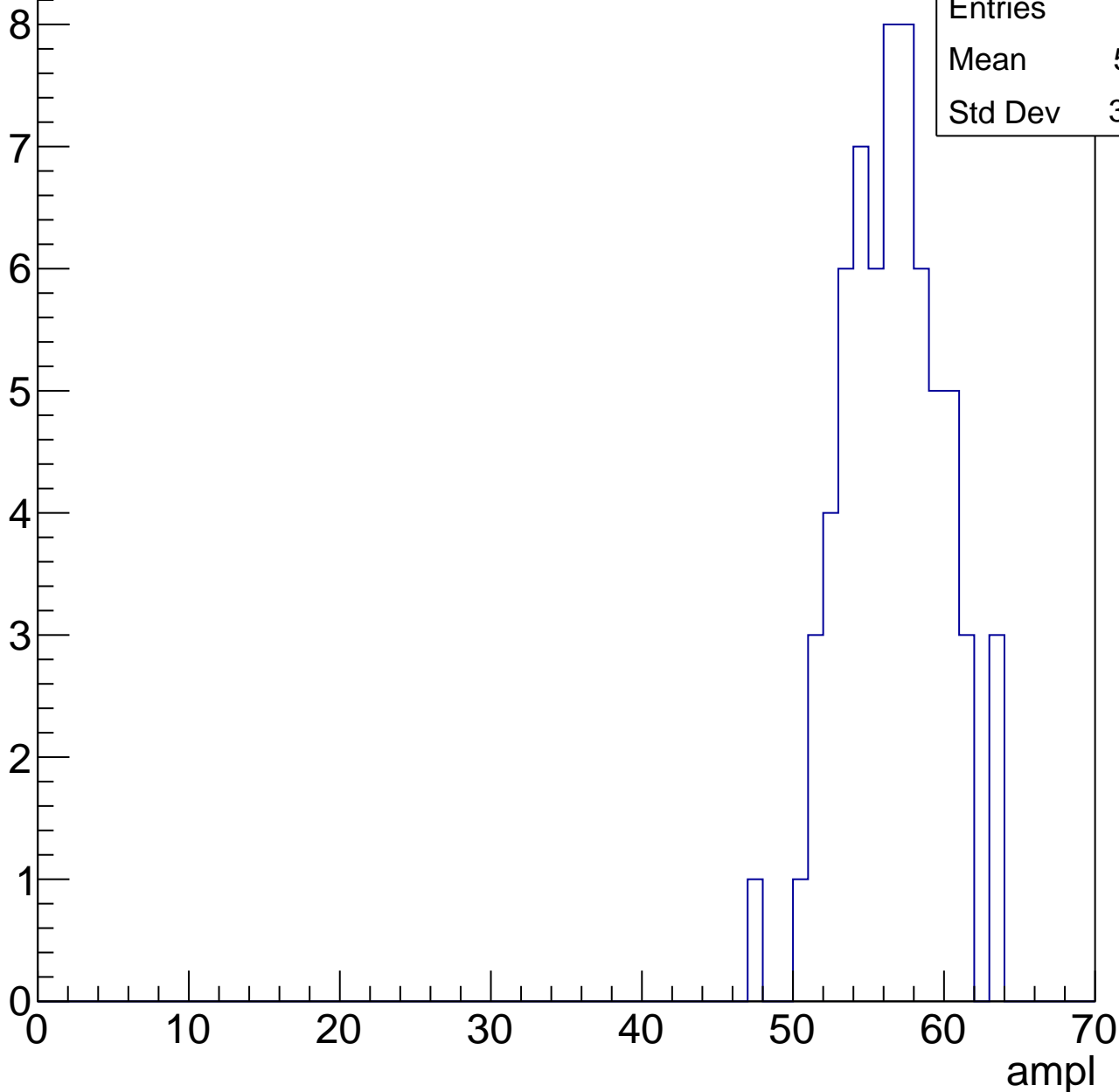


B0L001S, U14-ch119, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	66
Mean	56.11
Std Dev	3.308

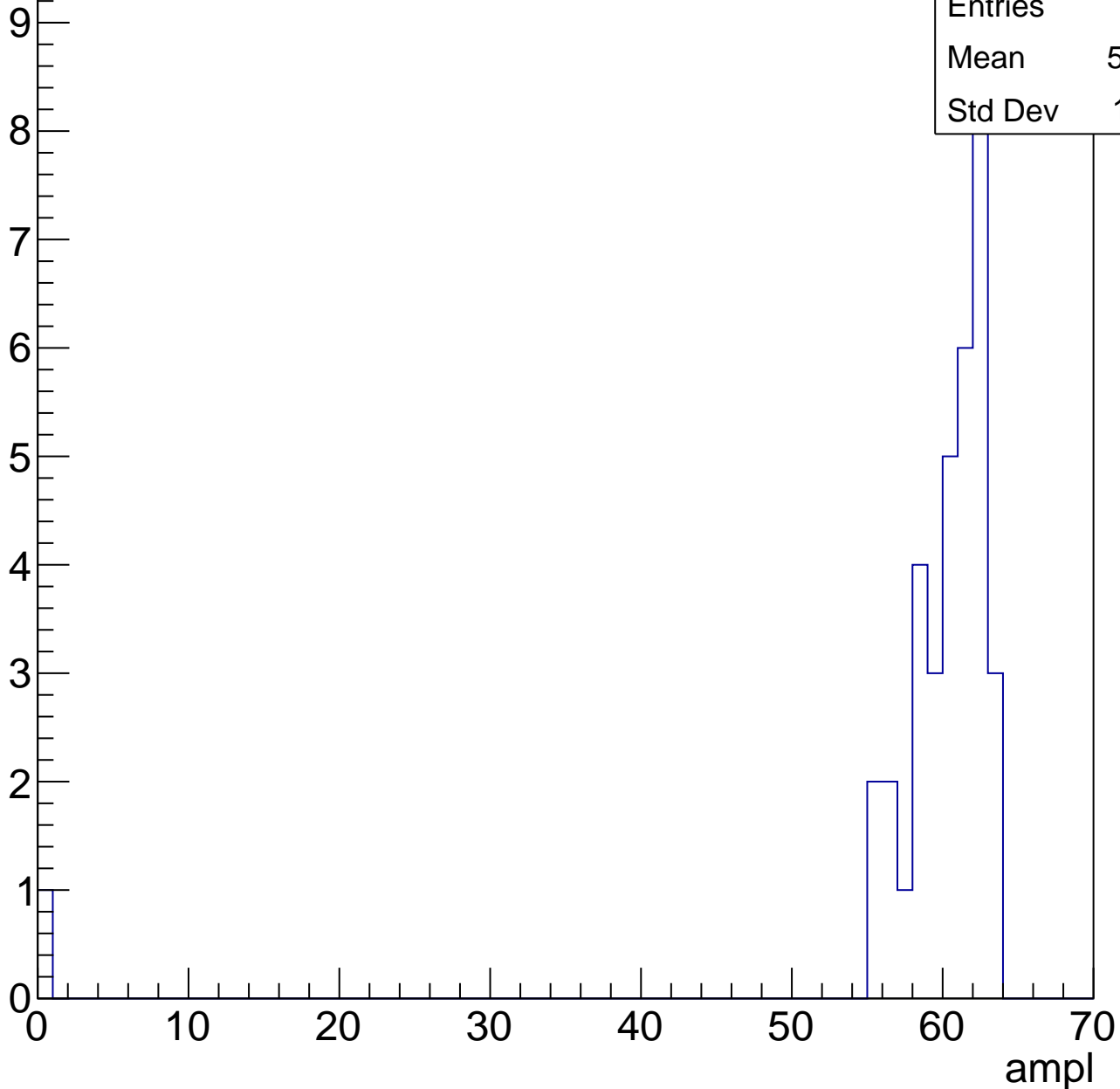


B0L001S, U14-ch119, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

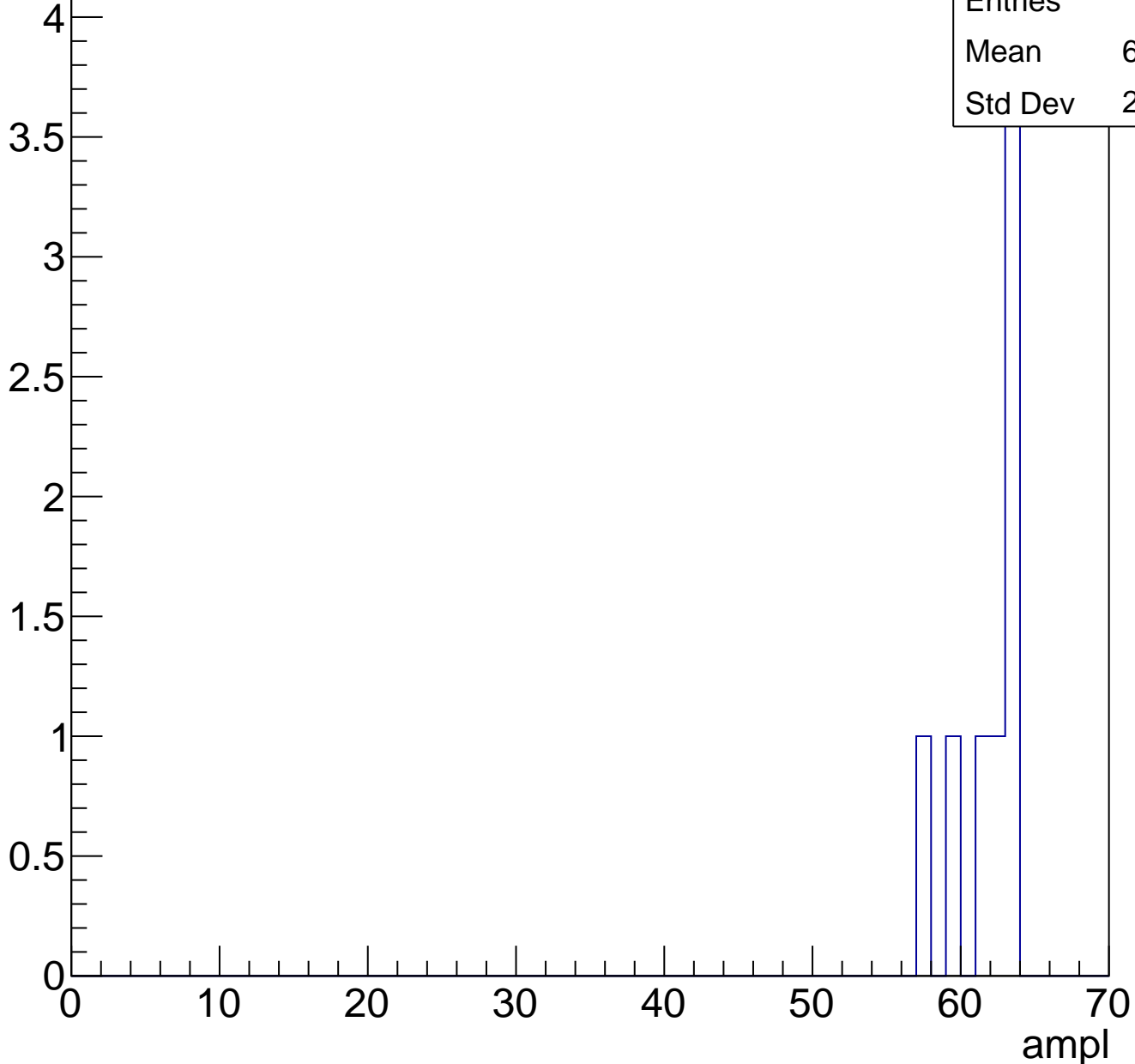
Entries	36
Mean	58.36
Std Dev	10.11



B0L001S, U14-ch119, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	8
Mean	61.38
Std Dev	2.118

B0L001S, U14-ch119, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch120, adc0

calib_packv5_042523_0143.root, FC#9, port A1

Entries	86
Mean	28.99
Std Dev	4.812

Gaus mean : 29.7821

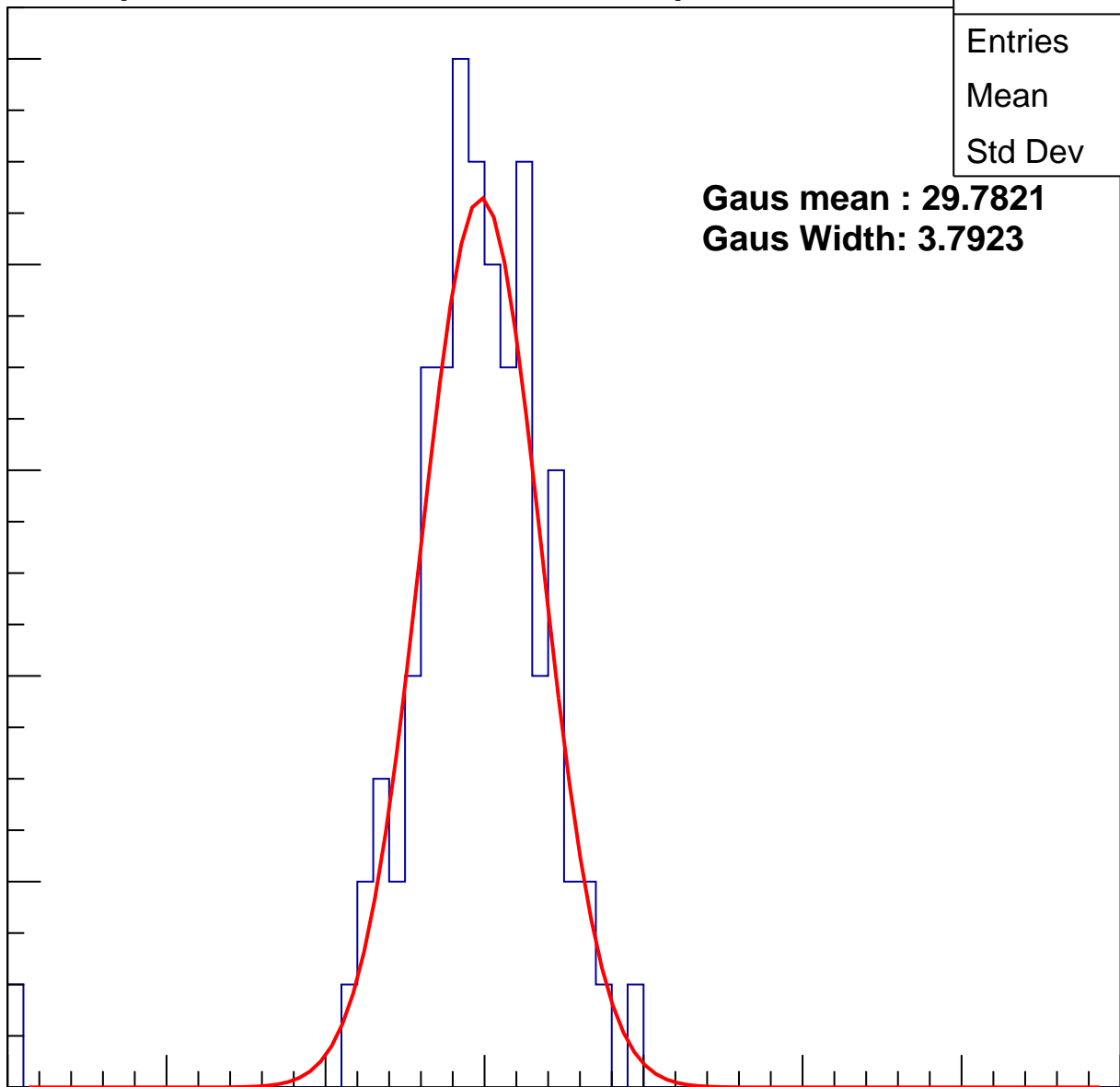
Gaus Width: 3.7923

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L001S, U14-ch120, adc1

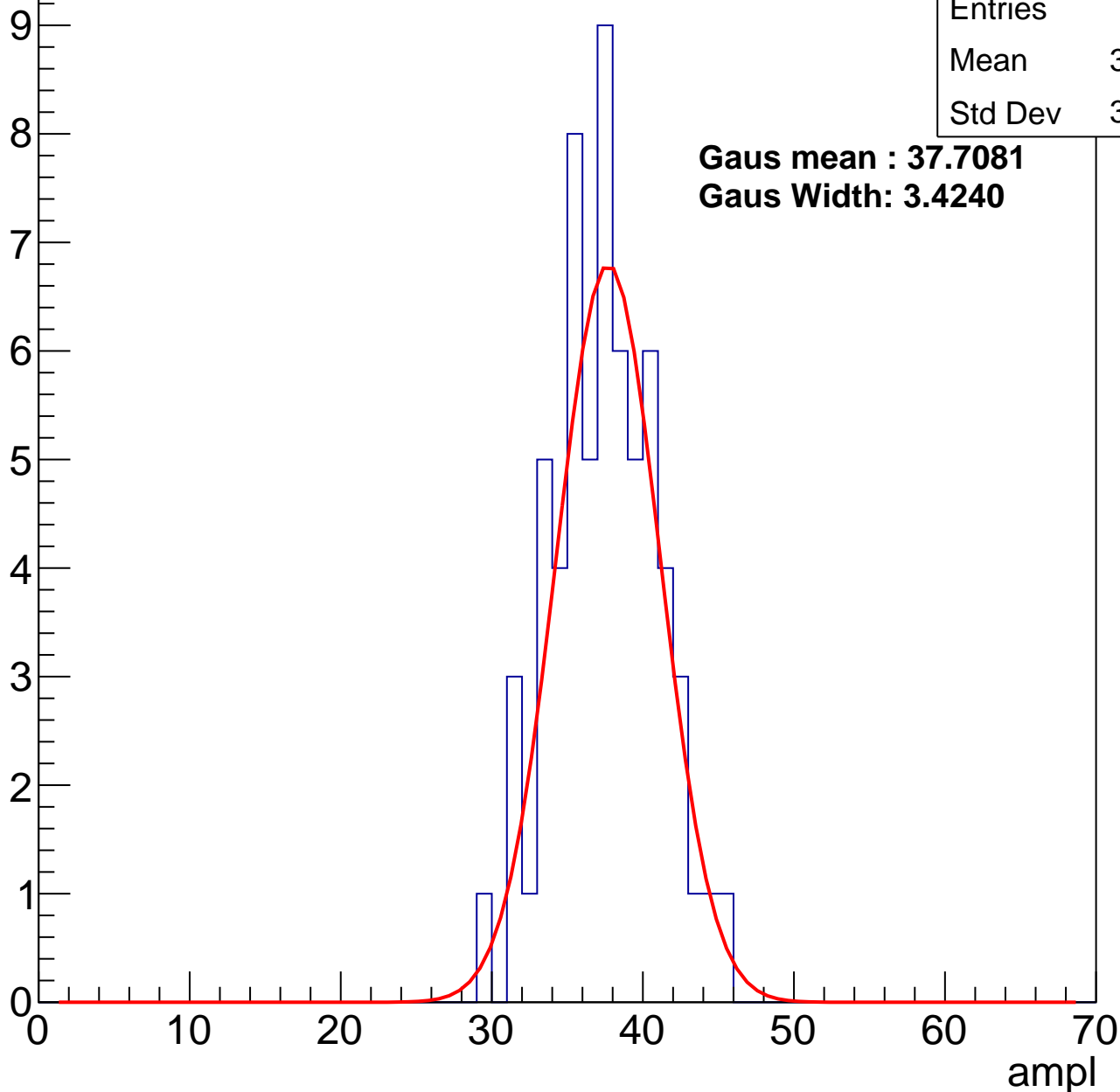
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	37.03
Std Dev	3.376

Gaus mean : 37.7081

Gaus Width: 3.4240

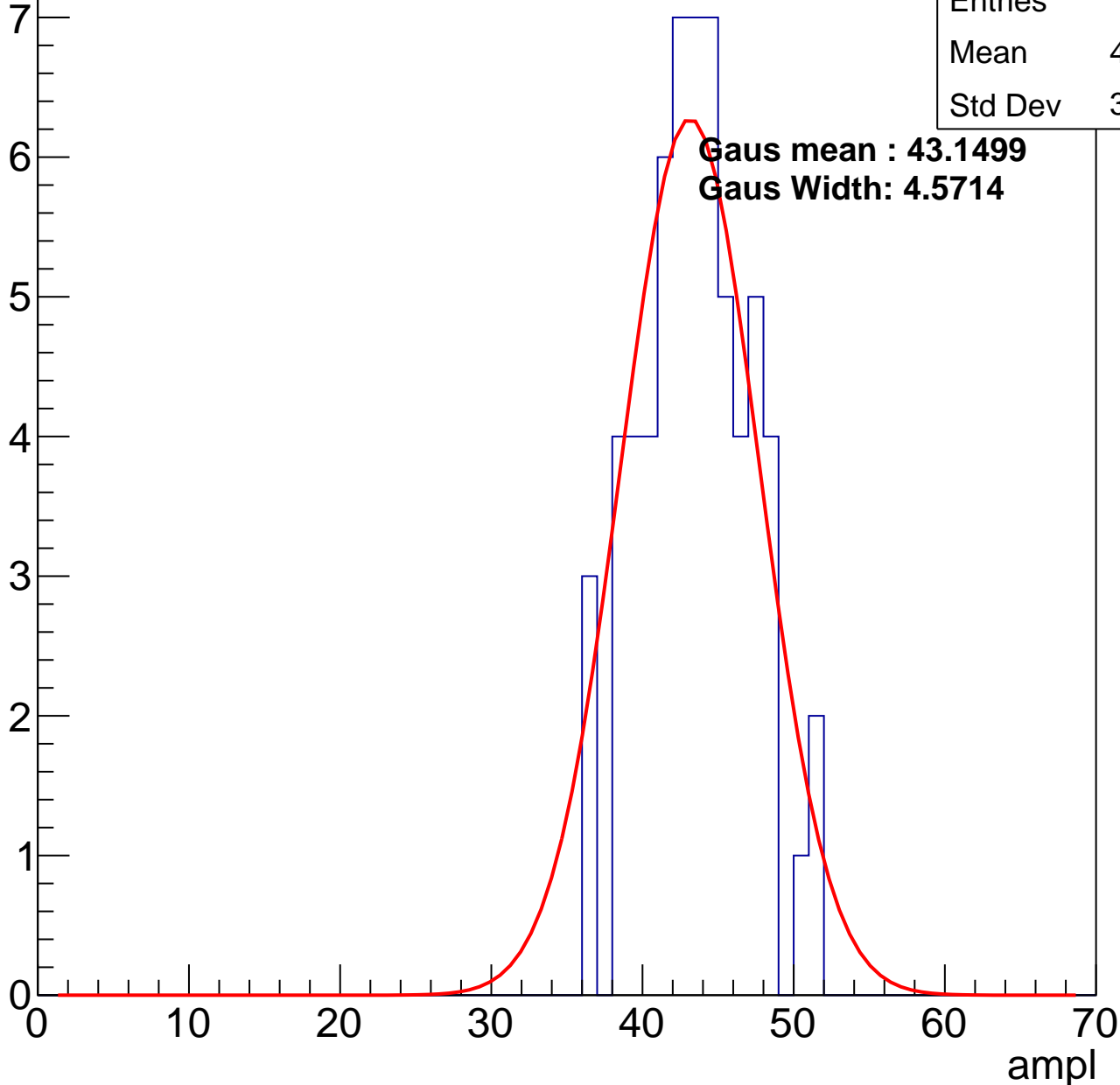


B0L001S, U14-ch120, adc2

calib_packv5_042523_0143.root, FC#9, port A1

Entry

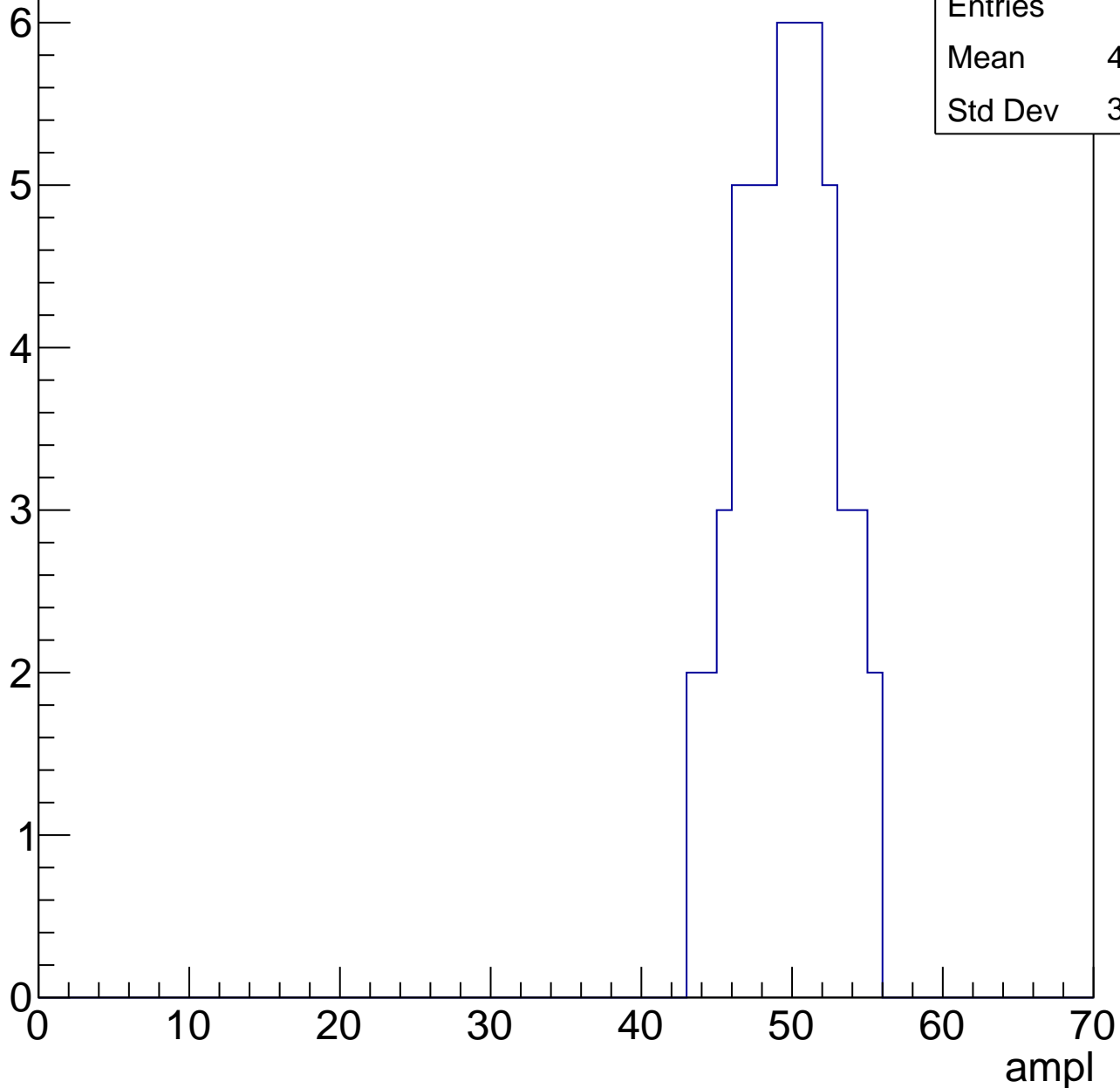
Entries	63
Mean	43.06
Std Dev	3.558



B0L001S, U14-ch120, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

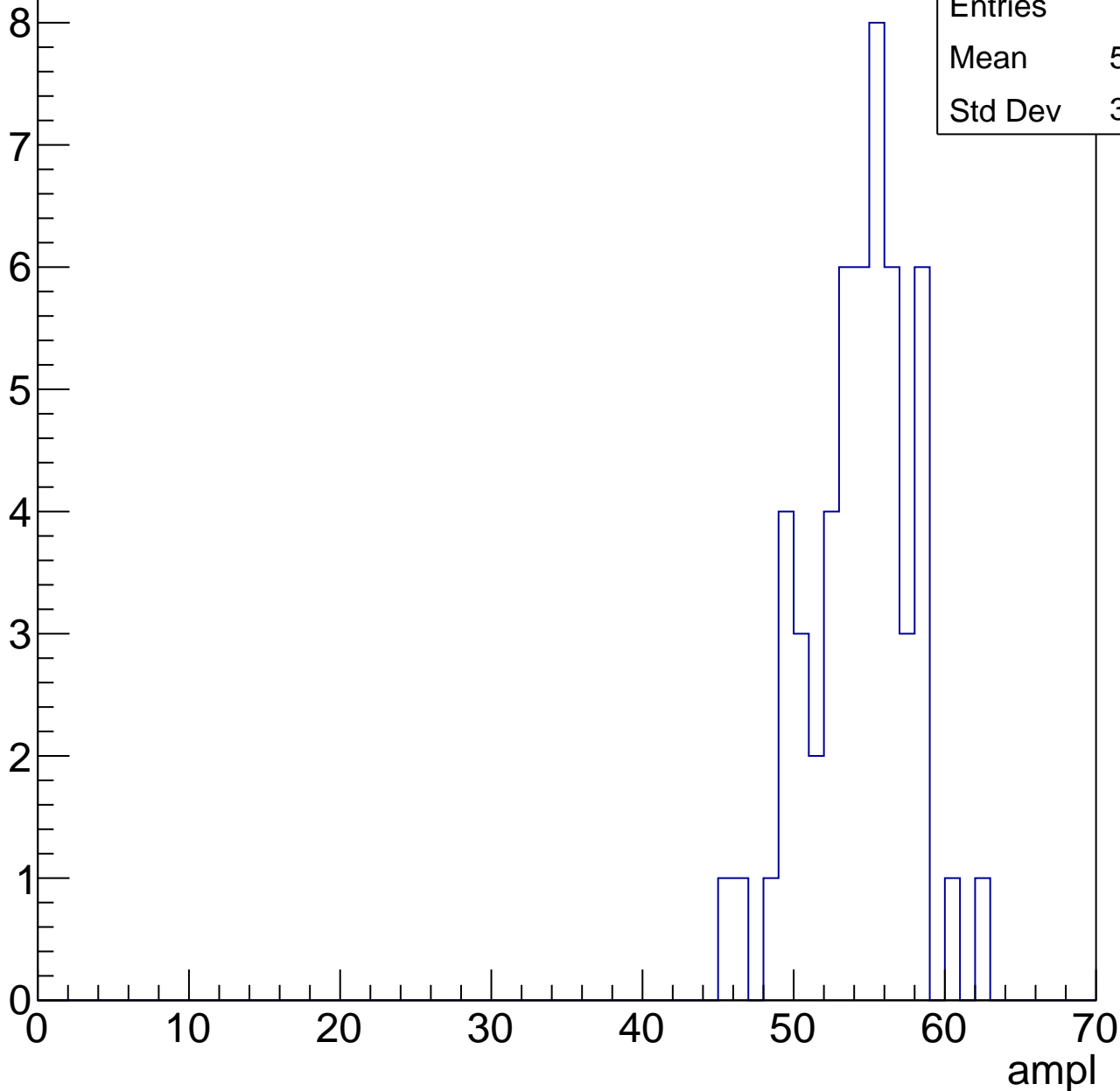


B0L001S, U14-ch120, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	53
Mean	53.85
Std Dev	3.428

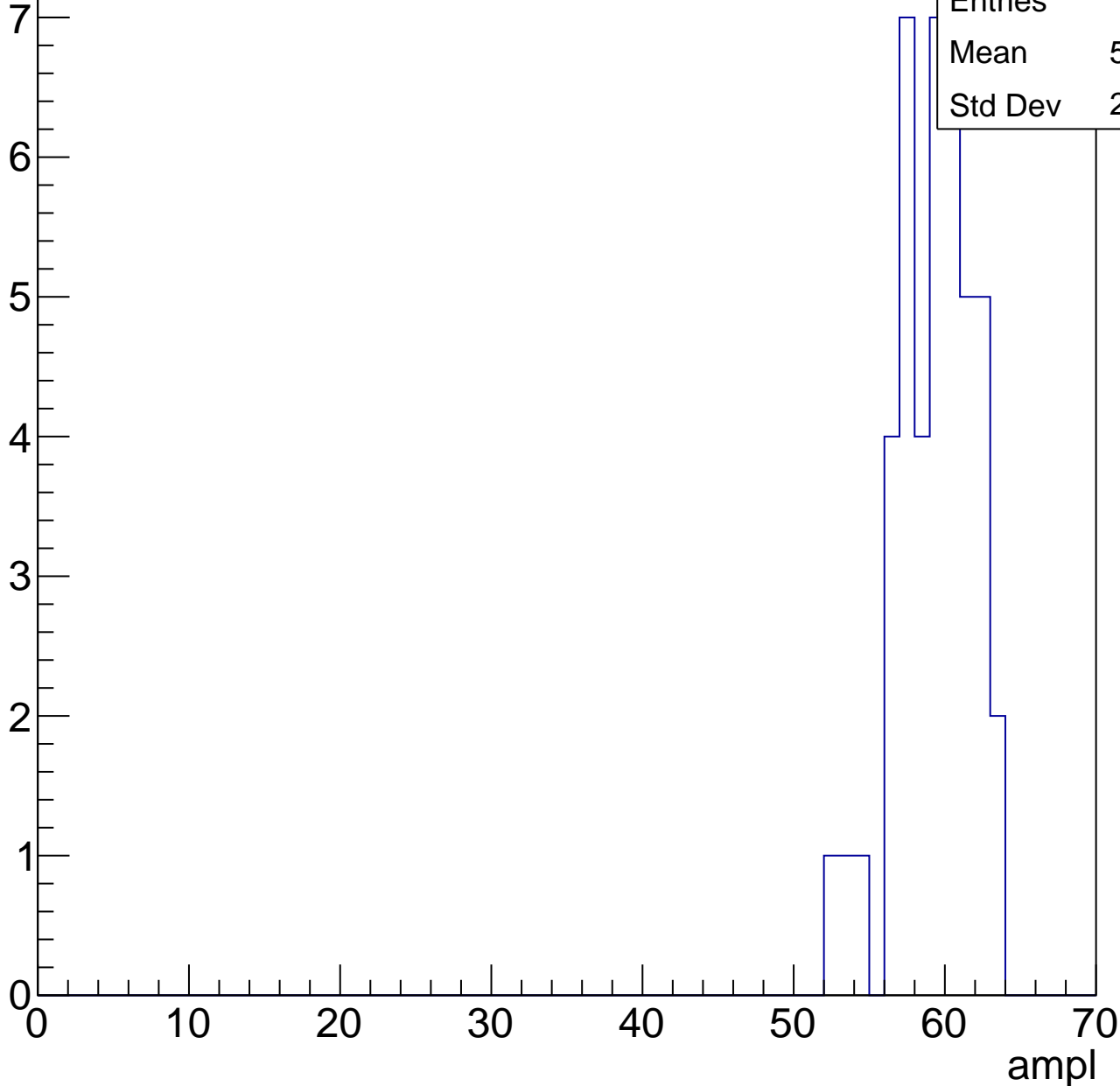


B0L001S, U14-ch120, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	44
Mean	58.82
Std Dev	2.525

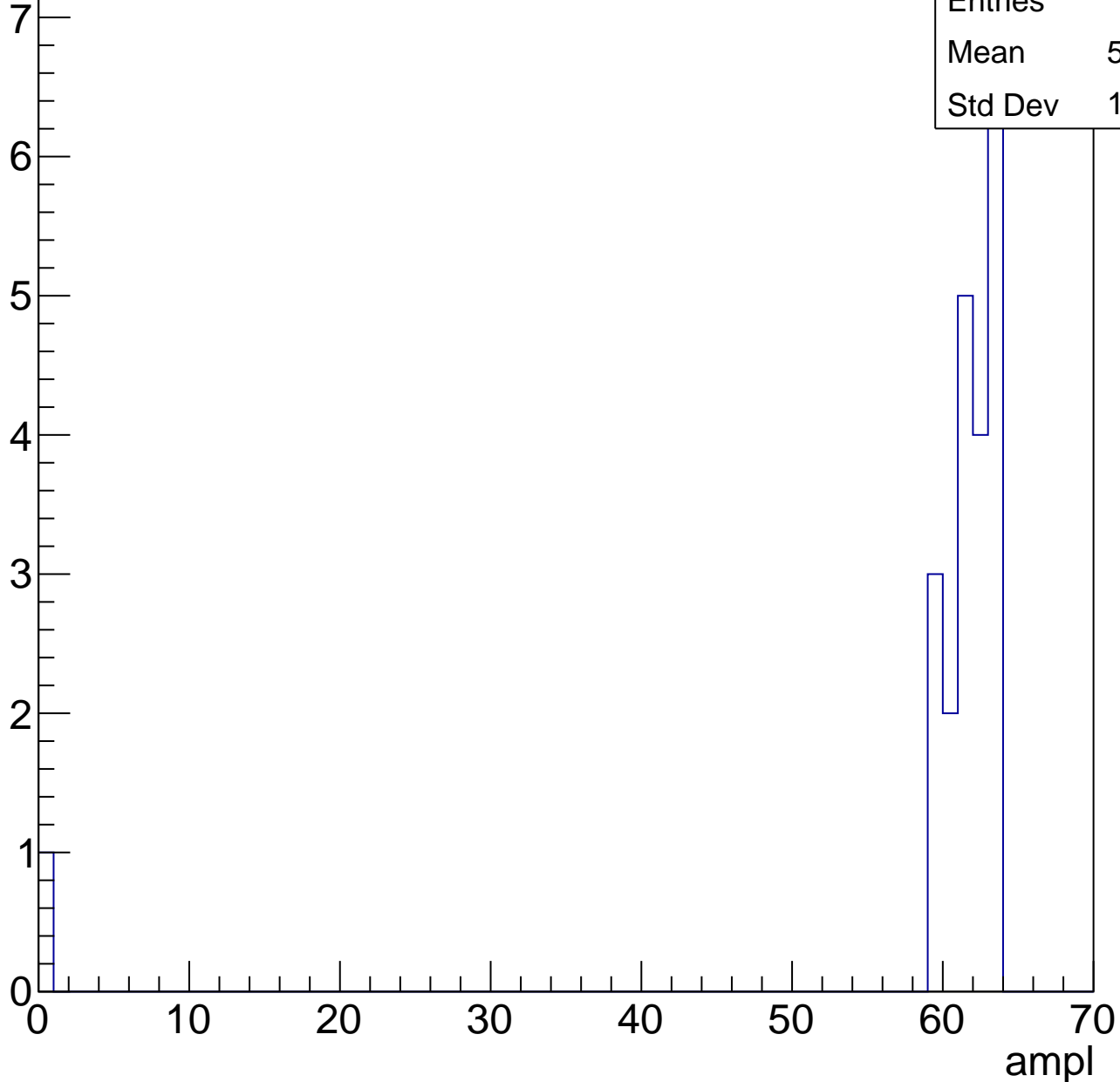


B0L001S, U14-ch120, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

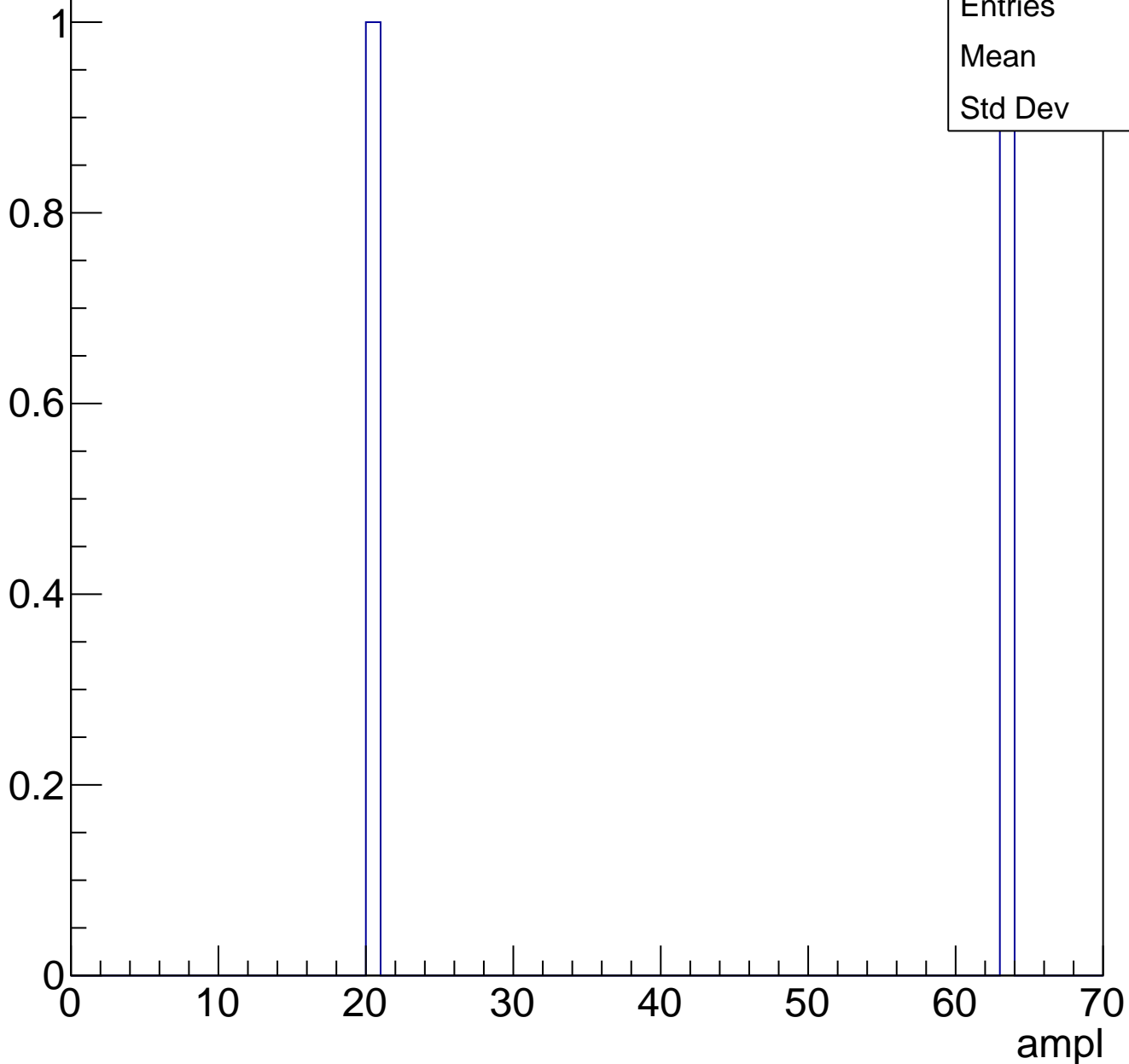
Entries	22
Mean	58.68
Std Dev	12.88



B0L001S, U14-ch120, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch121, adc0

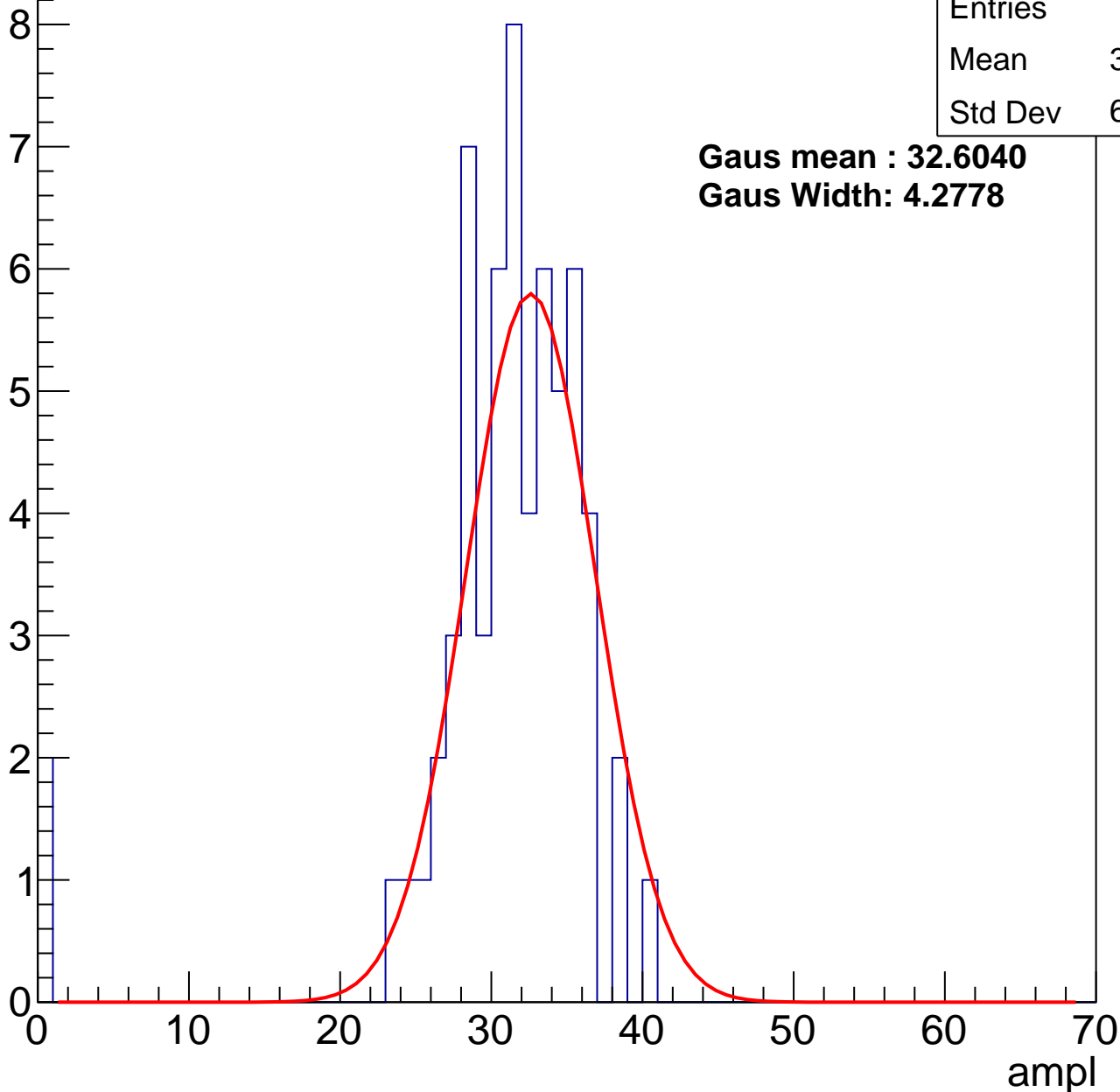
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	30.35
Std Dev	6.565

Gaus mean : 32.6040

Gaus Width: 4.2778



B0L001S, U14-ch121, adc1

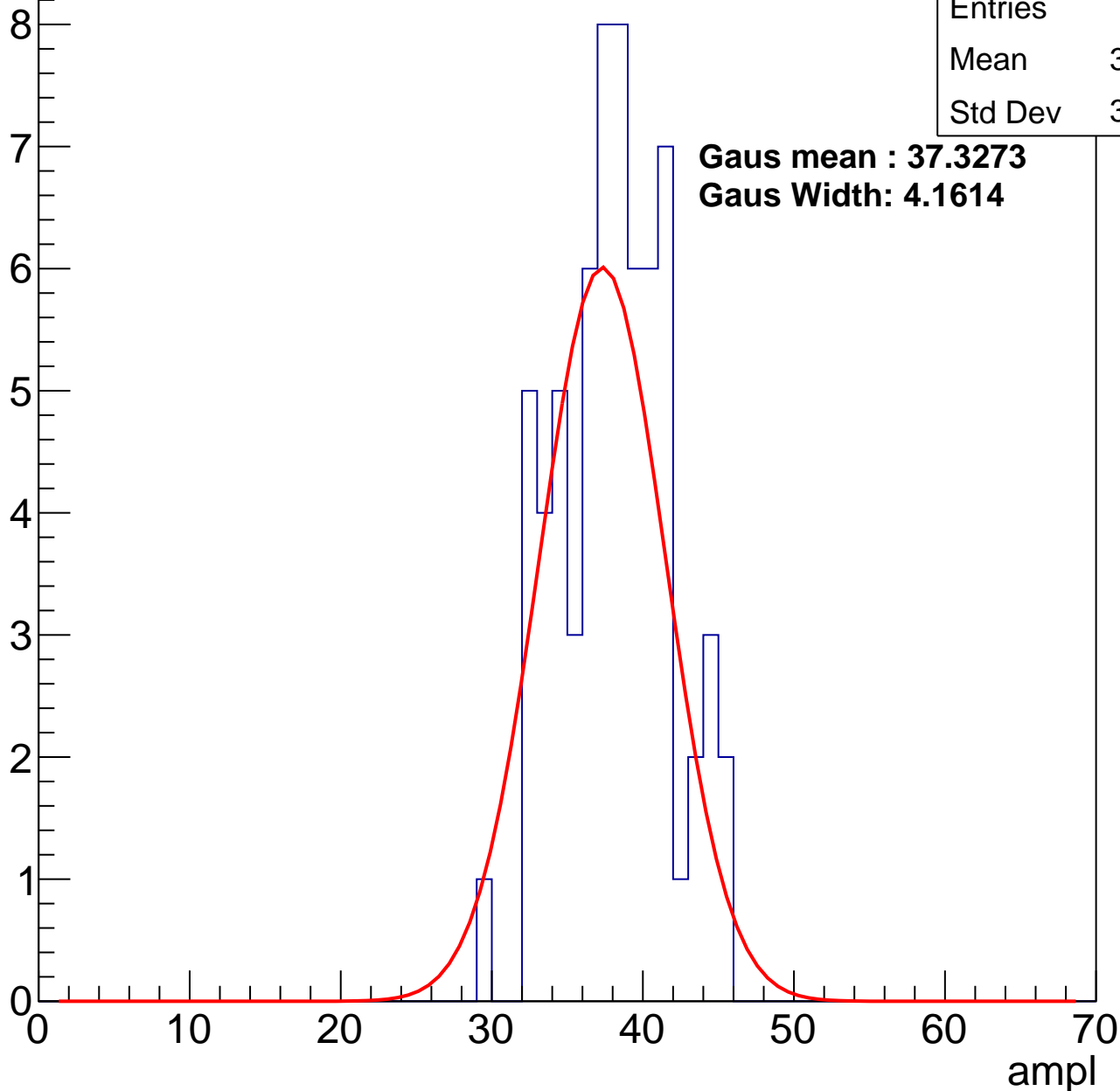
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.66
Std Dev	3.577

Gaus mean : 37.3273

Gaus Width: 4.1614



B0L001S, U14-ch121, adc2

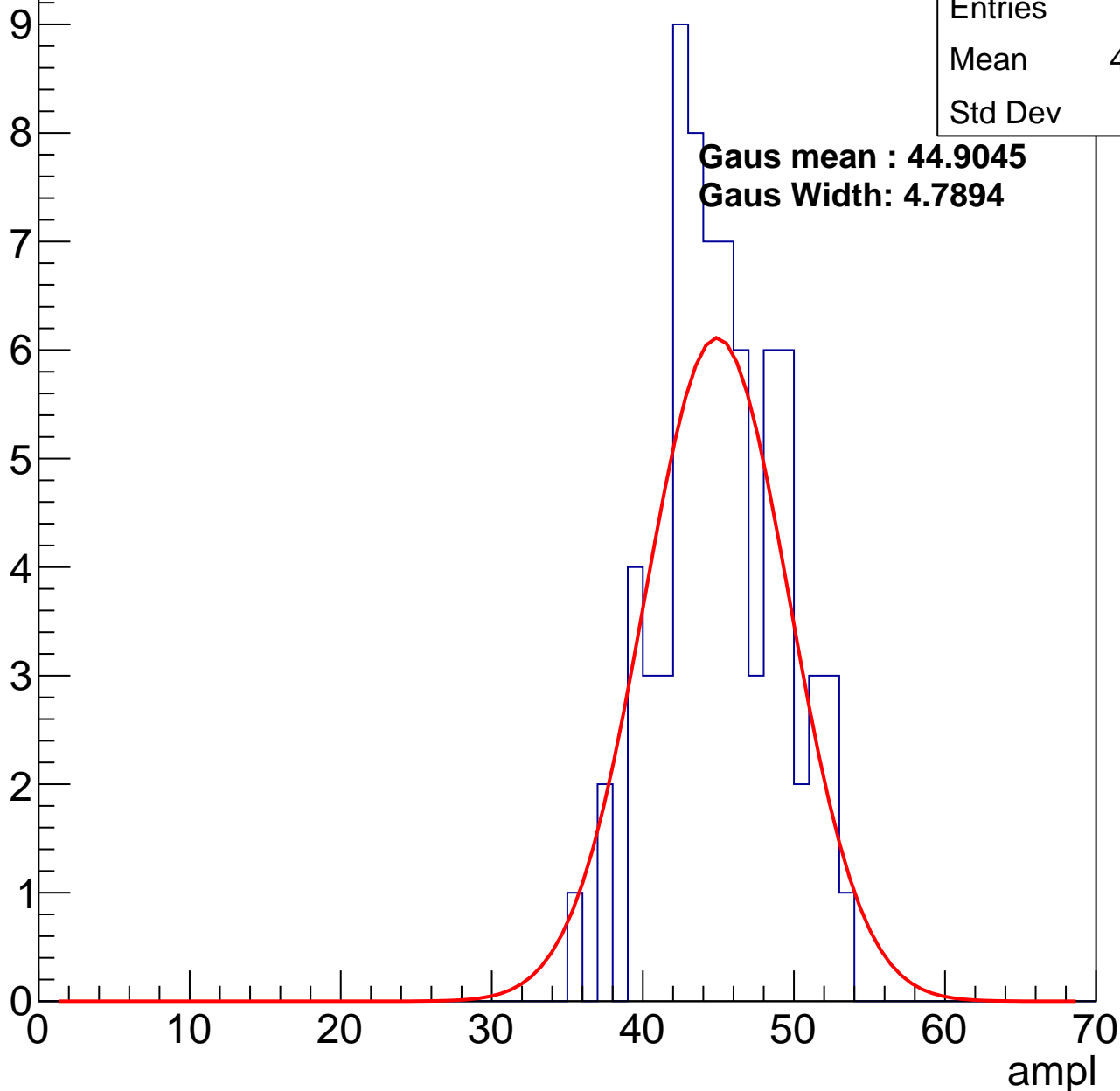
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	74
Mean	44.78
Std Dev	3.94

Gaus mean : 44.9045

Gaus Width: 4.7894

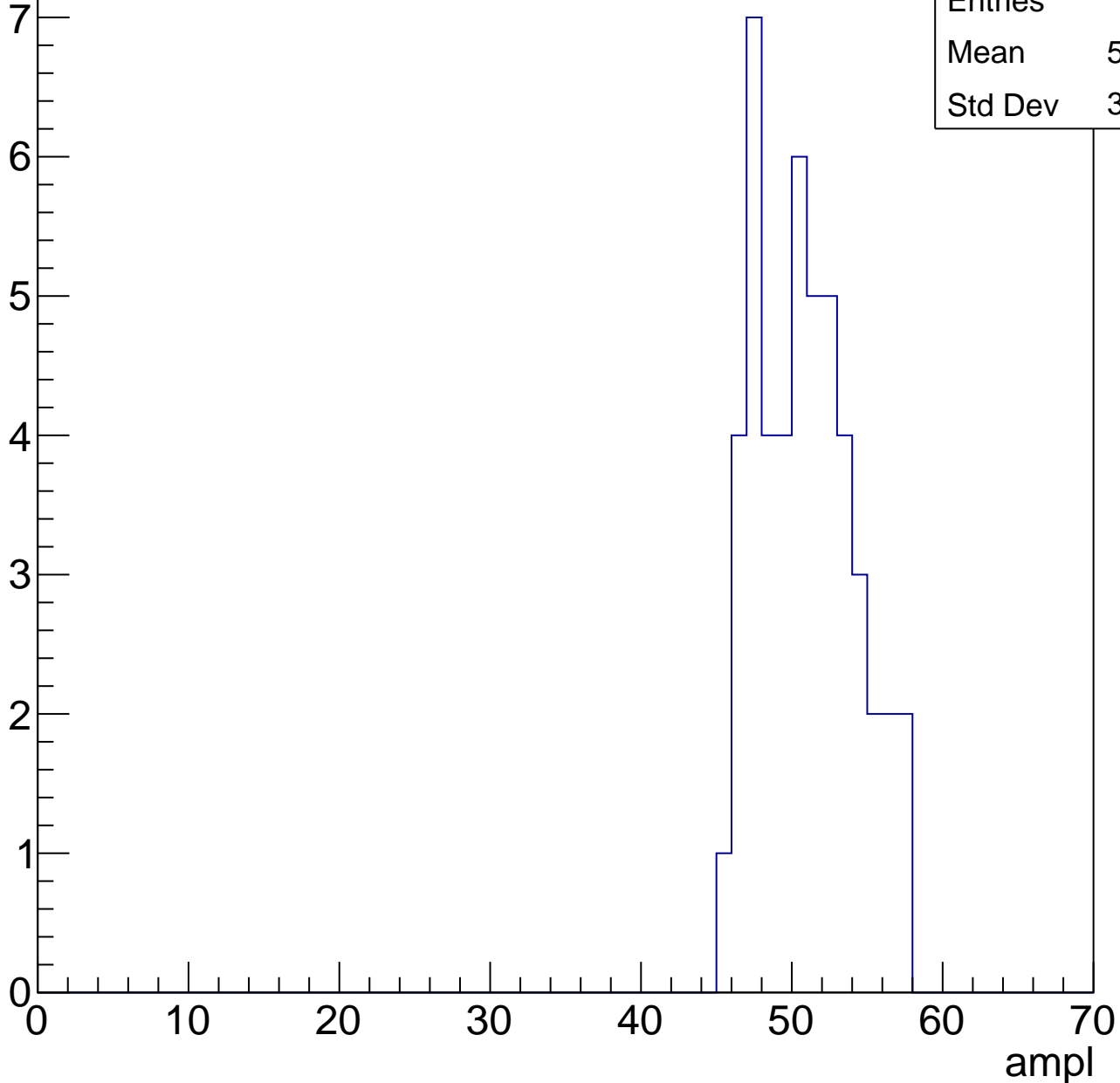


B0L001S, U14-ch121, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	49
Mean	50.43
Std Dev	3.169

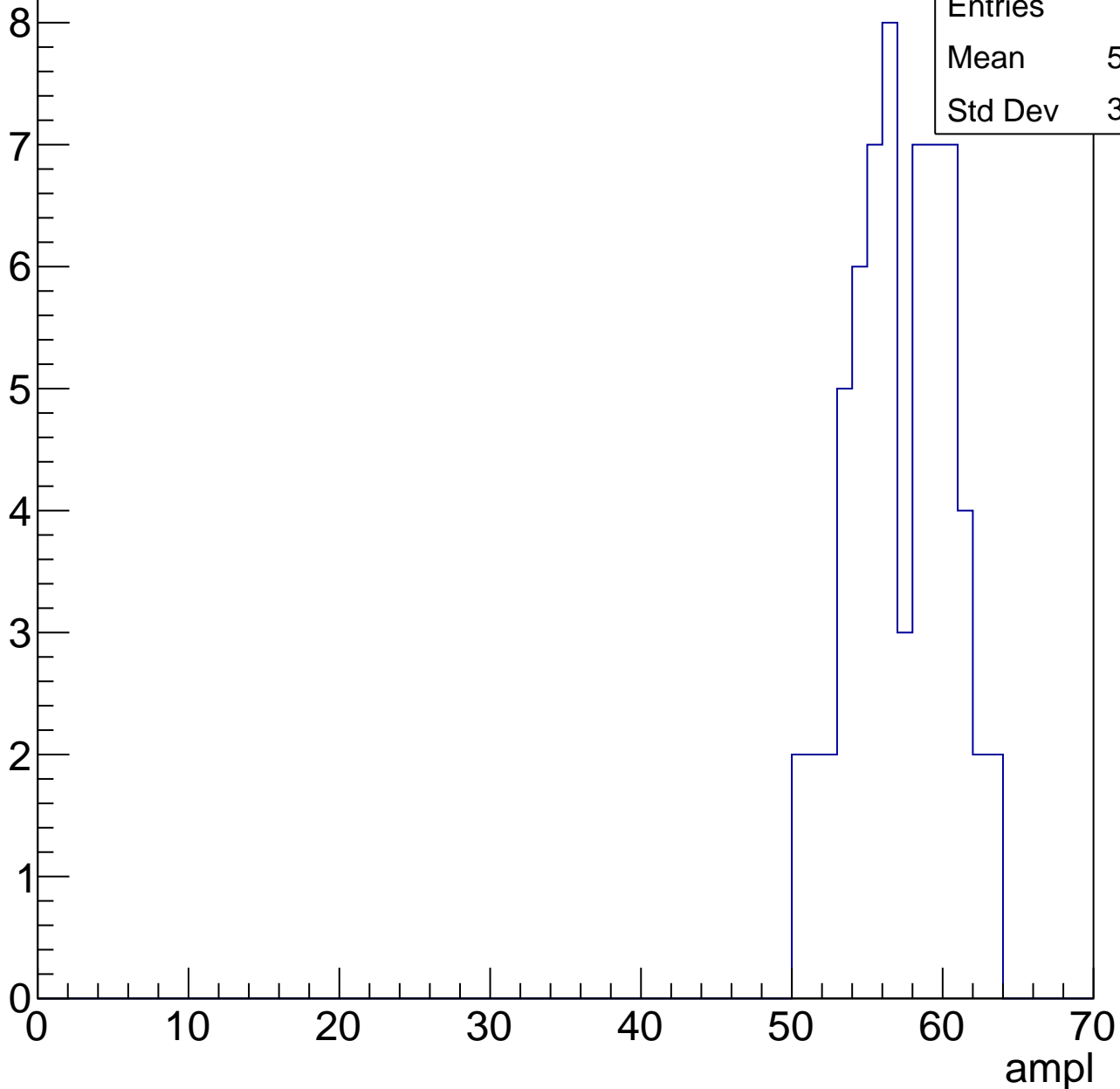


B0L001S, U14-ch121, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	56.75
Std Dev	3.236

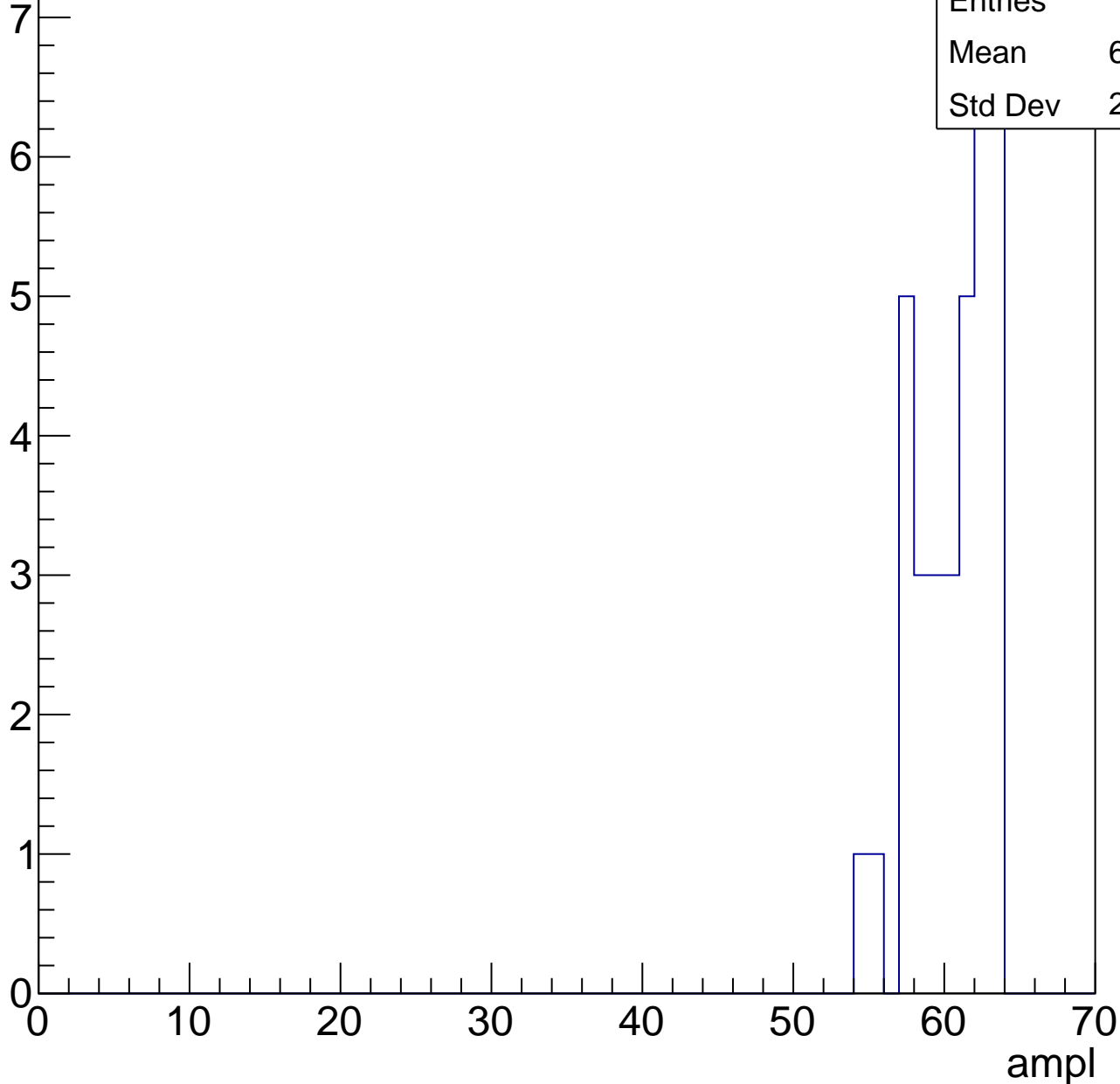


B0L001S, U14-ch121, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	35
Mean	60.14
Std Dev	2.486



B0L001S, U14-ch121, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch121, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	2
Mean	11
Std Dev	11

B0L001S, U14-ch122, adc0

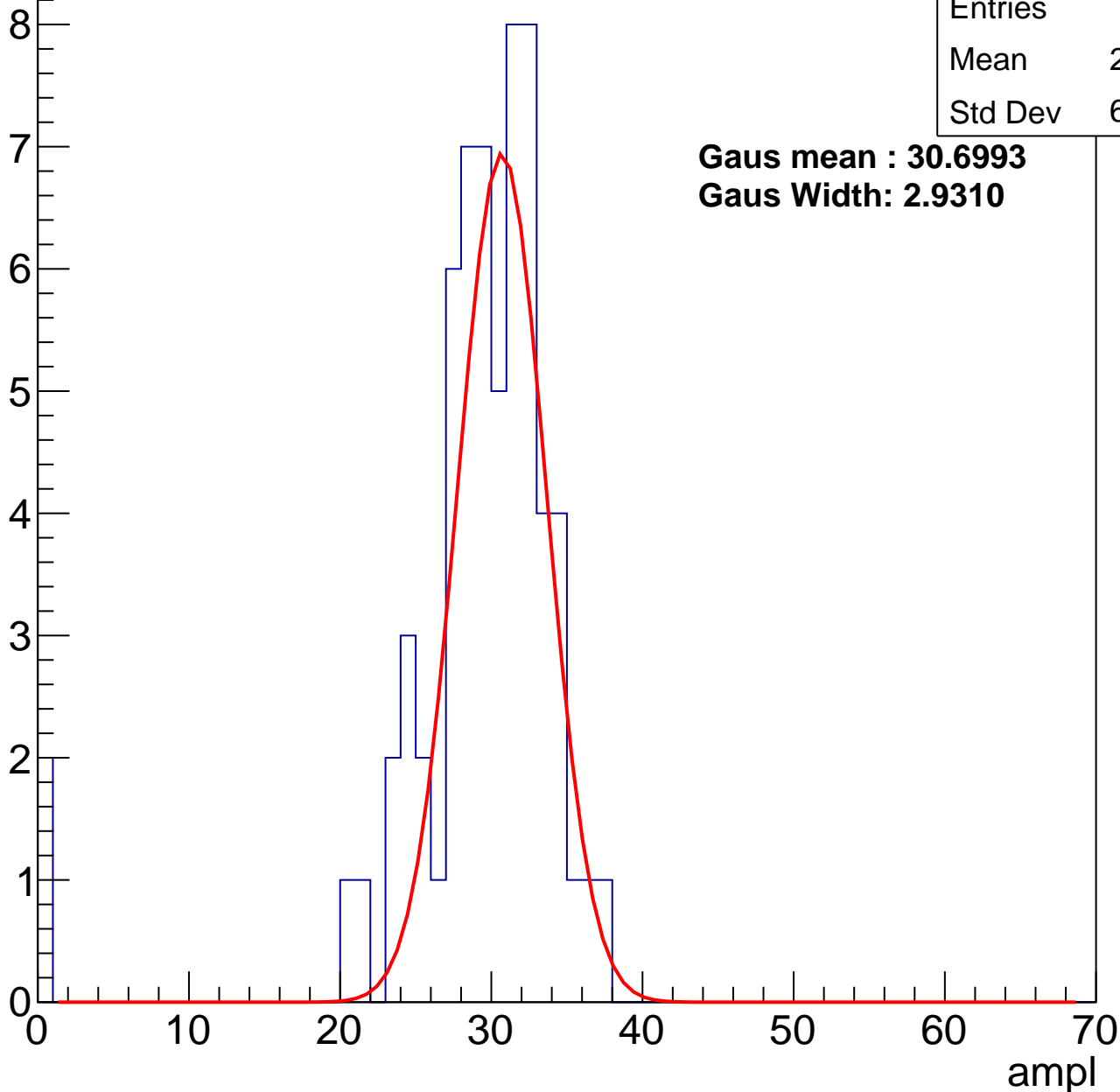
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	28.53
Std Dev	6.192

Gaus mean : 30.6993

Gaus Width: 2.9310



B0L001S, U14-ch122, adc1

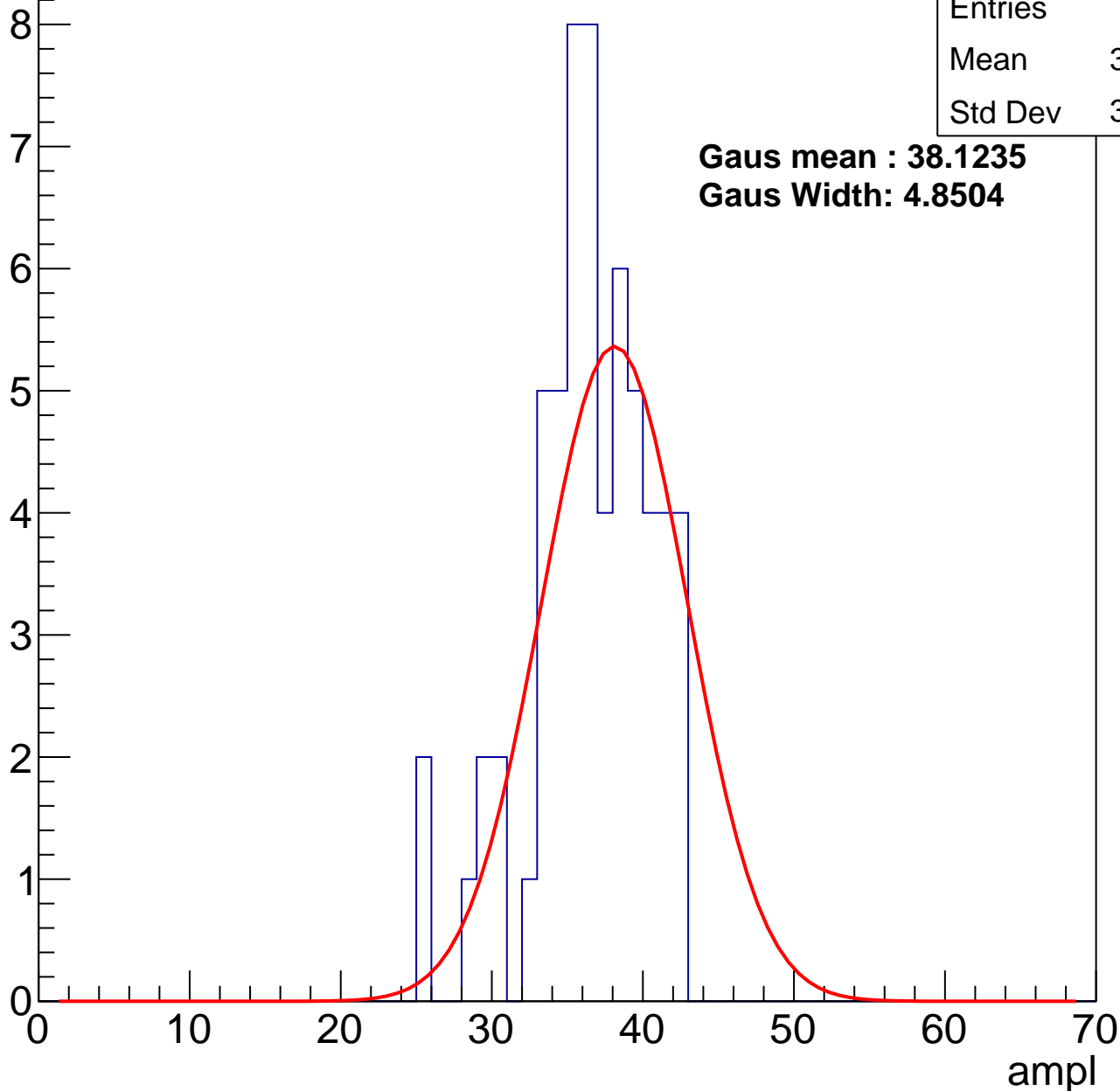
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	35.97
Std Dev	3.938

Gaus mean : 38.1235

Gaus Width: 4.8504



B0L001S, U14-ch122, adc2

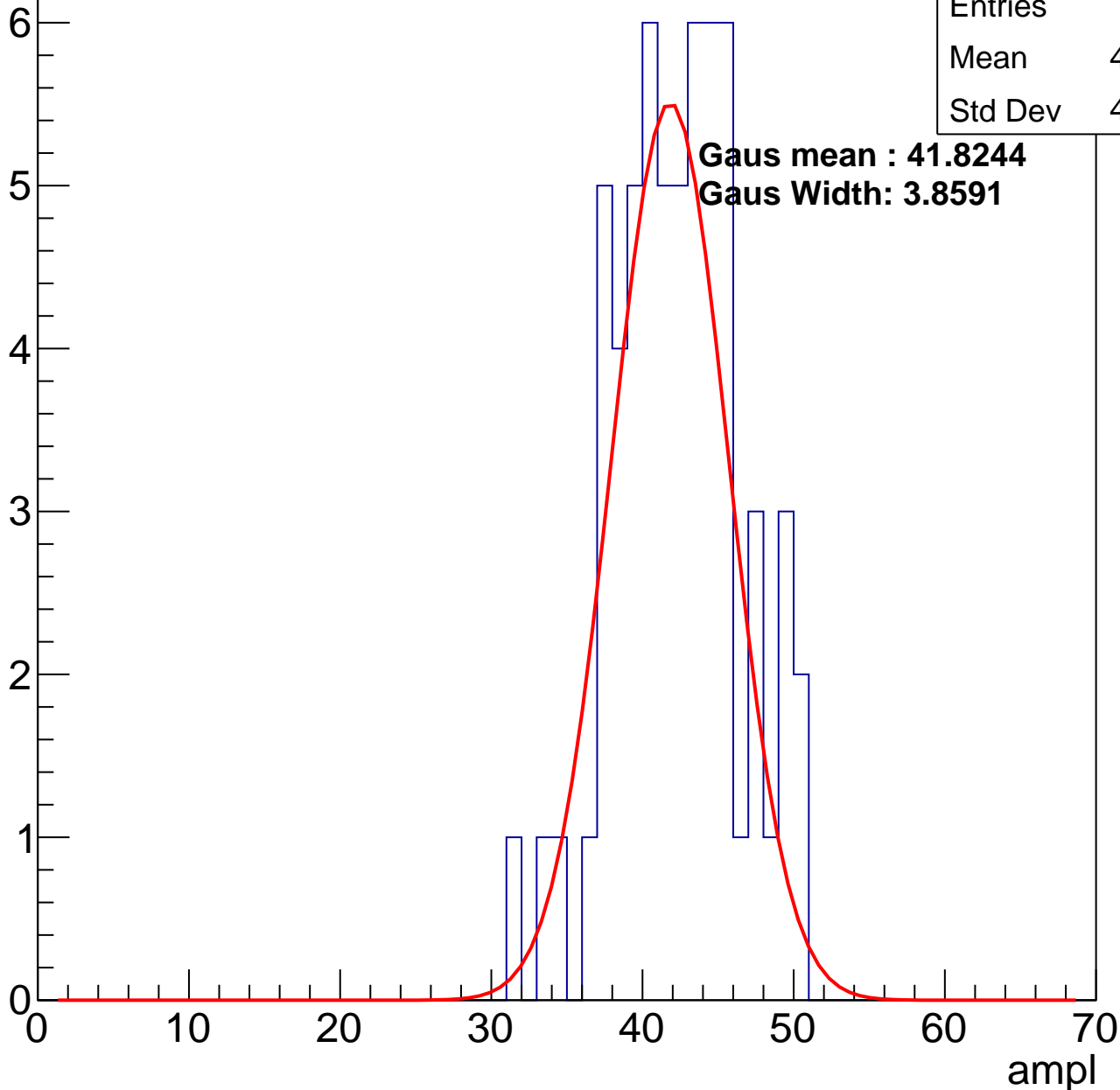
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	62
Mean	41.85
Std Dev	4.115

Gaus mean : 41.8244

Gaus Width: 3.8591

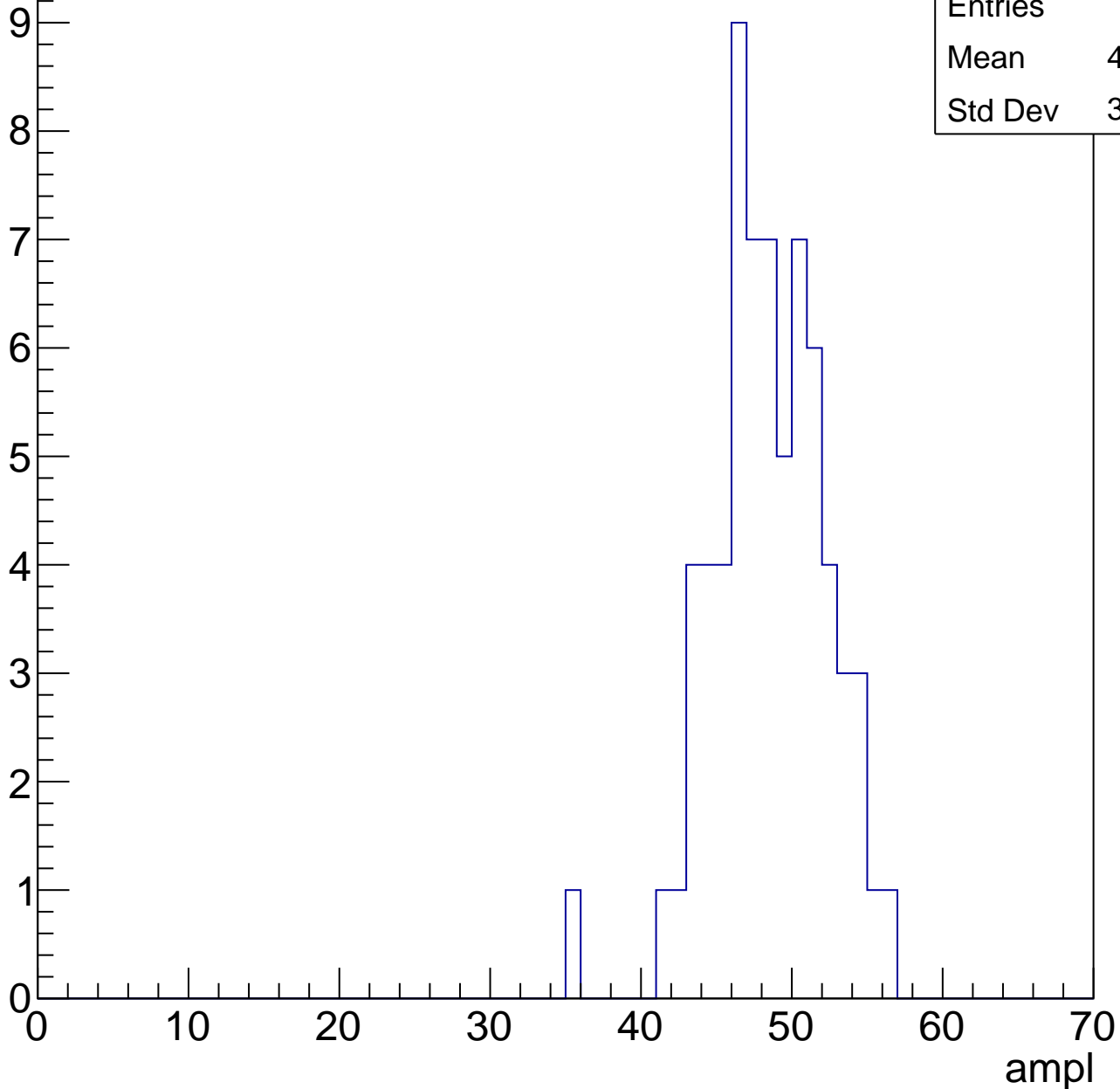


B0L001S, U14-ch122, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	68
Mean	48.03
Std Dev	3.726

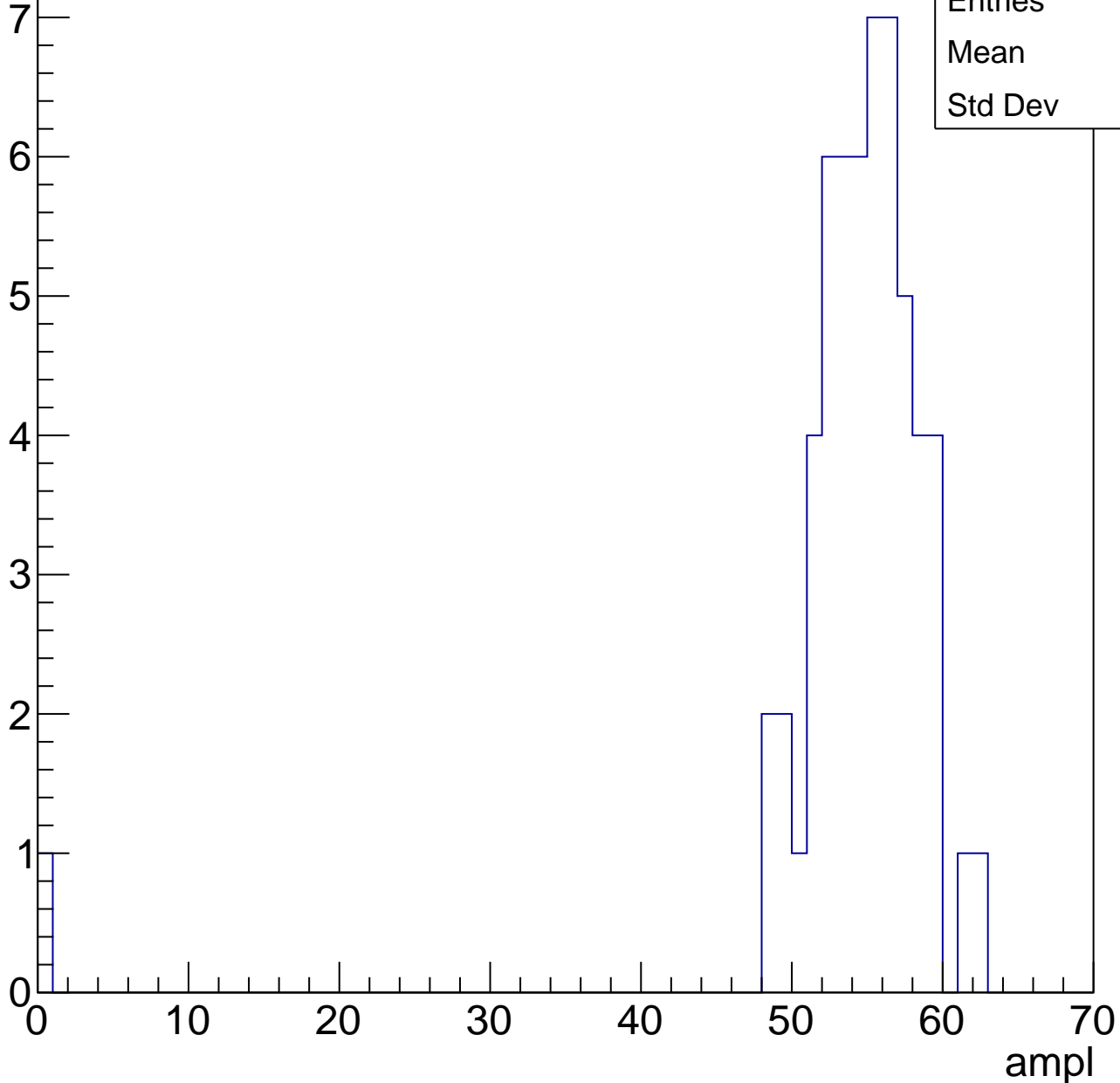


B0L001S, U14-ch122, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	57
Mean	53.6
Std Dev	7.8



B0L001S, U14-ch122, adc5

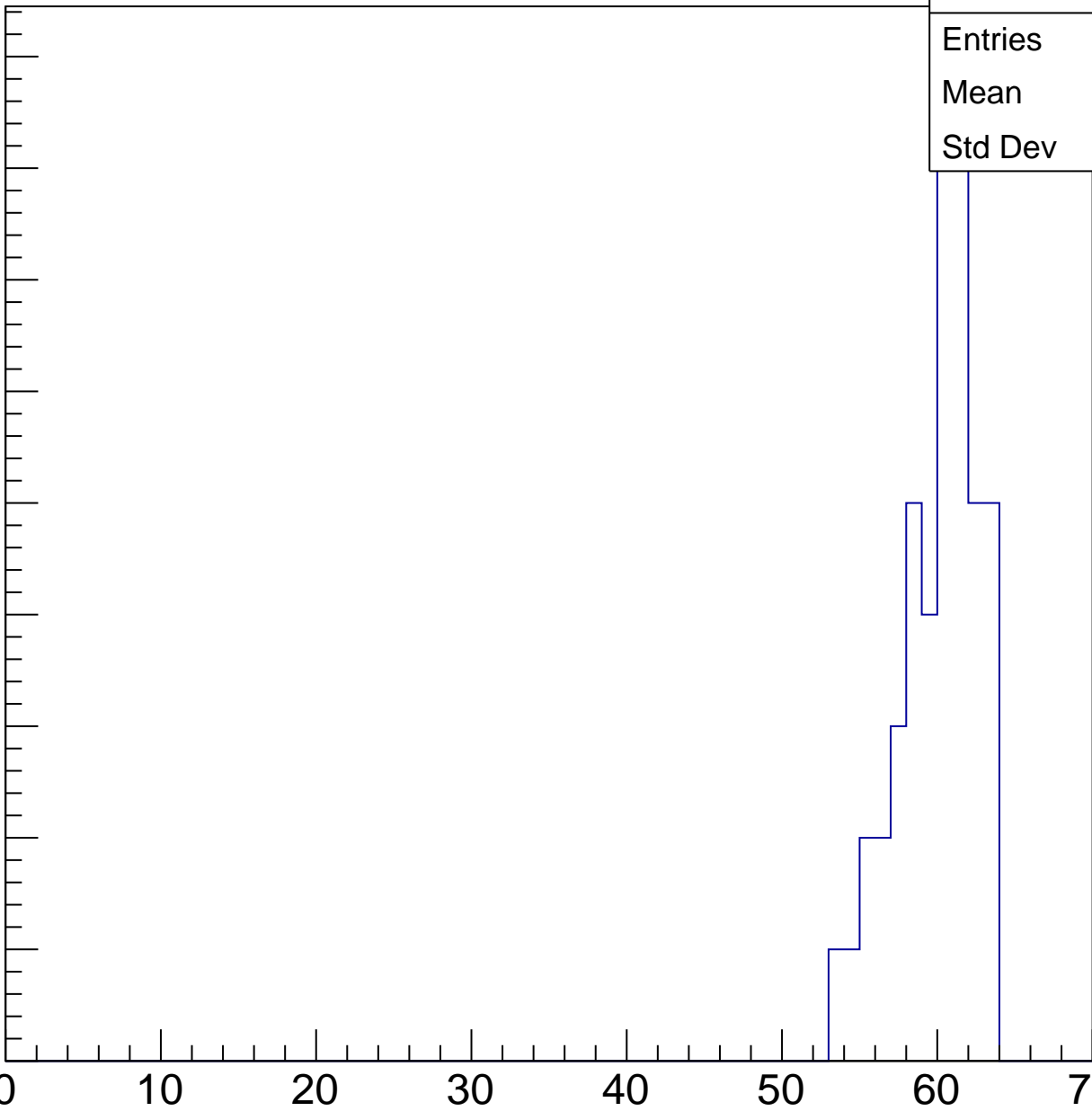
calib_packv5_042523_0143.root, FC#9, port A1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.57
Std Dev	2.482

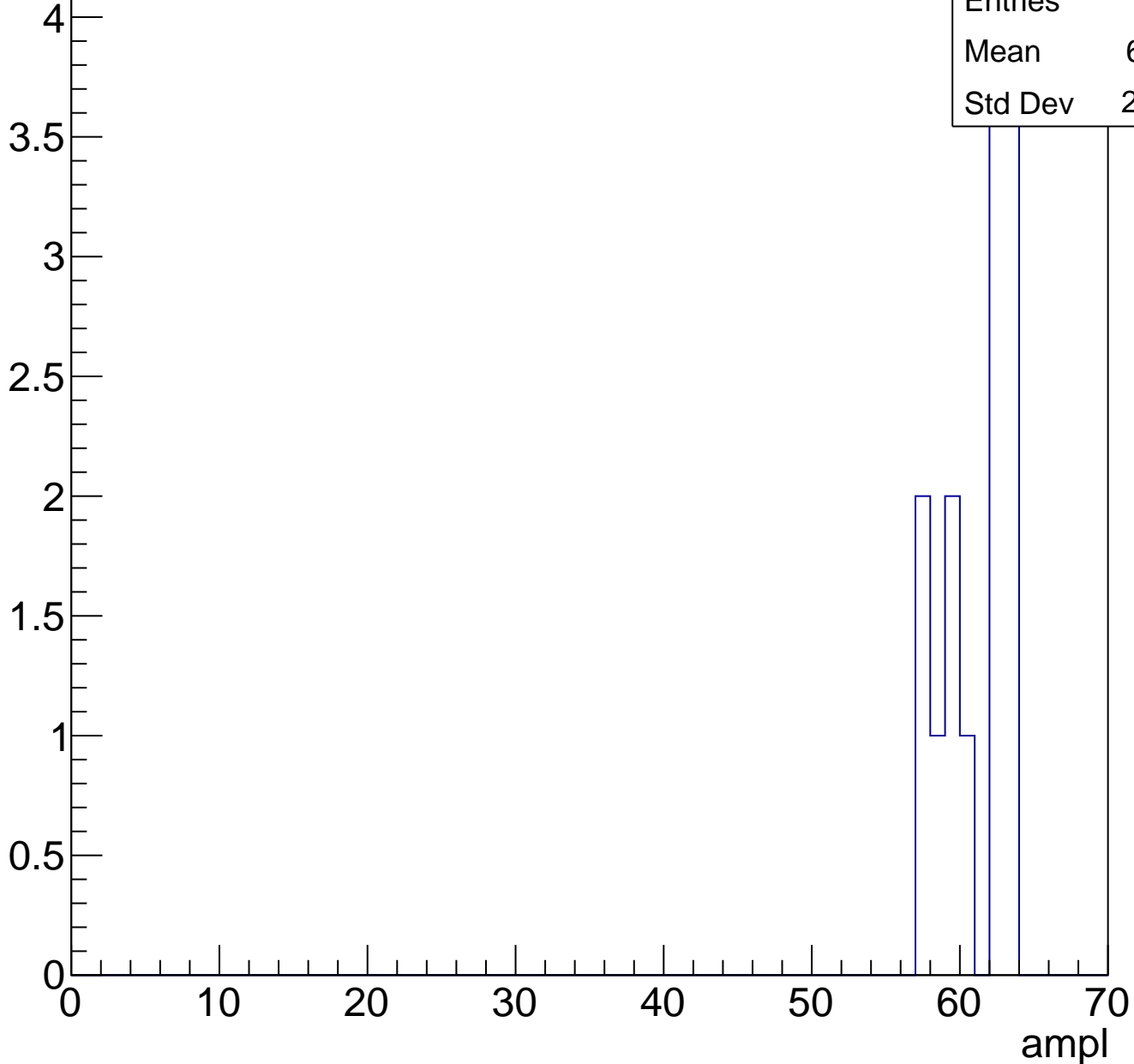
ampl



B0L001S, U14-ch122, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch122, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L001S, U14-ch123, adc0

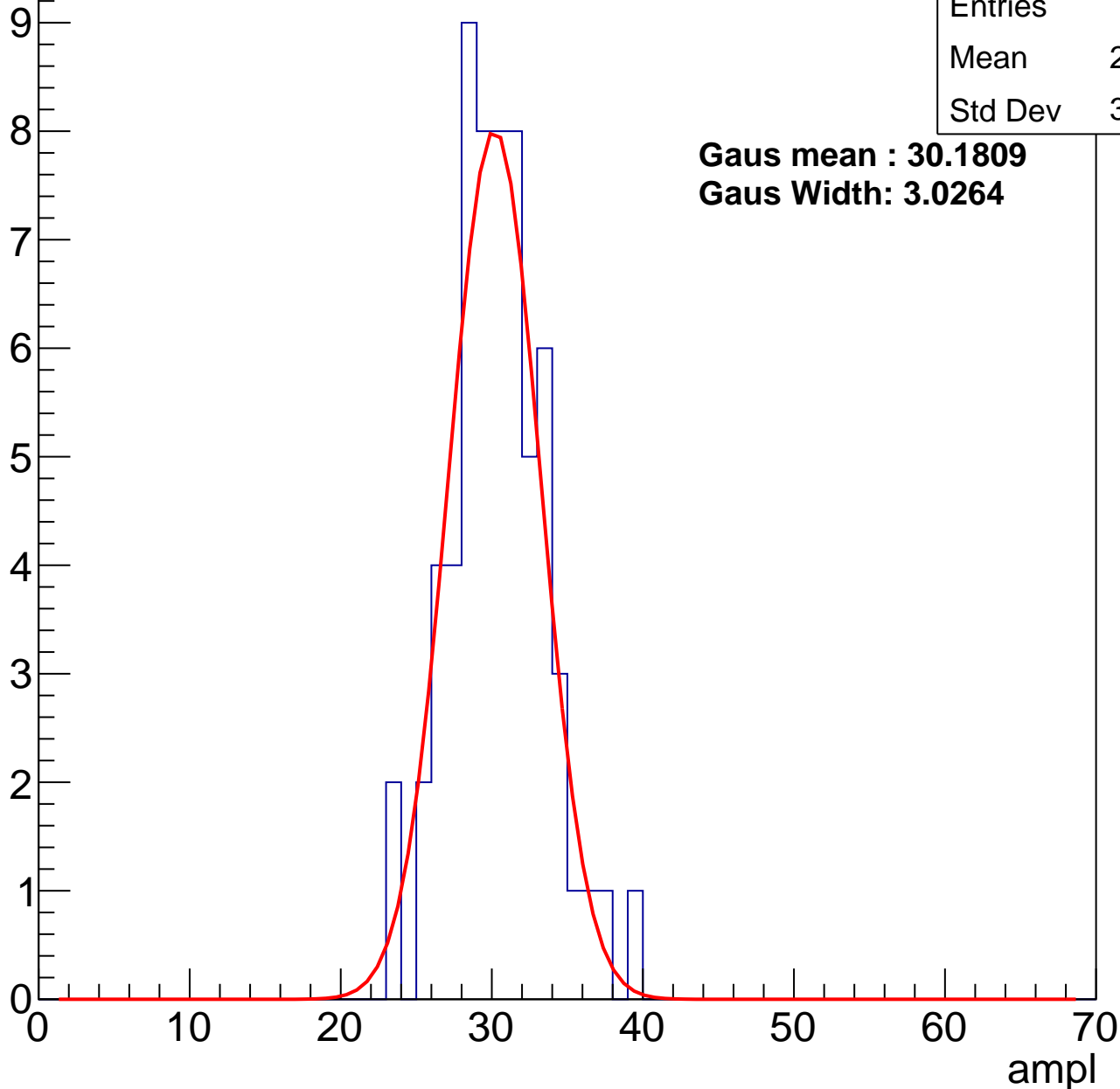
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	63
Mean	29.95
Std Dev	3.119

Gaus mean : 30.1809

Gaus Width: 3.0264



B0L001S, U14-ch123, adc1

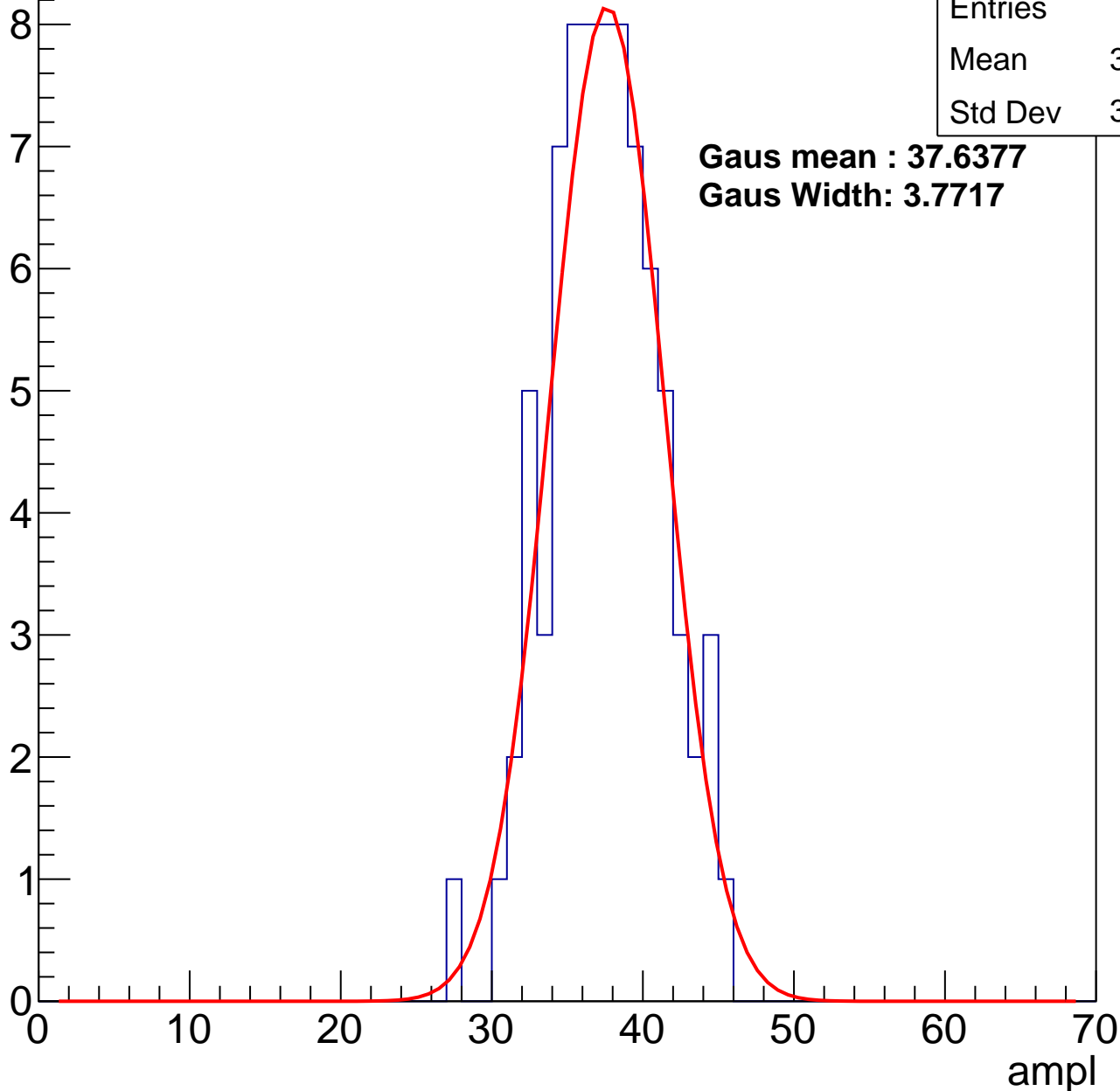
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	78
Mean	37.06
Std Dev	3.624

Gaus mean : 37.6377

Gaus Width: 3.7717



B0L001S, U14-ch123, adc2

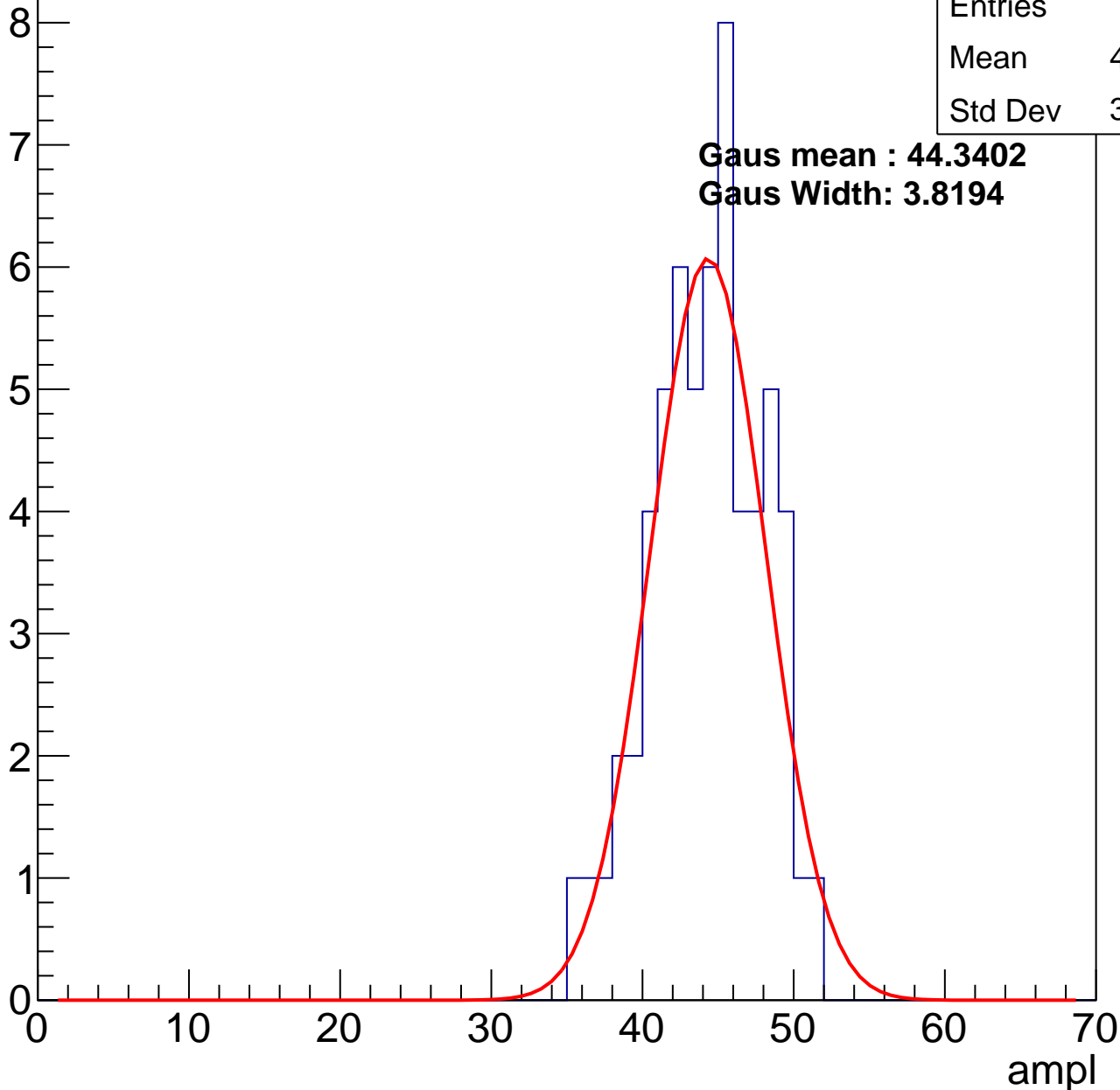
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.78
Std Dev	3.597

Gaus mean : 44.3402

Gaus Width: 3.8194



B0L001S, U14-ch123, adc3

calib_packv5_042523_0143.root, FC#9, port A1

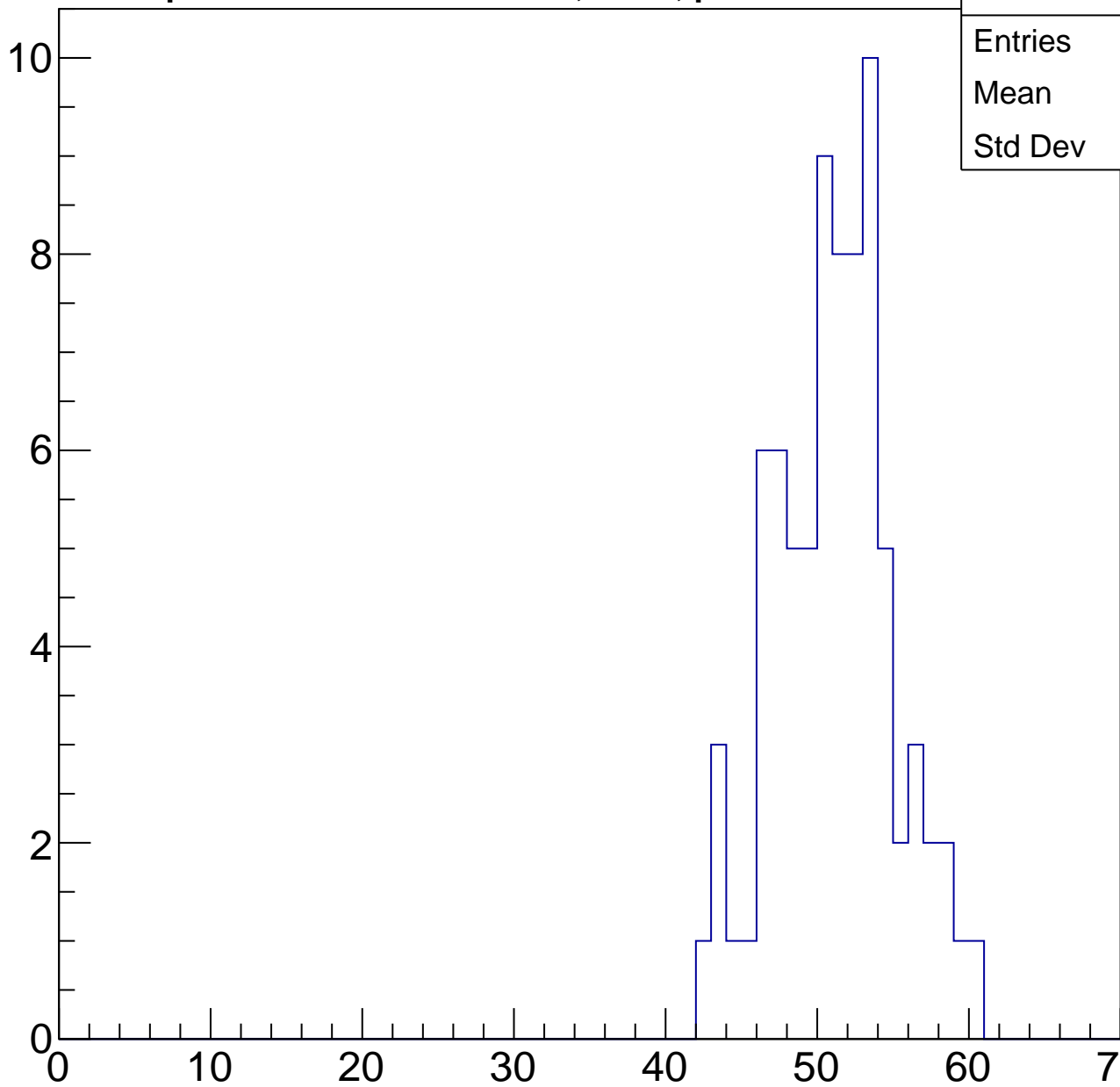
Entries	79
Mean	50.68
Std Dev	3.873

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

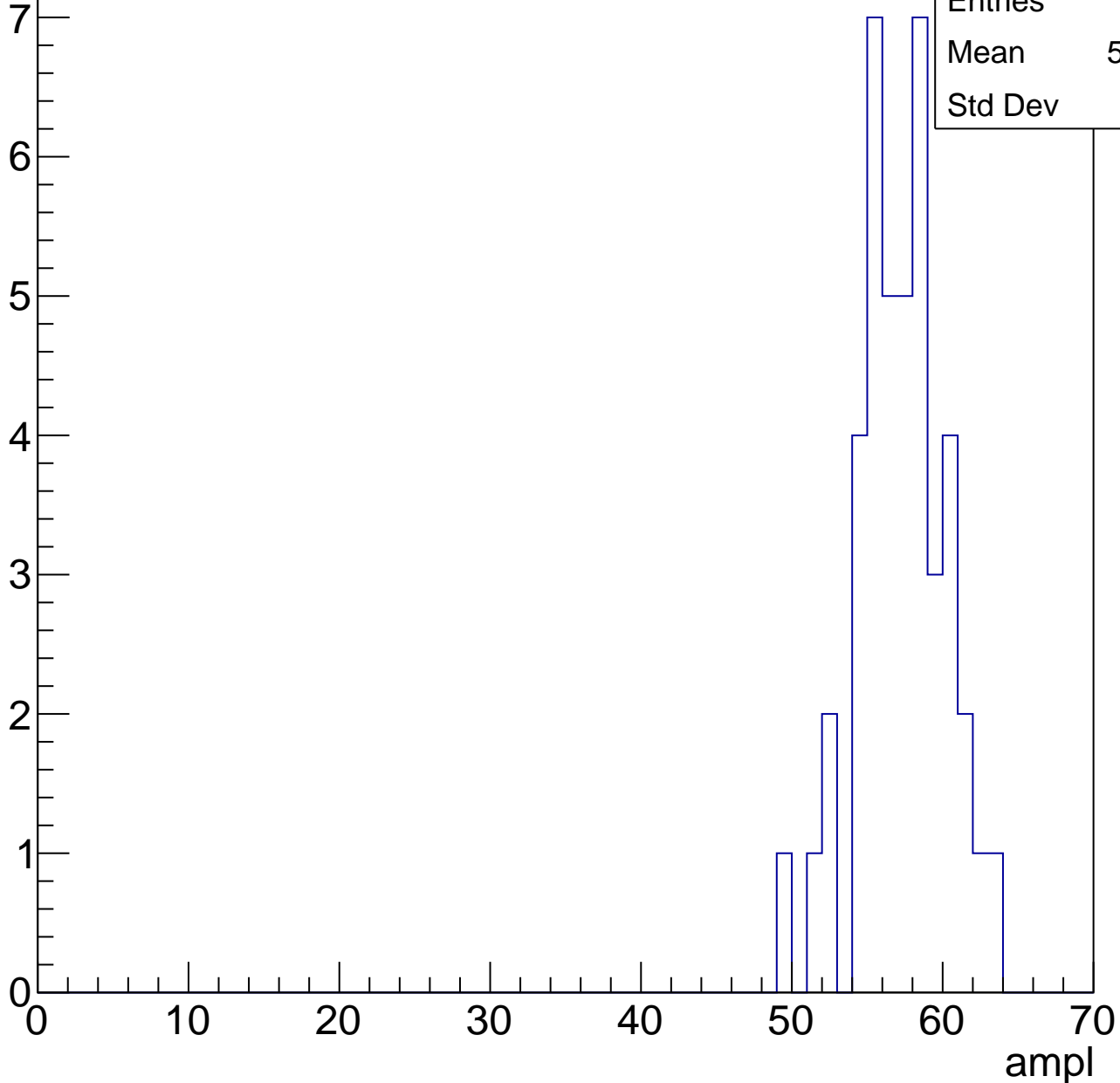


B0L001S, U14-ch123, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	43
Mean	56.74
Std Dev	2.91

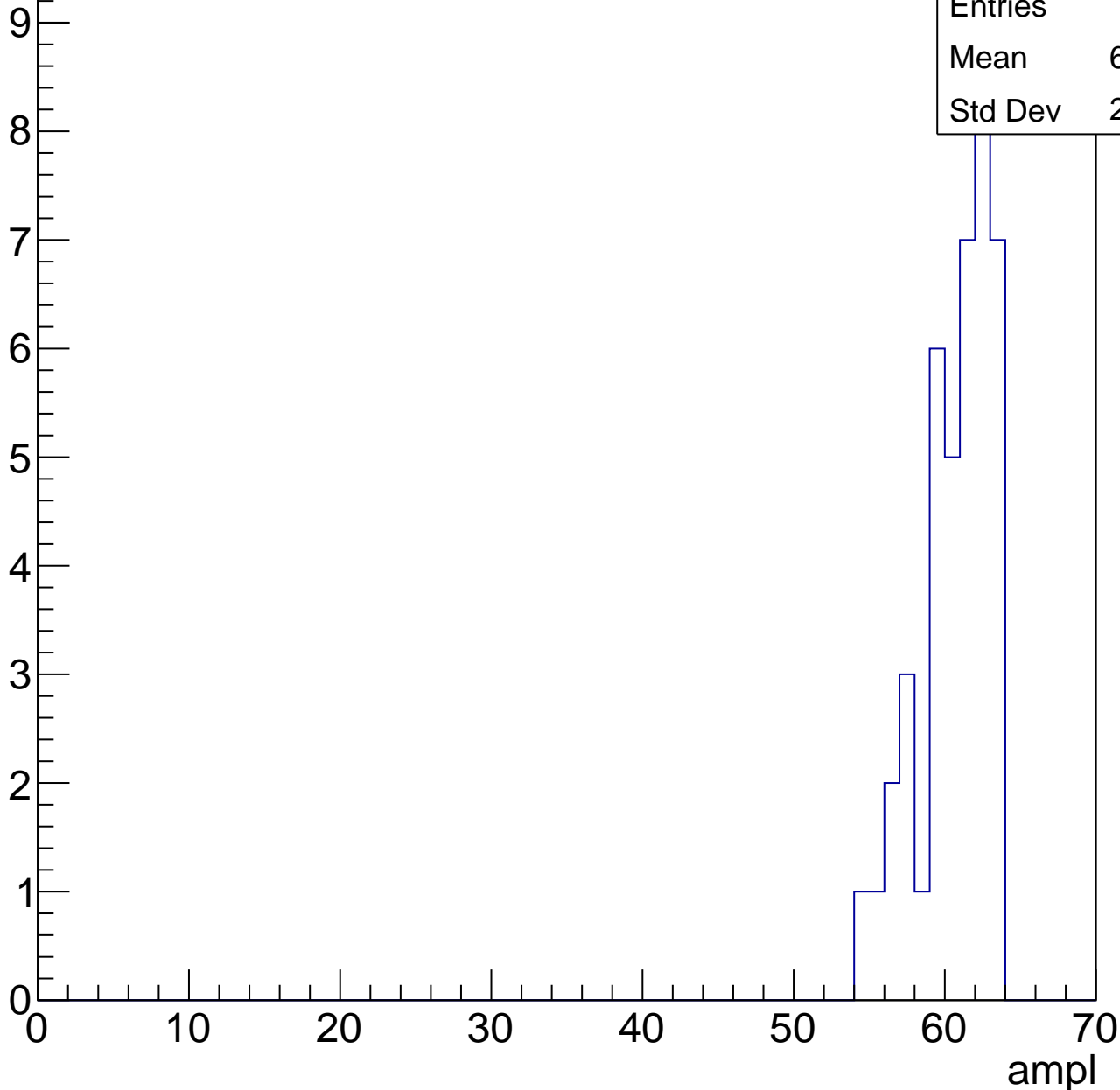


B0L001S, U14-ch123, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

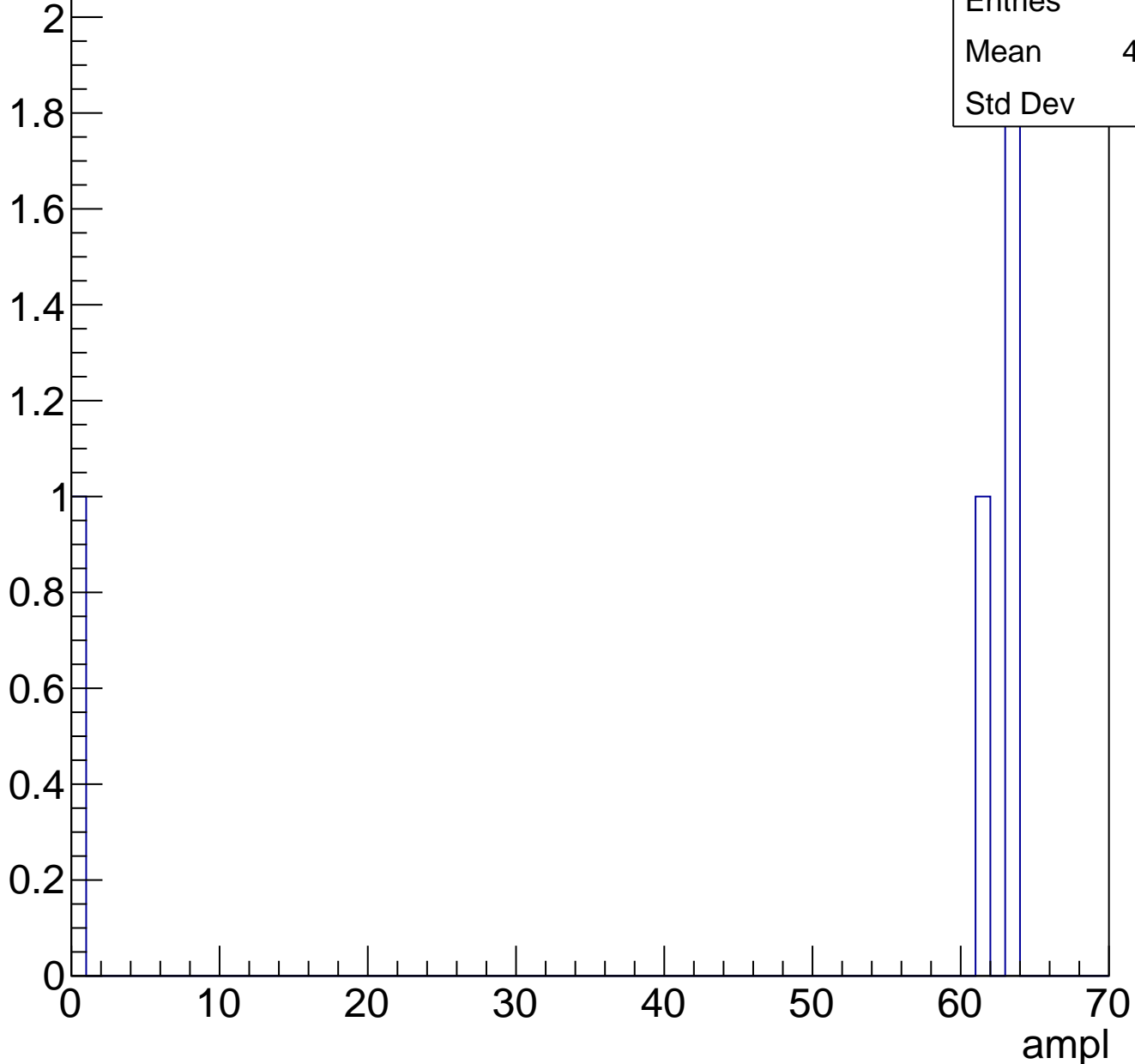
Entries	42
Mean	60.24
Std Dev	2.358



B0L001S, U14-ch123, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch123, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	0
Mean	0
Std Dev	0

B0L001S, U14-ch124, adc0

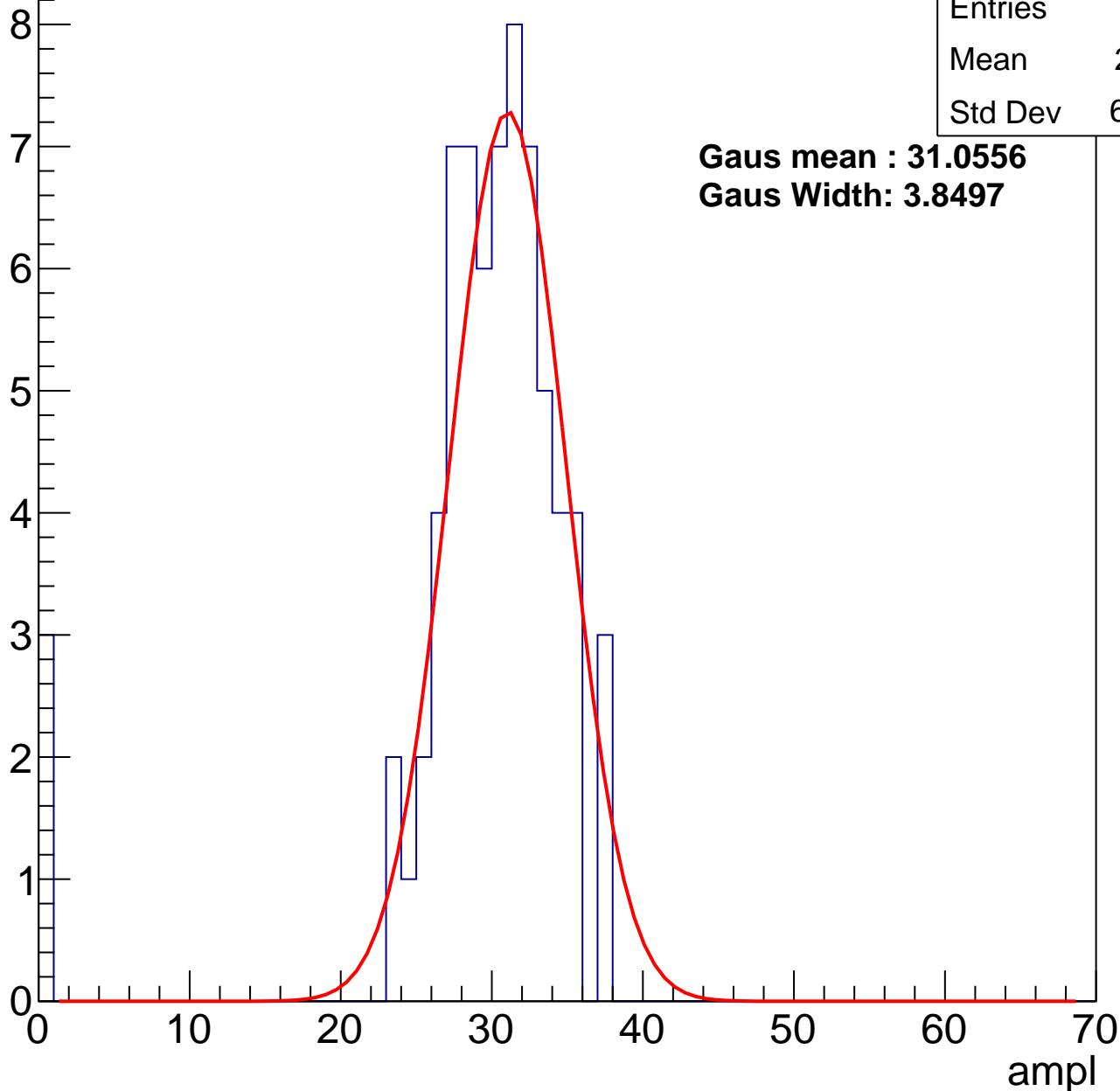
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	28.81
Std Dev	6.906

Gaus mean : 31.0556

Gaus Width: 3.8497



B0L001S, U14-ch124, adc1

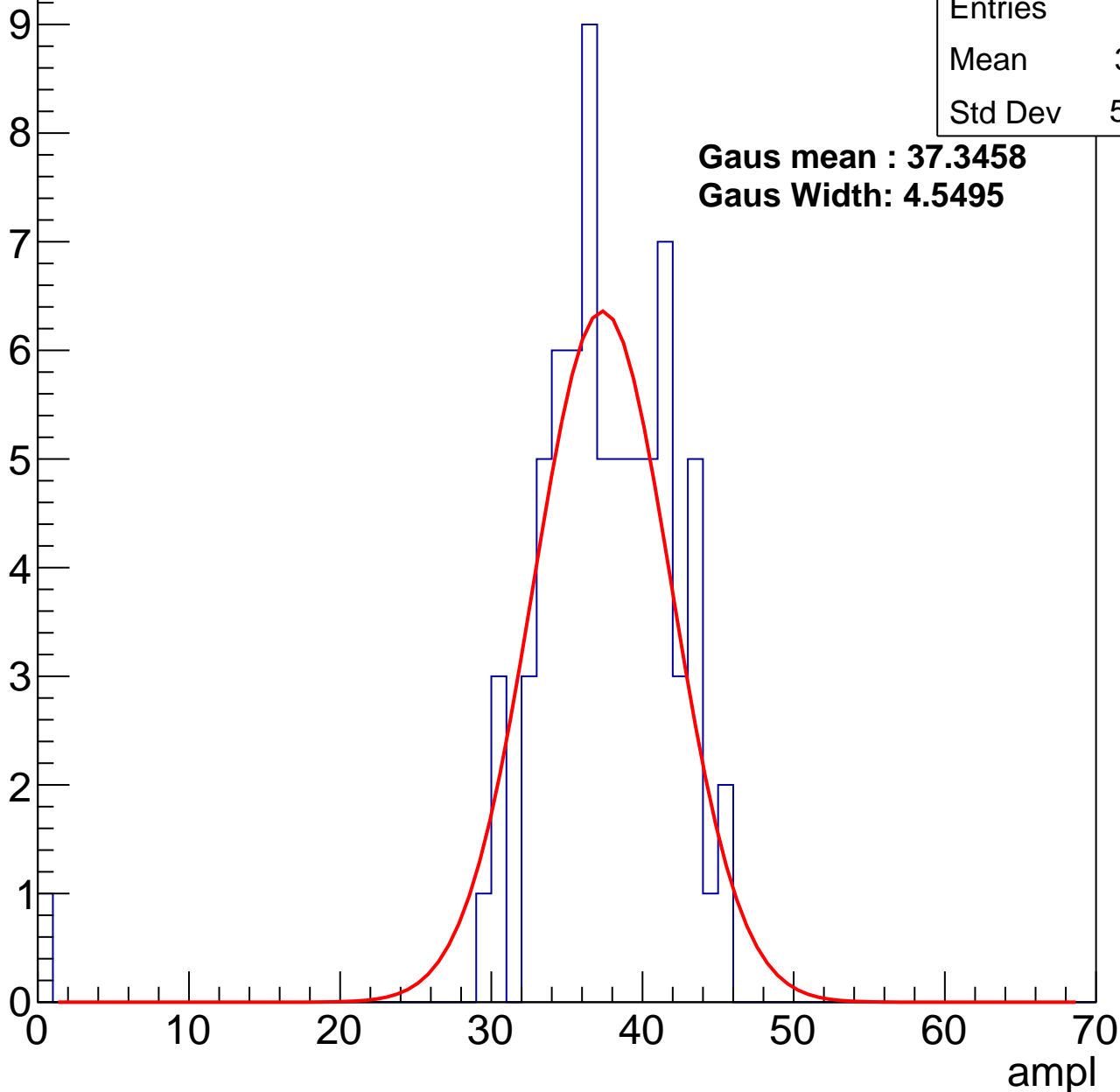
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	72
Mean	36.81
Std Dev	5.813

Gaus mean : 37.3458

Gaus Width: 4.5495



B0L001S, U14-ch124, adc2

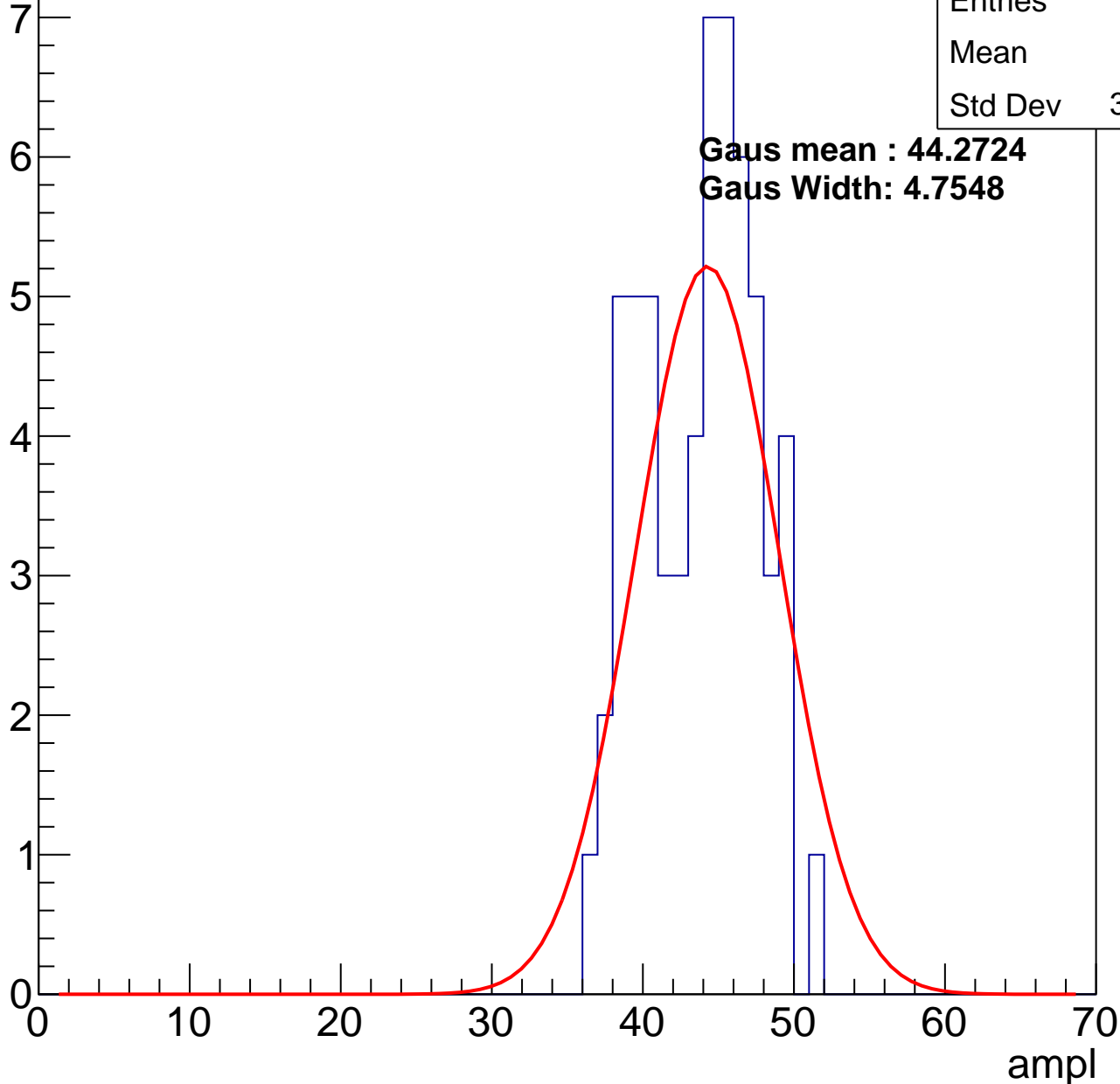
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	61
Mean	43.3
Std Dev	3.695

Gaus mean : 44.2724

Gaus Width: 4.7548

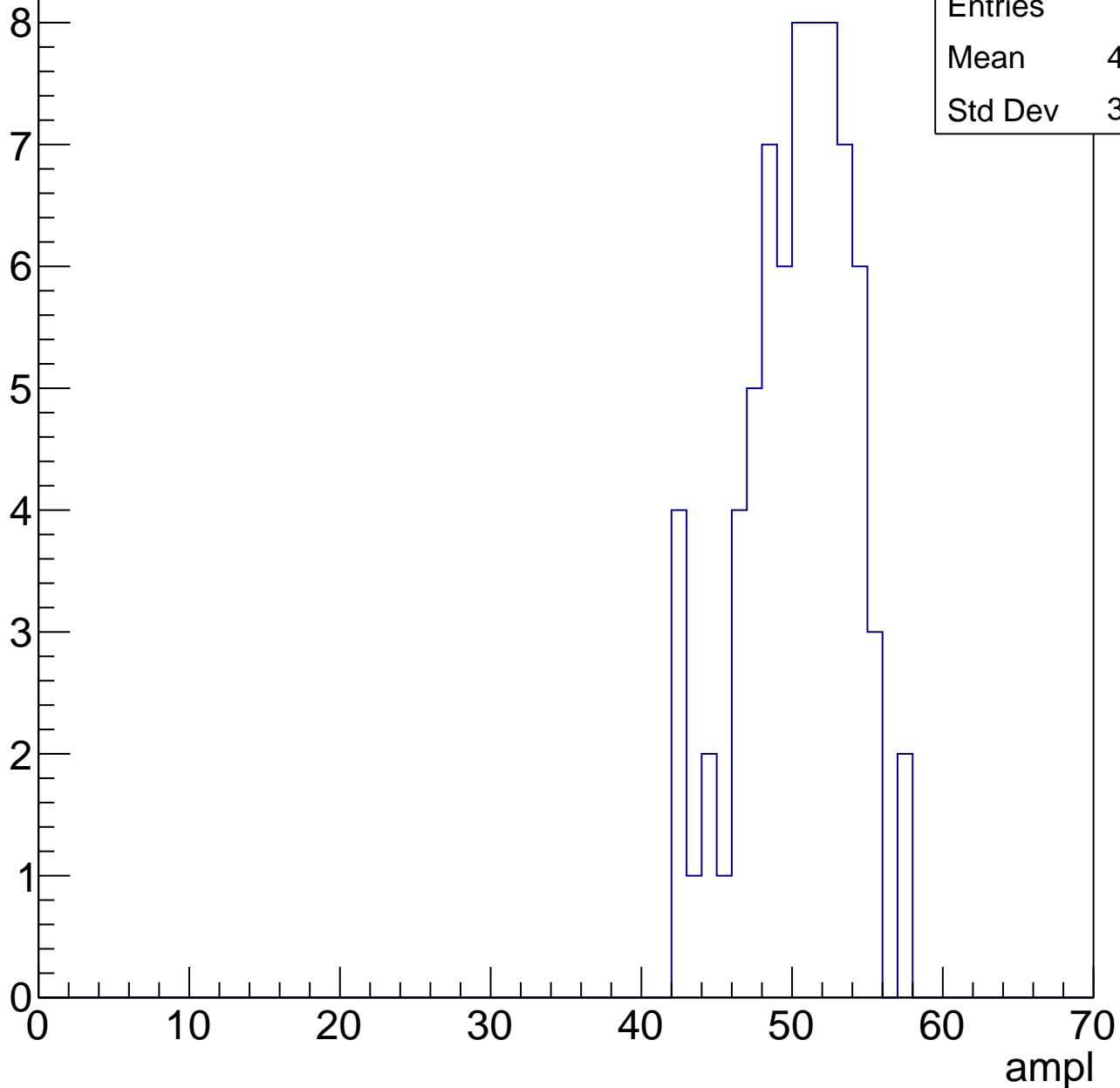


B0L001S, U14-ch124, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

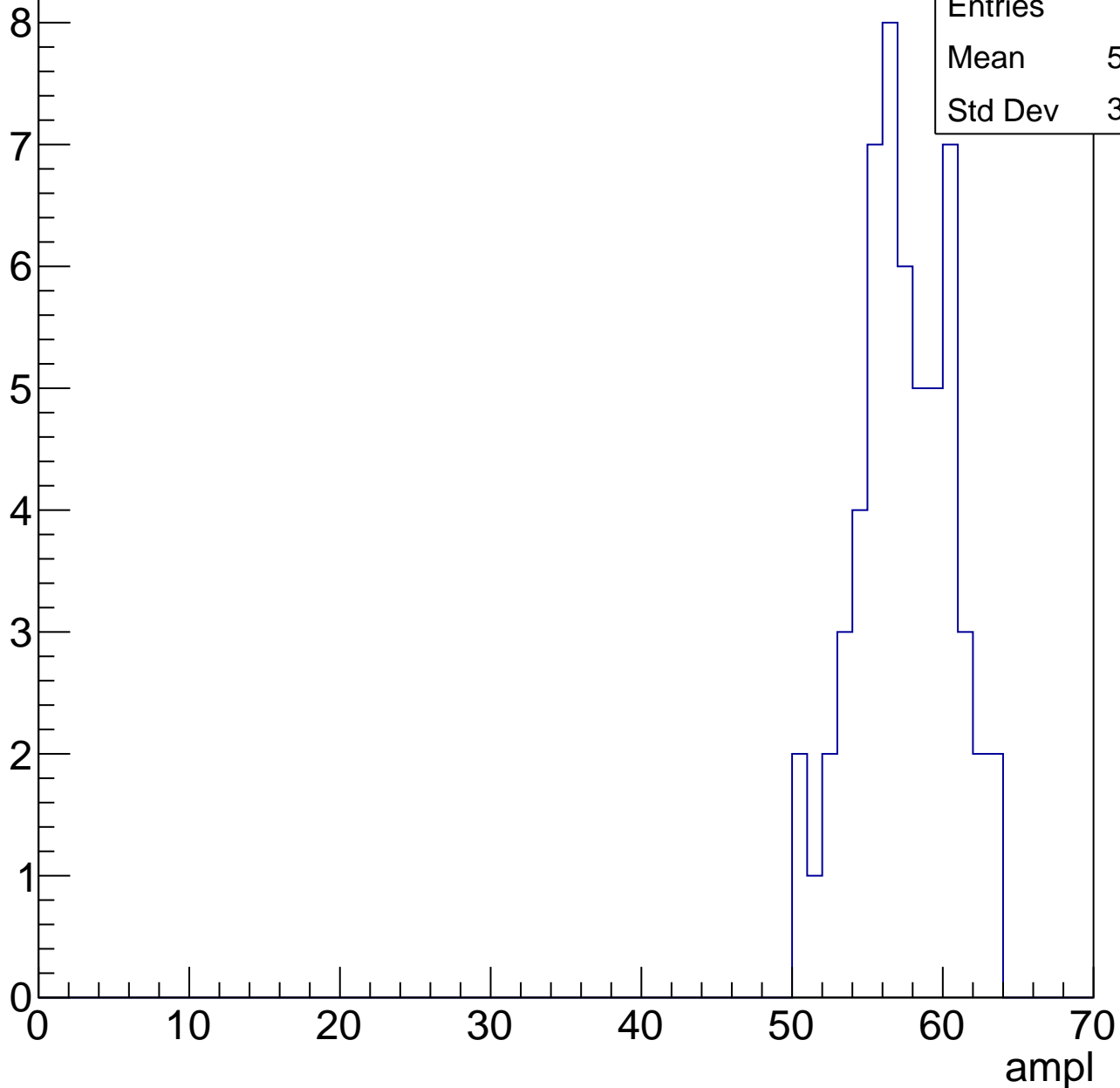
Entries	72
Mean	49.88
Std Dev	3.567



B0L001S, U14-ch124, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

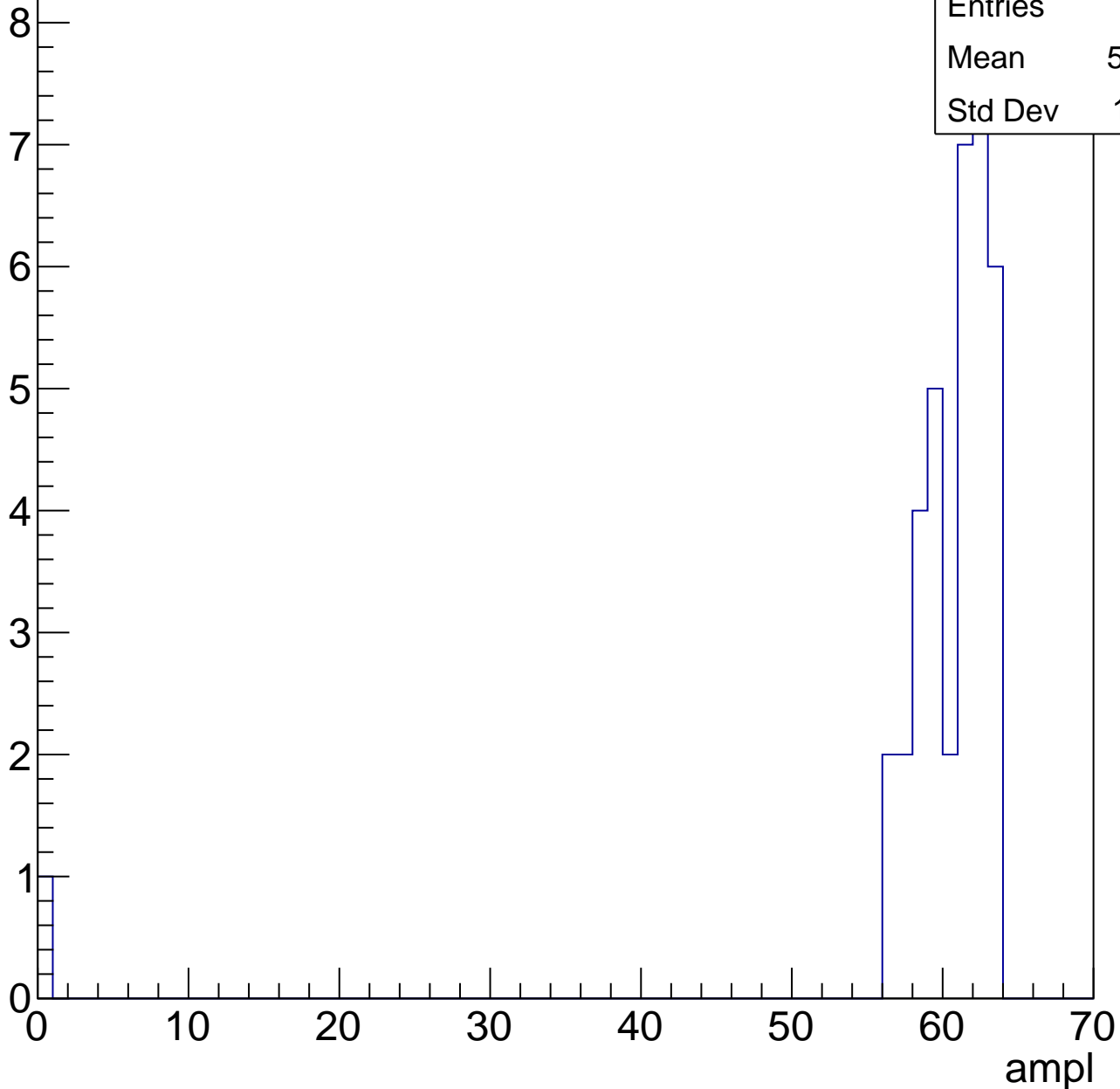


B0L001S, U14-ch124, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

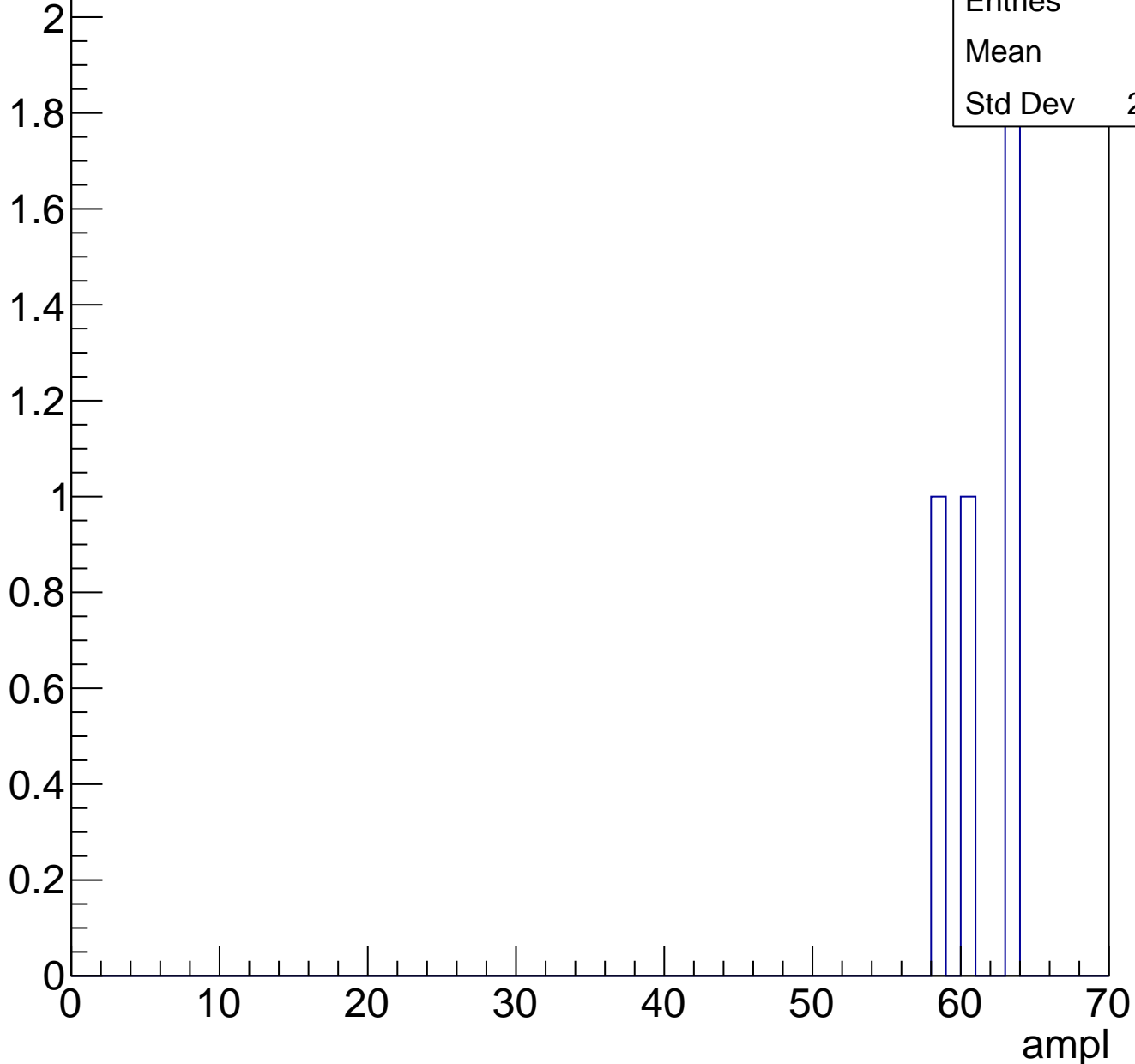
Entries	37
Mean	58.76
Std Dev	10.01



B0L001S, U14-ch124, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch124, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch125, adc0

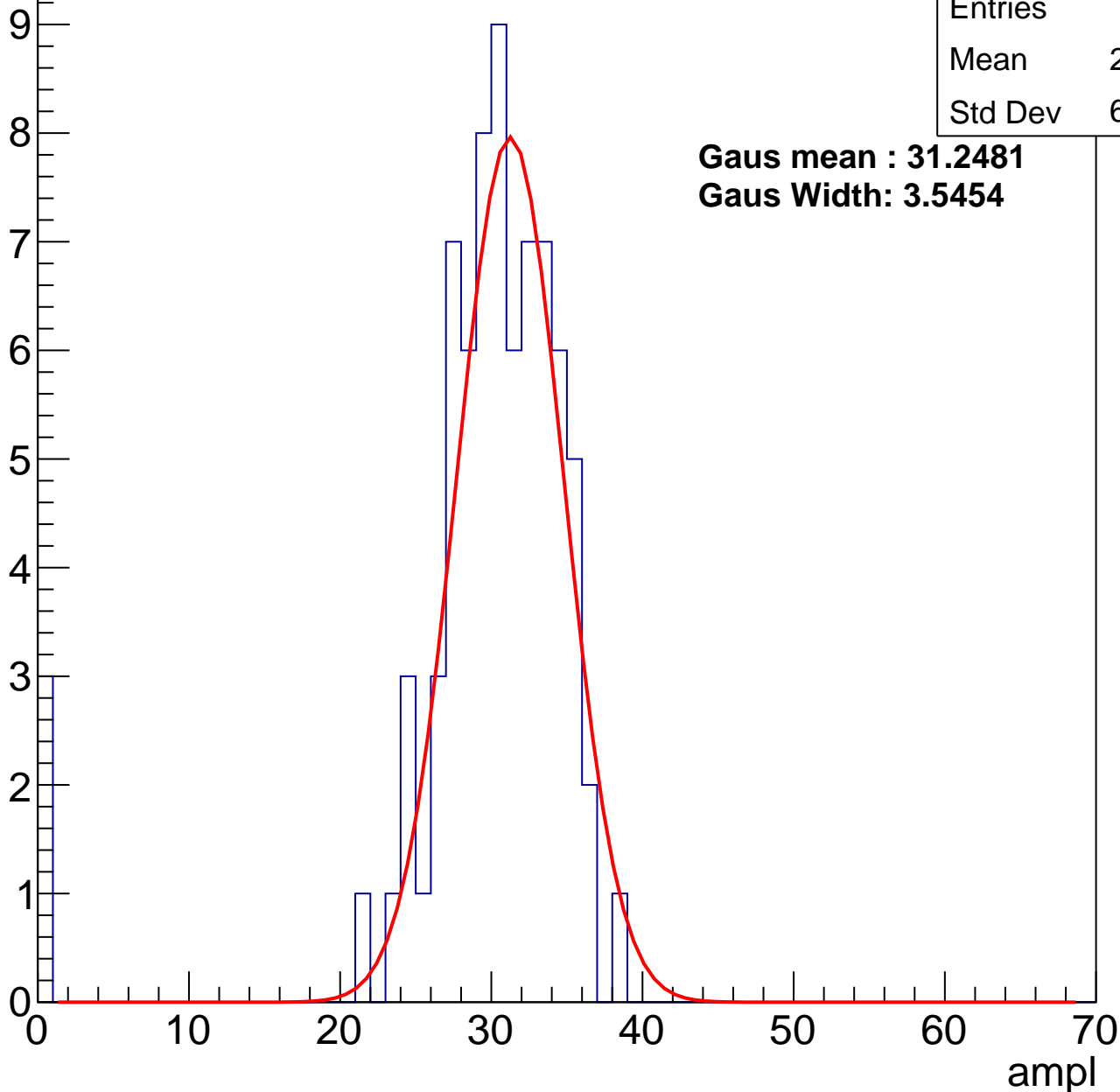
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	76
Mean	29.05
Std Dev	6.778

Gaus mean : 31.2481

Gaus Width: 3.5454



B0L001S, U14-ch125, adc1

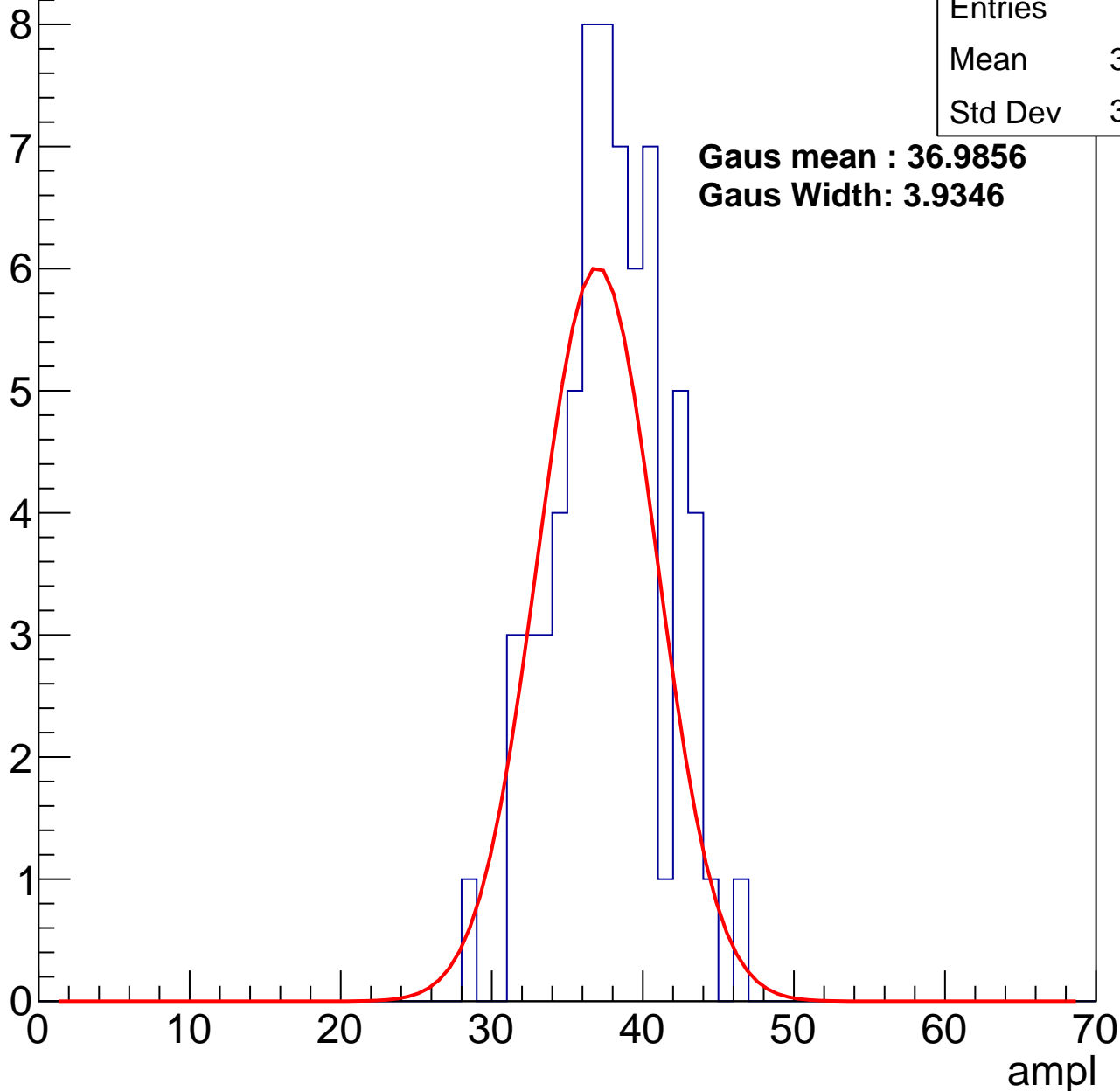
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	67
Mean	37.37
Std Dev	3.615

Gaus mean : 36.9856

Gaus Width: 3.9346



B0L001S, U14-ch125, adc2

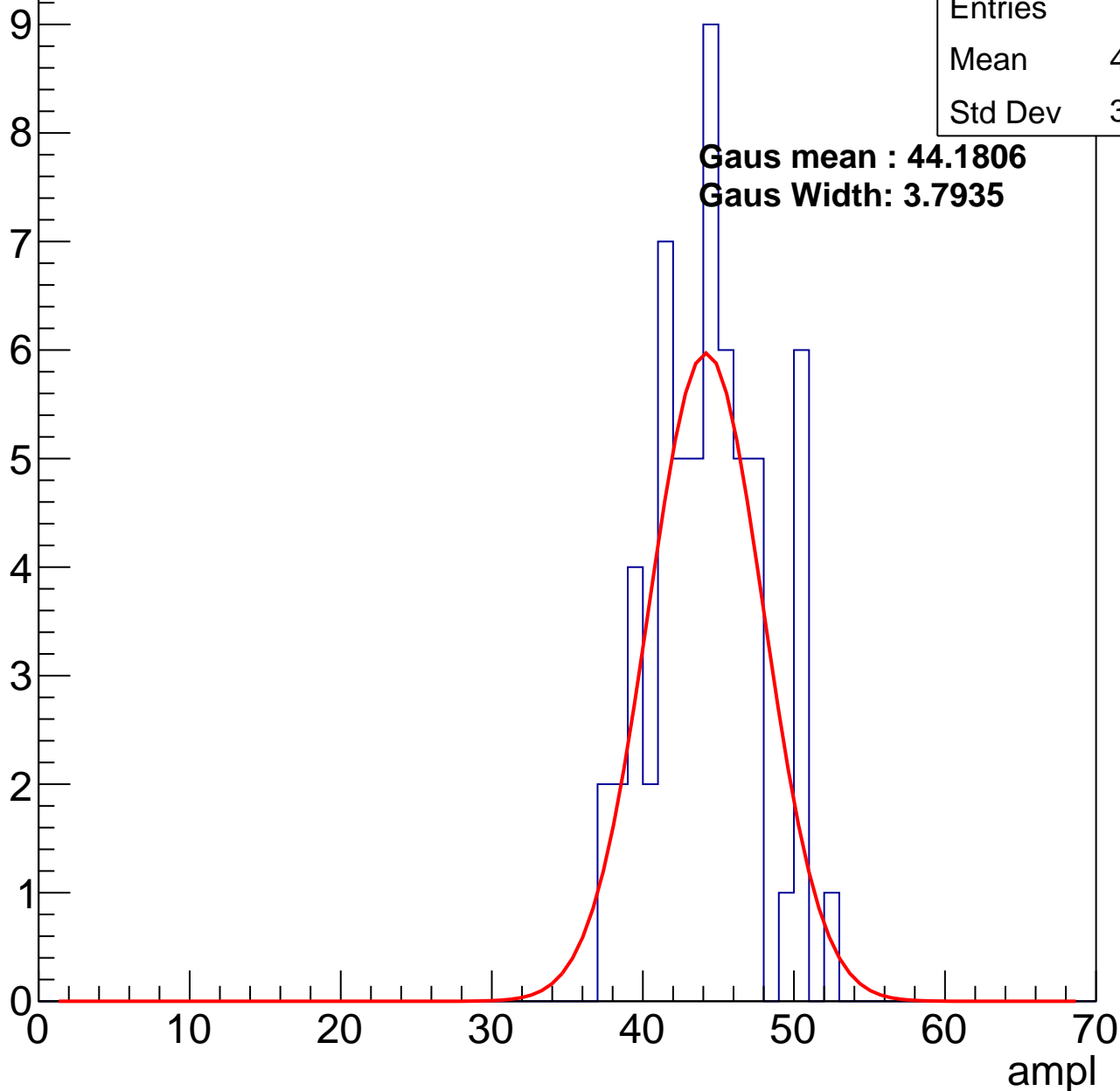
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	43.83
Std Dev	3.569

Gaus mean : 44.1806

Gaus Width: 3.7935



B0L001S, U14-ch125, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entries	67
Mean	49.81
Std Dev	4.027

Entry

10

8

6

4

2

0

0

10

20

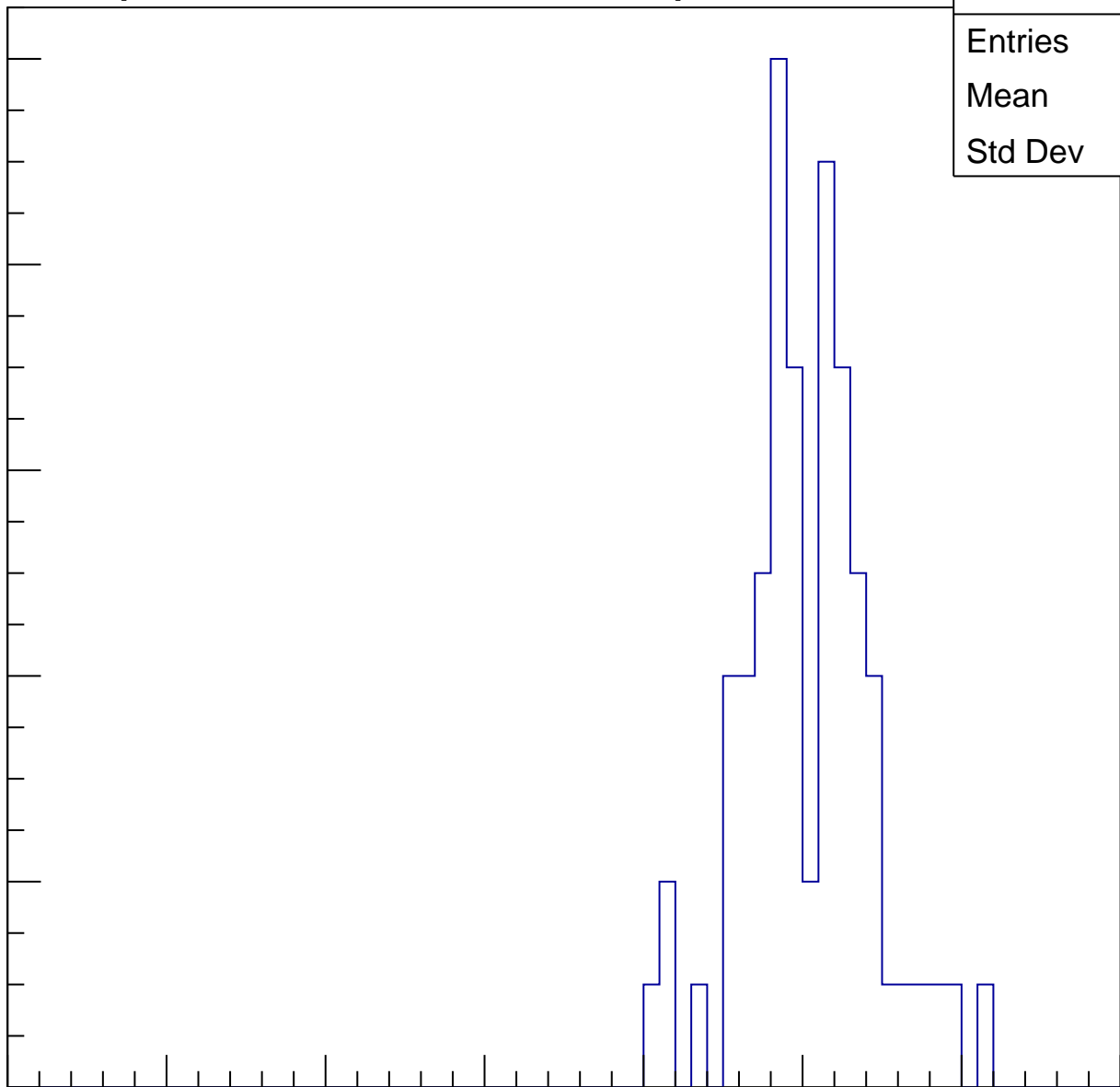
30

40

50

60

ampl

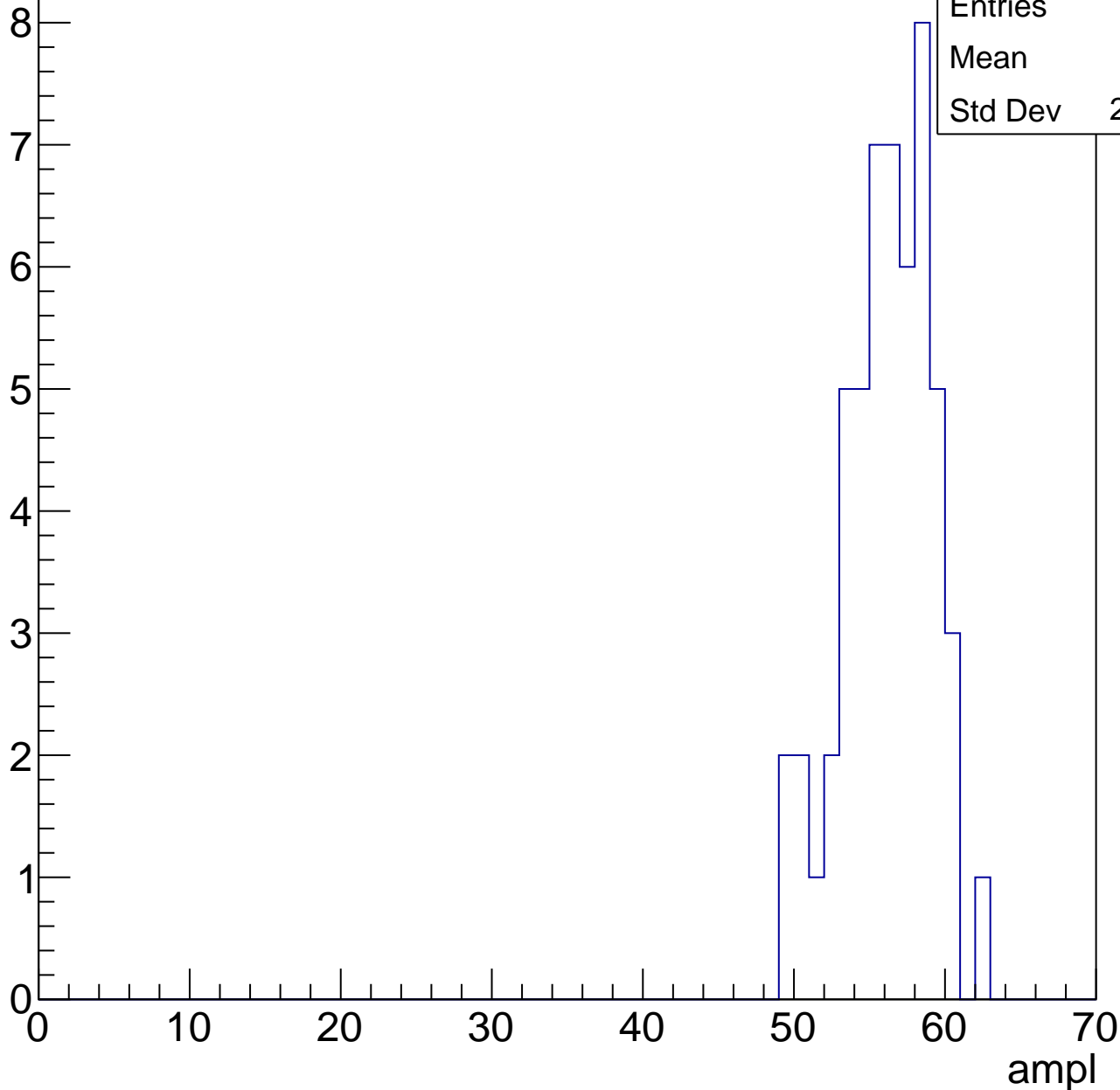


B0L001S, U14-ch125, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	54
Mean	55.7
Std Dev	2.923

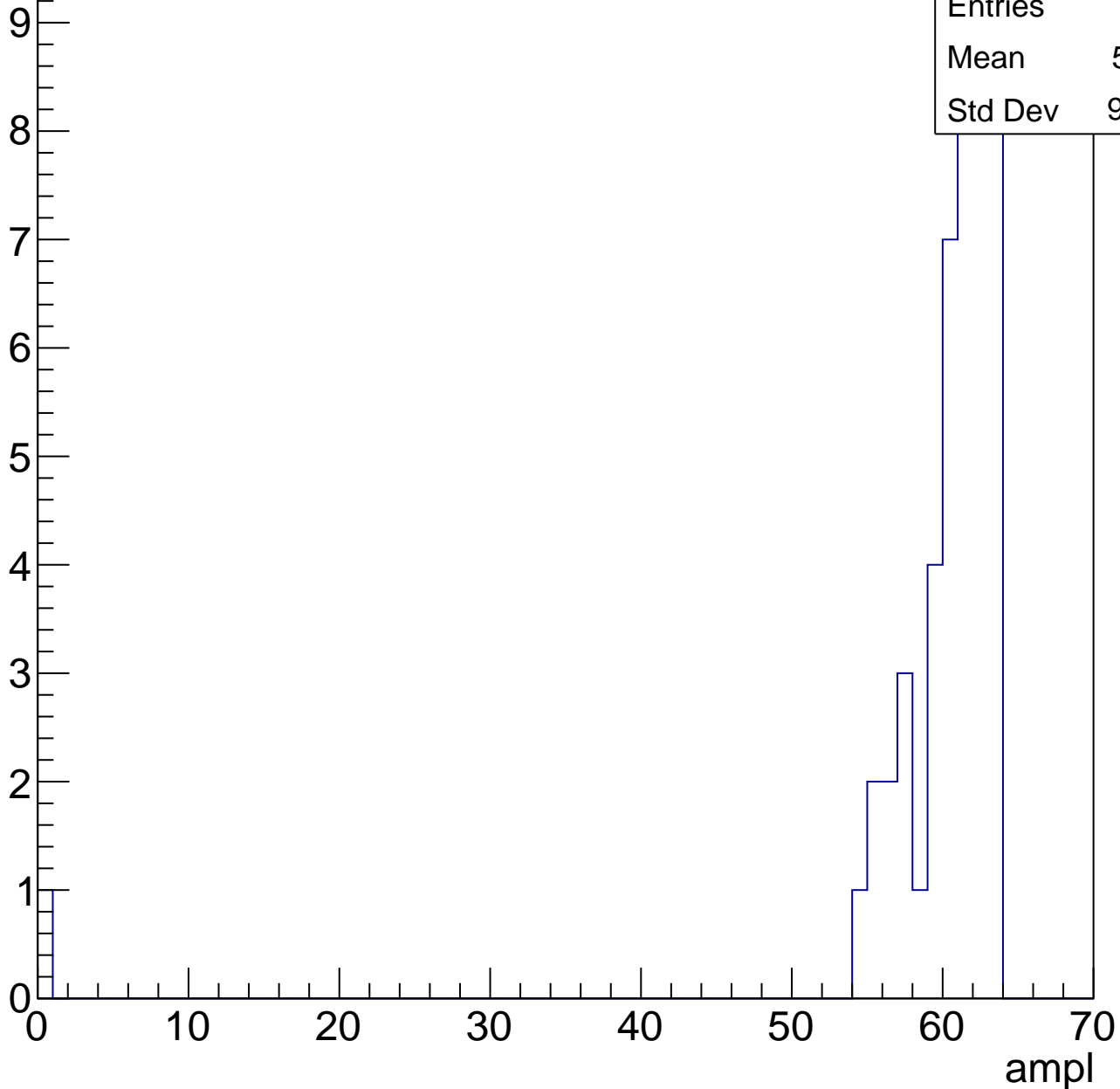


B0L001S, U14-ch125, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

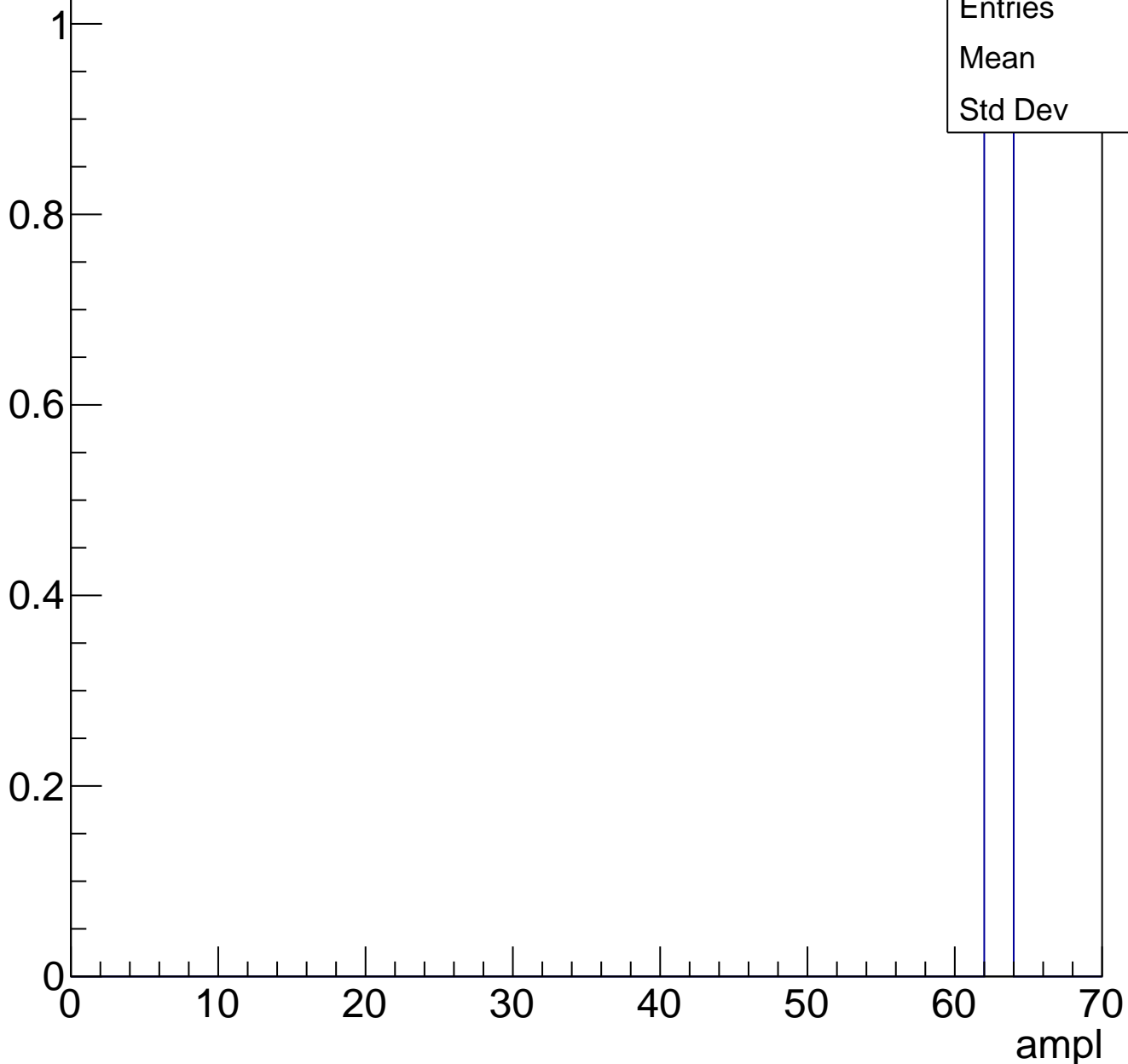
Entries	46
Mean	58.91
Std Dev	9.103



B0L001S, U14-ch125, adc6

calib_packv5_042523_0143.root, FC#9, port A1

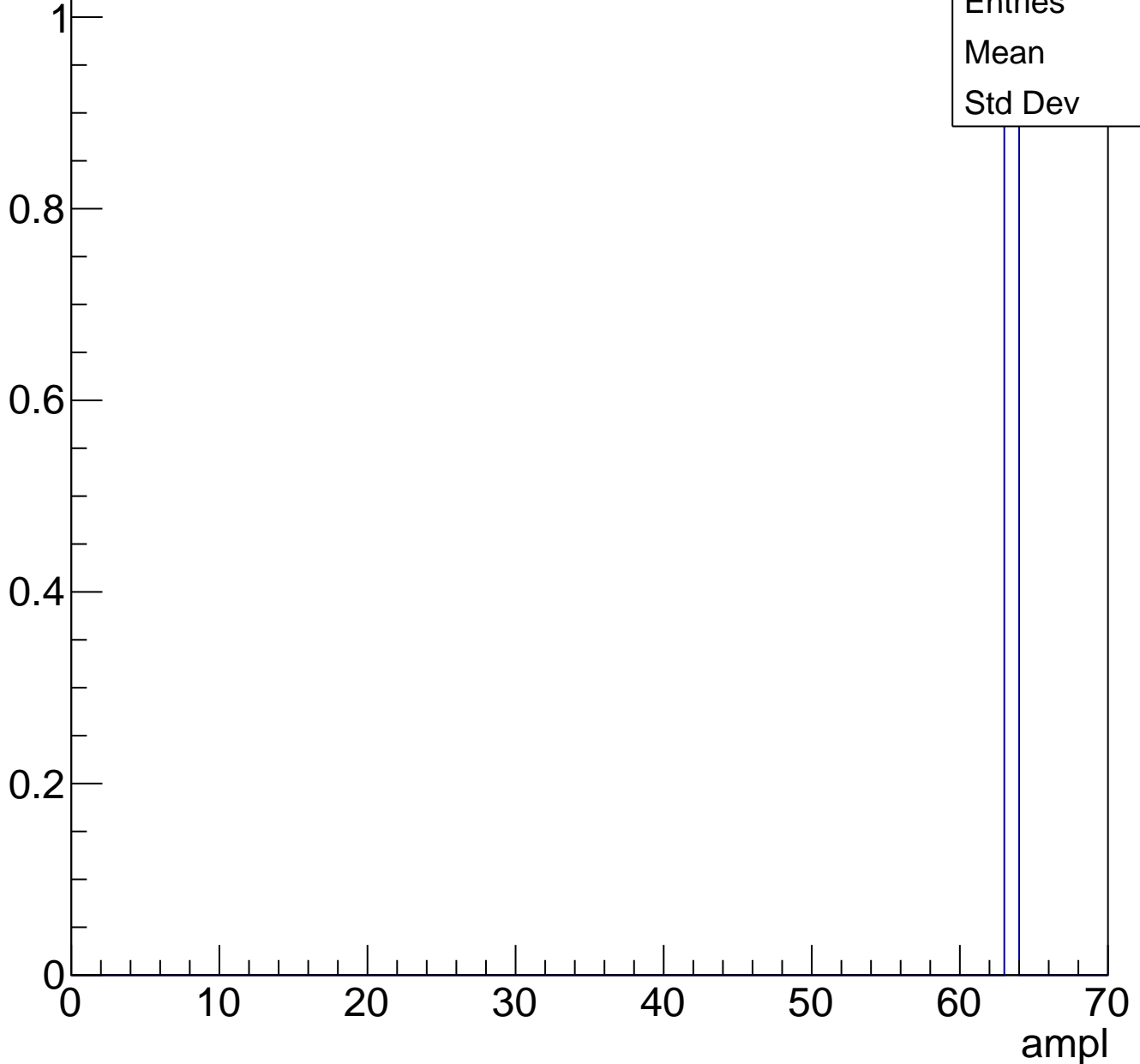
Entry



B0L001S, U14-ch125, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch126, adc0

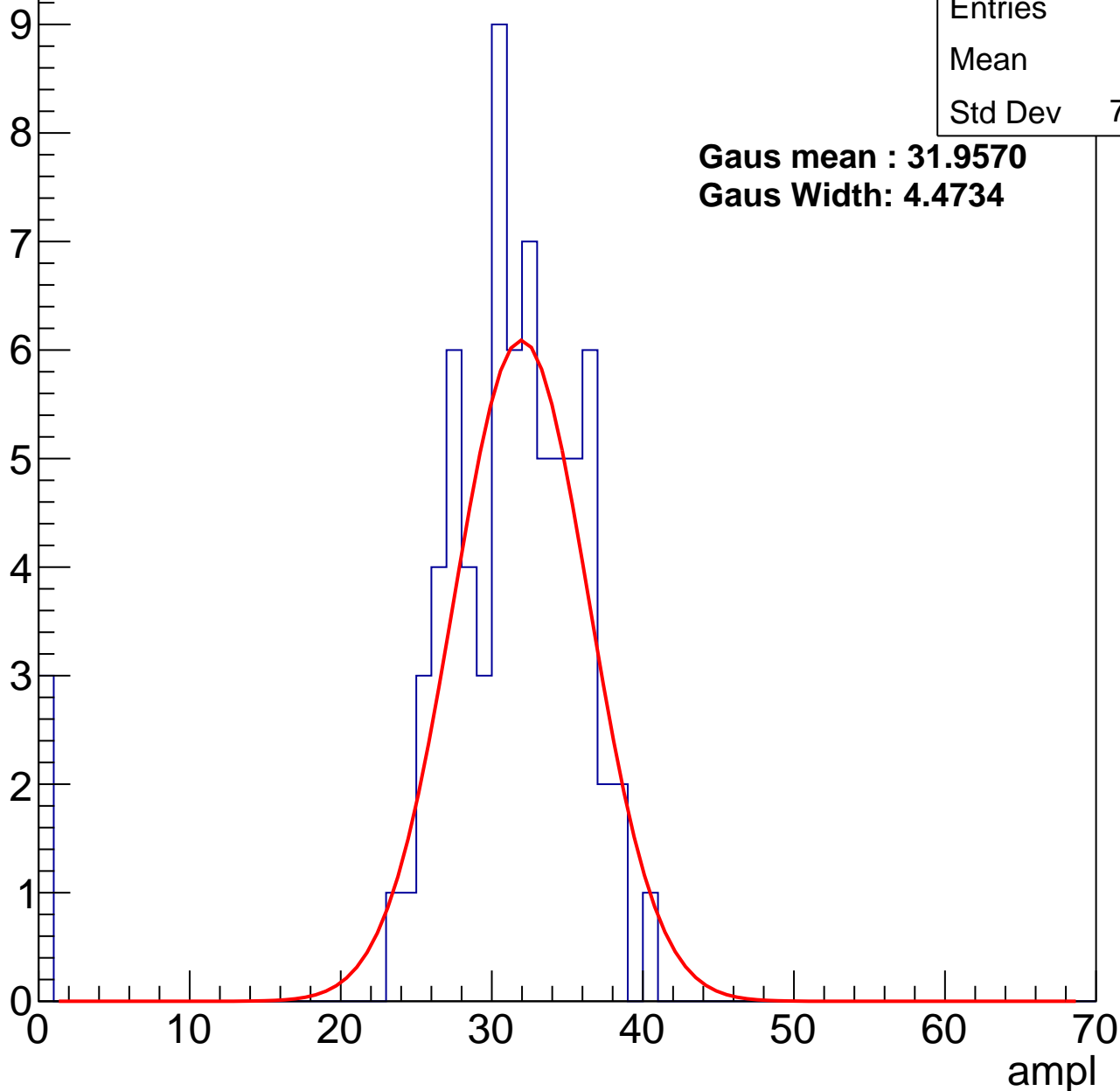
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	73
Mean	29.9
Std Dev	7.236

Gaus mean : 31.9570

Gaus Width: 4.4734



B0L001S, U14-ch126, adc1

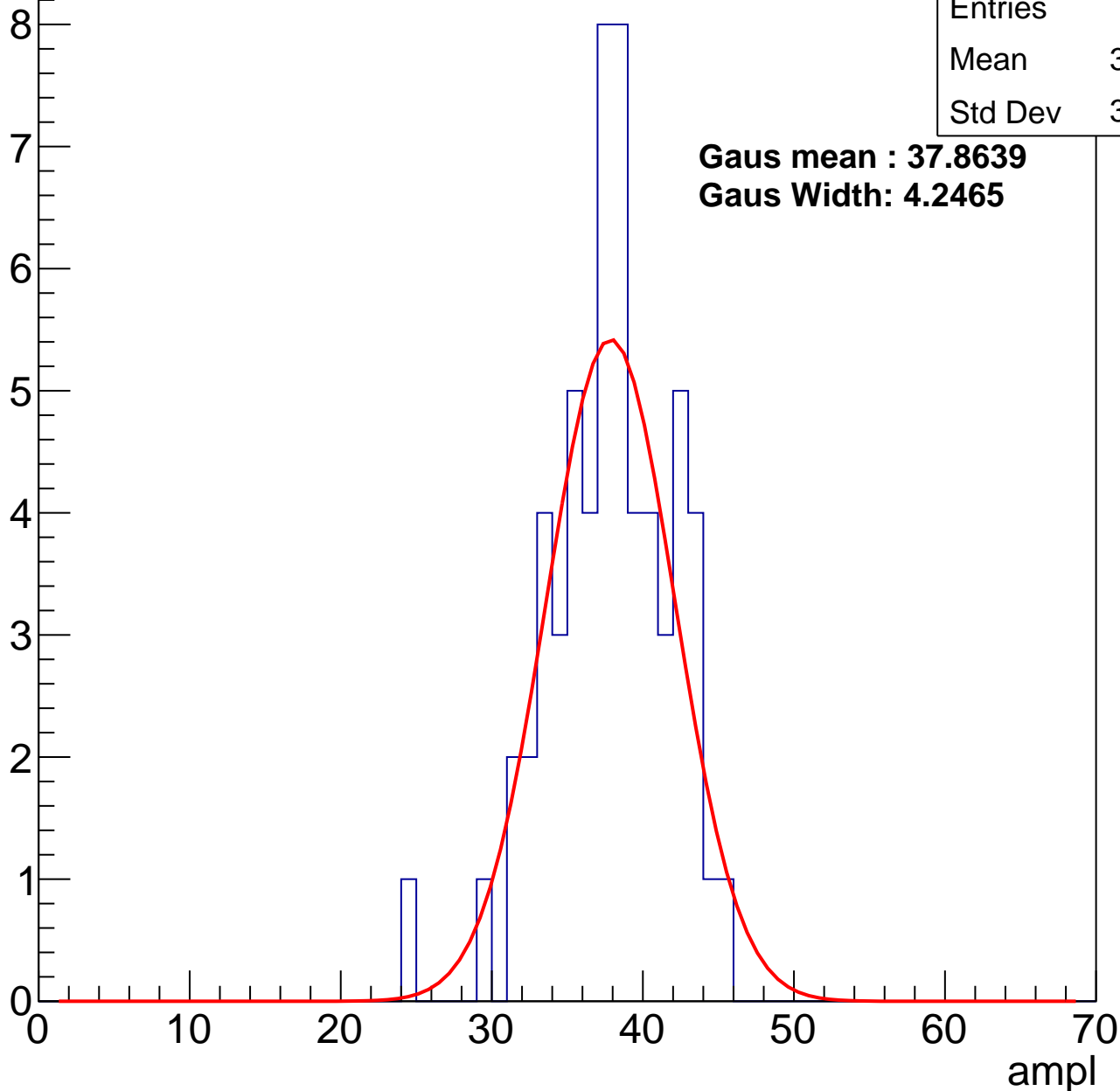
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	60
Mean	37.37
Std Dev	3.983

Gaus mean : 37.8639

Gaus Width: 4.2465



B0L001S, U14-ch126, adc2

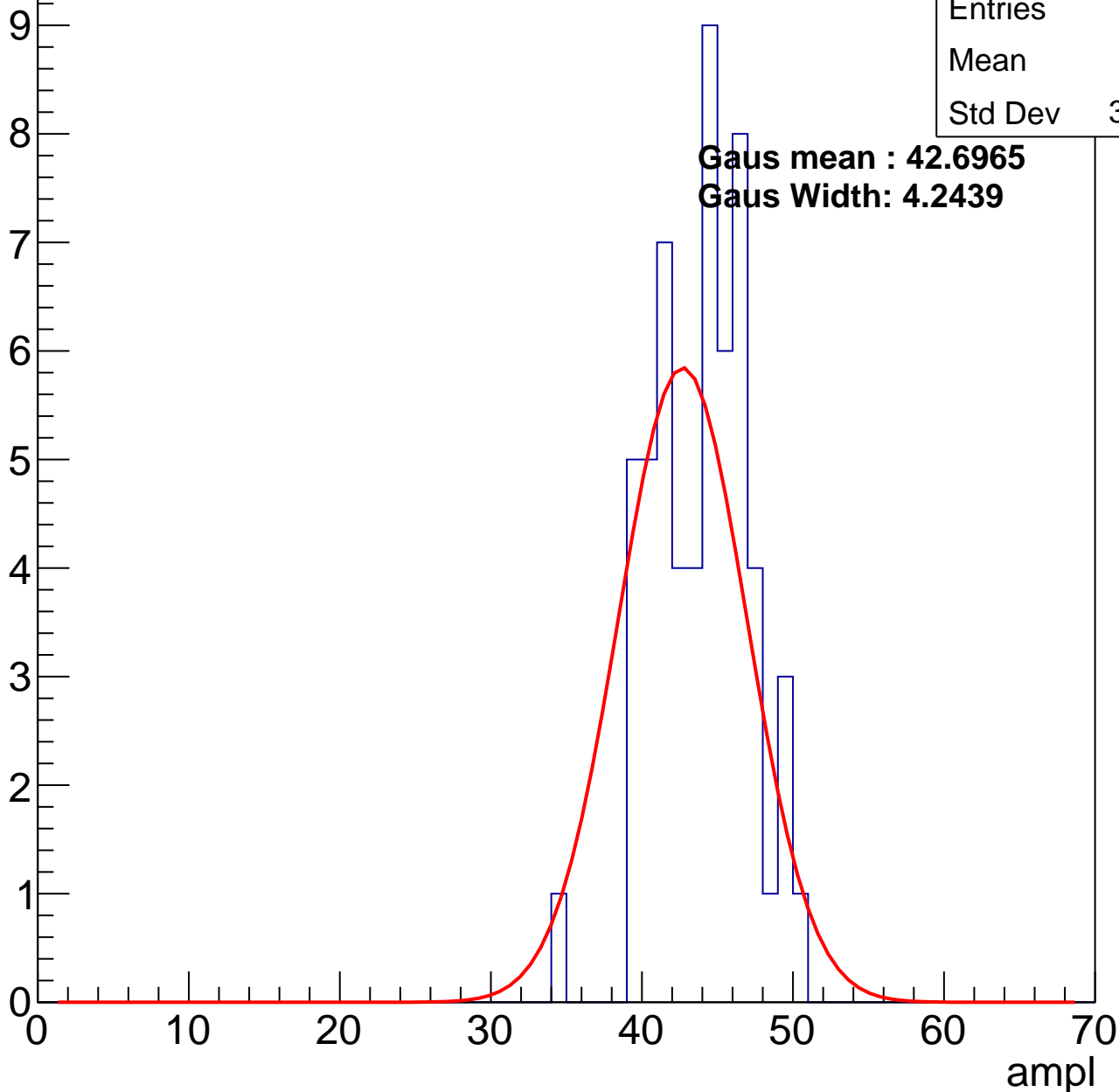
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	58
Mean	43.5
Std Dev	3.147

Gaus mean : 42.6965

Gaus Width: 4.2439

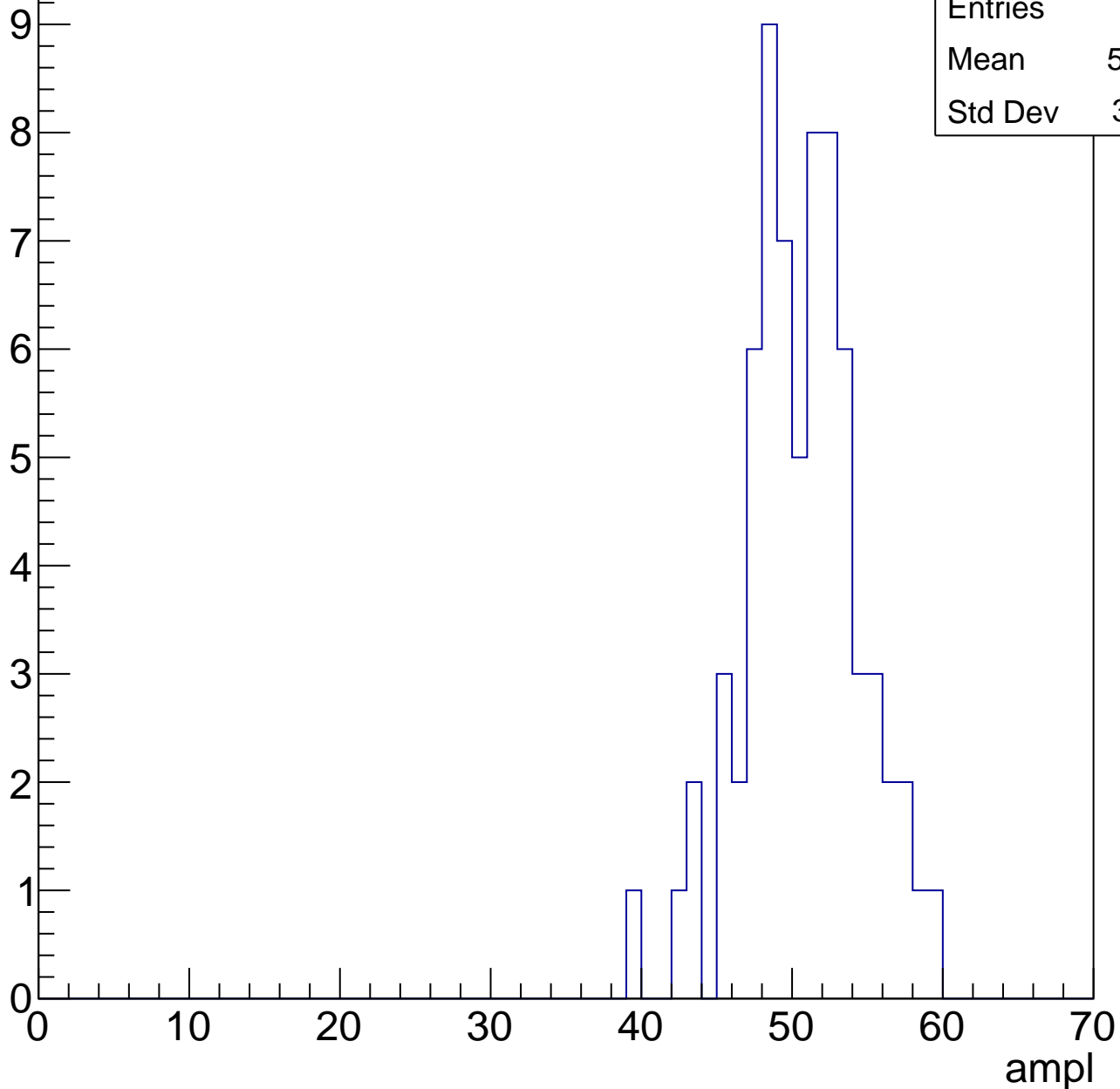


B0L001S, U14-ch126, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	70
Mean	50.19
Std Dev	3.811

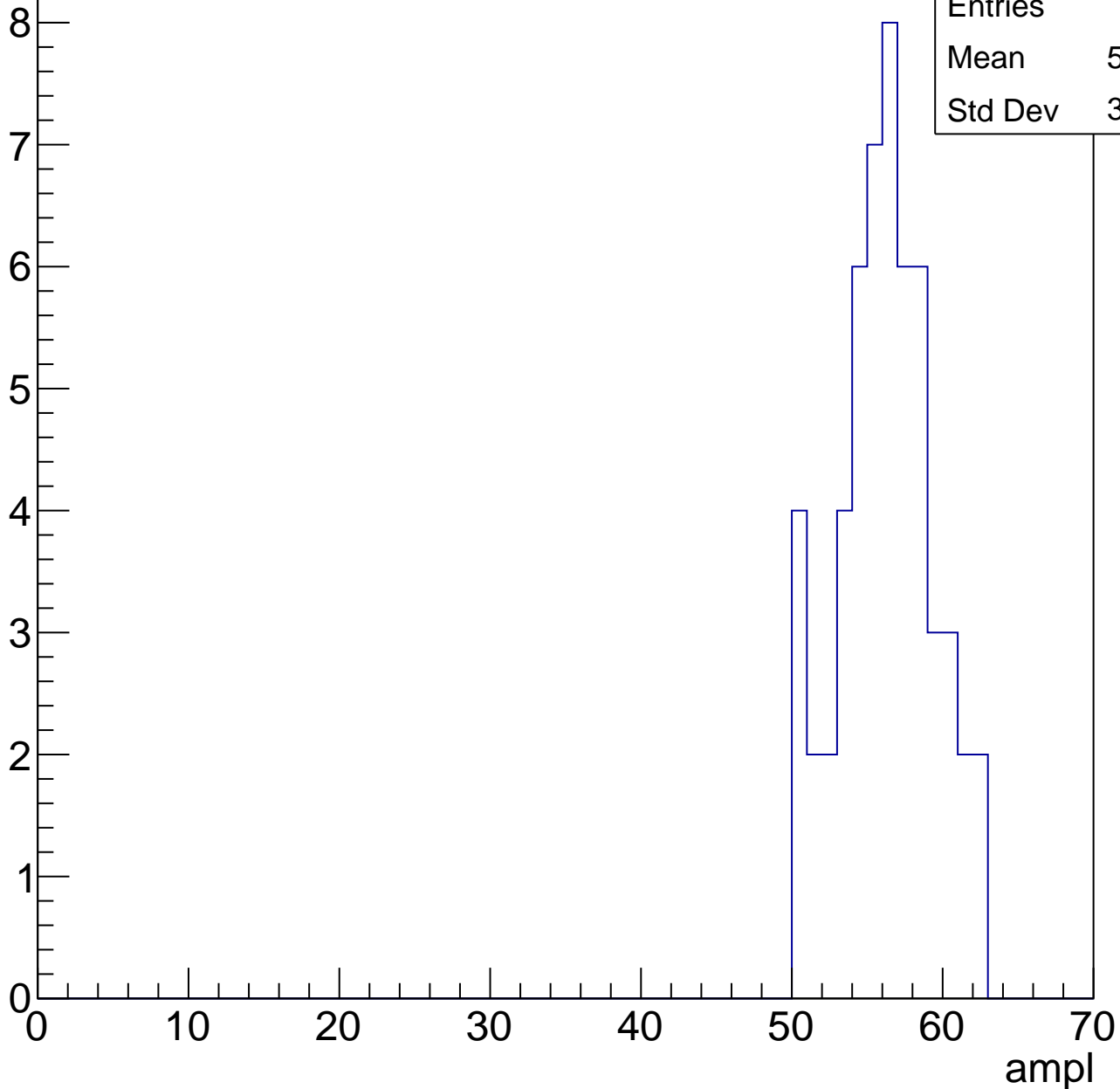


B0L001S, U14-ch126, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	55.78
Std Dev	3.067

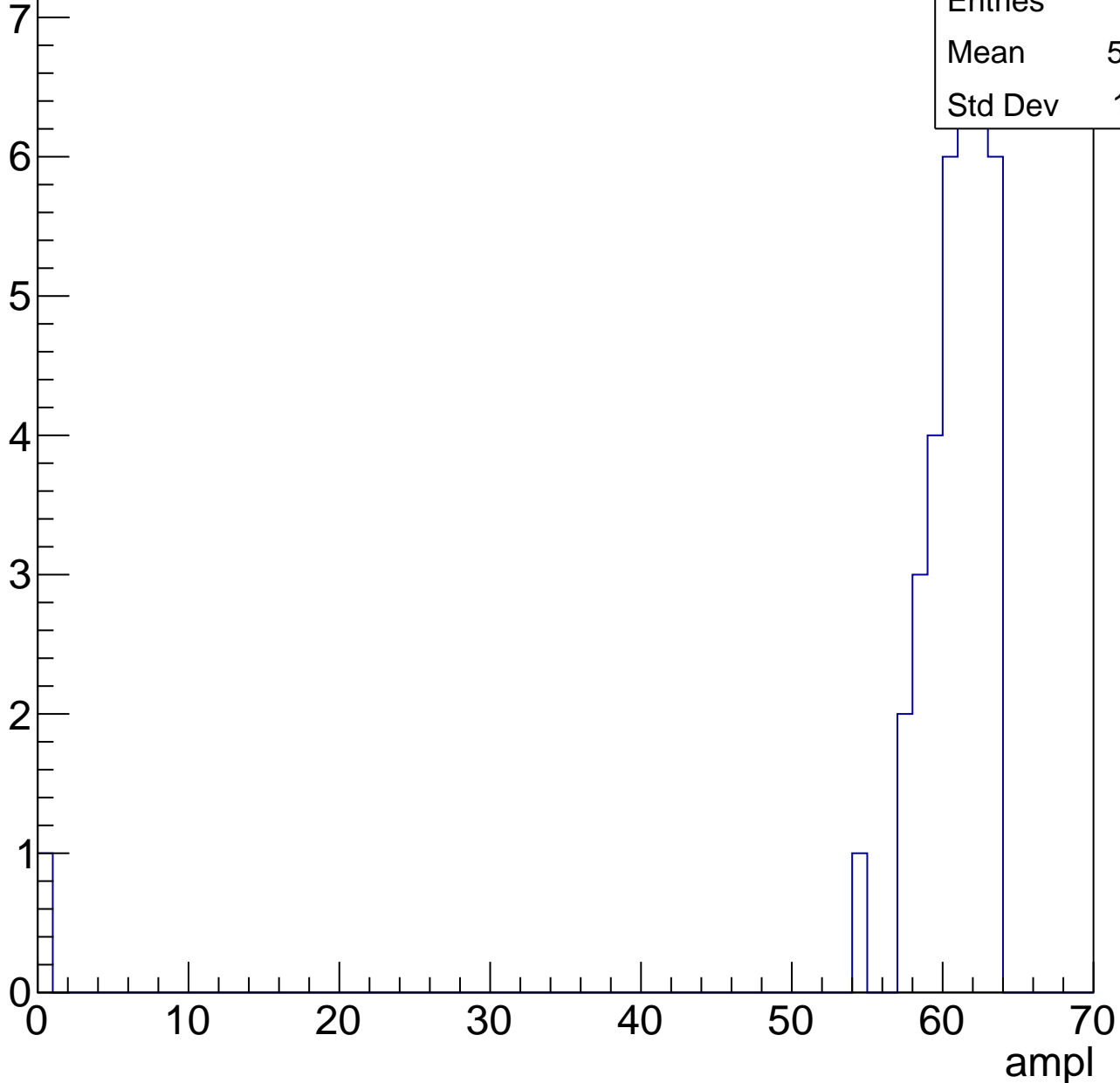


B0L001S, U14-ch126, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entry

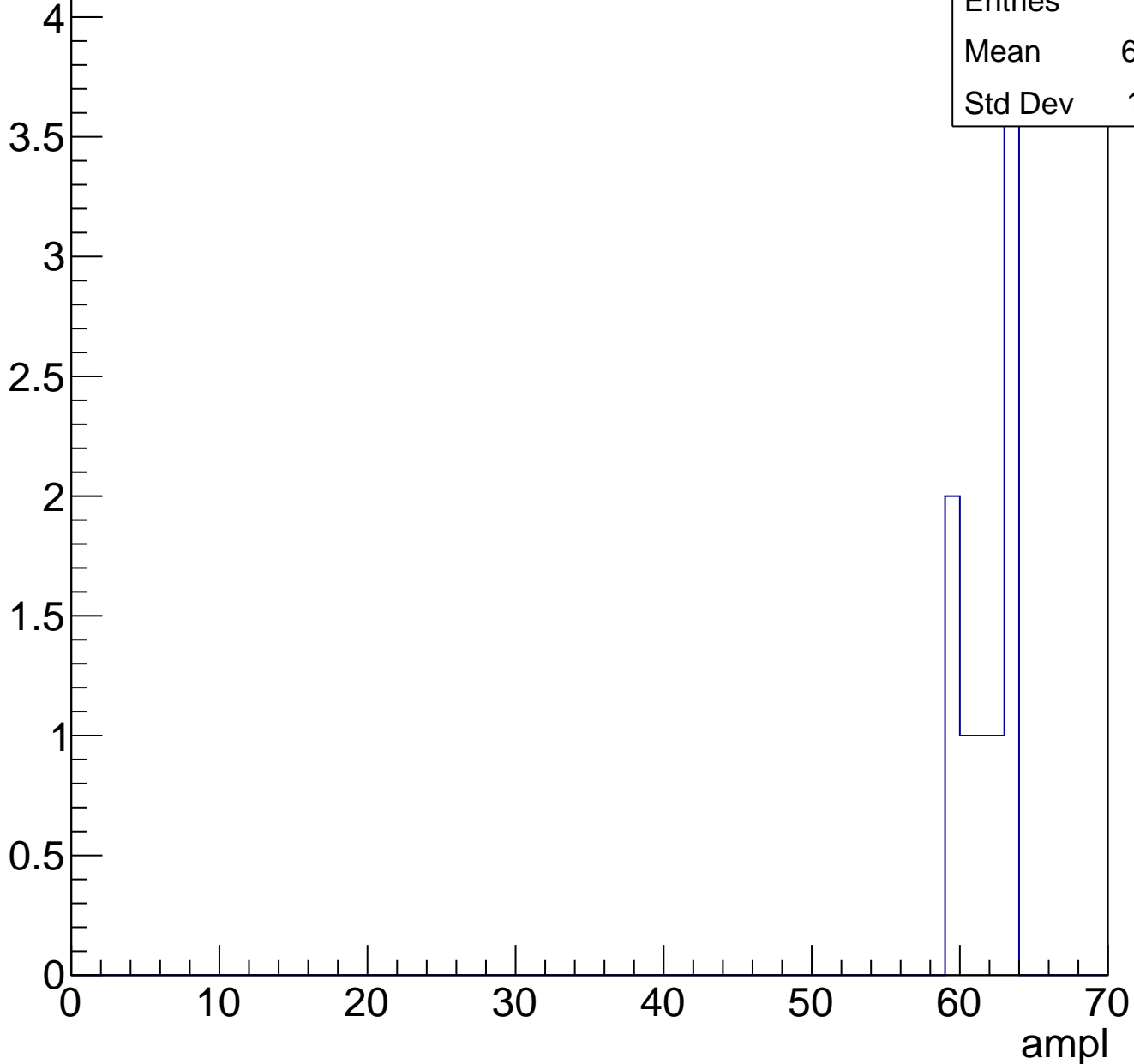
Entries	37
Mean	58.84
Std Dev	10.01



B0L001S, U14-ch126, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch126, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



Entries	1
Mean	0
Std Dev	0

B0L001S, U14-ch127, adc0

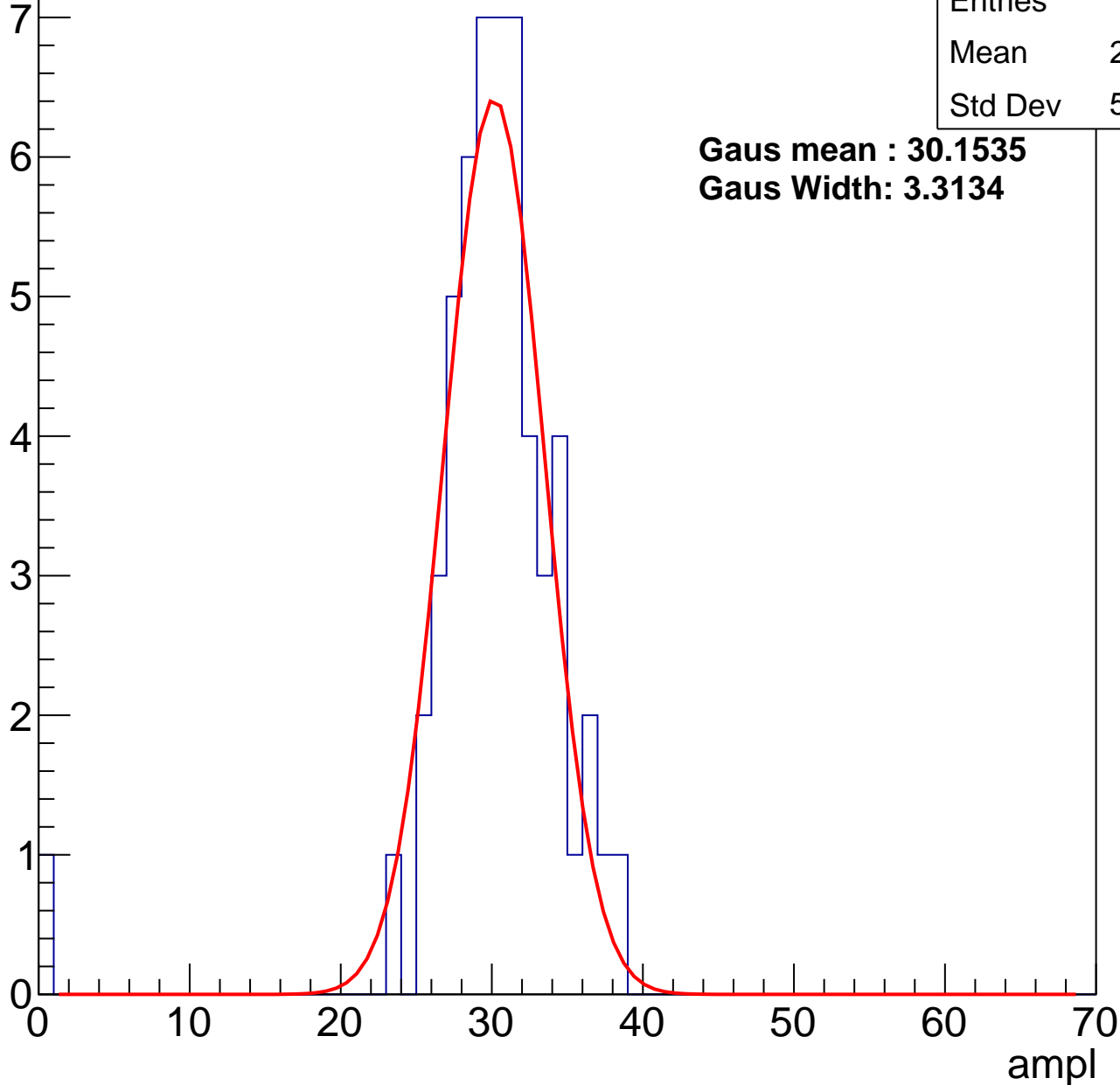
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	29.62
Std Dev	5.115

Gaus mean : 30.1535

Gaus Width: 3.3134



B0L001S, U14-ch127, adc1

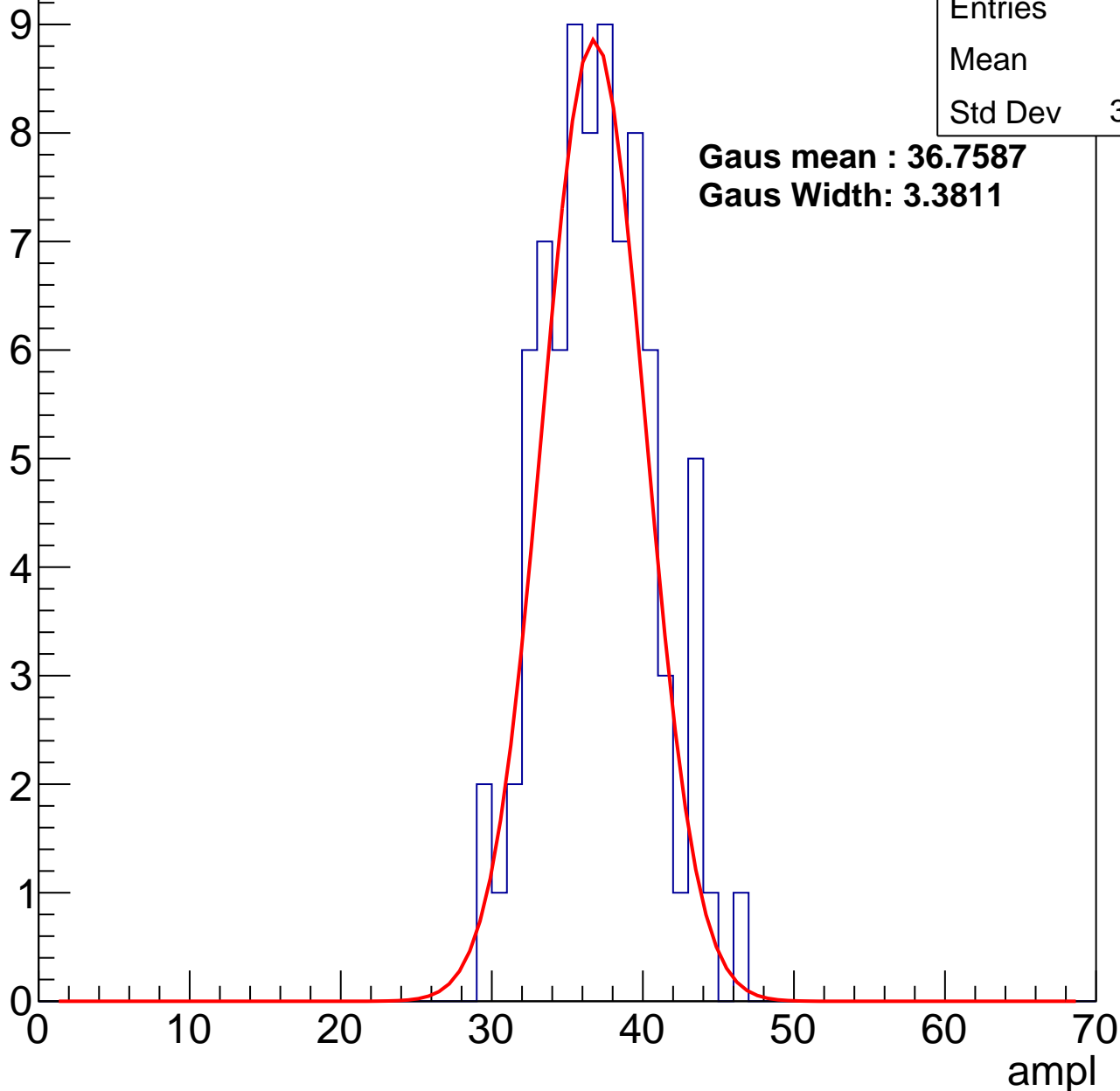
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	82
Mean	36.6
Std Dev	3.615

Gaus mean : 36.7587

Gaus Width: 3.3811



B0L001S, U14-ch127, adc2

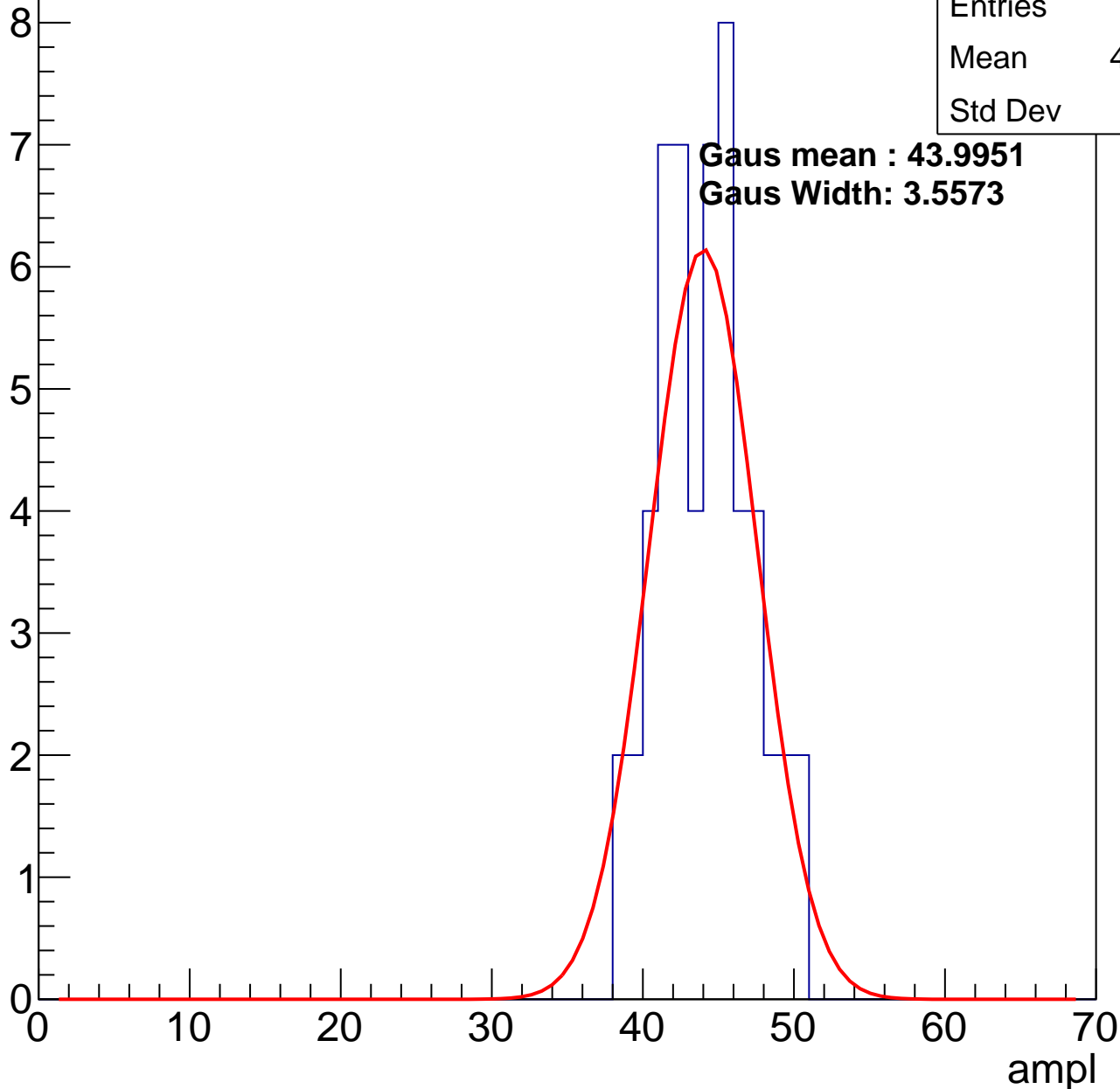
calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	55
Mean	43.65
Std Dev	2.98

Gaus mean : 43.9951

Gaus Width: 3.5573

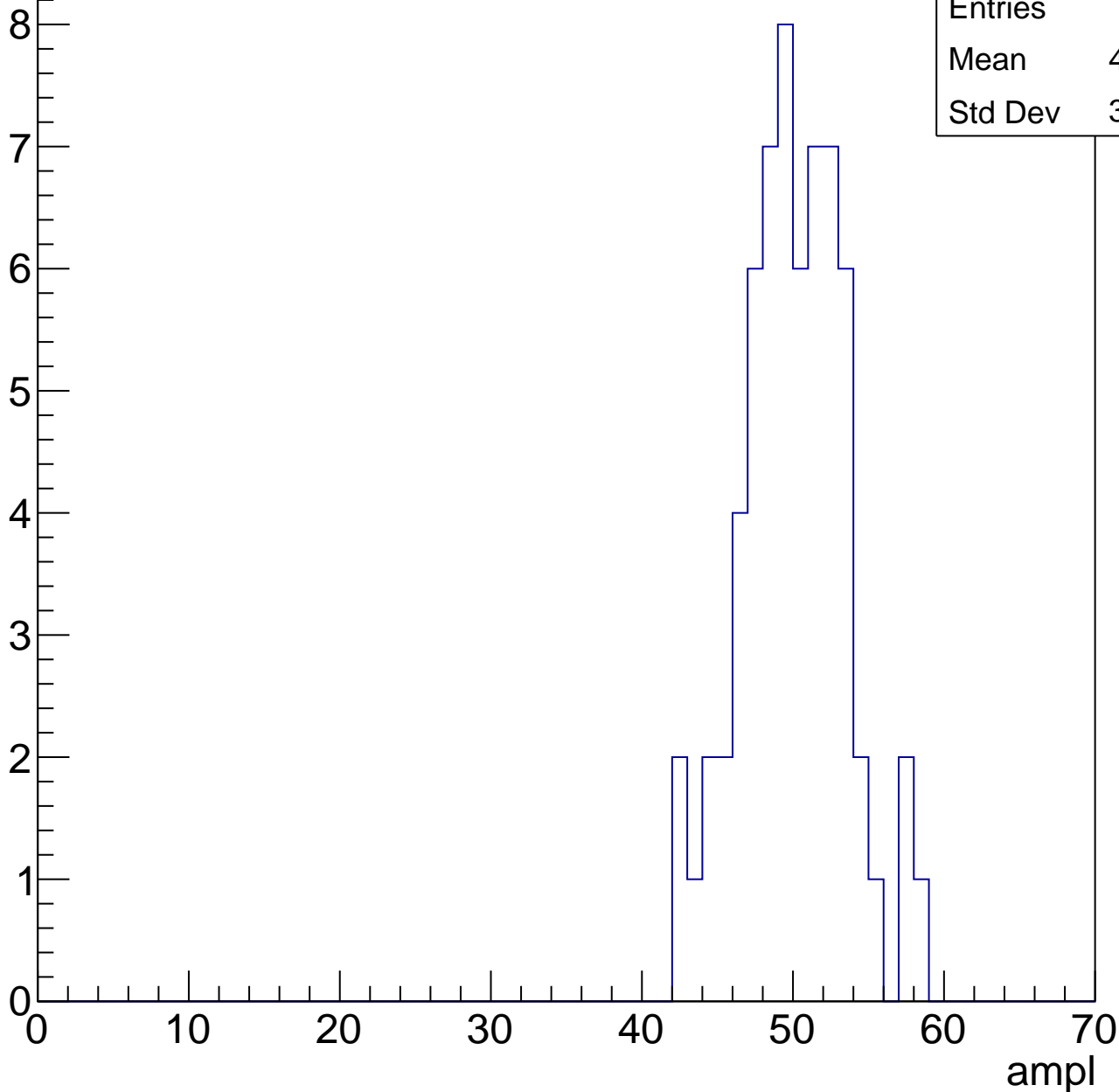


B0L001S, U14-ch127, adc3

calib_packv5_042523_0143.root, FC#9, port A1

Entry

Entries	64
Mean	49.58
Std Dev	3.422

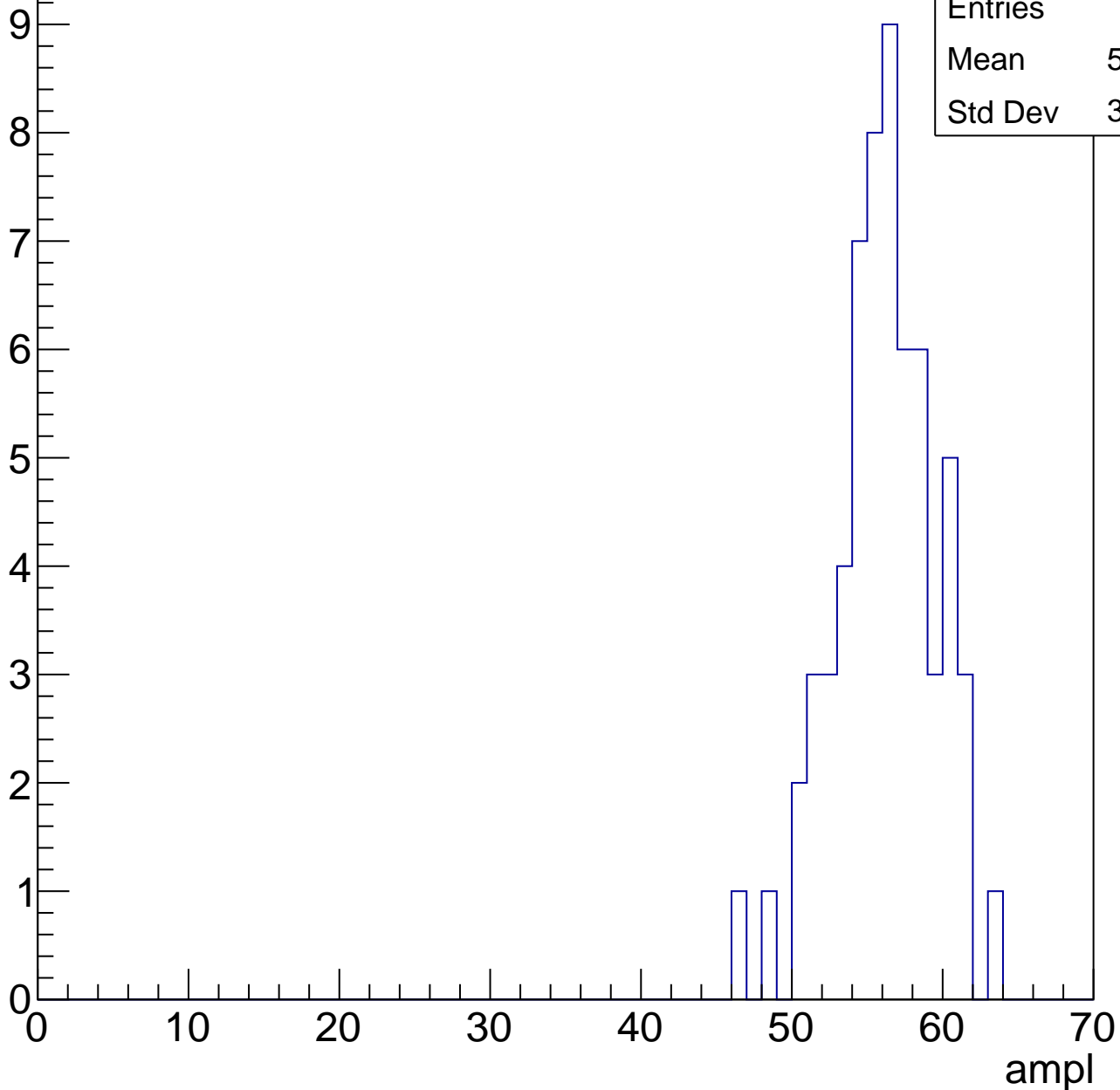


B0L001S, U14-ch127, adc4

calib_packv5_042523_0143.root, FC#9, port A1

Entry

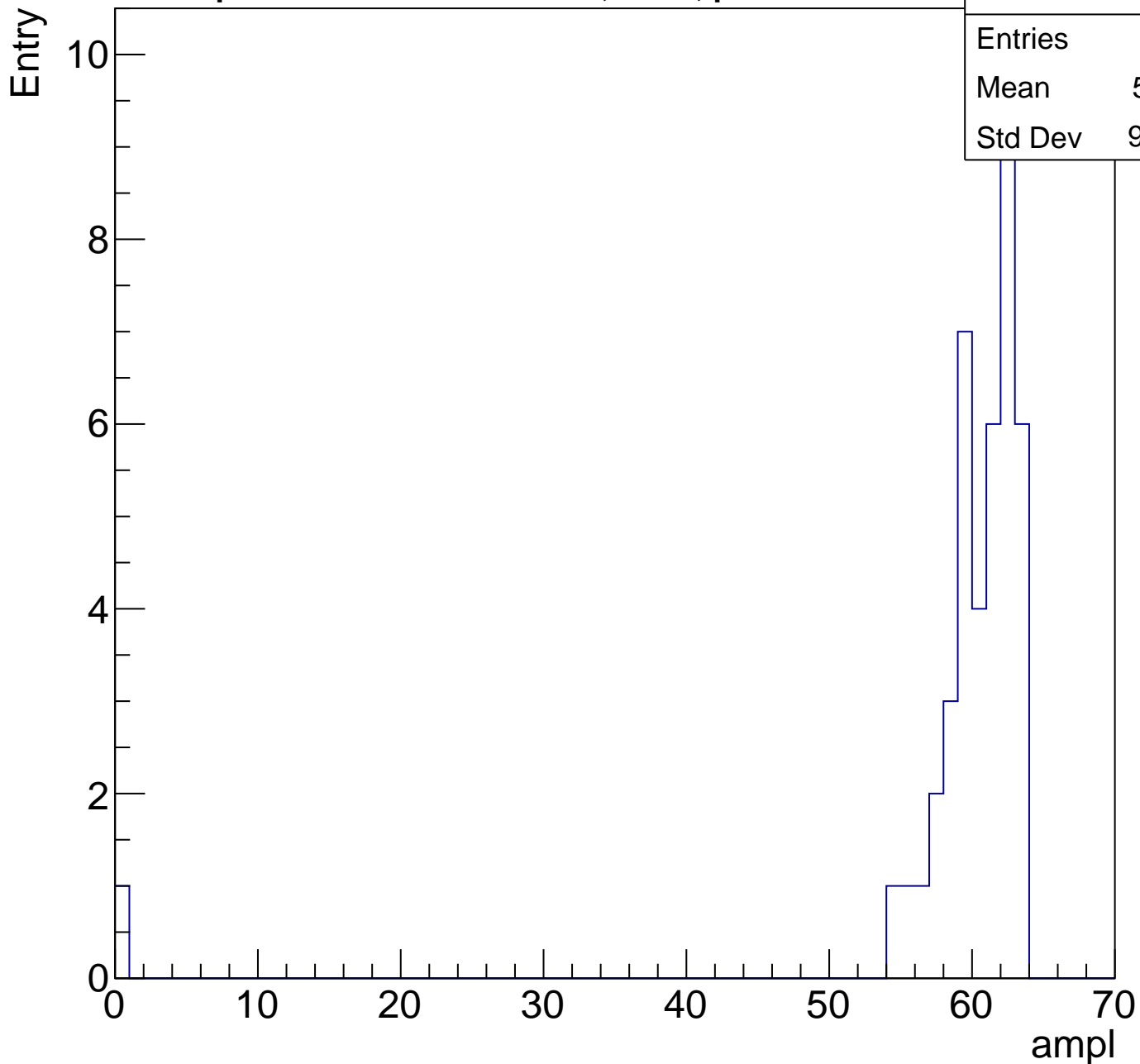
Entries	62
Mean	55.65
Std Dev	3.322



B0L001S, U14-ch127, adc5

calib_packv5_042523_0143.root, FC#9, port A1

Entries	42
Mean	58.81
Std Dev	9.455



B0L001S, U14-ch127, adc6

calib_packv5_042523_0143.root, FC#9, port A1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	1.299

0 10 20 30 40 50 60 70

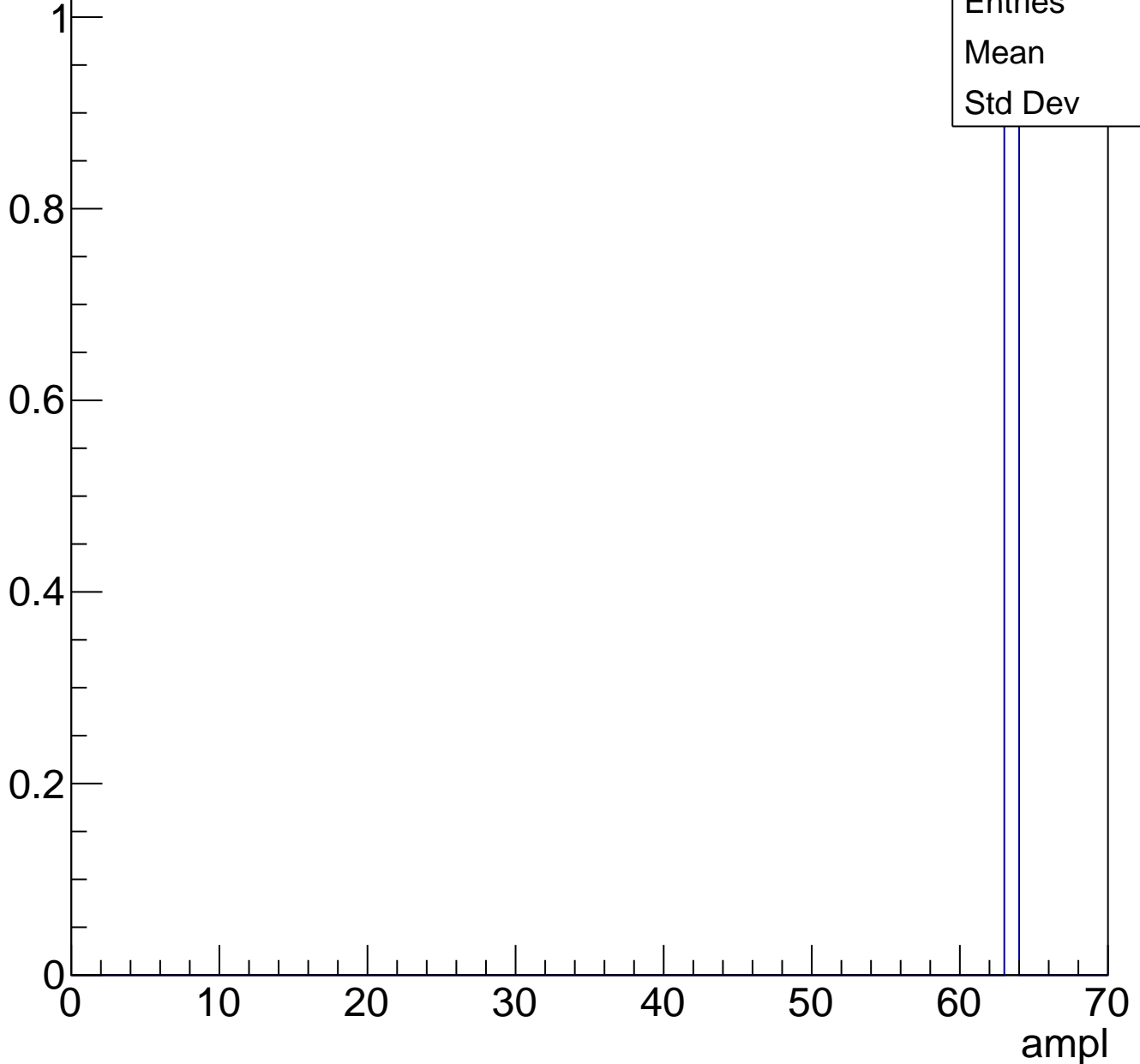
ampl

1

B0L001S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry



B0L001S, U14-ch127, adc7

calib_packv5_042523_0143.root, FC#9, port A1

Entry

