

B1L101S, U15-ch0, adc0

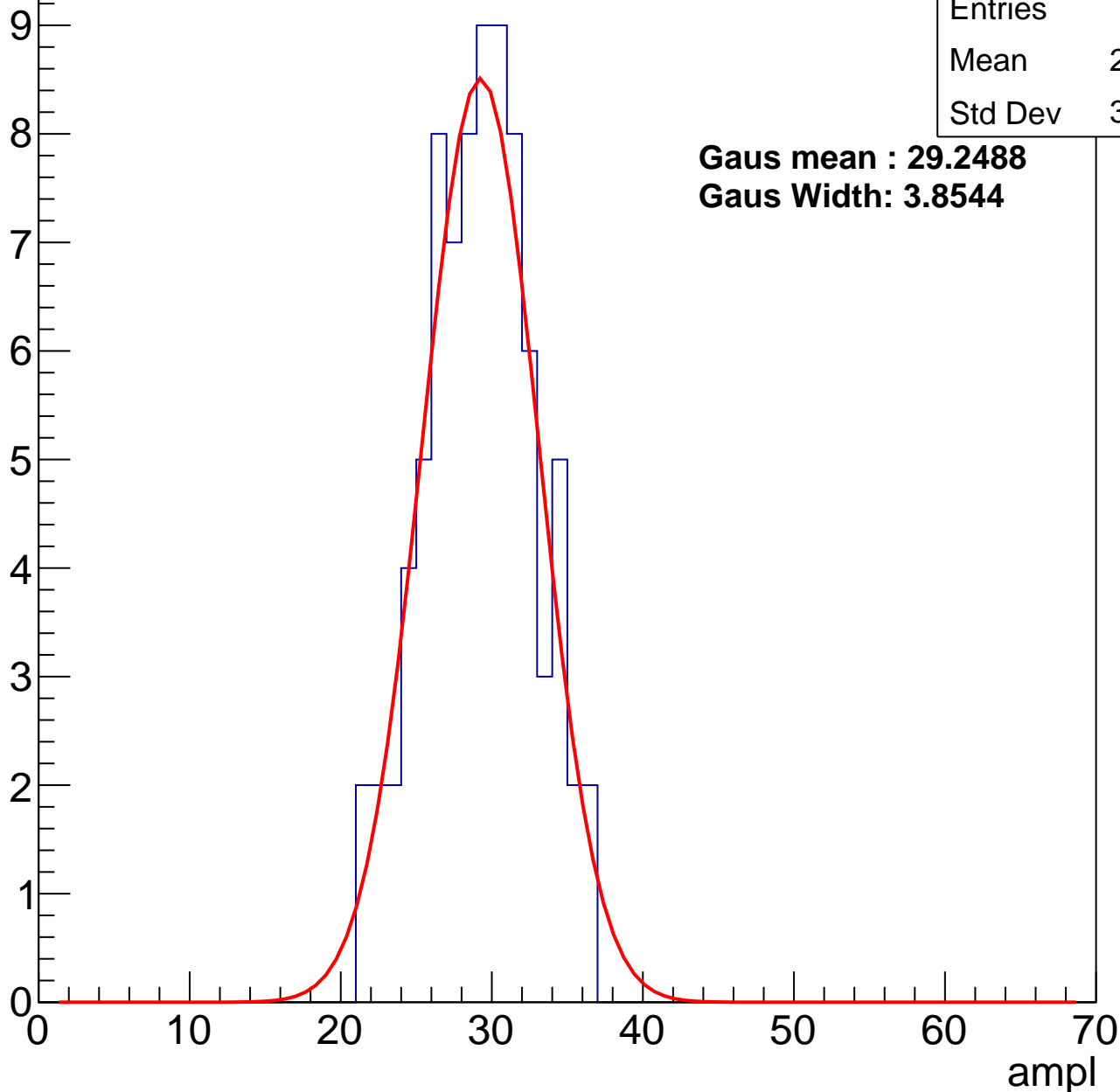
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	28.73
Std Dev	3.524

Gaus mean : 29.2488

Gaus Width: 3.8544



B1L101S, U15-ch0, adc1

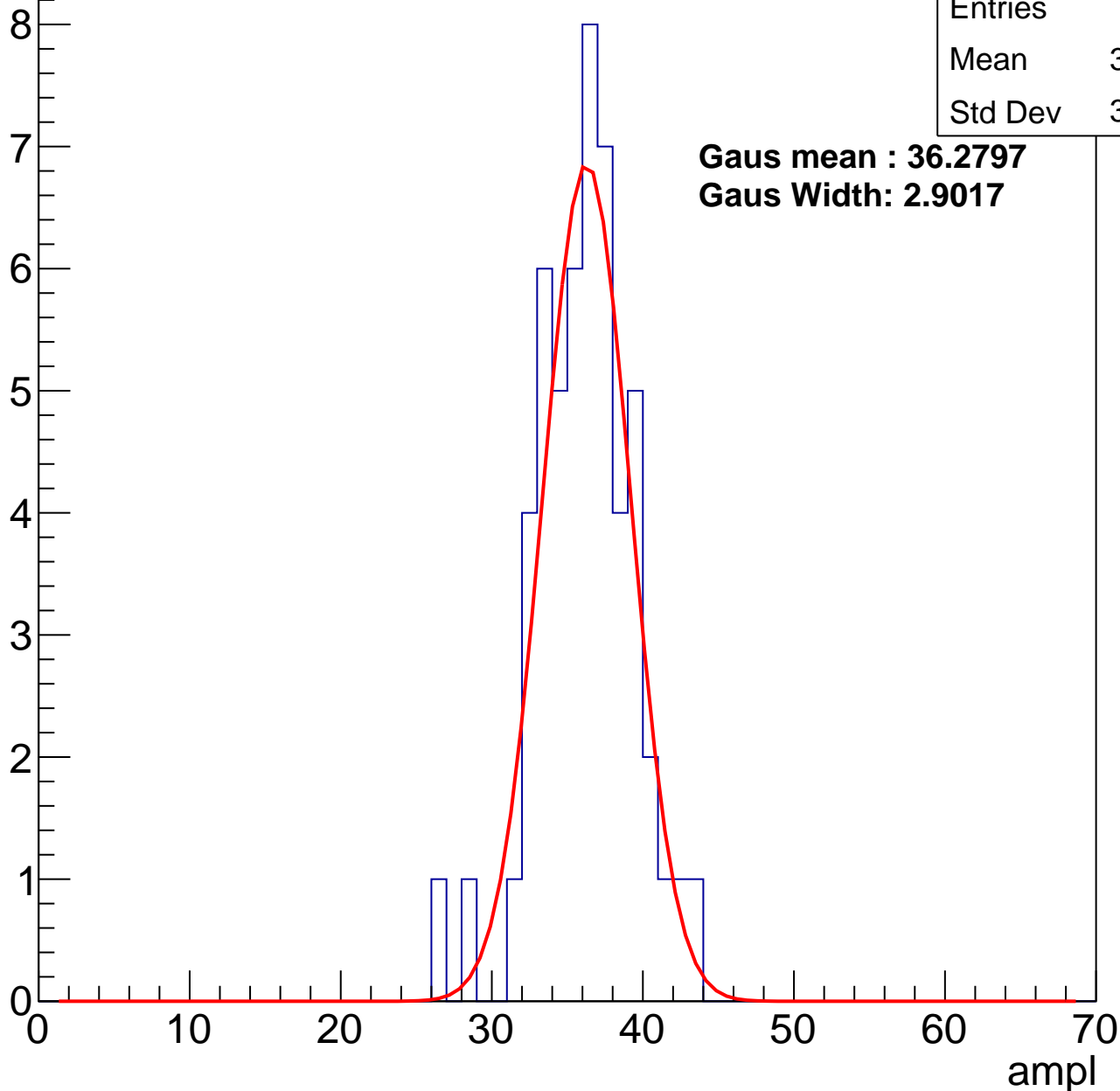
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	35.68
Std Dev	3.185

Gaus mean : 36.2797

Gaus Width: 2.9017



B1L101S, U15-ch0, adc2

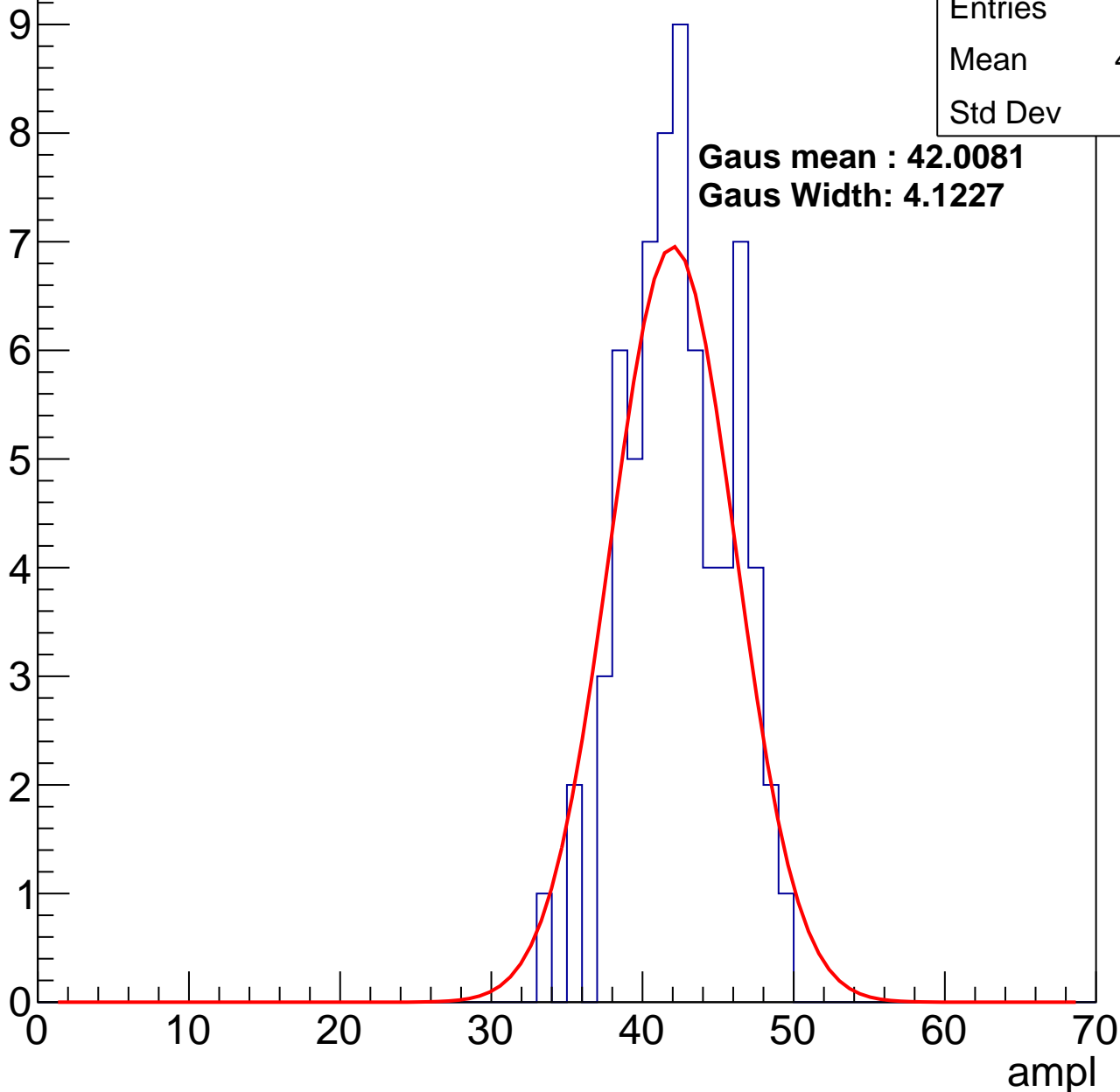
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	41.91
Std Dev	3.45

Gaus mean : 42.0081

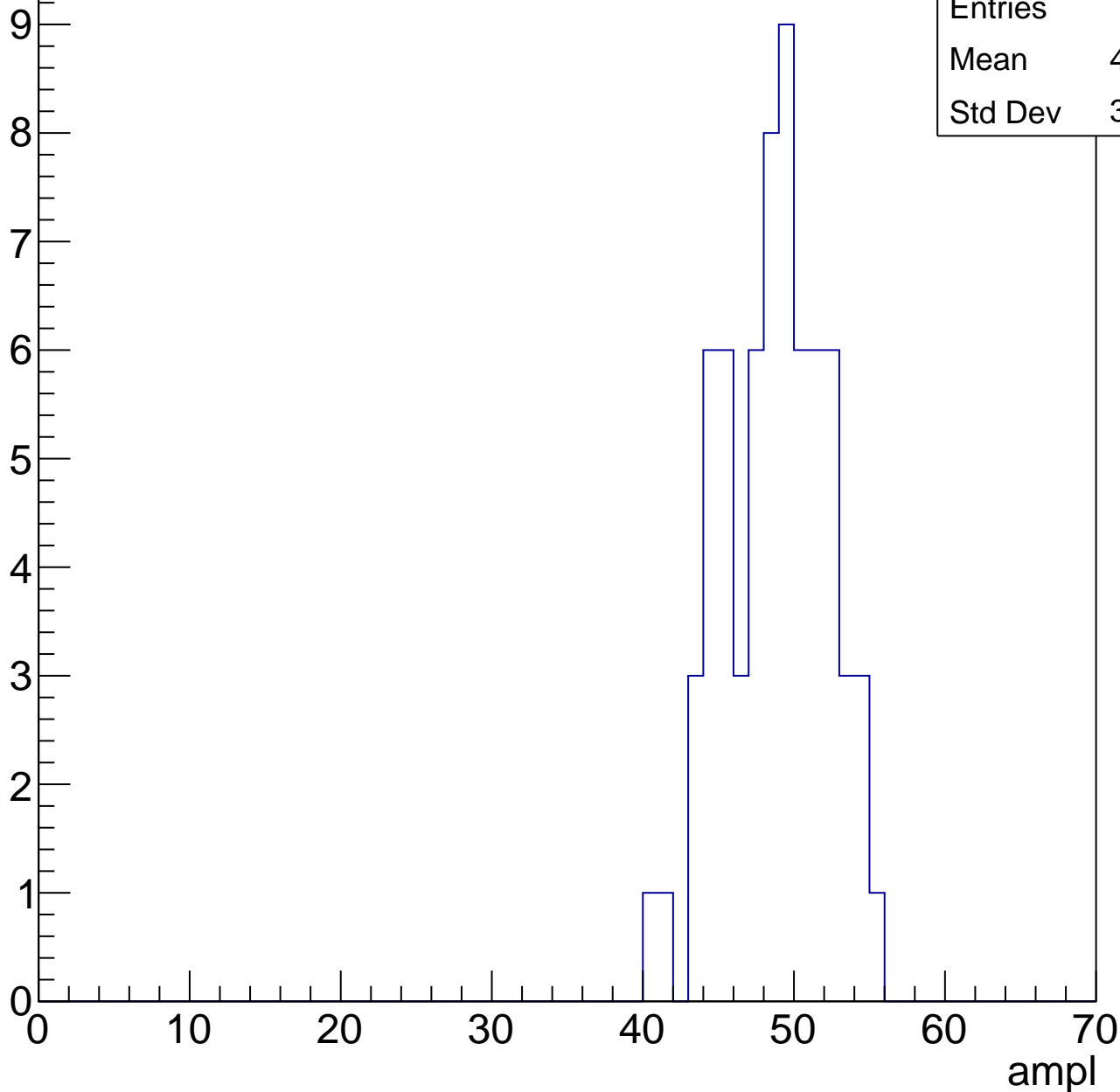
Gaus Width: 4.1227



B1L101S, U15-ch0, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



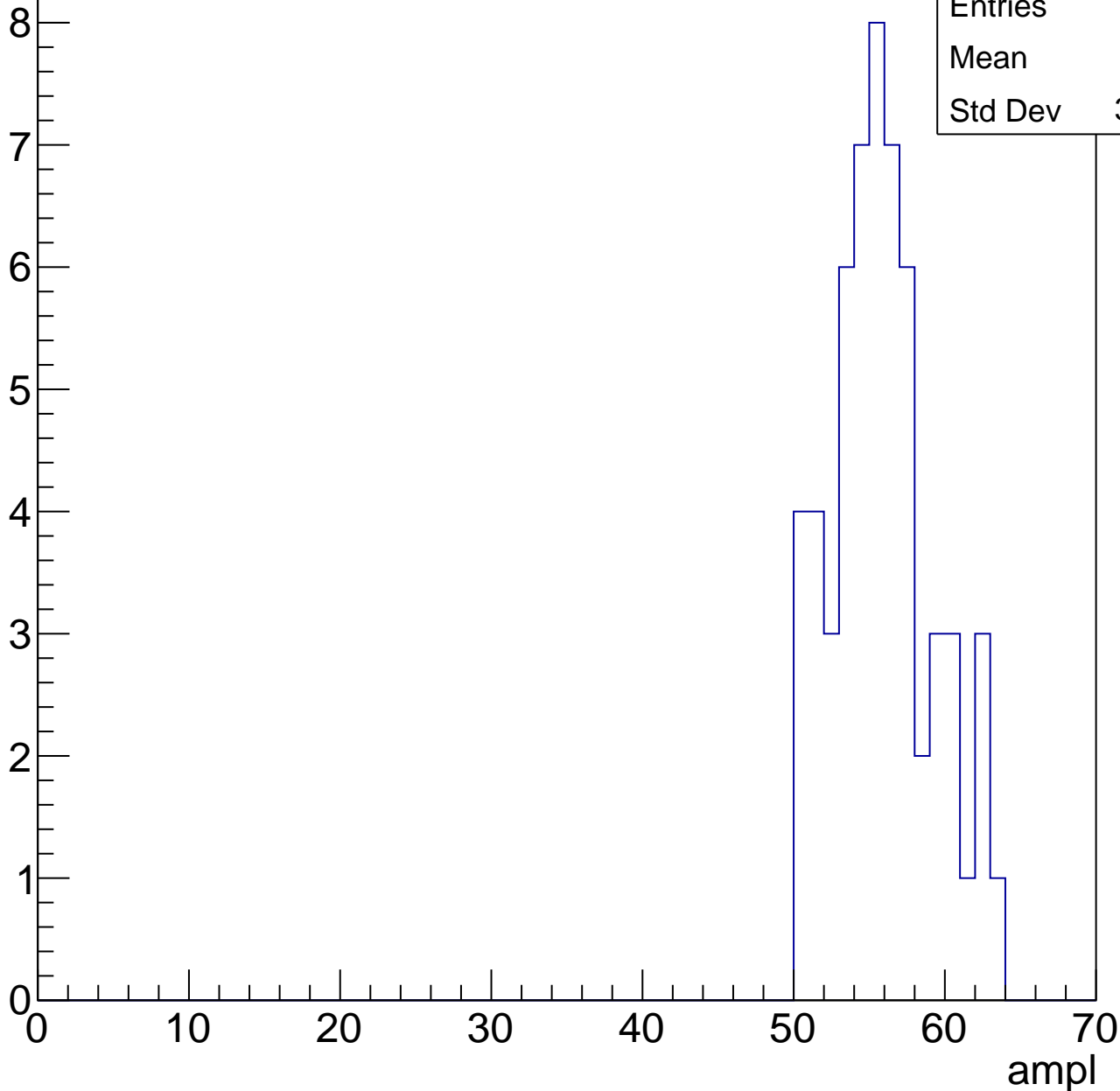
Entries	68
Mean	48.28
Std Dev	3.356

B1L101S, U15-ch0, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	55.4
Std Dev	3.301

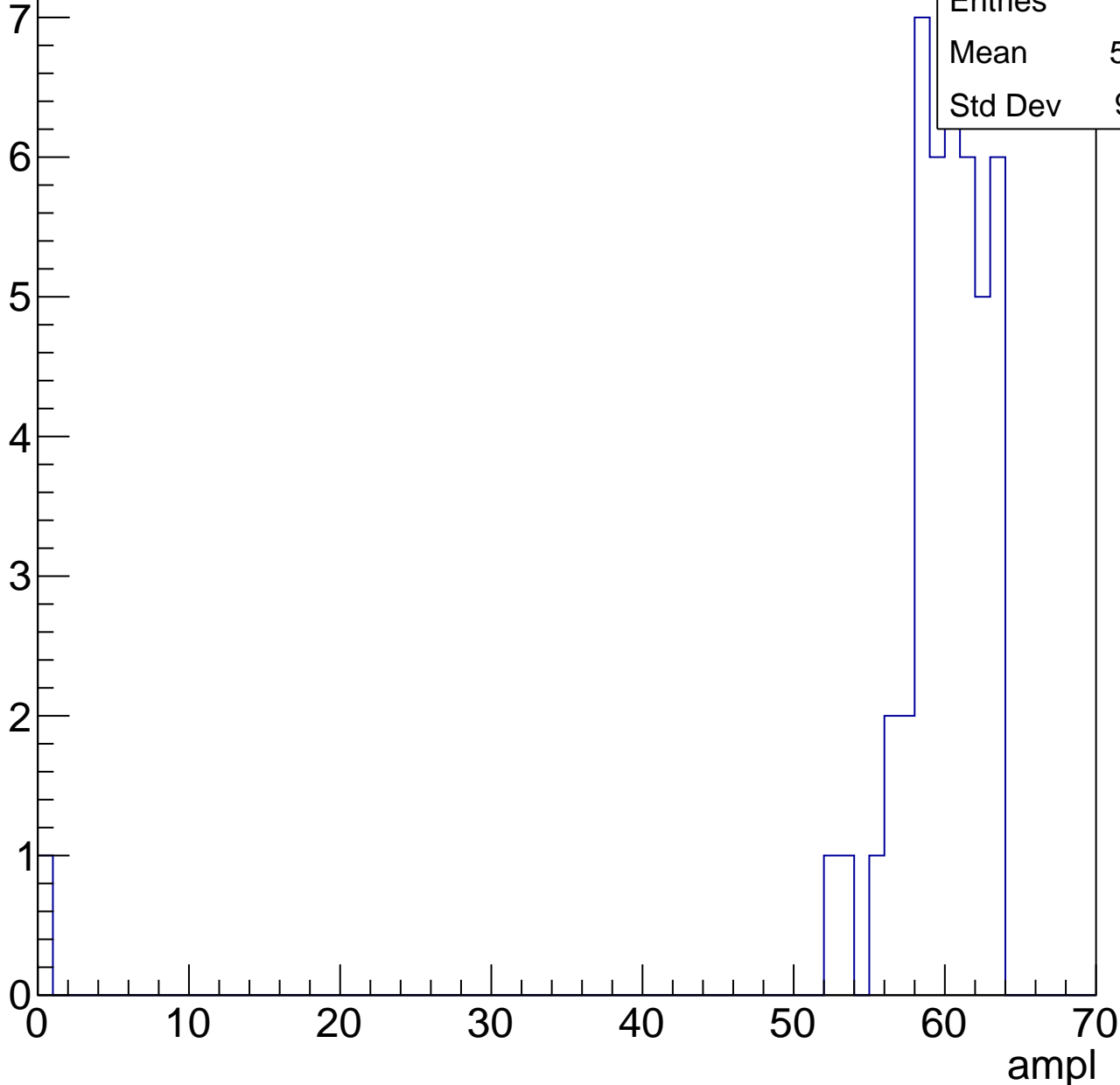


B1L101S, U15-ch0, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.22
Std Dev	9.141



B1L101S, U15-ch0, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

ampl

Entries

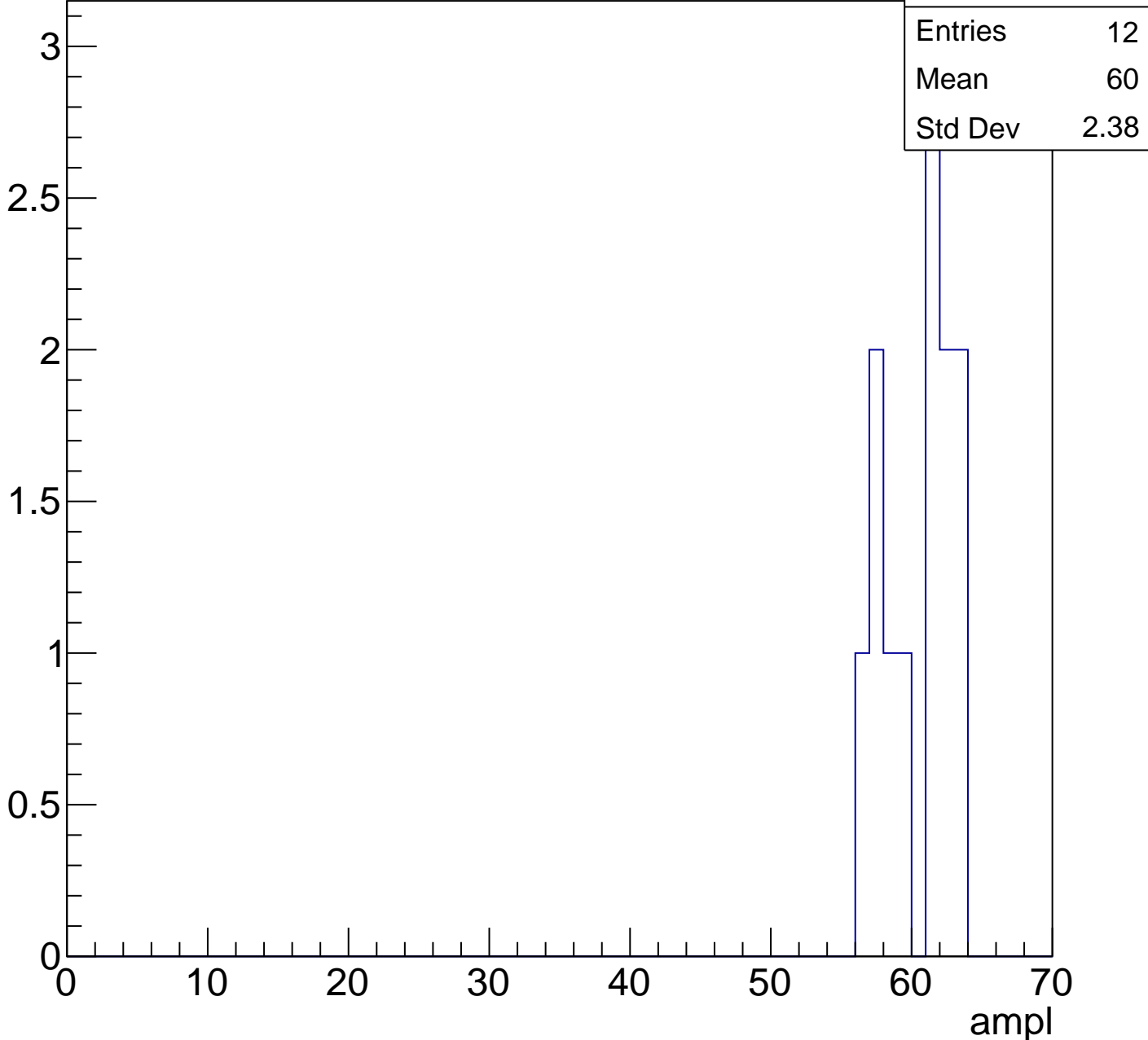
12

Mean

60

Std Dev

2.38



B1L101S, U15-ch0, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch1, adc0

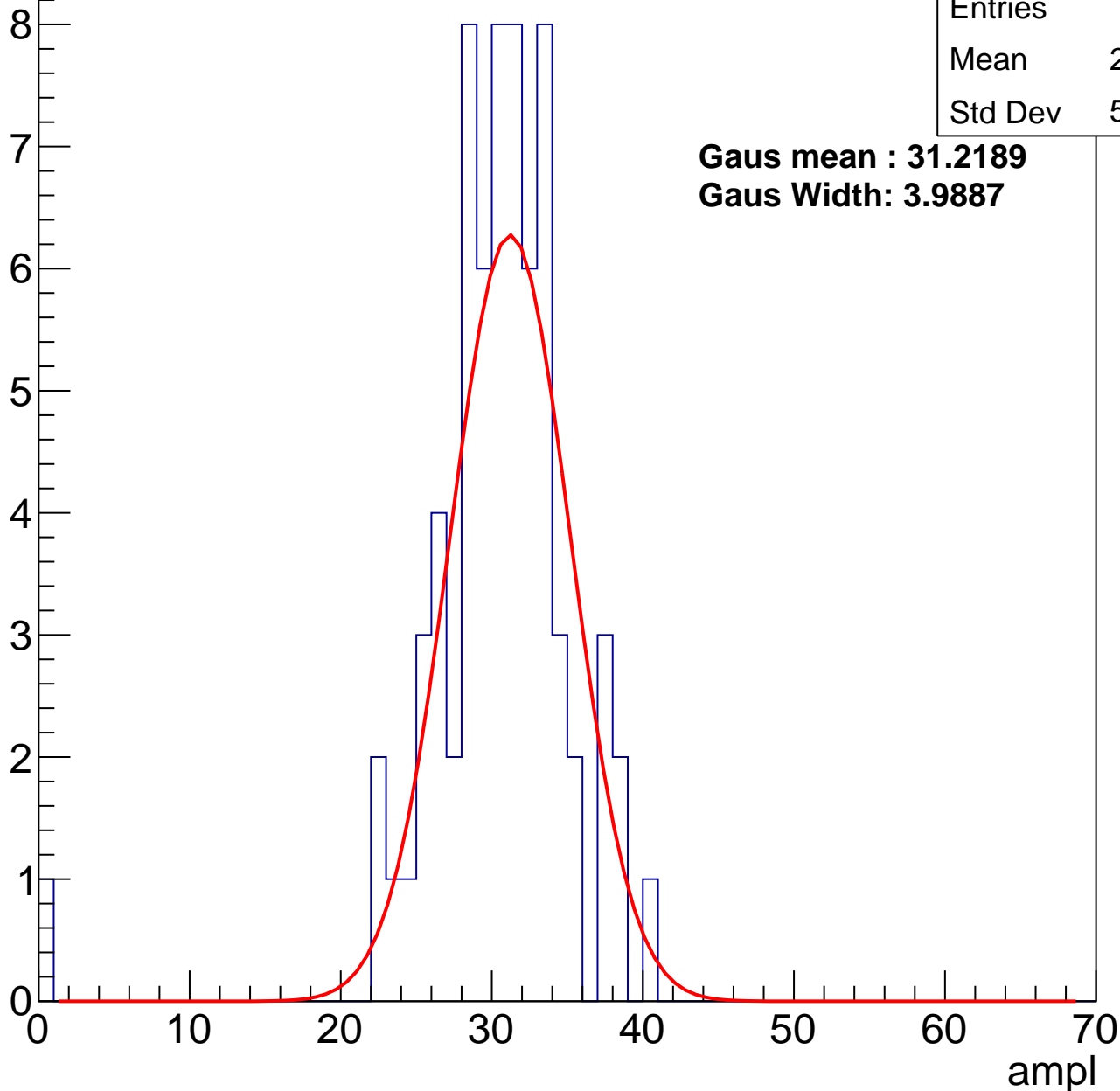
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.93
Std Dev	5.226

Gaus mean : 31.2189

Gaus Width: 3.9887



B1L101S, U15-ch1, adc1

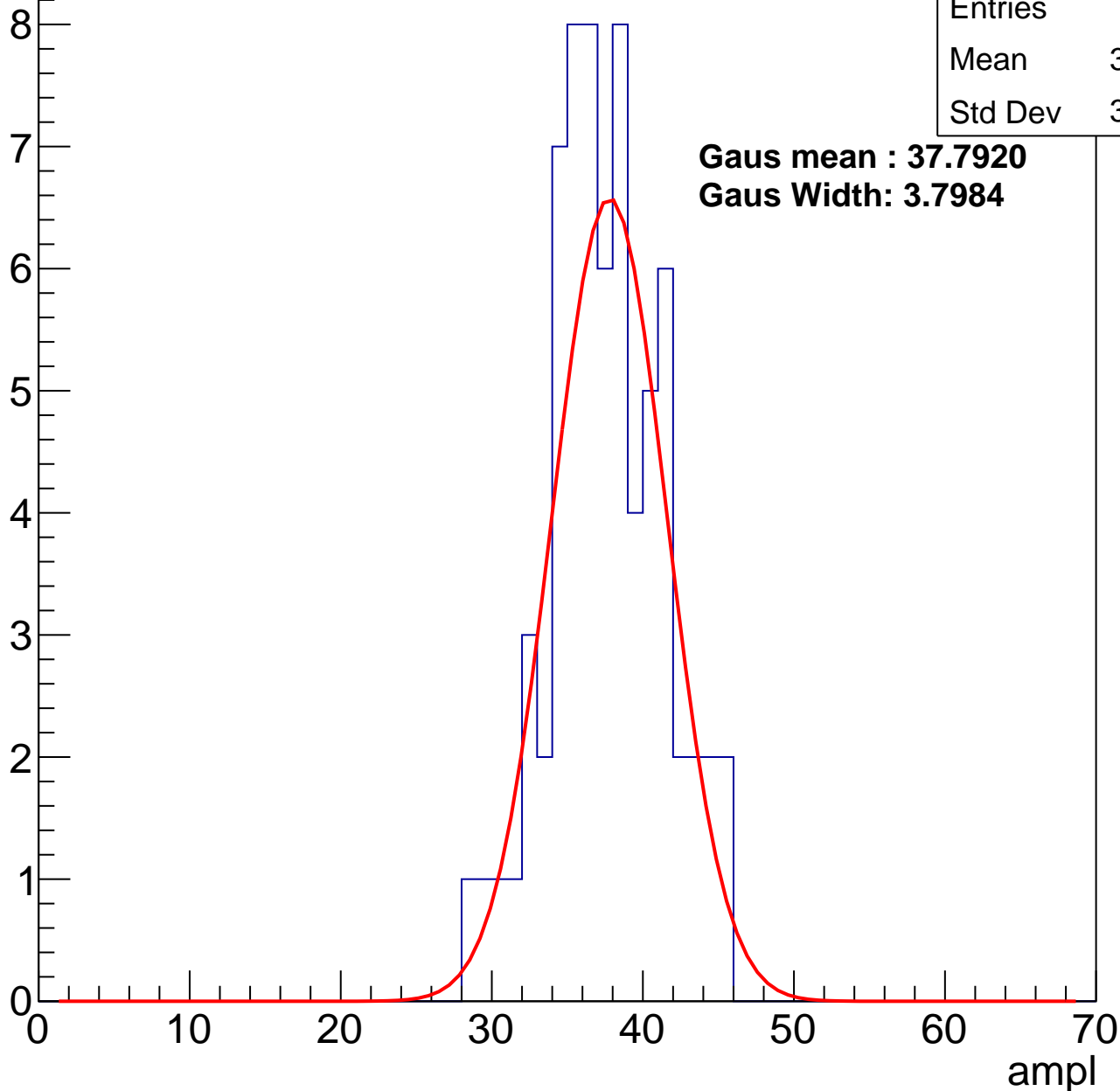
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.13
Std Dev	3.714

Gaus mean : 37.7920

Gaus Width: 3.7984



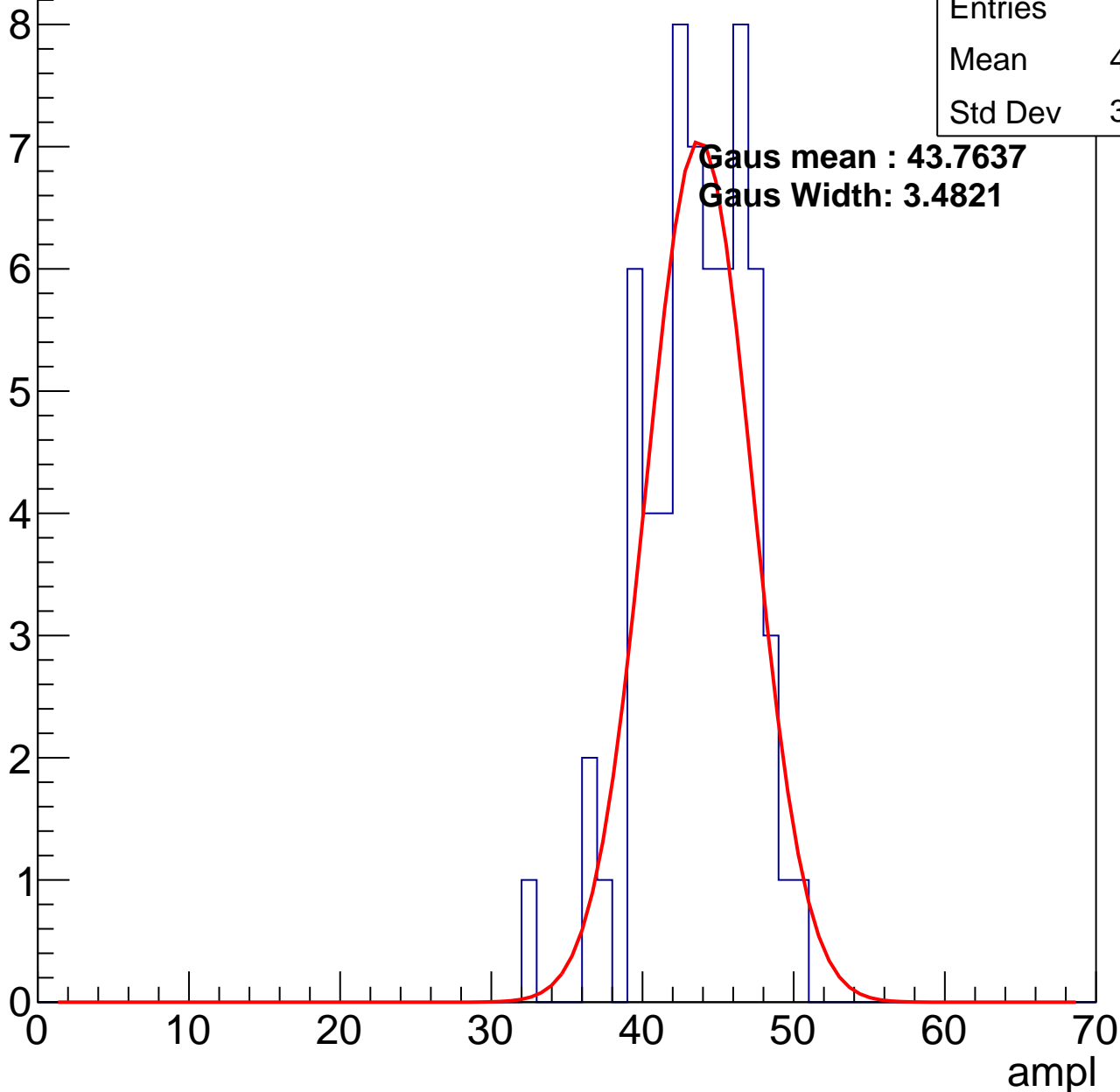
B1L101S, U15-ch1, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

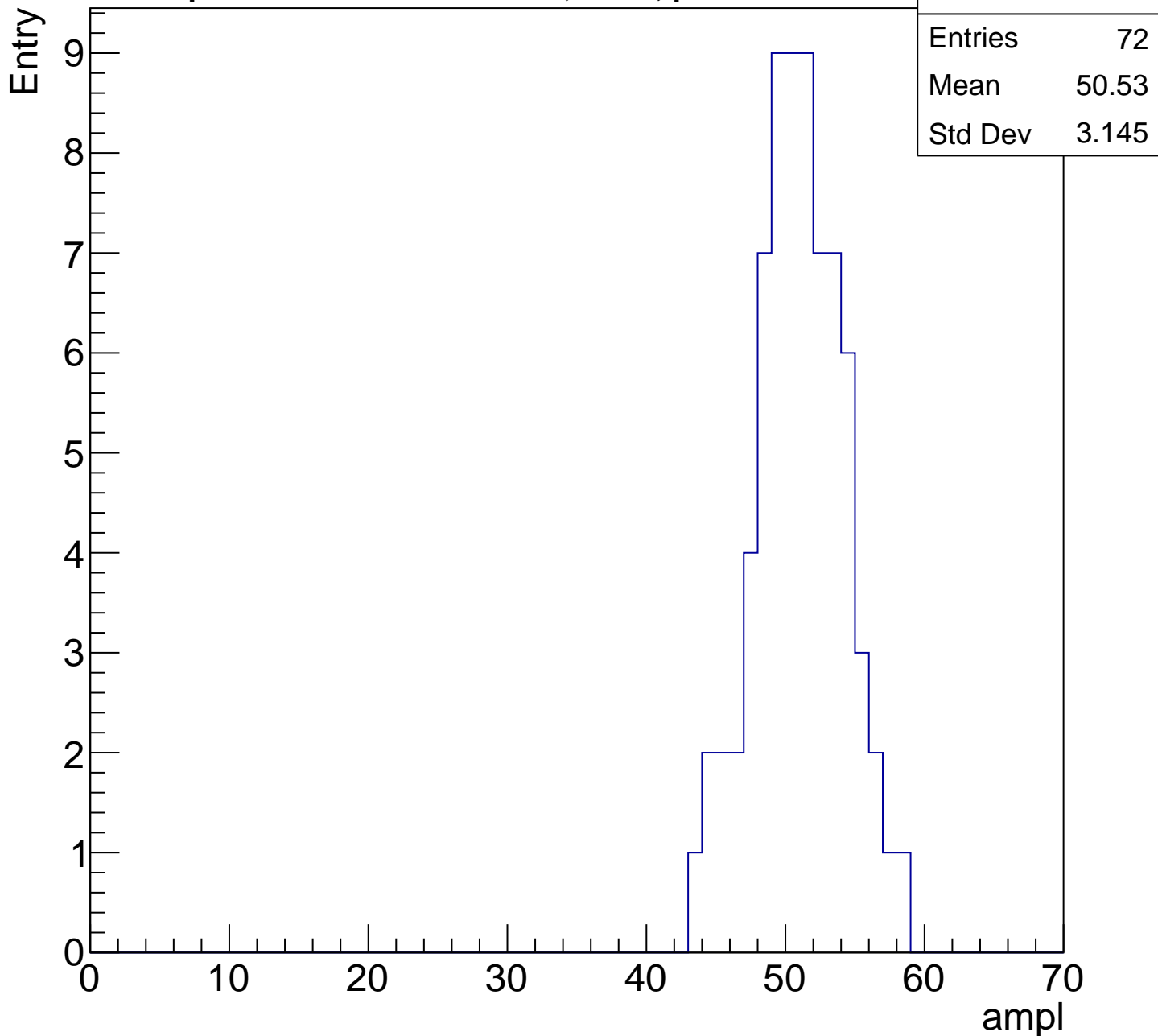
Entries	64
Mean	43.17
Std Dev	3.462

Gaus mean : 43.7637
Gaus Width: 3.4821



B1L101S, U15-ch1, adc3

calib_packv5_042523_0143.root, FC#0, port D2

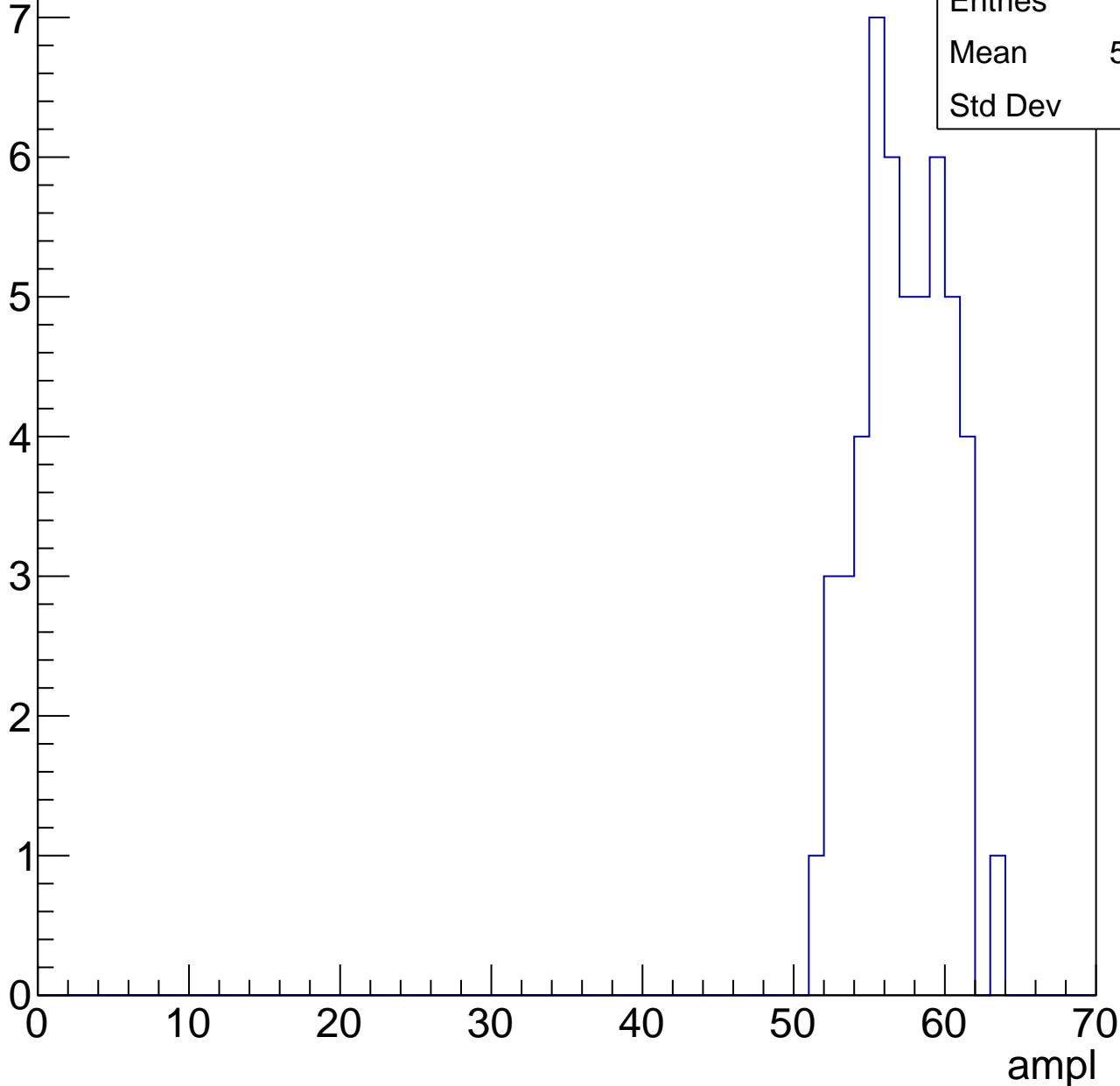


B1L101S, U15-ch1, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	56.78
Std Dev	2.83

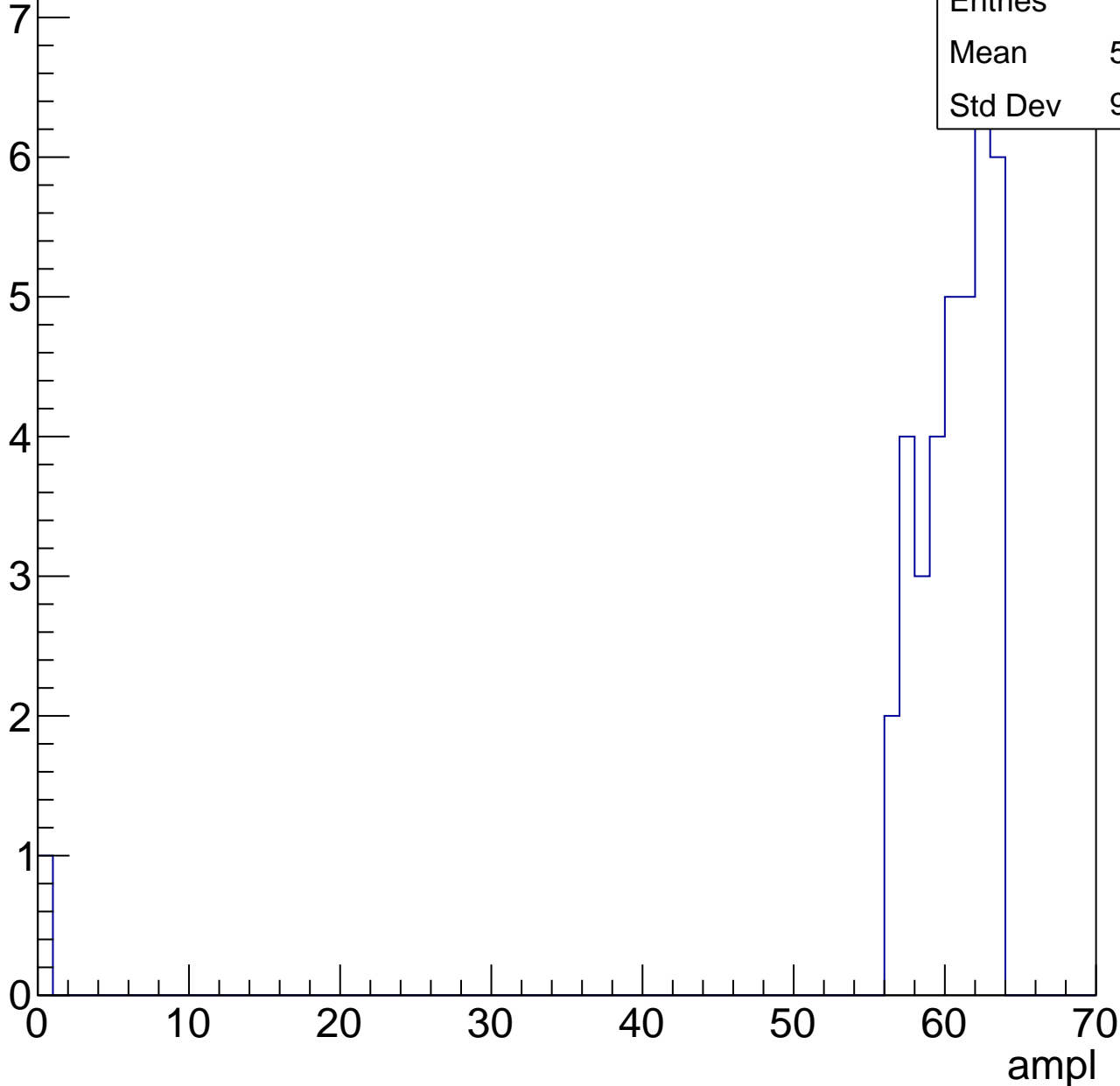


B1L101S, U15-ch1, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

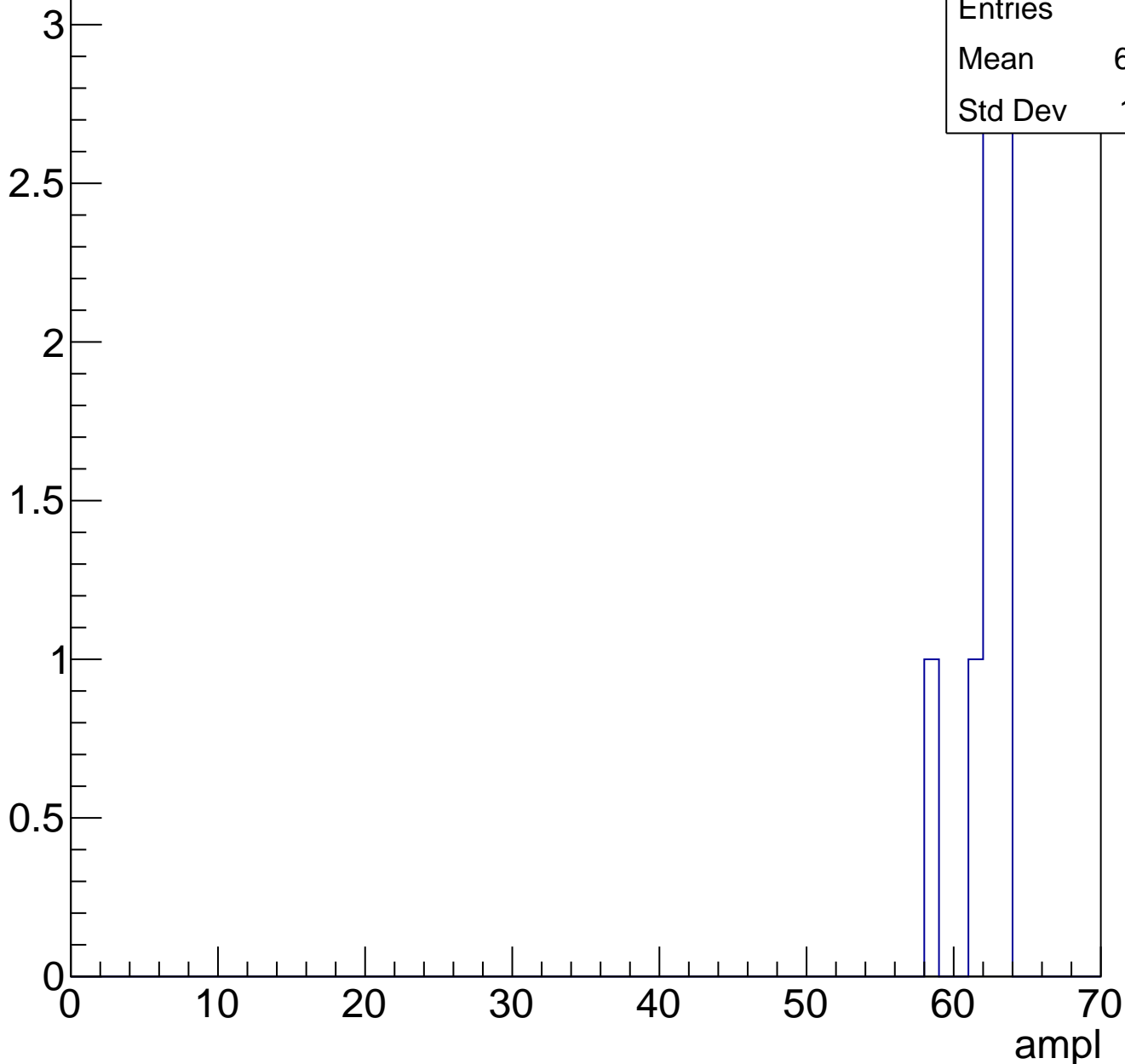
Entries	37
Mean	58.57
Std Dev	9.993



B1L101S, U15-ch1, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch1, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch2, adc0

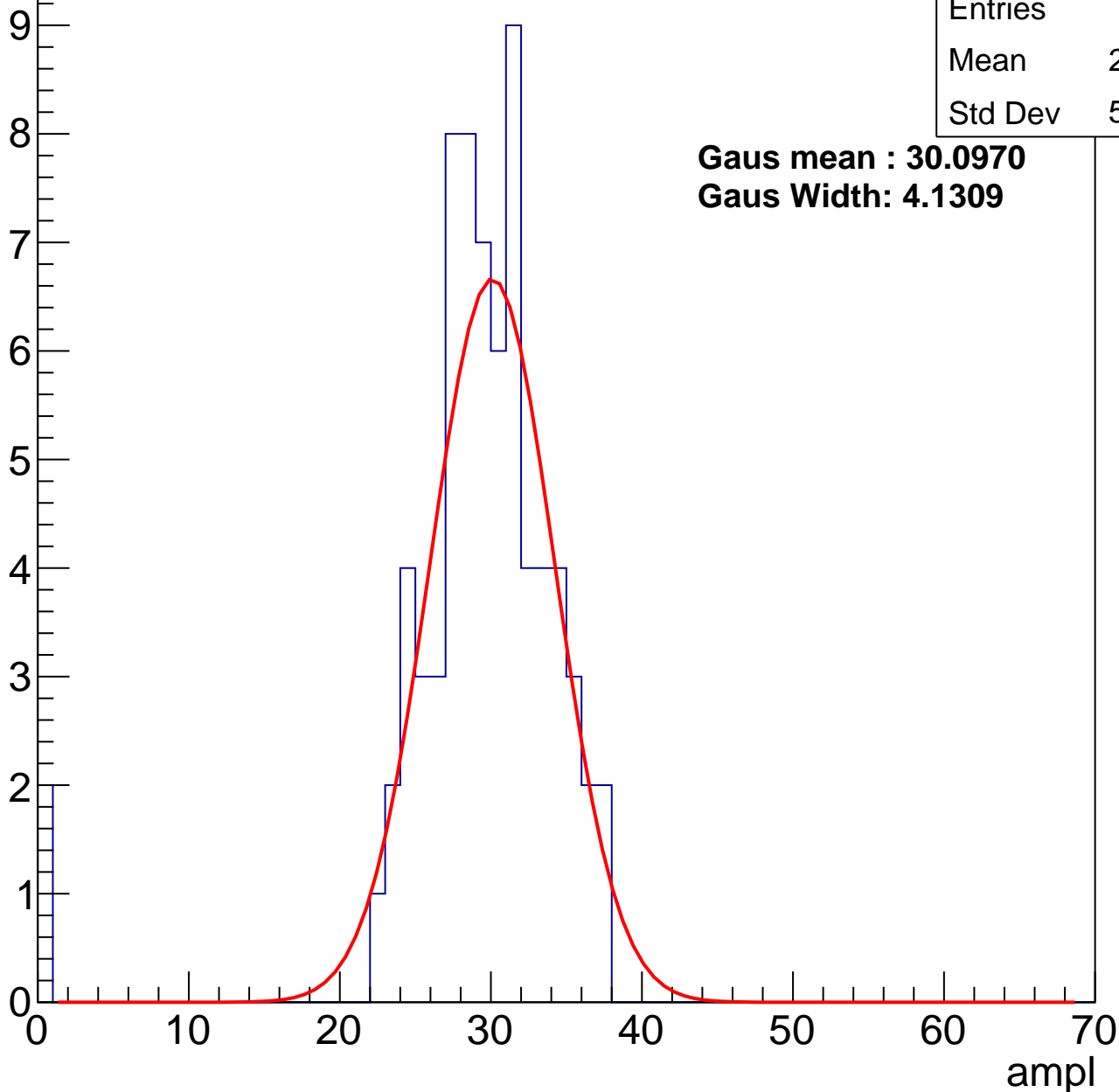
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.69
Std Dev	5.995

Gaus mean : 30.0970

Gaus Width: 4.1309



B1L101S, U15-ch2, adc1

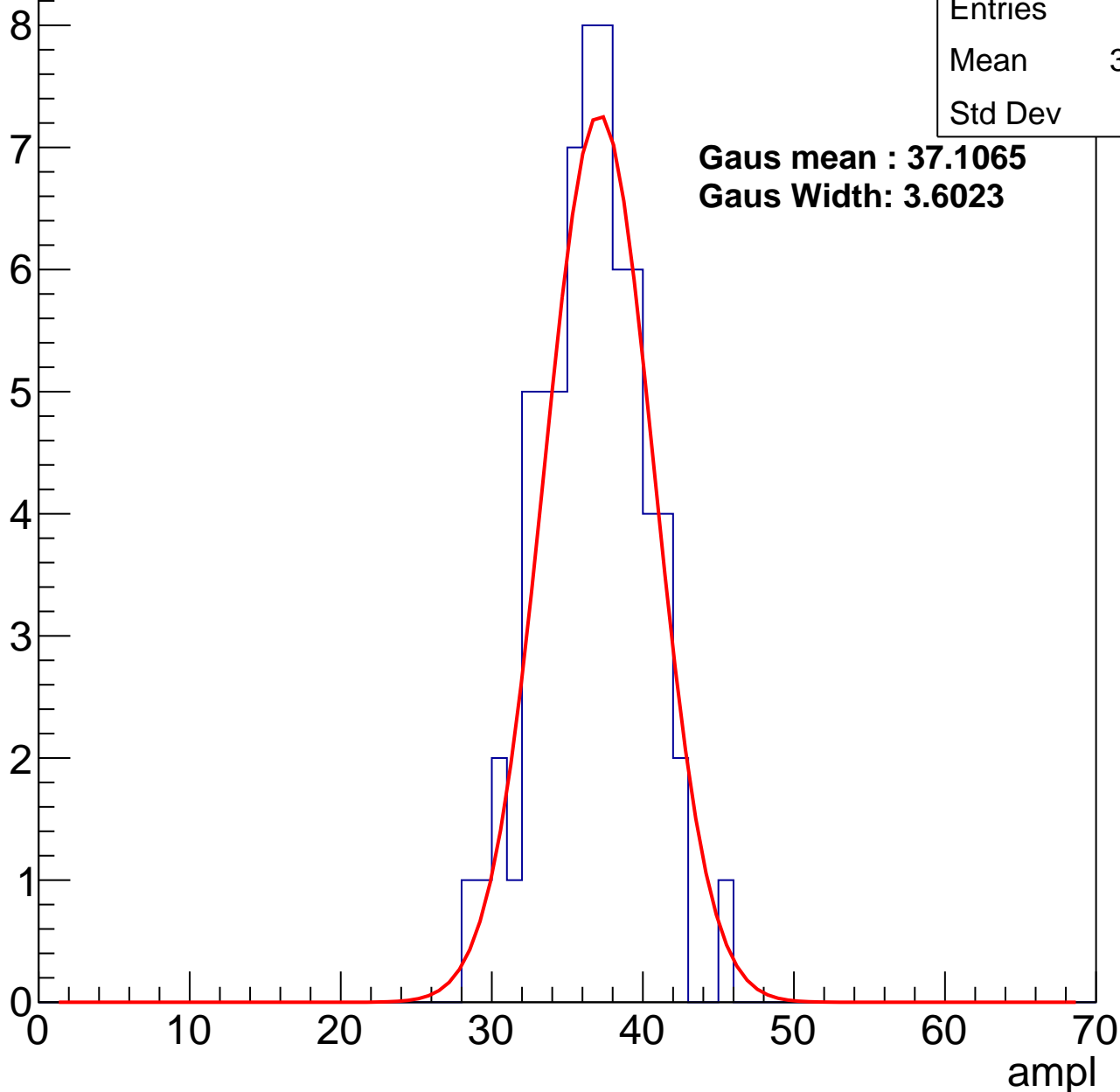
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.17
Std Dev	3.4

Gaus mean : 37.1065

Gaus Width: 3.6023



B1L101S, U15-ch2, adc2

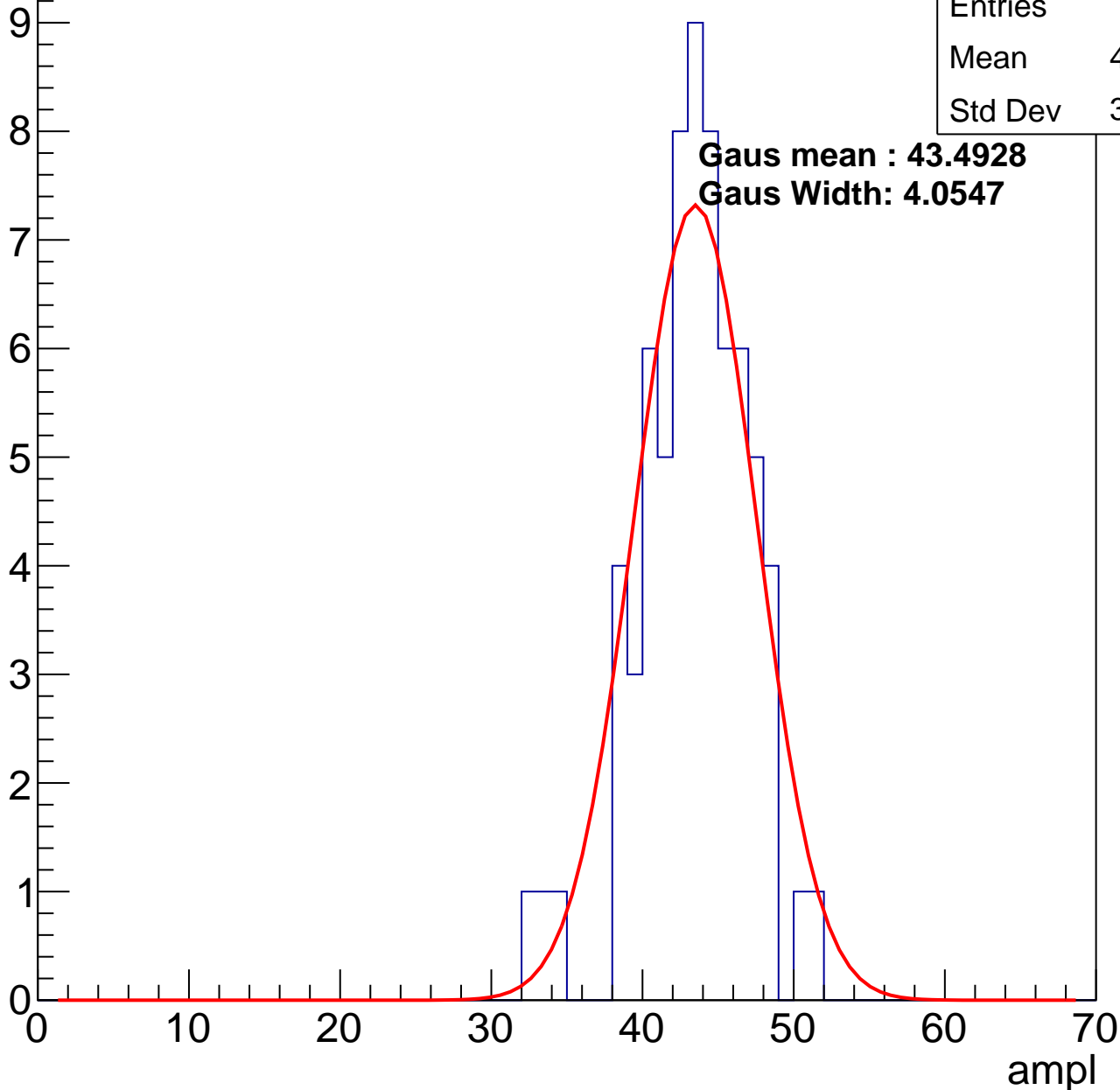
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.93
Std Dev	3.633

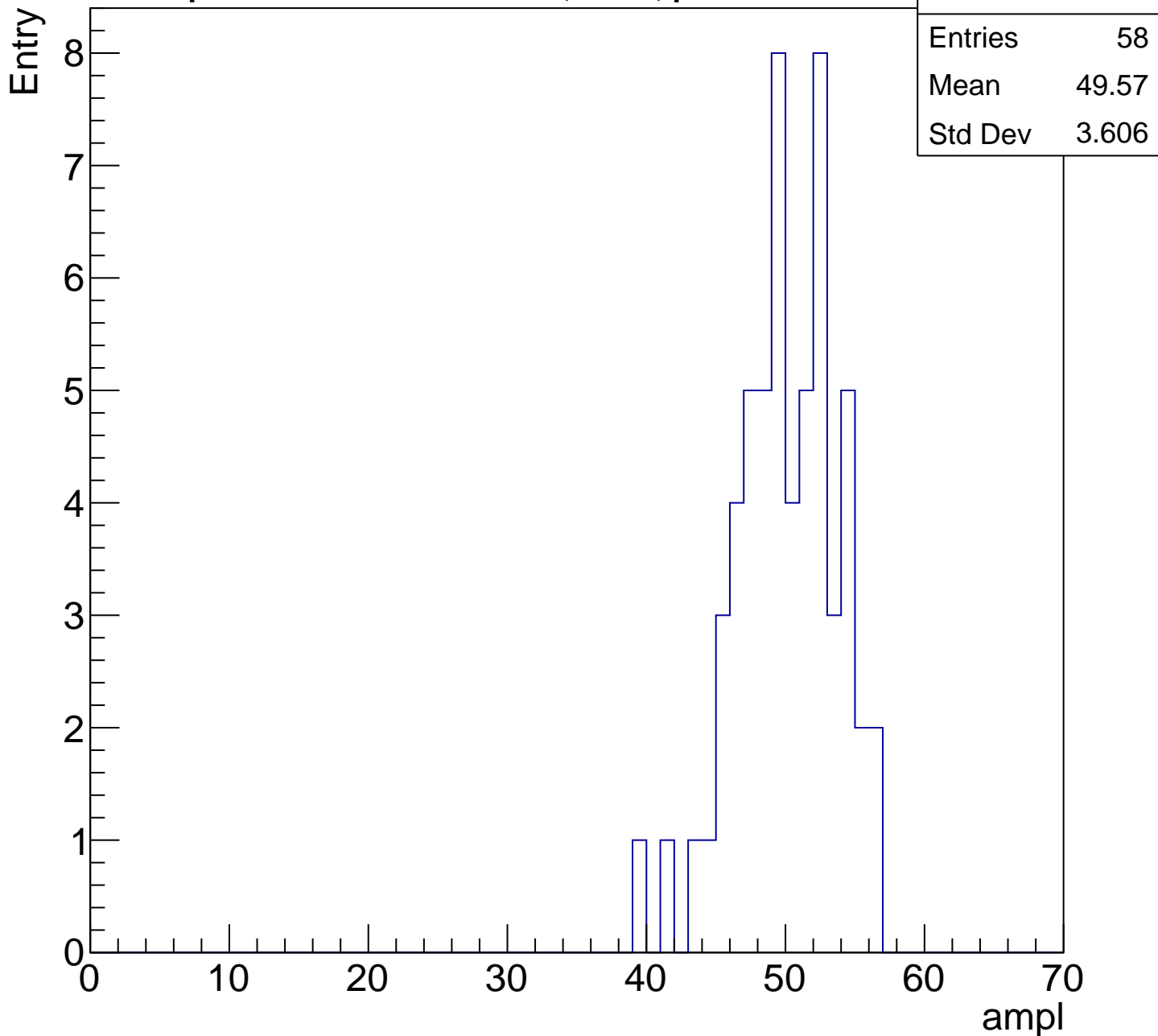
Gaus mean : 43.4928

Gaus Width: 4.0547



B1L101S, U15-ch2, adc3

calib_packv5_042523_0143.root, FC#0, port D2

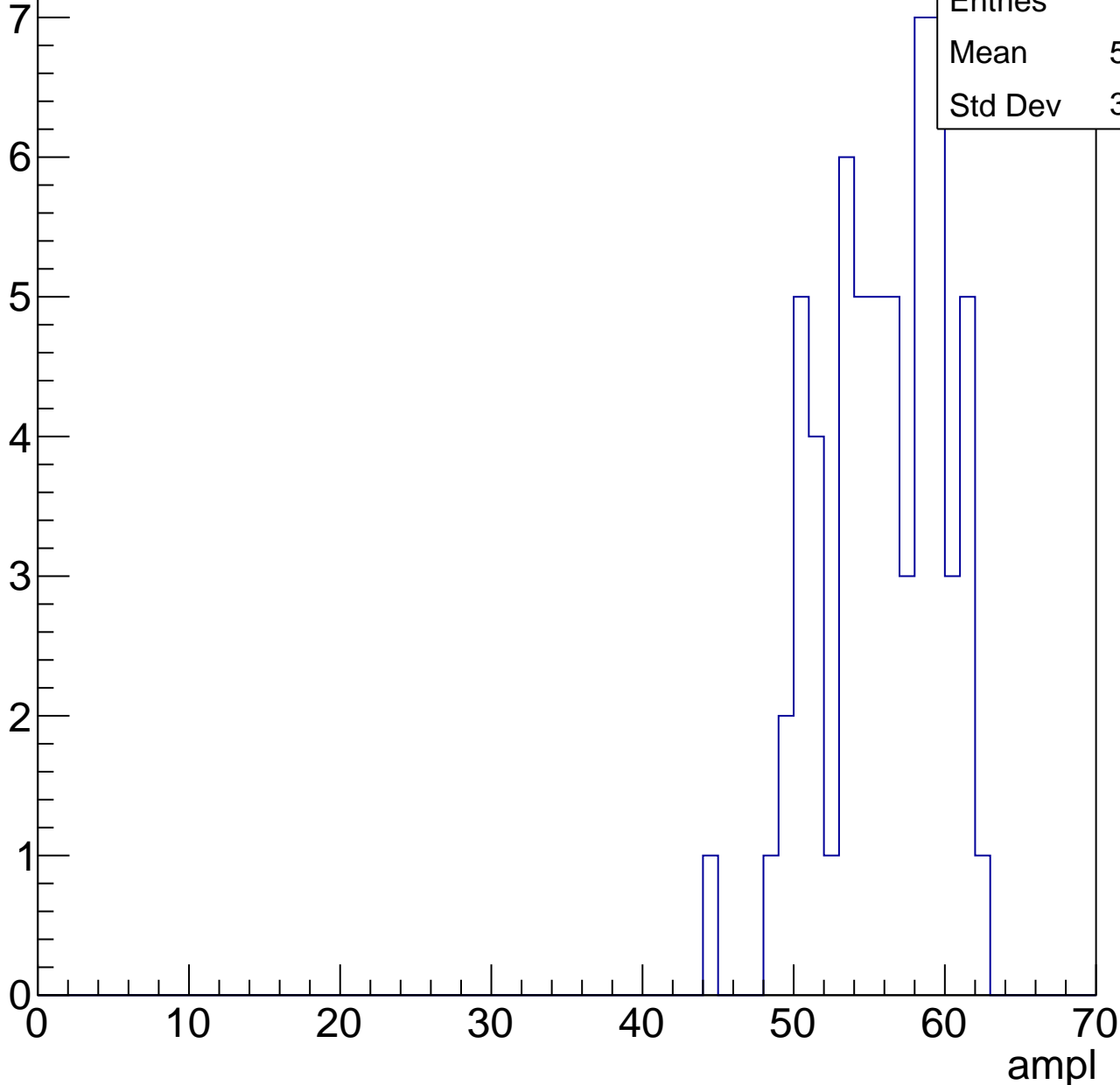


B1L101S, U15-ch2, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.34
Std Dev	3.962

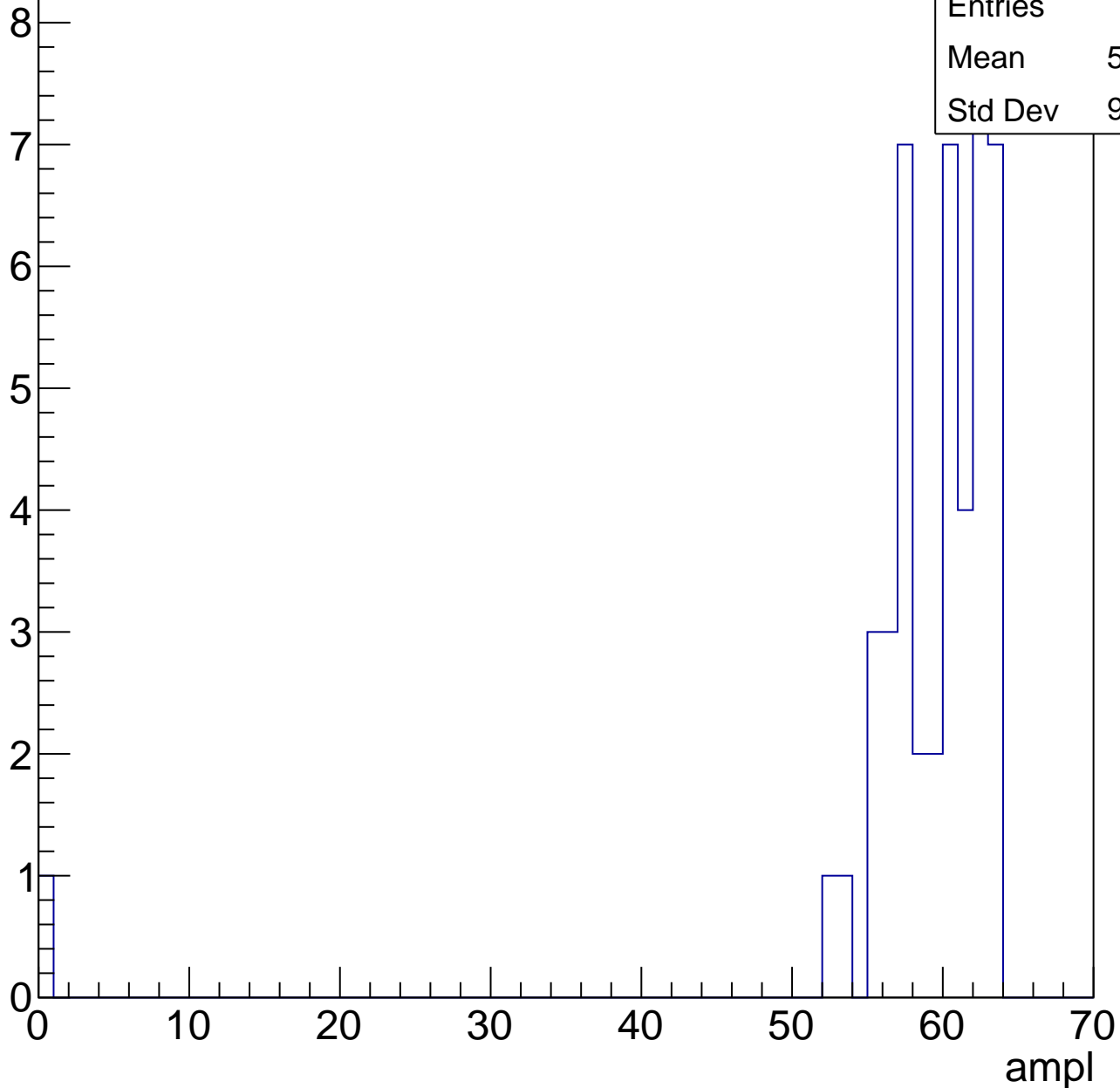


B1L101S, U15-ch2, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

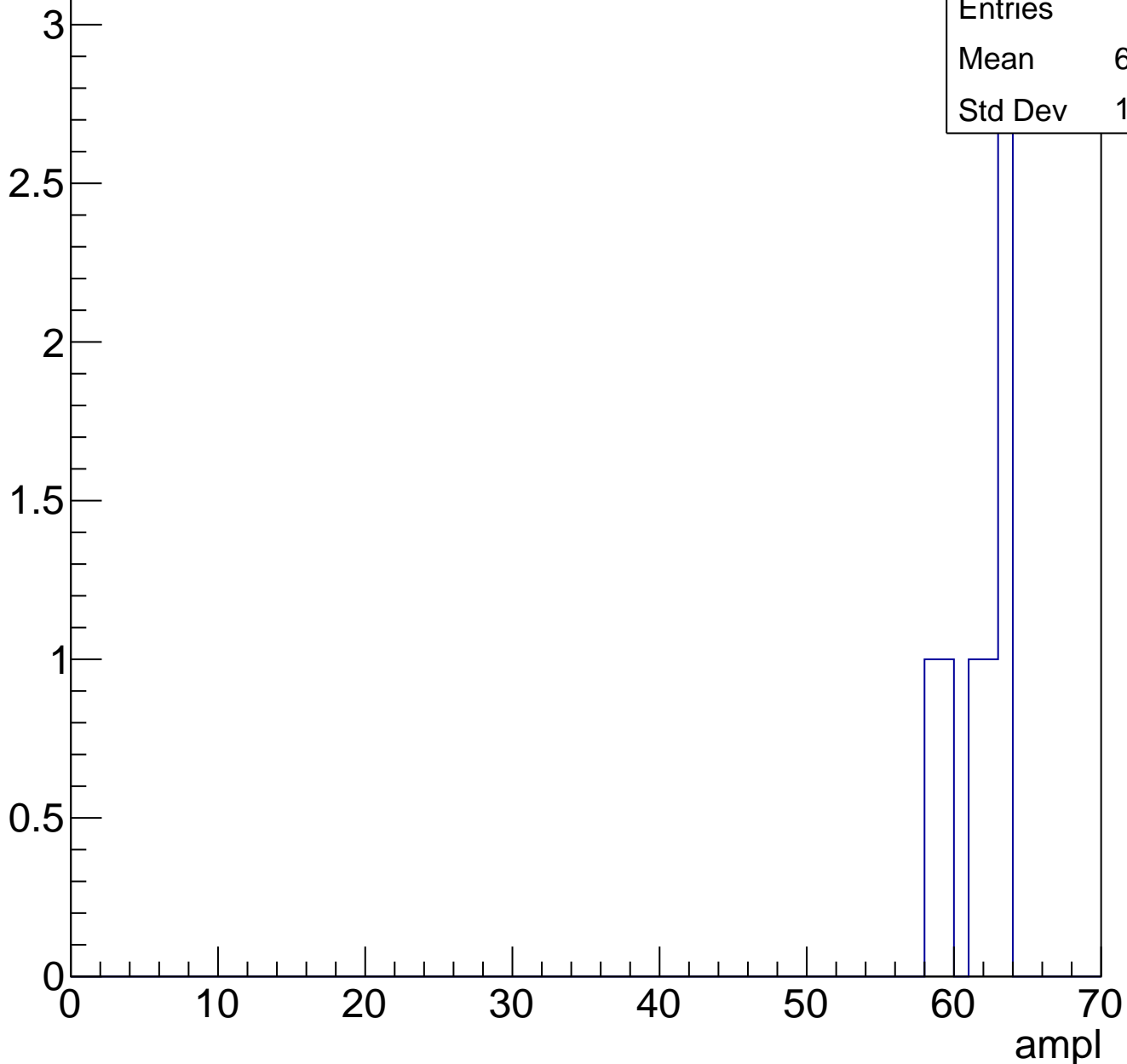
Entries	46
Mean	58.09
Std Dev	9.136



B1L101S, U15-ch2, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch2, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch3, adc0

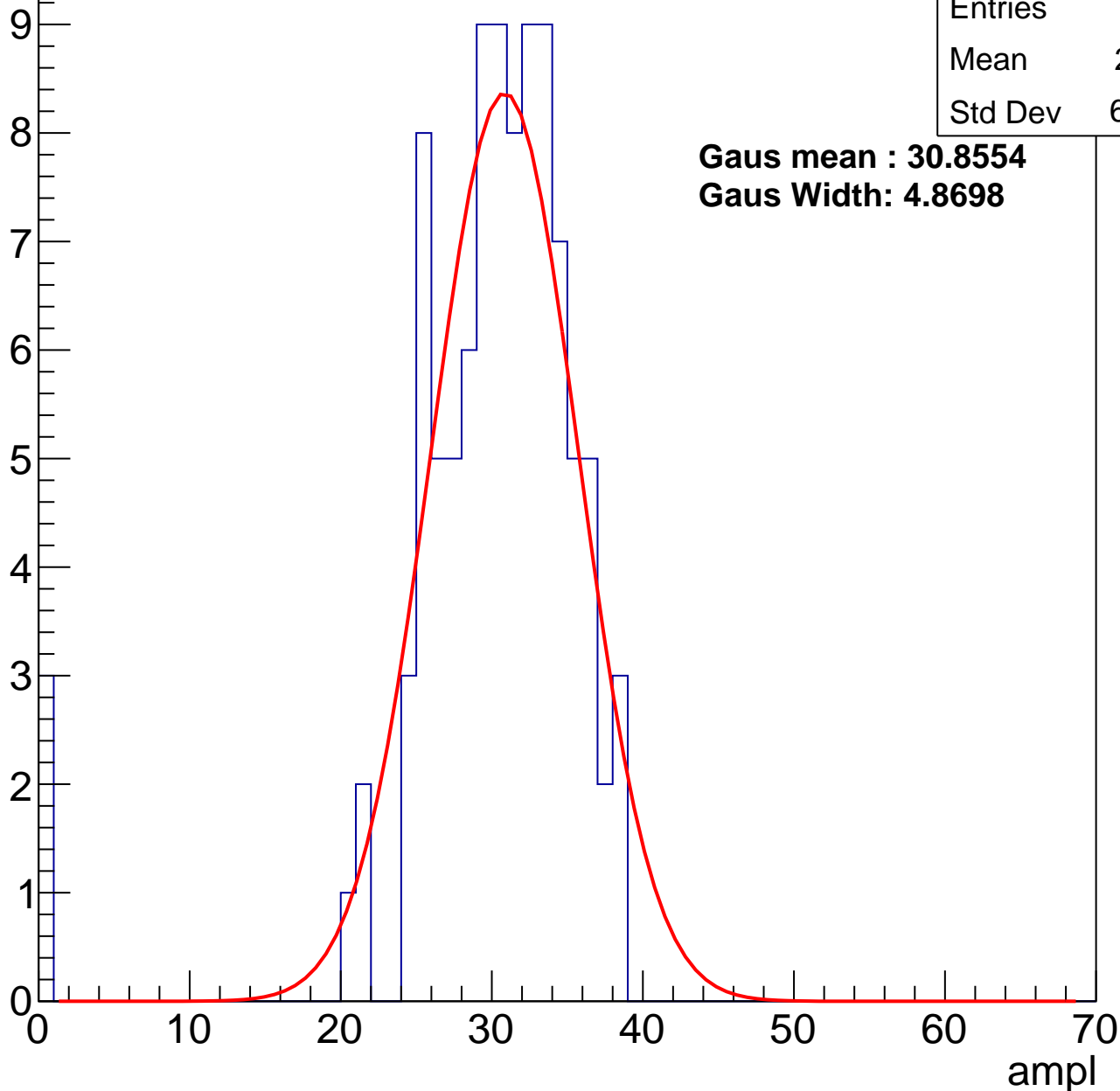
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	99
Mean	29.41
Std Dev	6.534

Gaus mean : 30.8554

Gaus Width: 4.8698



B1L101S, U15-ch3, adc1

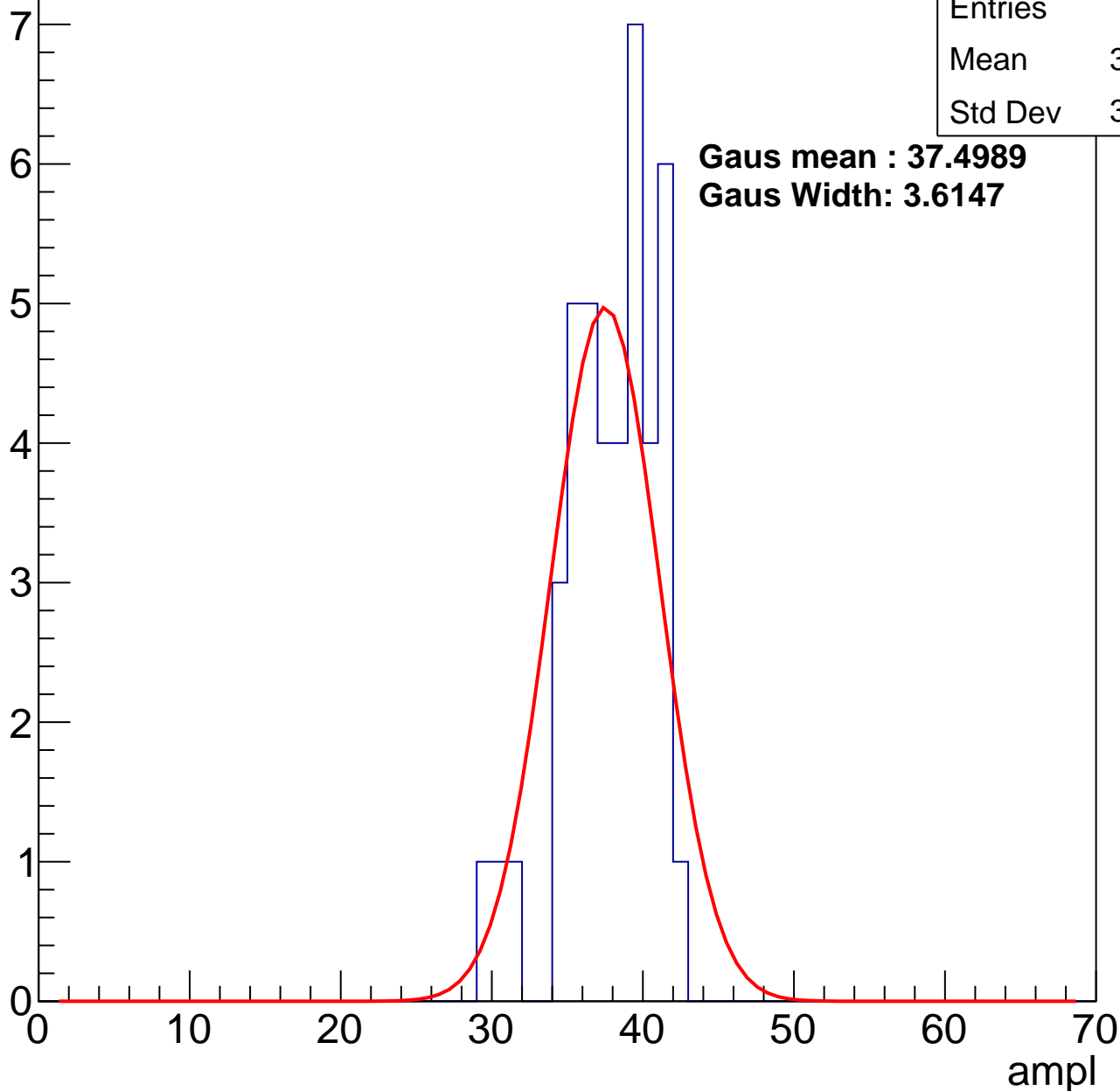
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	37.33
Std Dev	3.029

Gaus mean : 37.4989

Gaus Width: 3.6147



B1L101S, U15-ch3, adc2

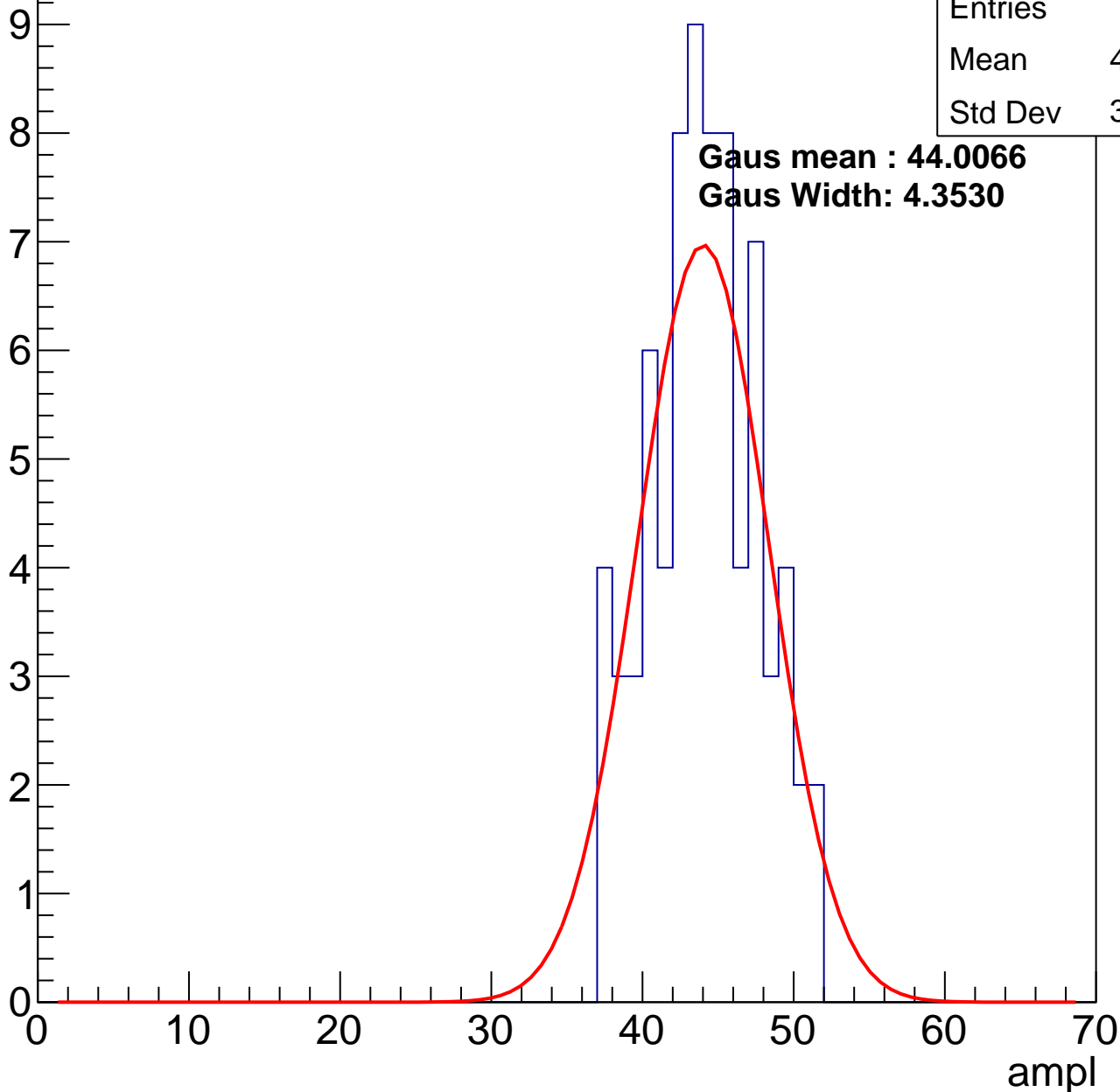
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.64
Std Dev	3.554

Gaus mean : 44.0066

Gaus Width: 4.3530

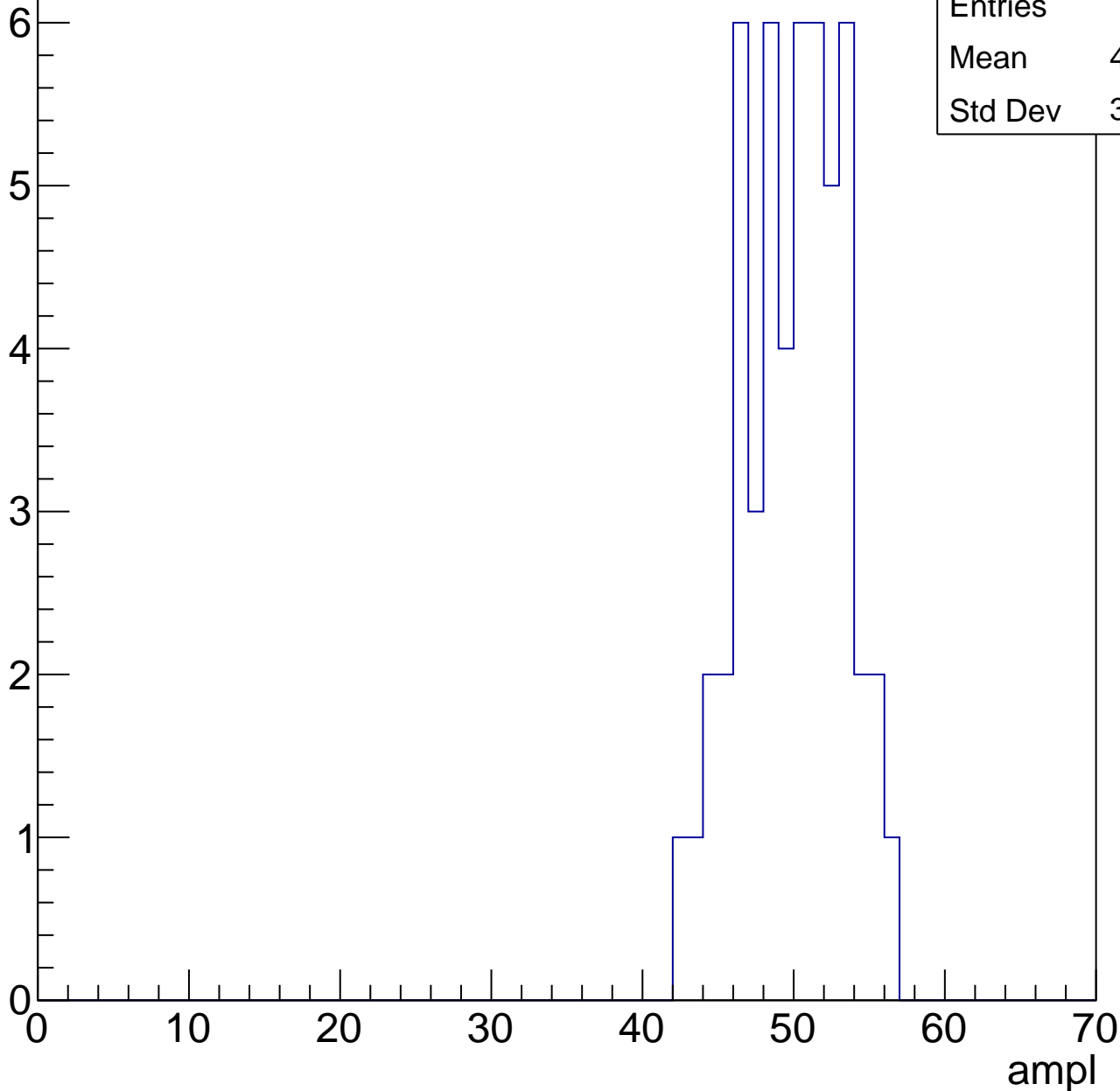


B1L101S, U15-ch3, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	49.47
Std Dev	3.277

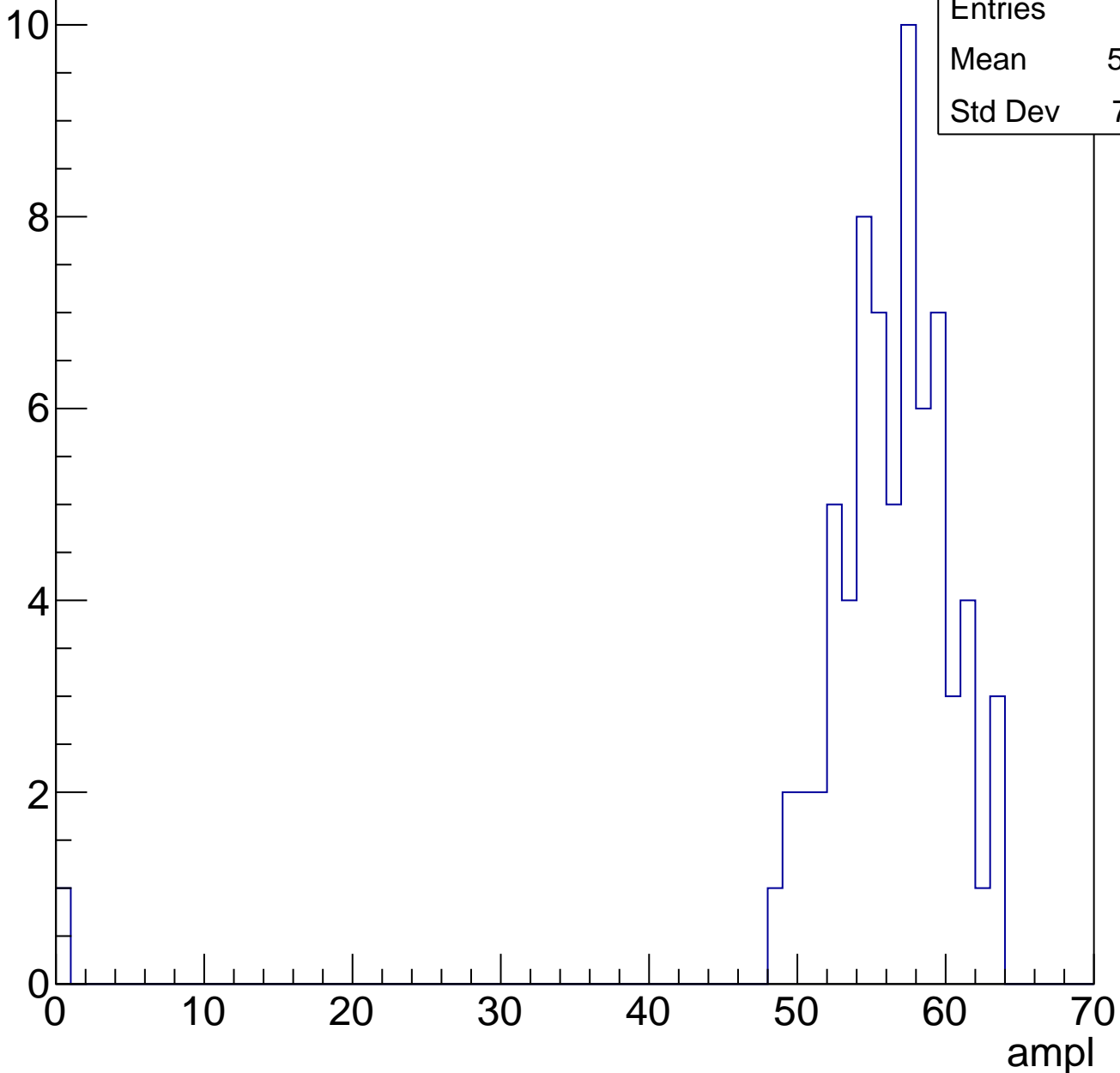


B1L101S, U15-ch3, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	55.25
Std Dev	7.471

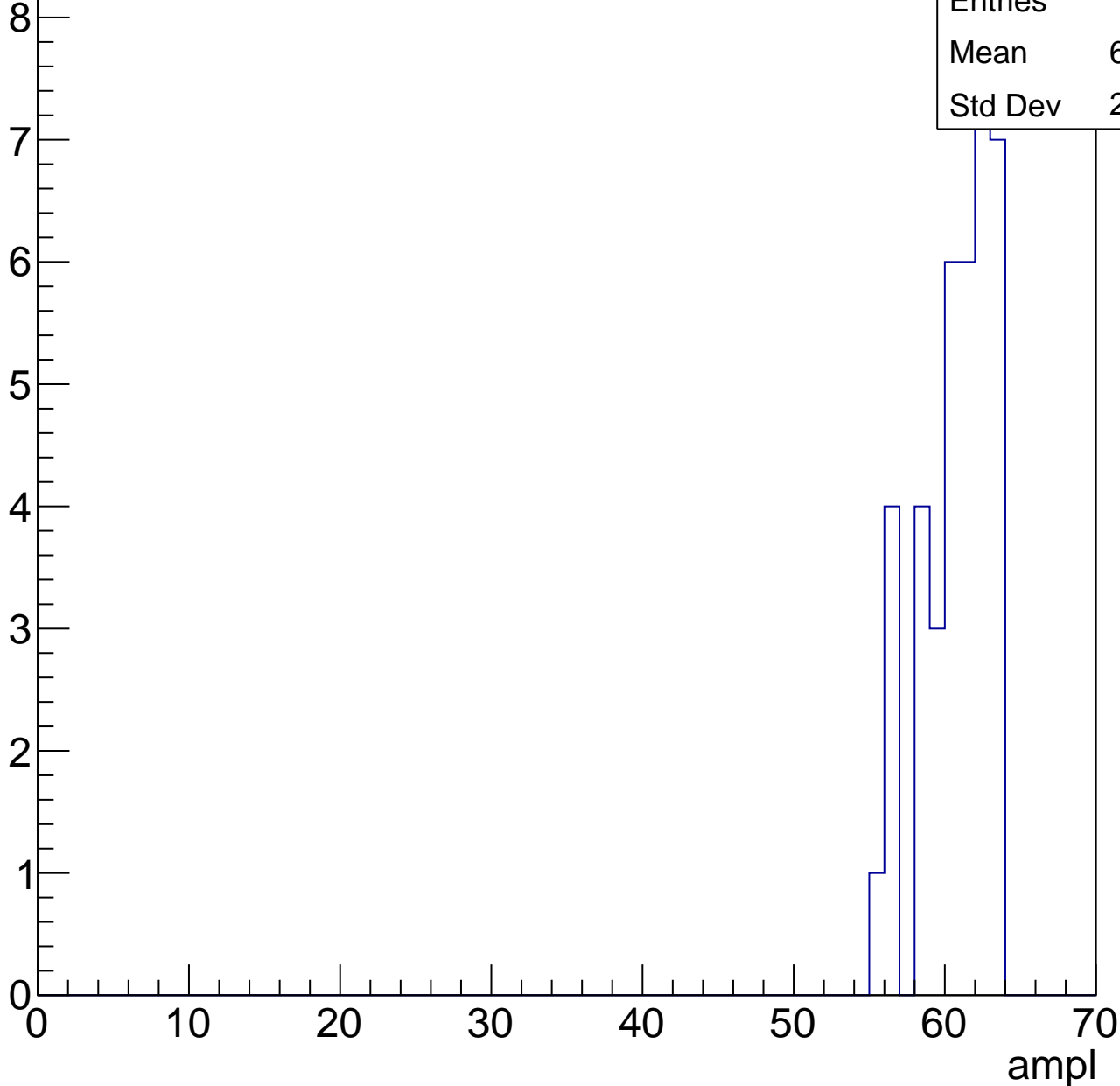


B1L101S, U15-ch3, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

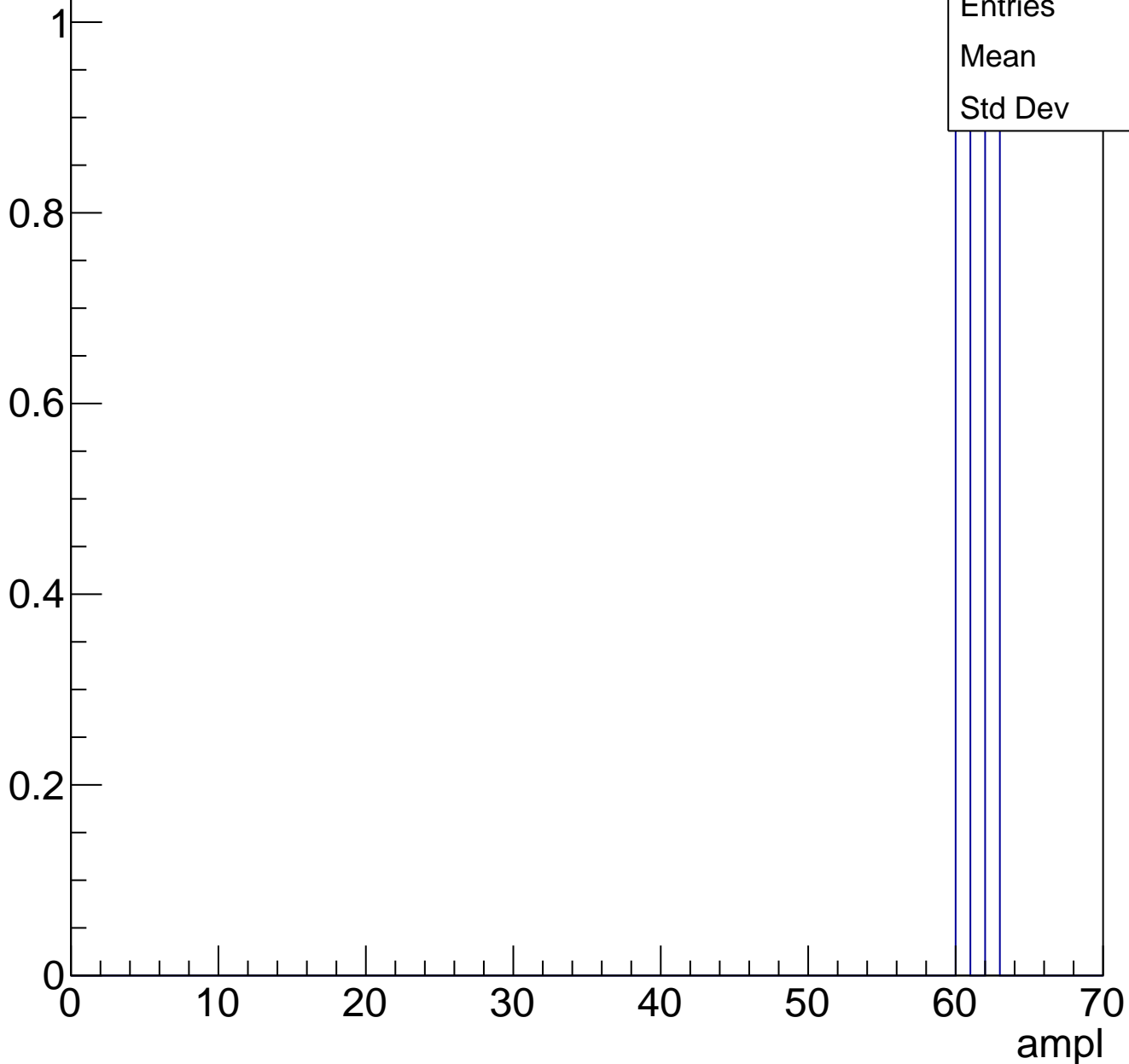
Entries	39
Mean	60.28
Std Dev	2.298



B1L101S, U15-ch3, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch3, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch4, adc0

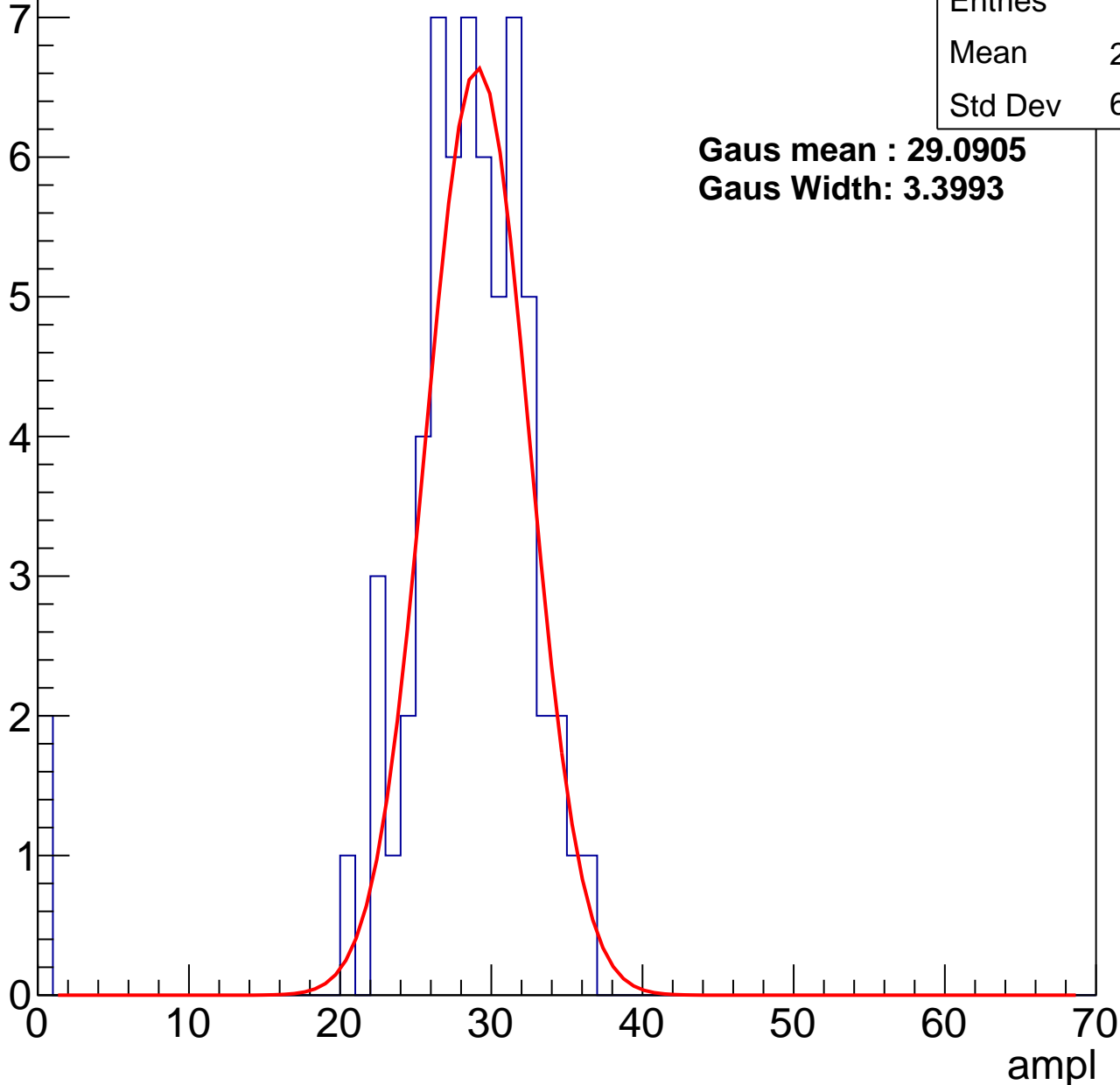
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	27.47
Std Dev	6.029

Gaus mean : 29.0905

Gaus Width: 3.3993



B1L101S, U15-ch4, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries	64
Mean	34.88
Std Dev	3.412

Gaus mean : 35.6887

Gaus Width: 4.2870

0

10

20

30

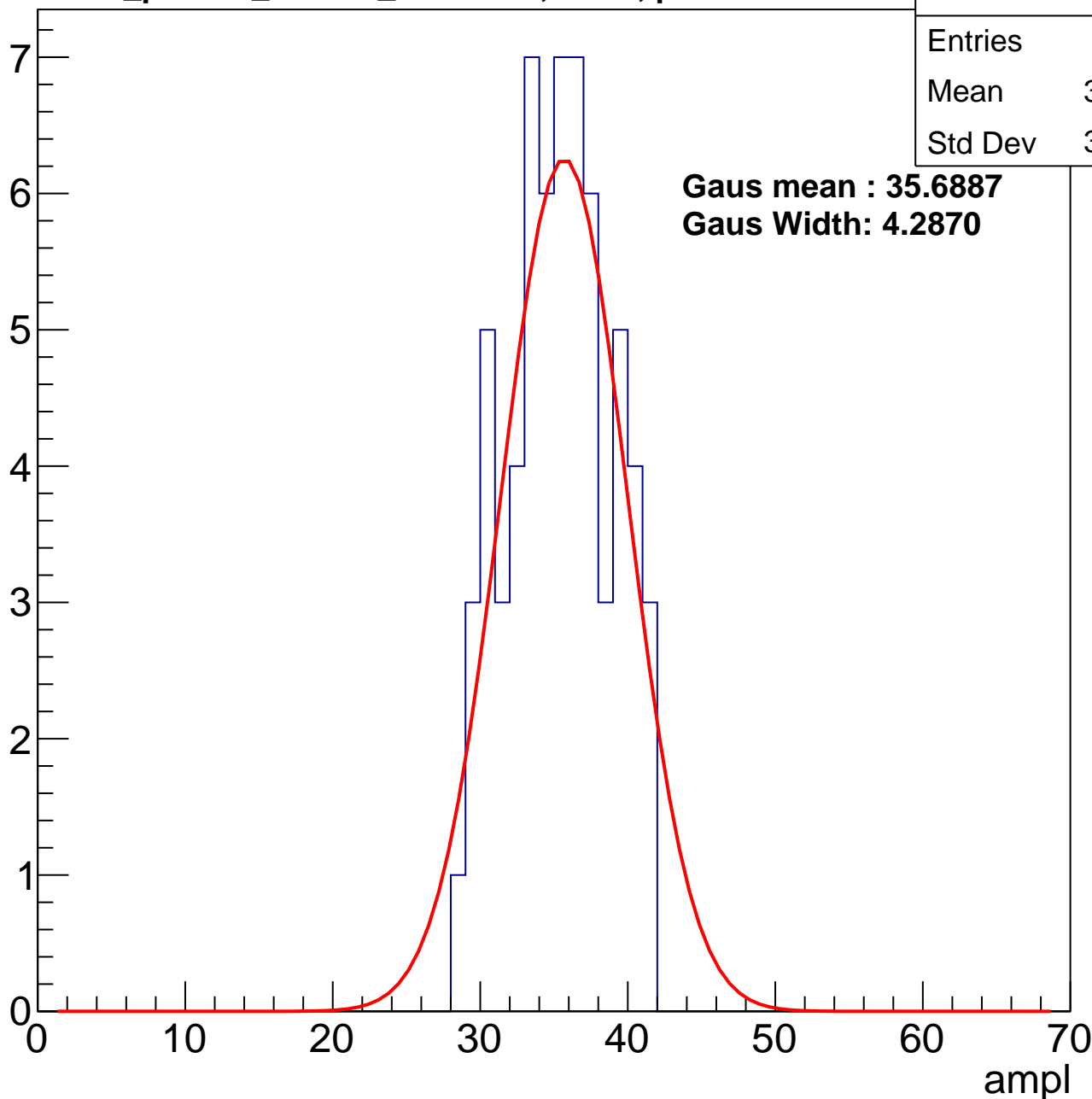
40

50

60

70

ampl



B1L101S, U15-ch4, adc2

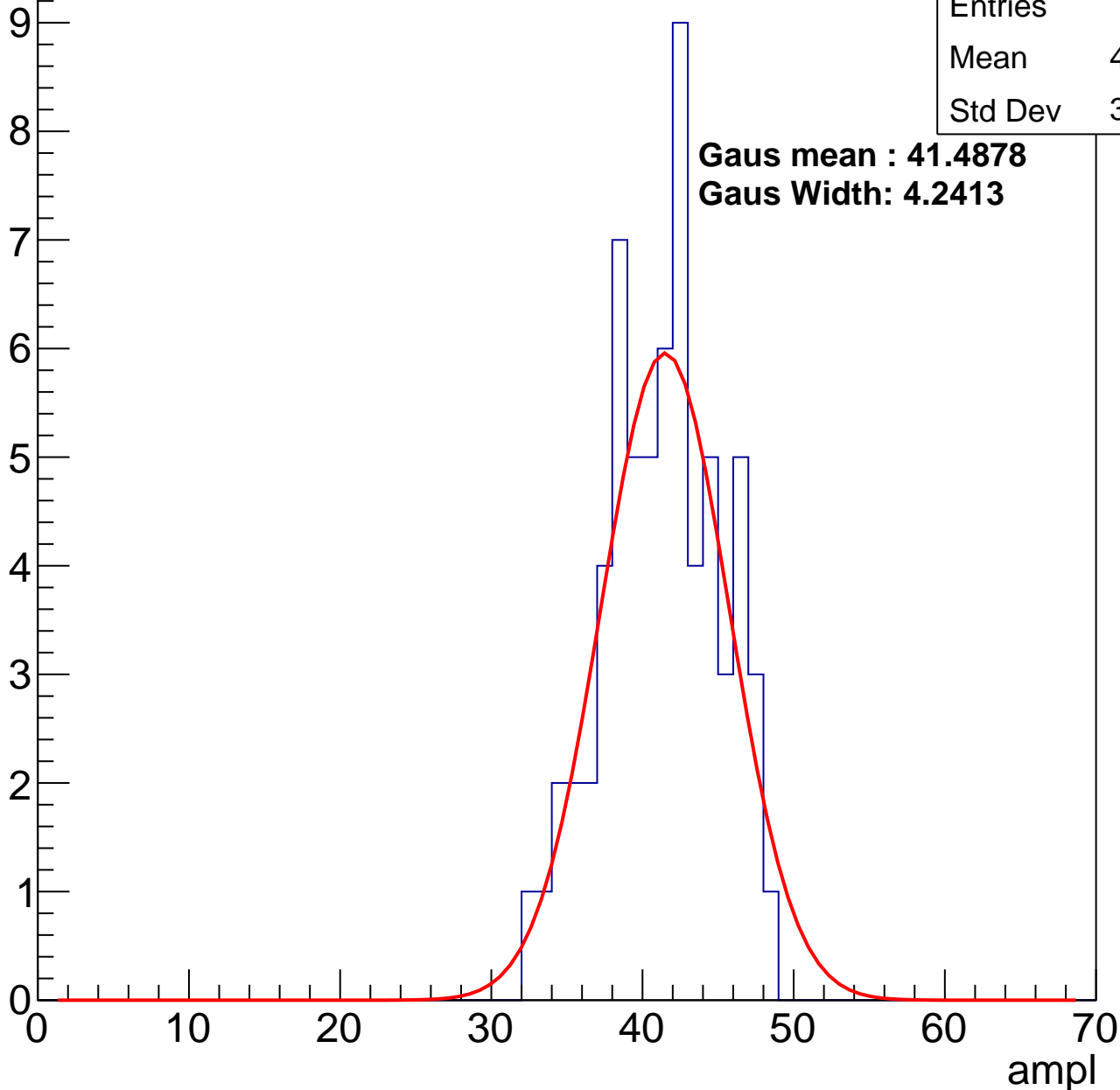
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	40.83
Std Dev	3.752

Gaus mean : 41.4878

Gaus Width: 4.2413

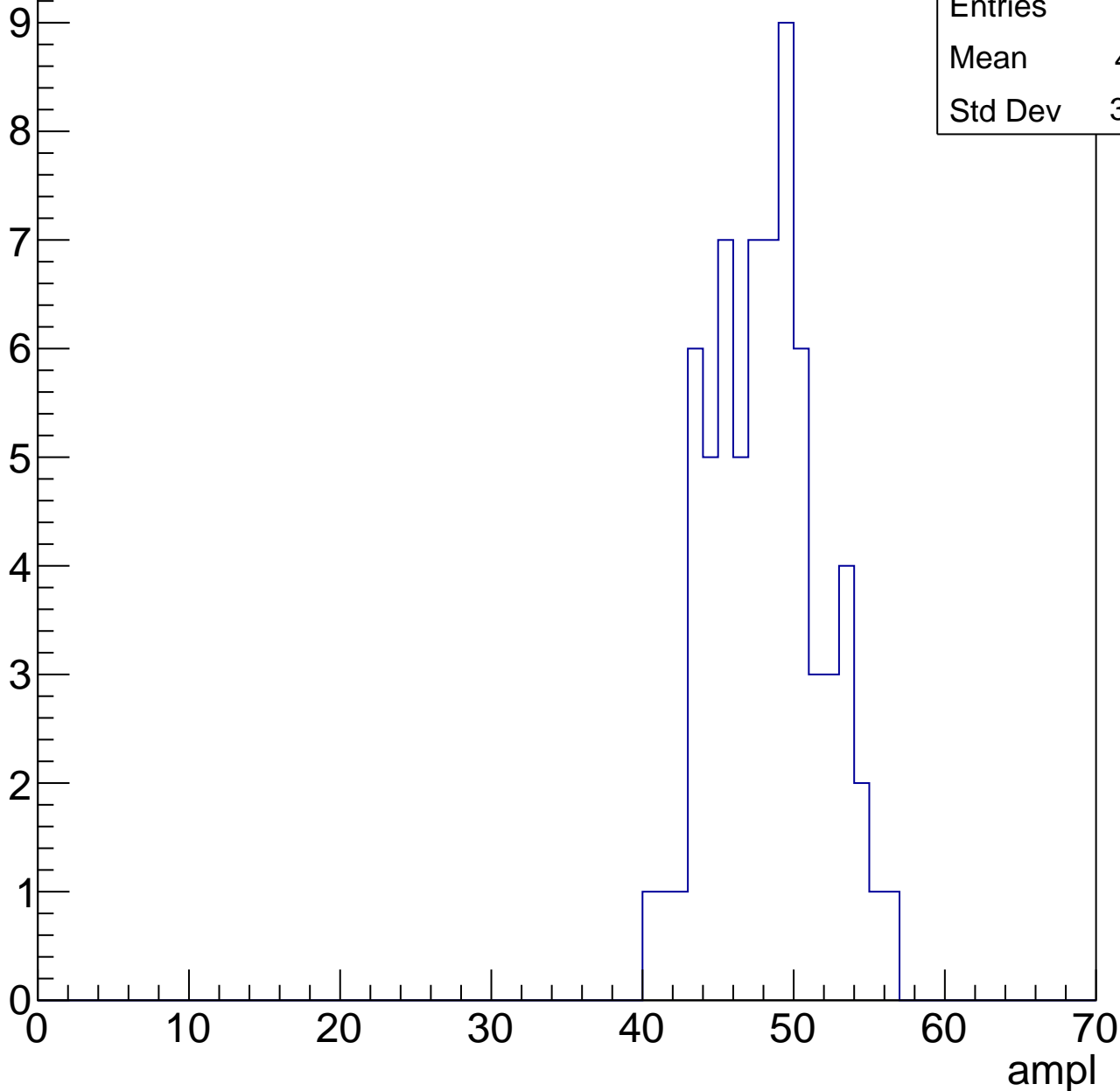


B1L101S, U15-ch4, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	47.71
Std Dev	3.523

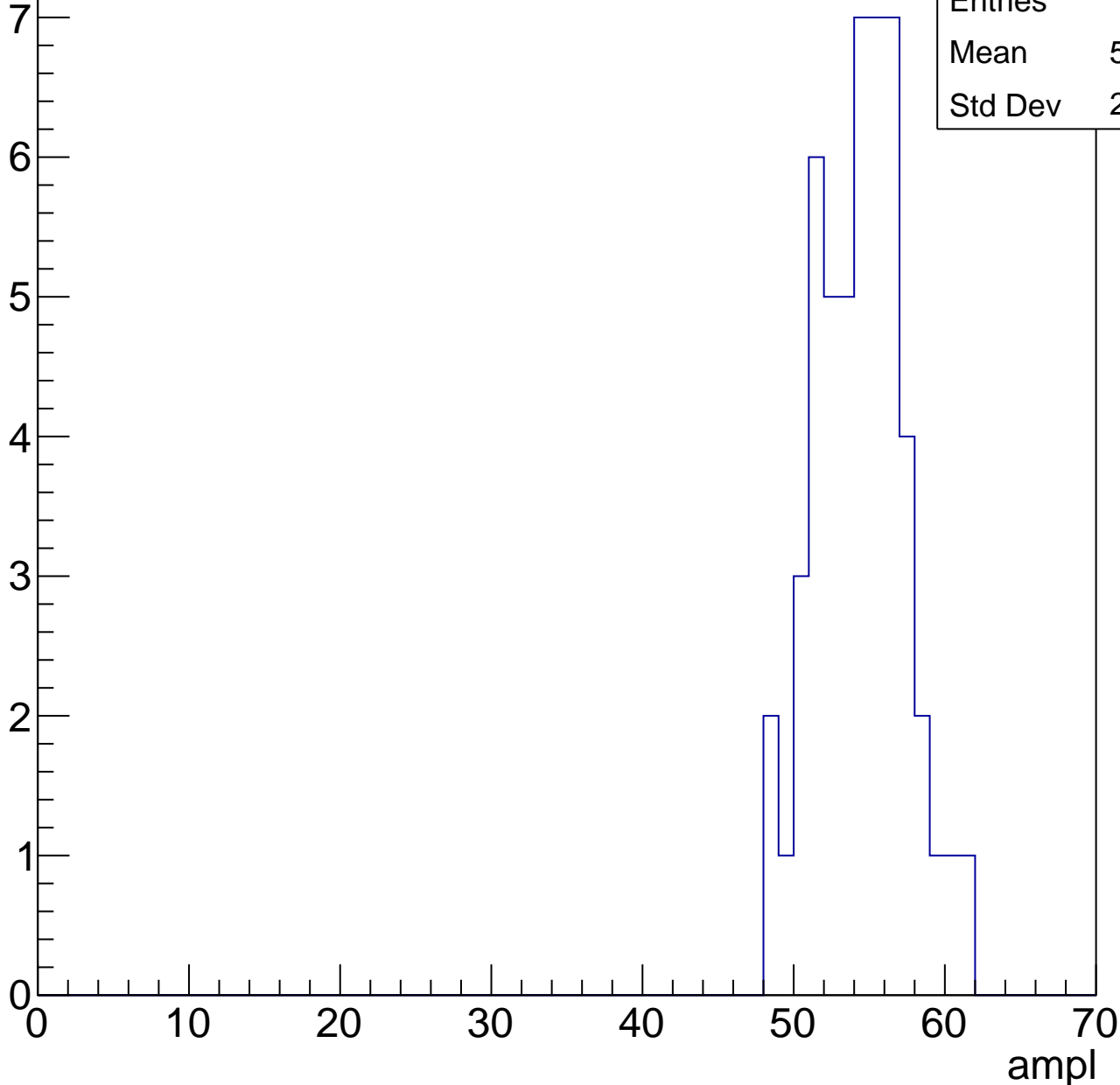


B1L101S, U15-ch4, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	53.94
Std Dev	2.898



B1L101S, U15-ch4, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

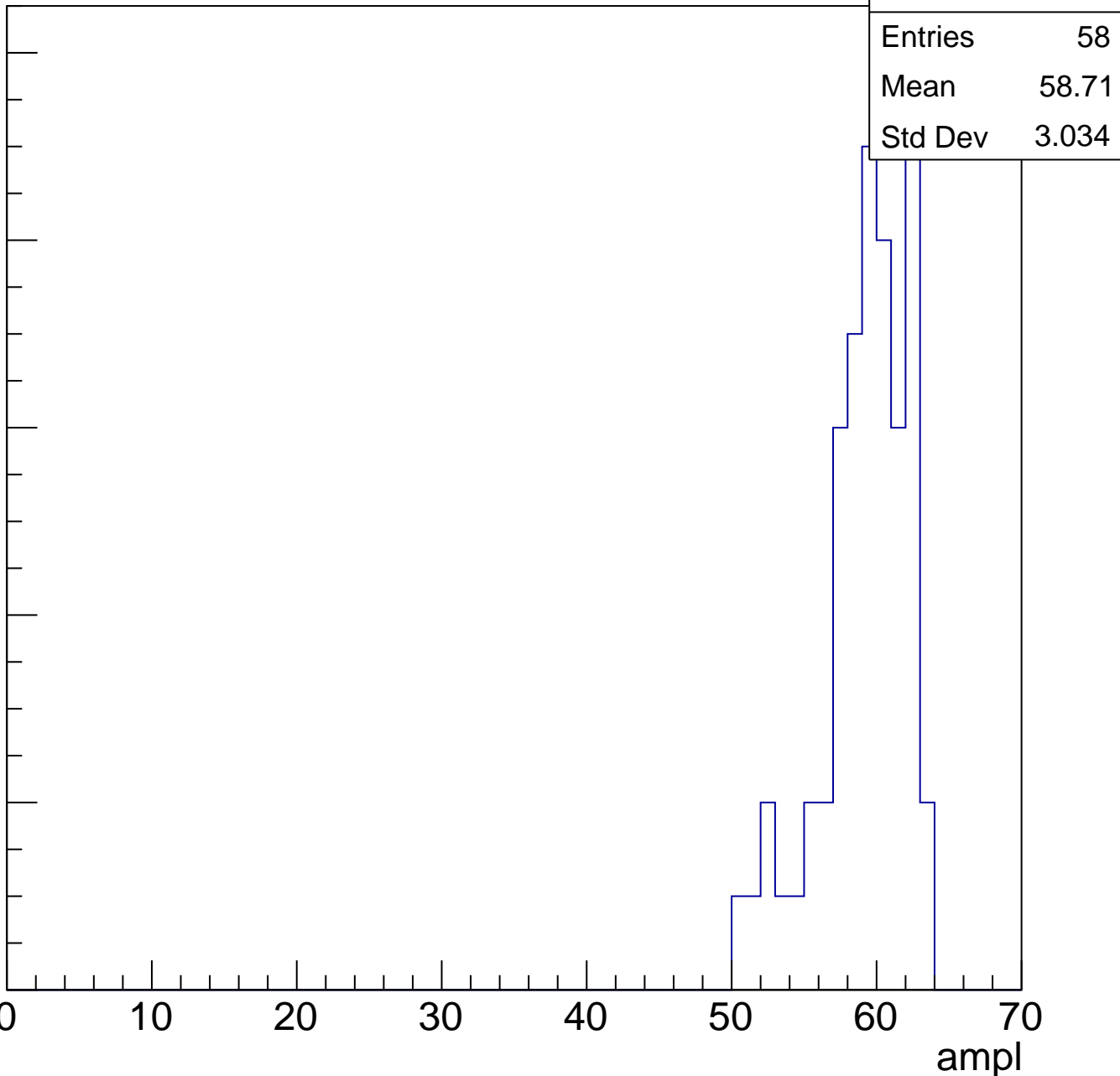
40

50

60

ampl

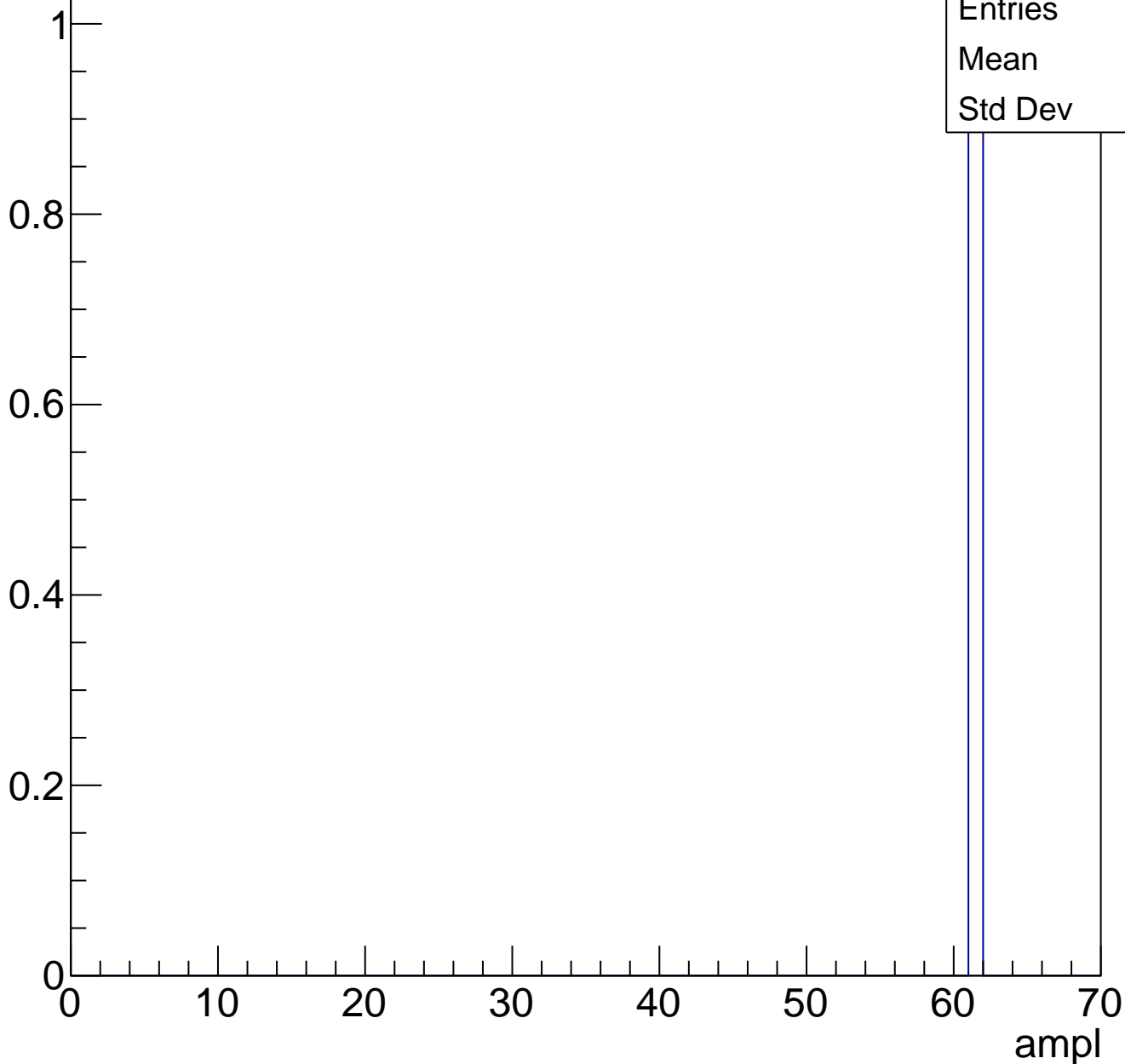
Entries	58
Mean	58.71
Std Dev	3.034



B1L101S, U15-ch4, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch5, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	30.38
Std Dev	5.035

Gaus mean : 31.7576

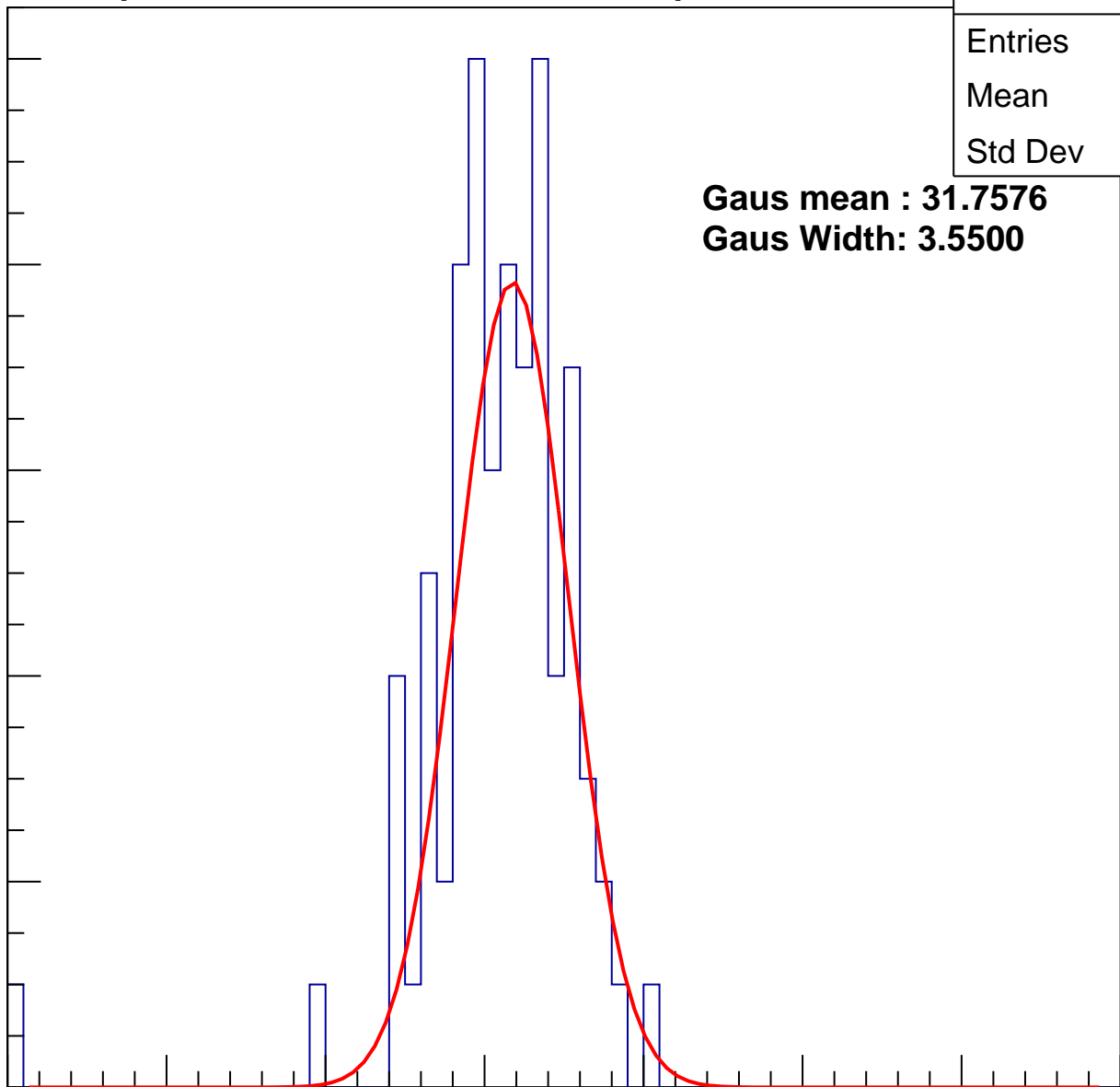
Gaus Width: 3.5500

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch5, adc1

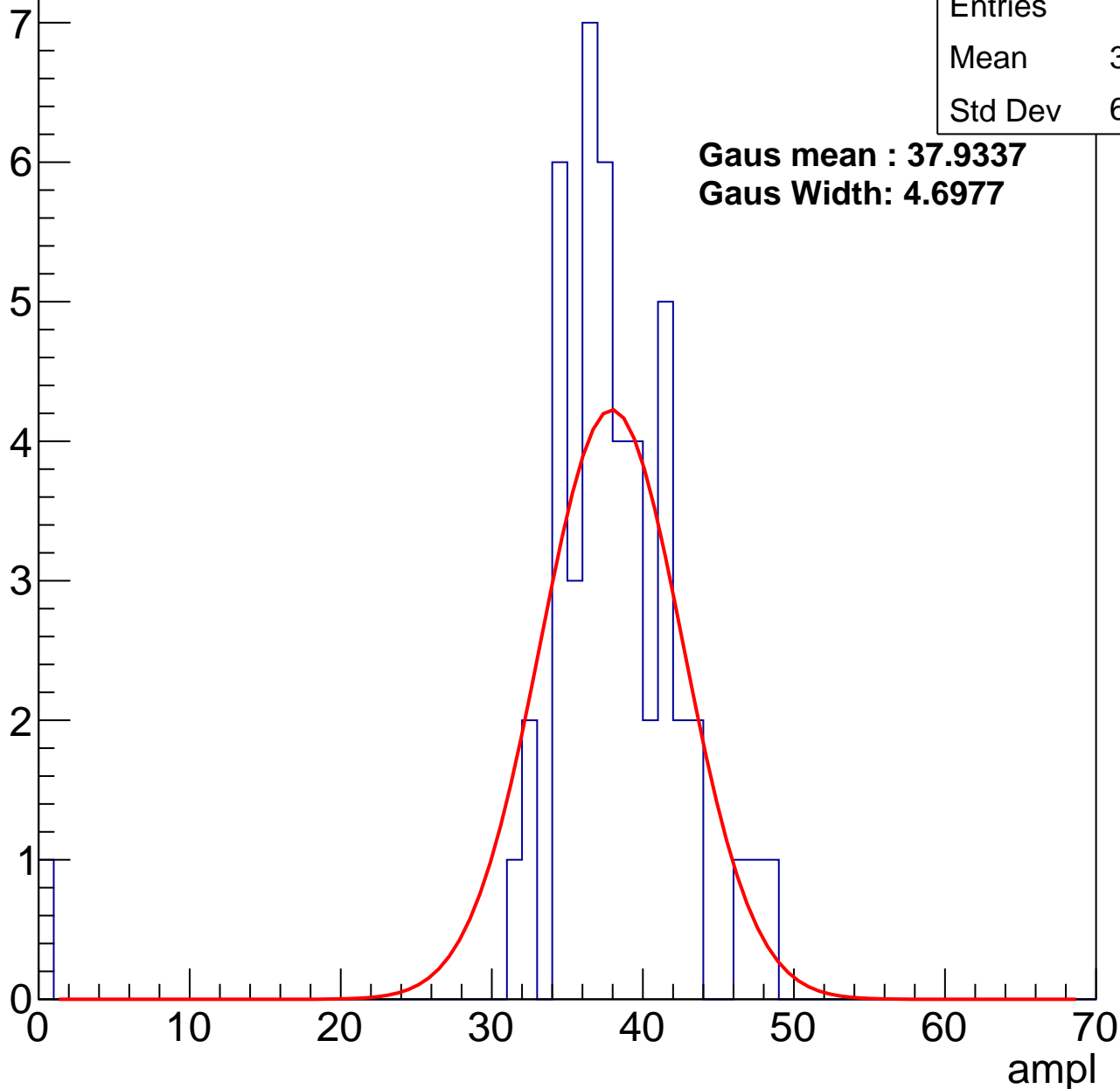
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	37.12
Std Dev	6.566

Gaus mean : 37.9337

Gaus Width: 4.6977



B1L101S, U15-ch5, adc2

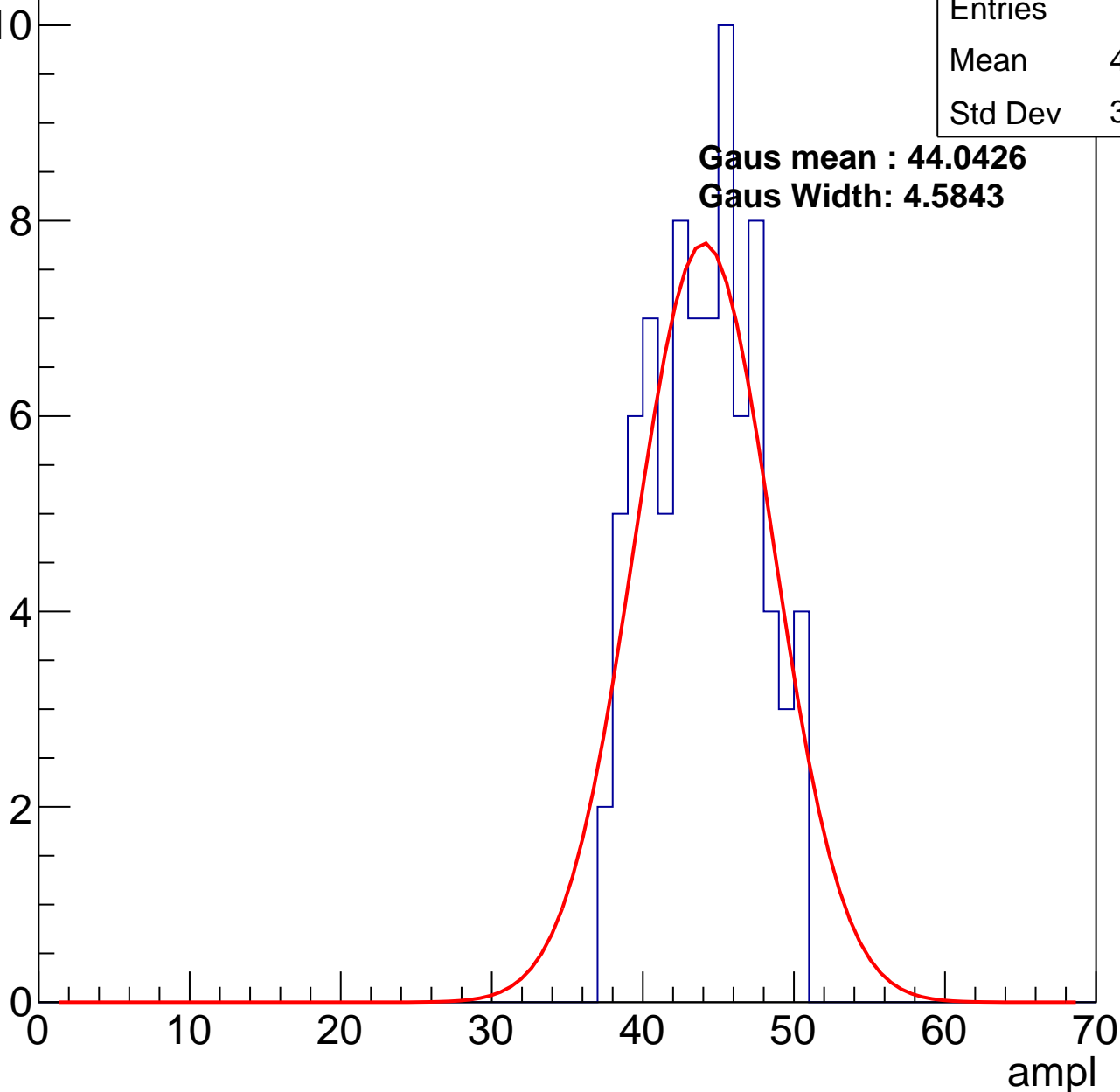
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	43.52
Std Dev	3.482

Gaus mean : 44.0426

Gaus Width: 4.5843

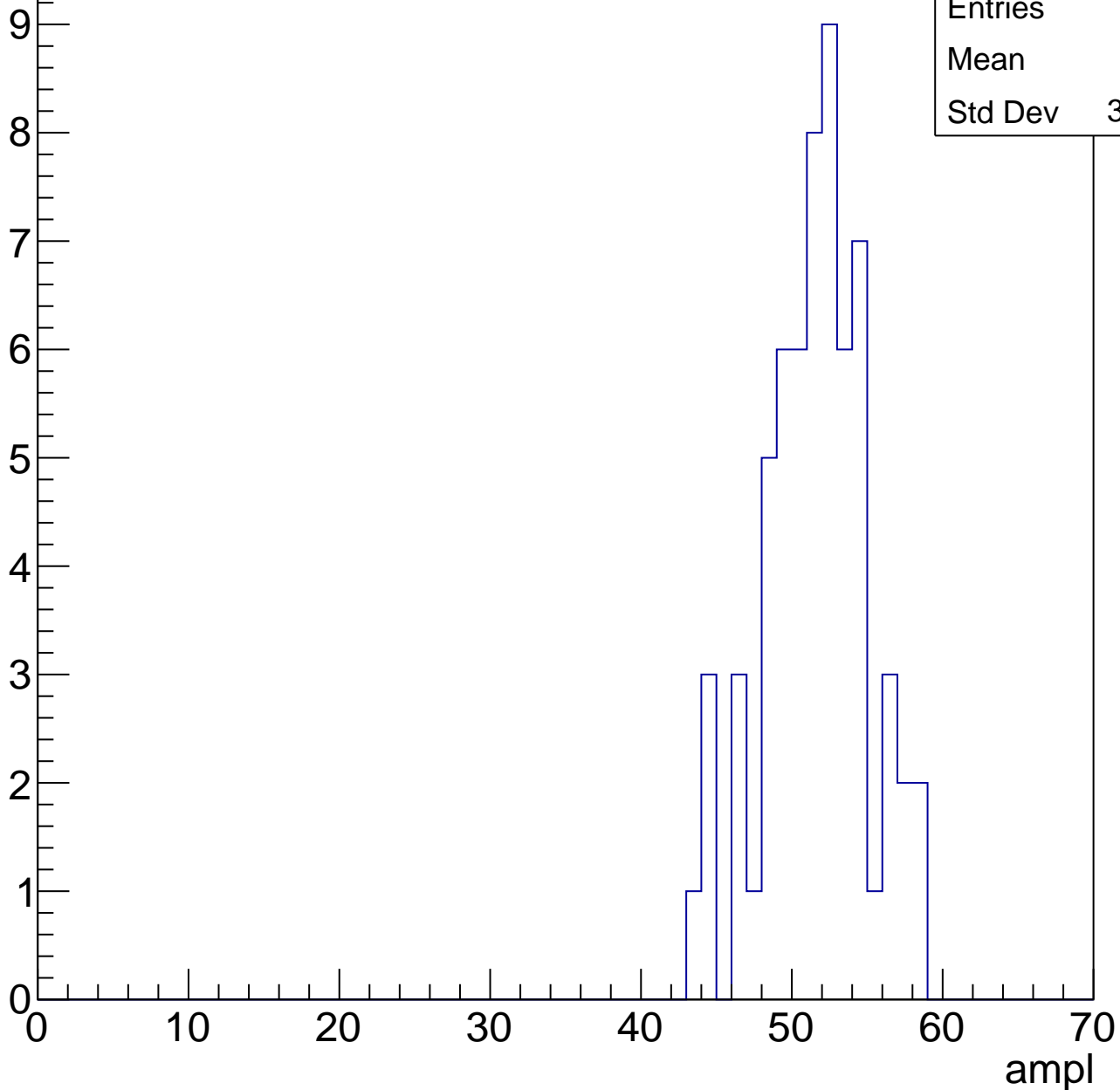


B1L101S, U15-ch5, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	51.1
Std Dev	3.412

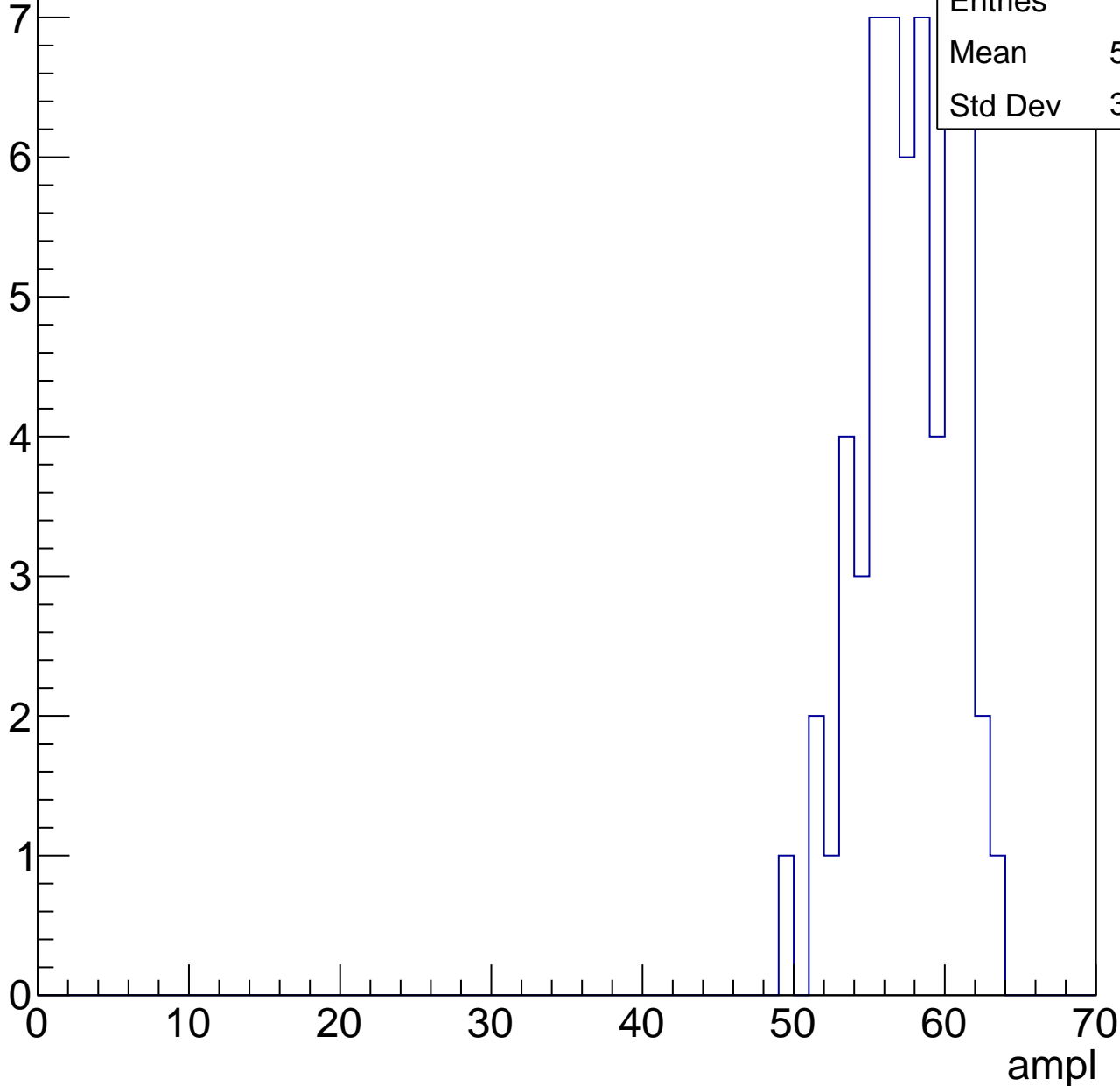


B1L101S, U15-ch5, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	57.15
Std Dev	3.107

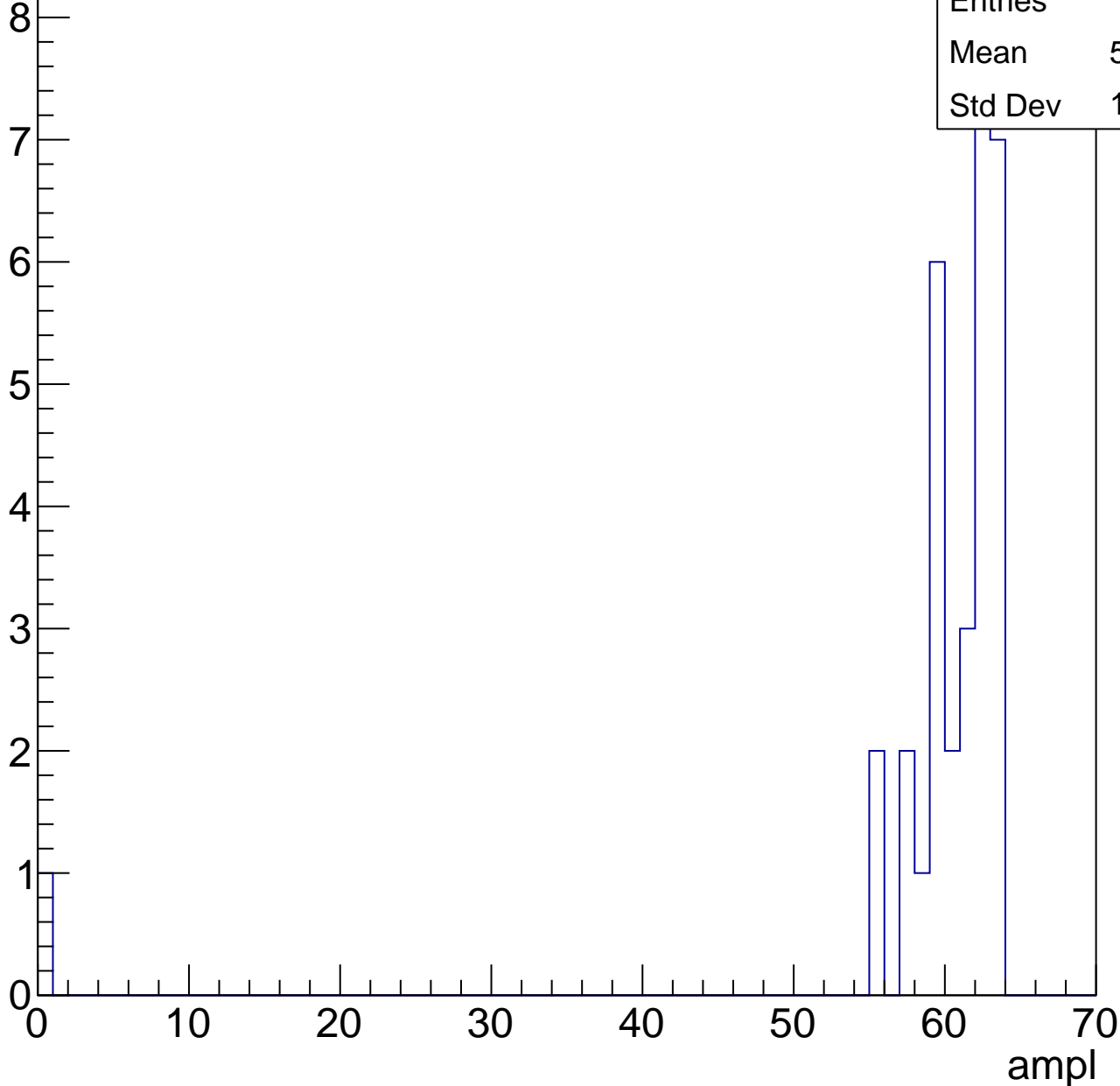


B1L101S, U15-ch5, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	58.62
Std Dev	10.78



B1L101S, U15-ch5, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

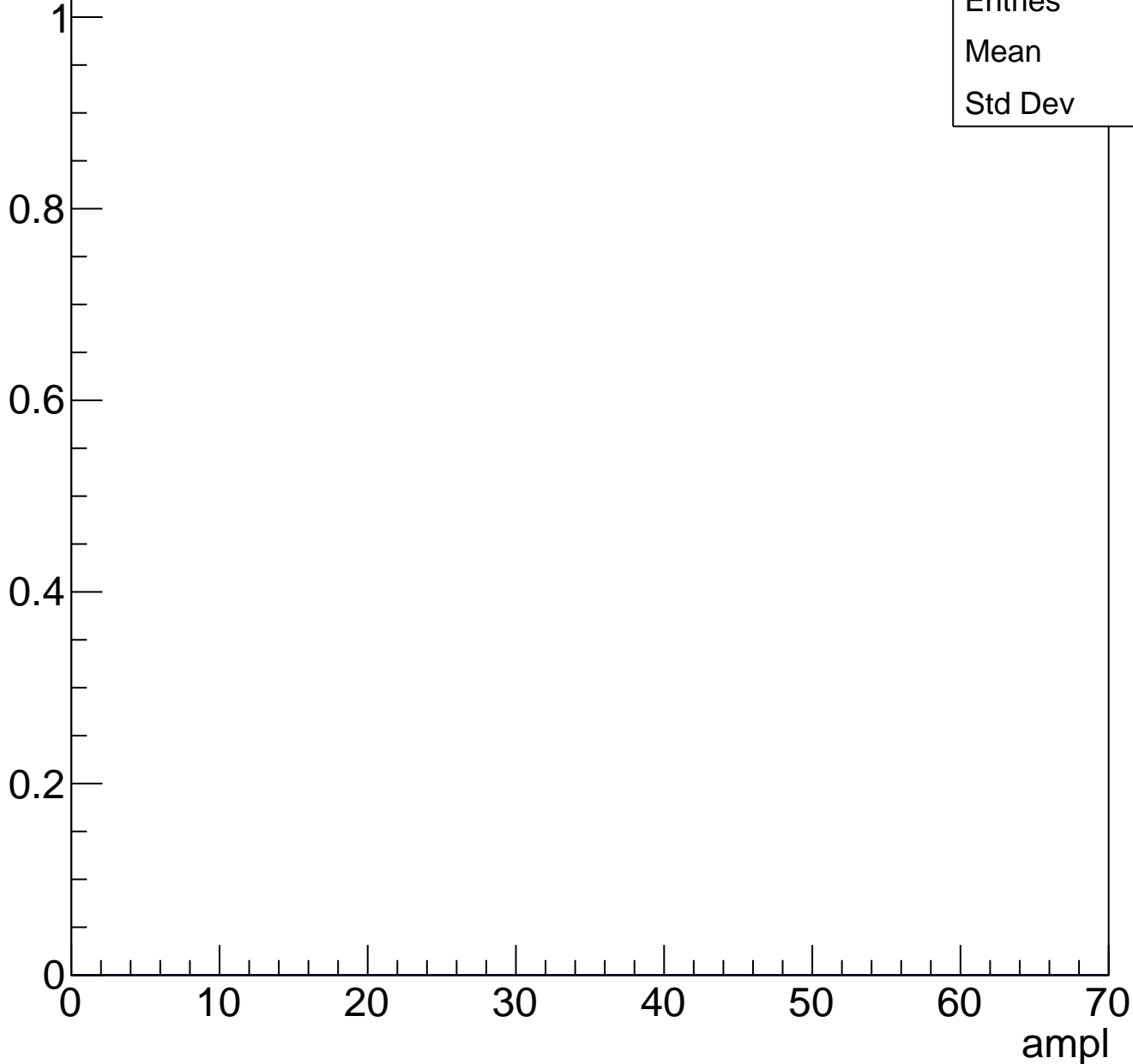


Entries	3
Mean	62
Std Dev	1.414

B1L101S, U15-ch5, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch6, adc0

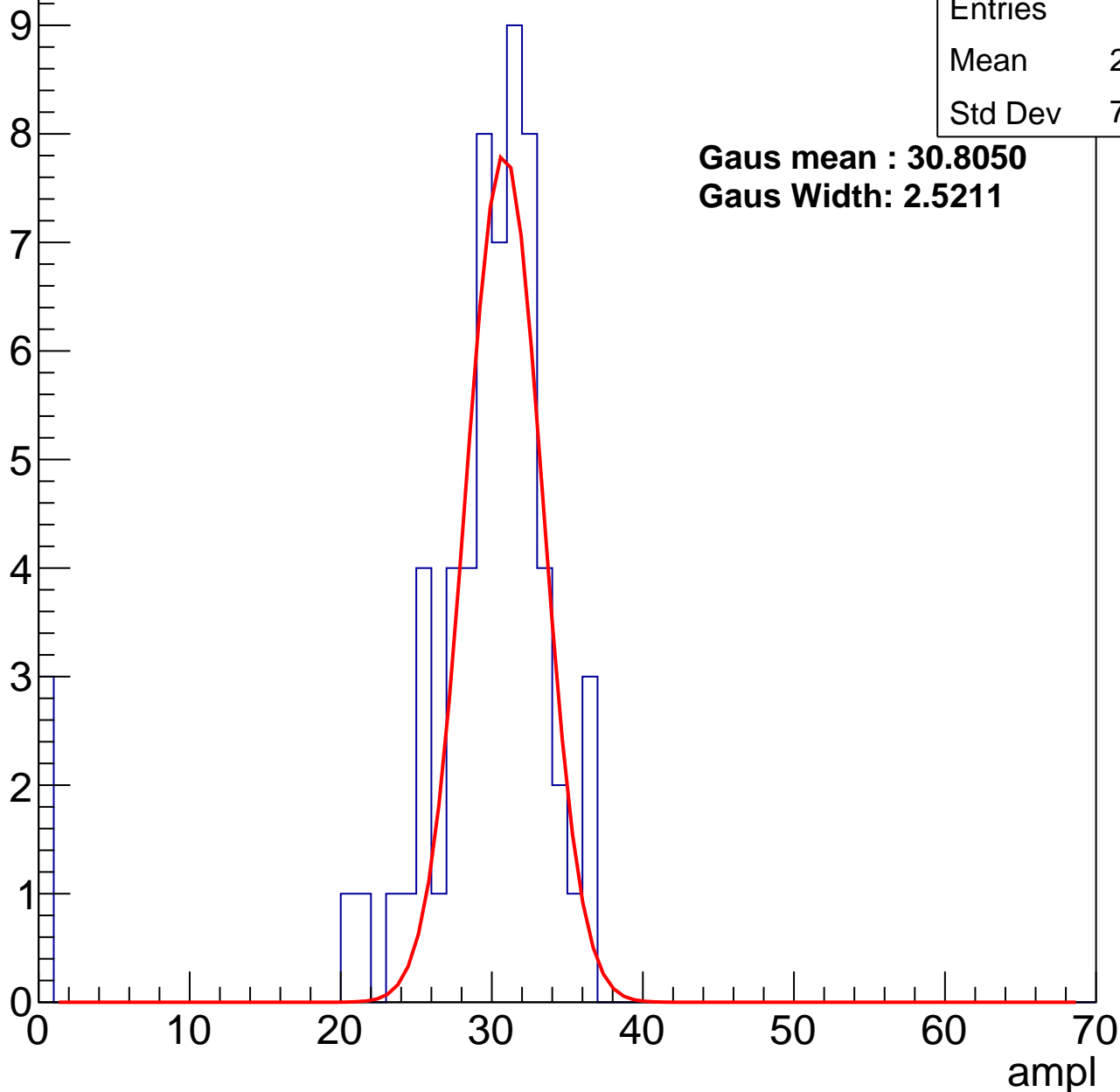
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	28.29
Std Dev	7.187

Gaus mean : 30.8050

Gaus Width: 2.5211



B1L101S, U15-ch6, adc1

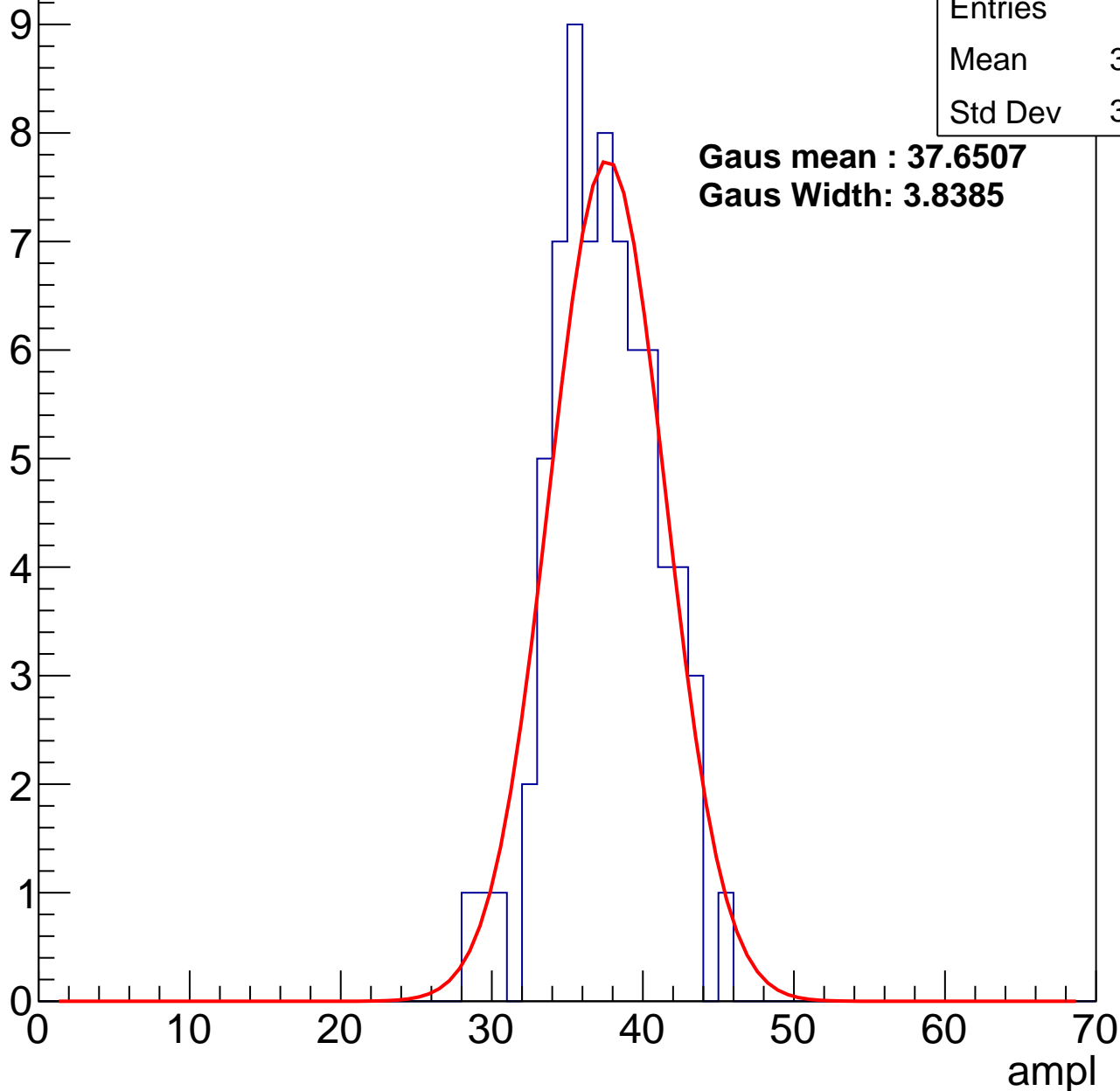
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.99
Std Dev	3.438

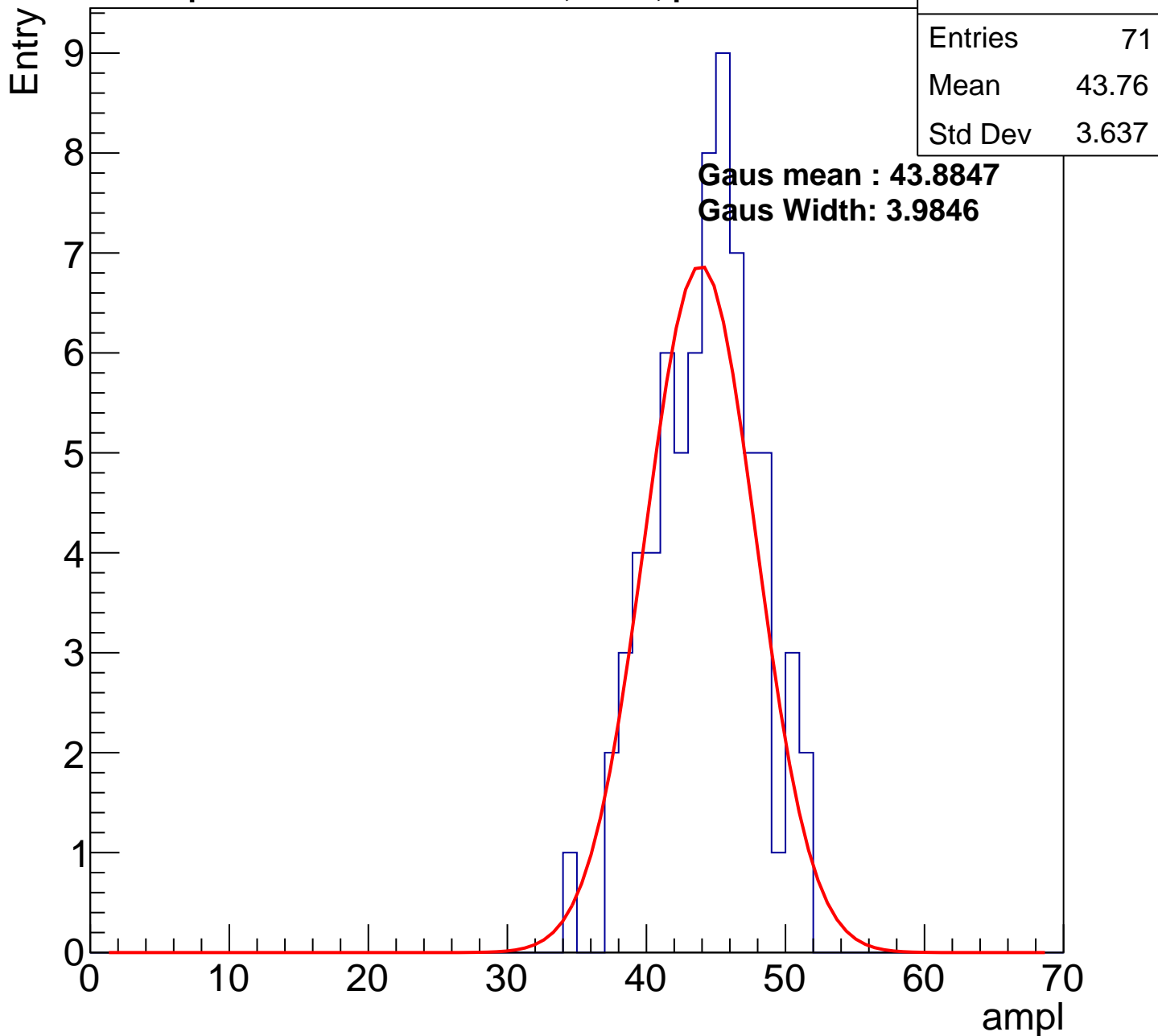
Gaus mean : 37.6507

Gaus Width: 3.8385



B1L101S, U15-ch6, adc2

calib_packv5_042523_0143.root, FC#0, port D2

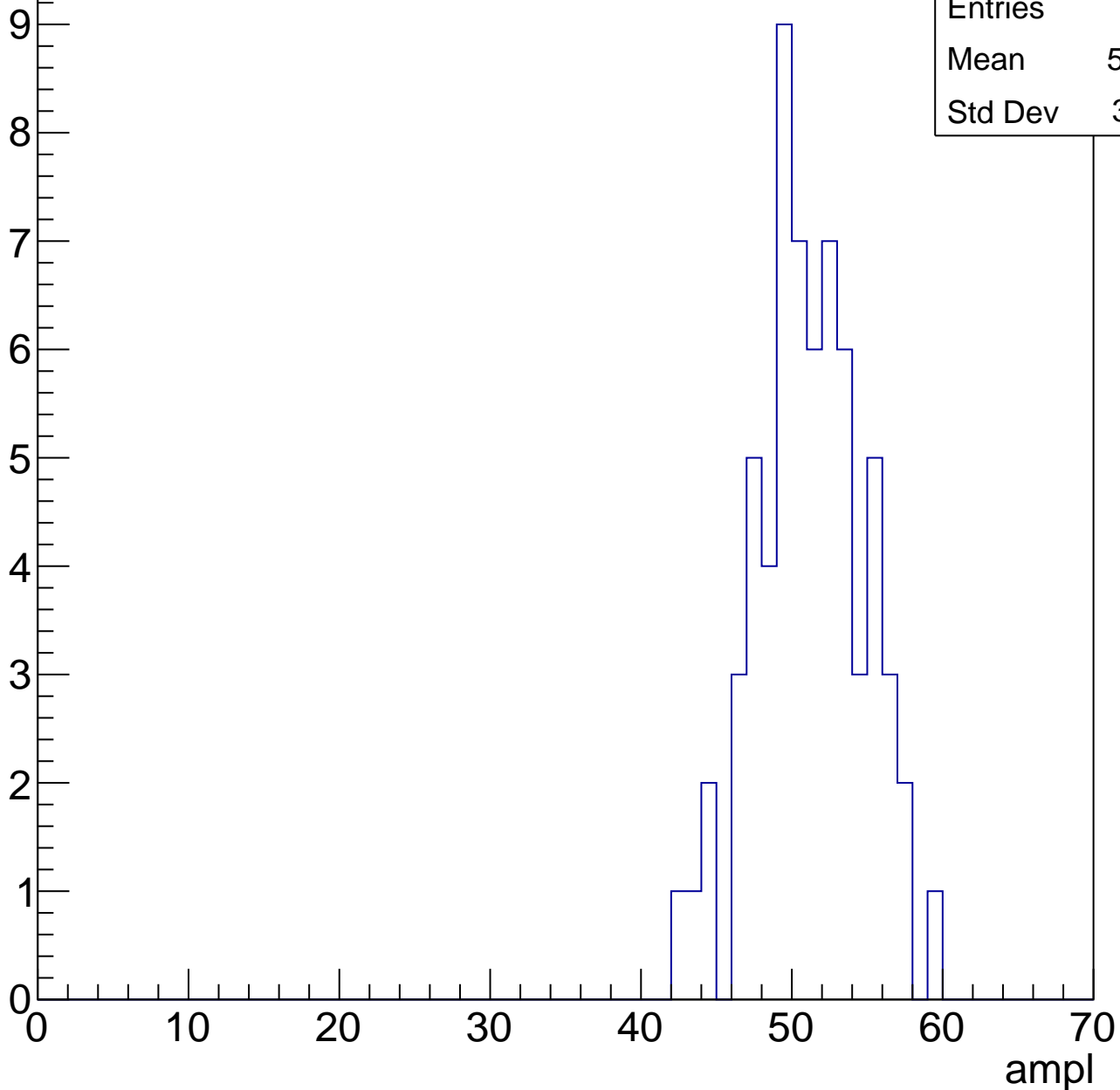


B1L101S, U15-ch6, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	50.69
Std Dev	3.551

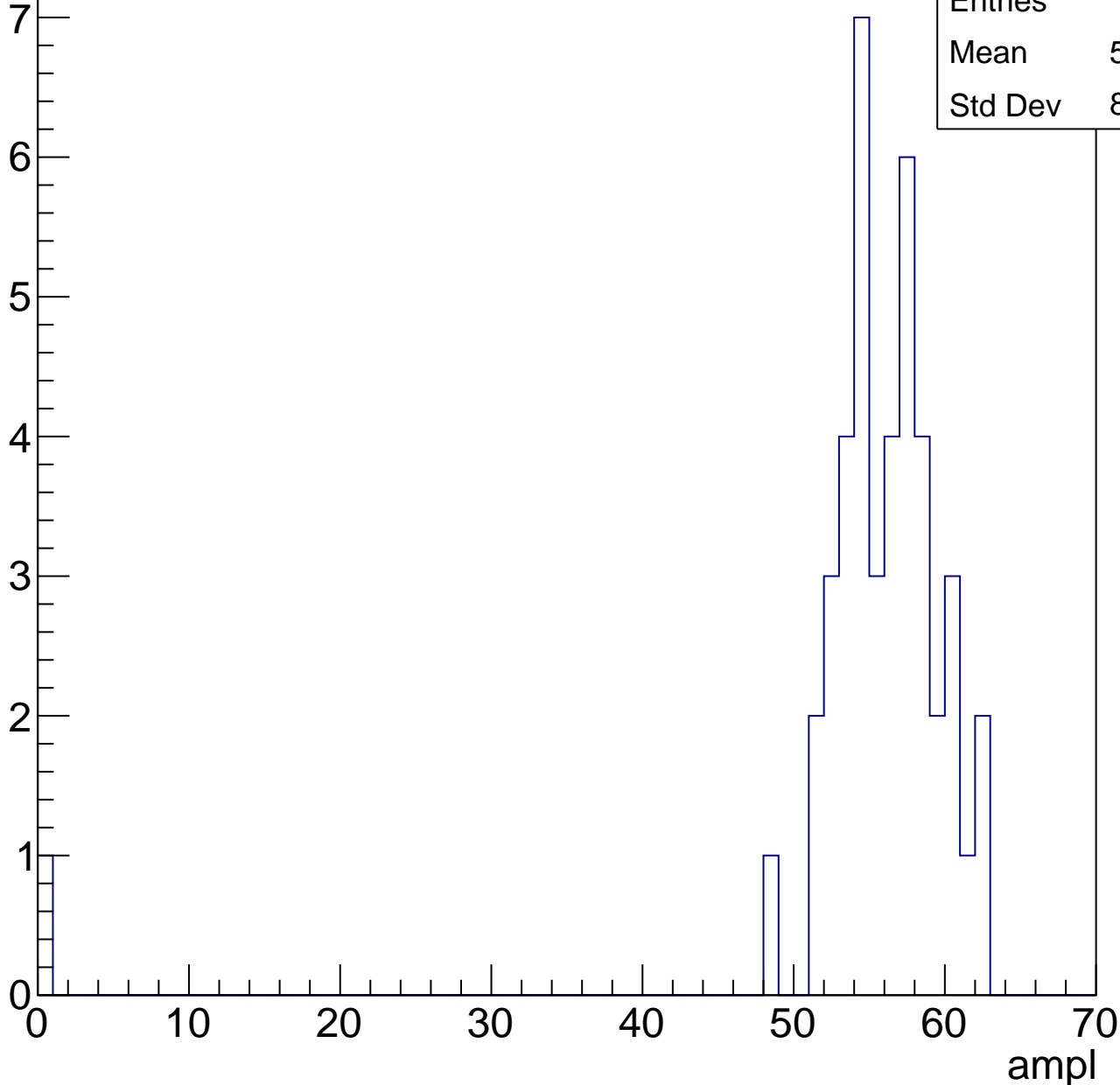


B1L101S, U15-ch6, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	54.47
Std Dev	8.956

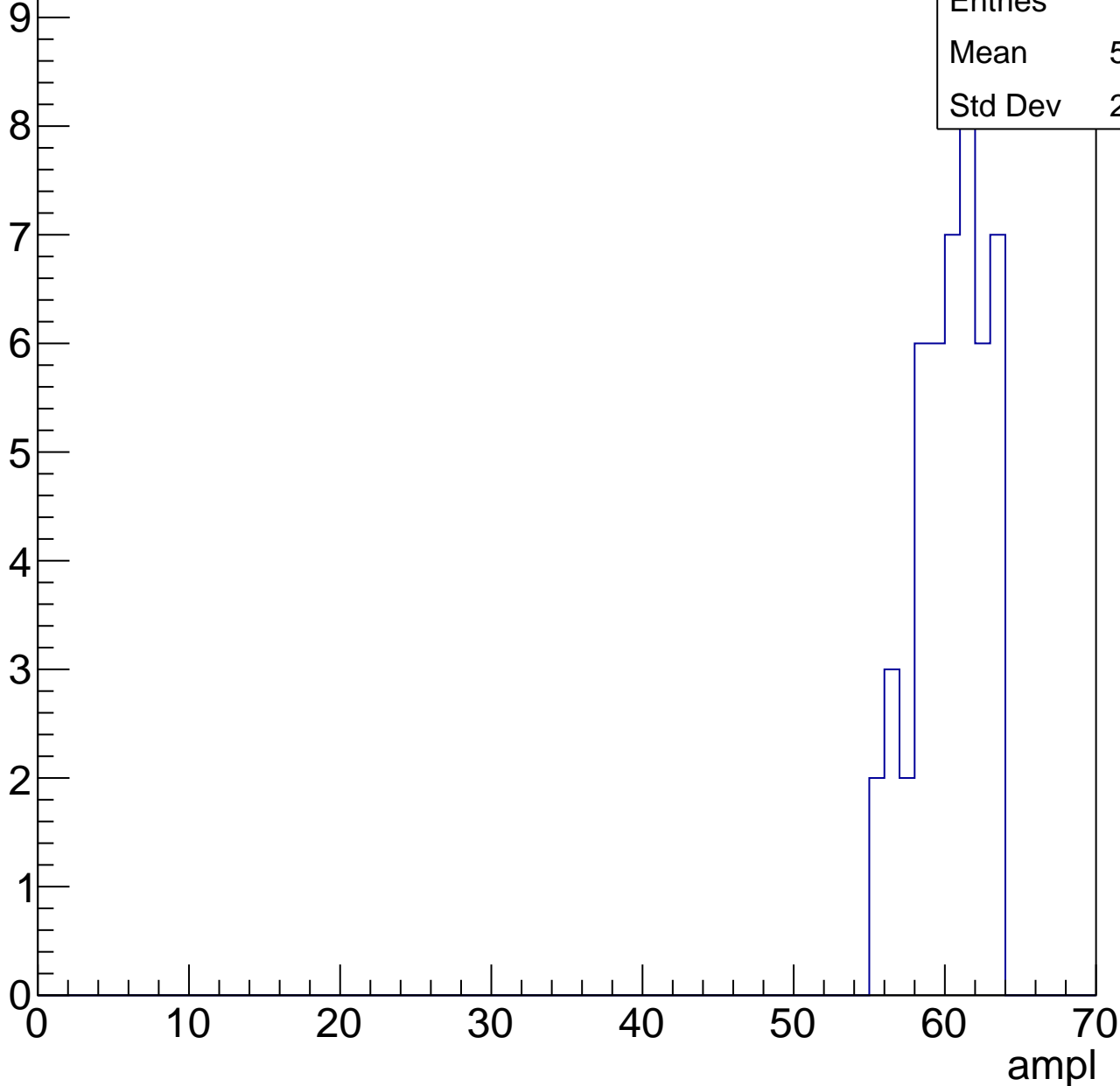


B1L101S, U15-ch6, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	59.92
Std Dev	2.244



B1L101S, U15-ch6, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch6, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch7, adc0

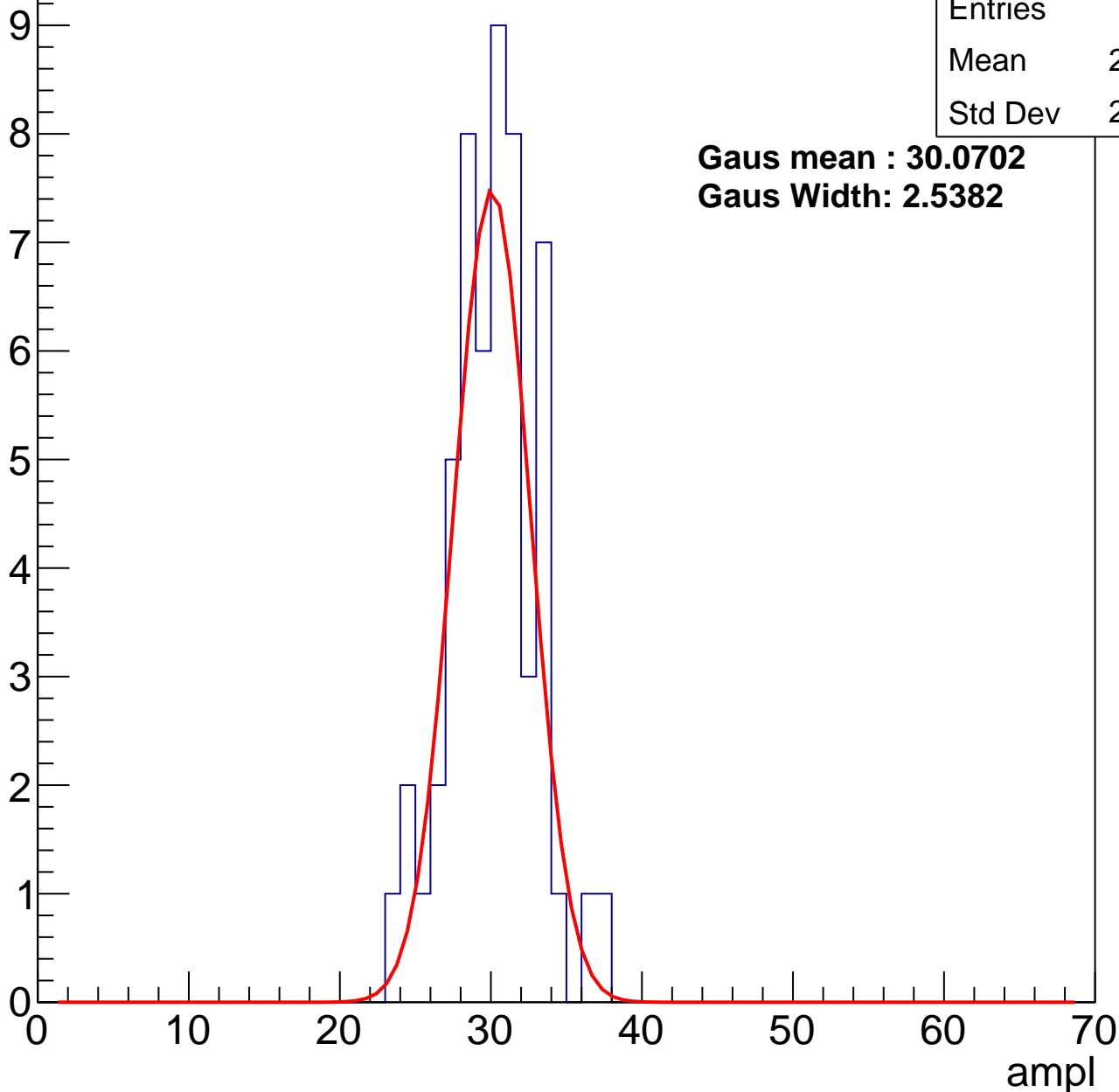
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	29.69
Std Dev	2.828

Gaus mean : 30.0702

Gaus Width: 2.5382



B1L101S, U15-ch7, adc1

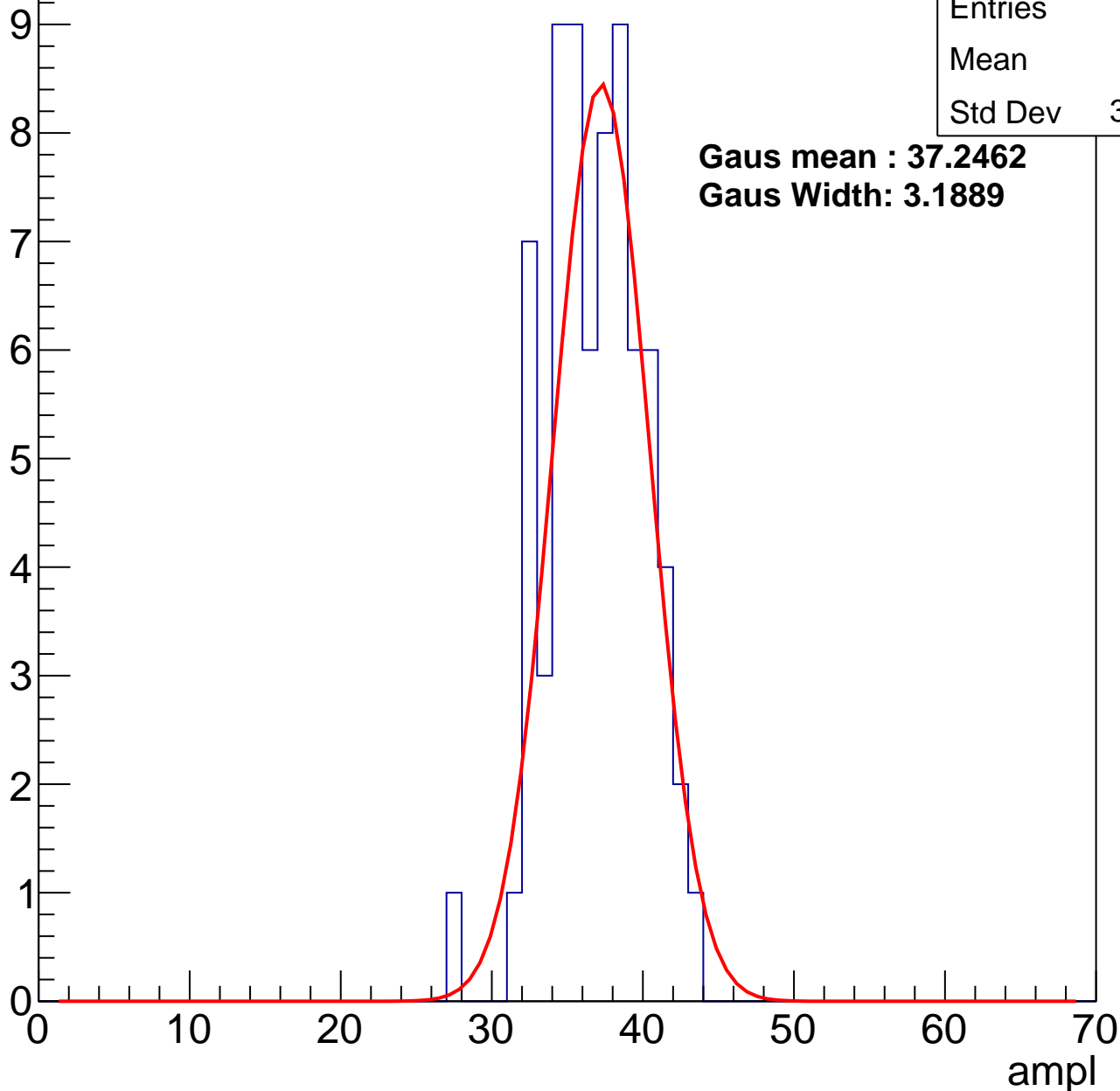
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.4
Std Dev	3.103

Gaus mean : 37.2462

Gaus Width: 3.1889



B1L101S, U15-ch7, adc2

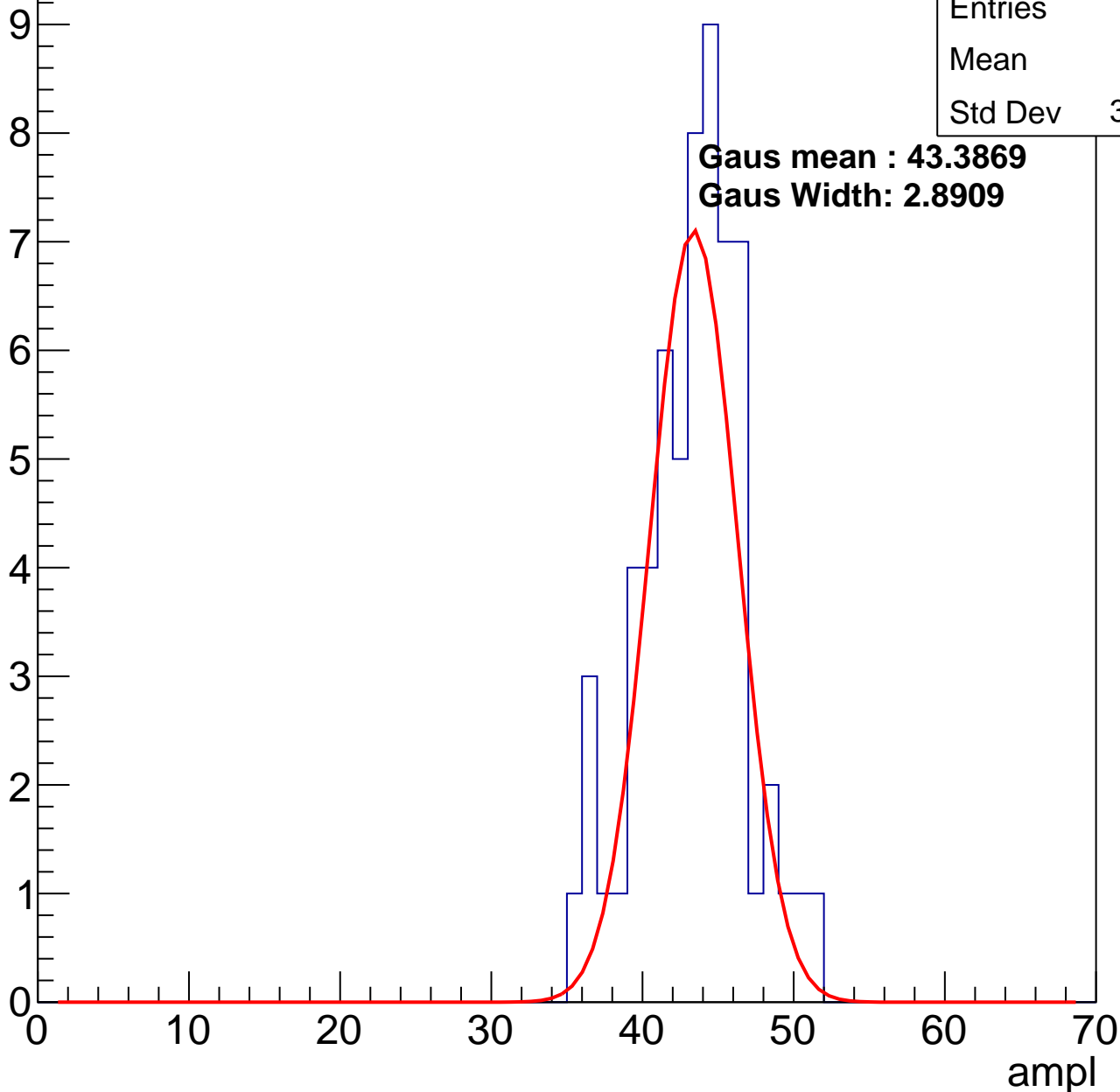
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.9
Std Dev	3.397

Gaus mean : 43.3869

Gaus Width: 2.8909

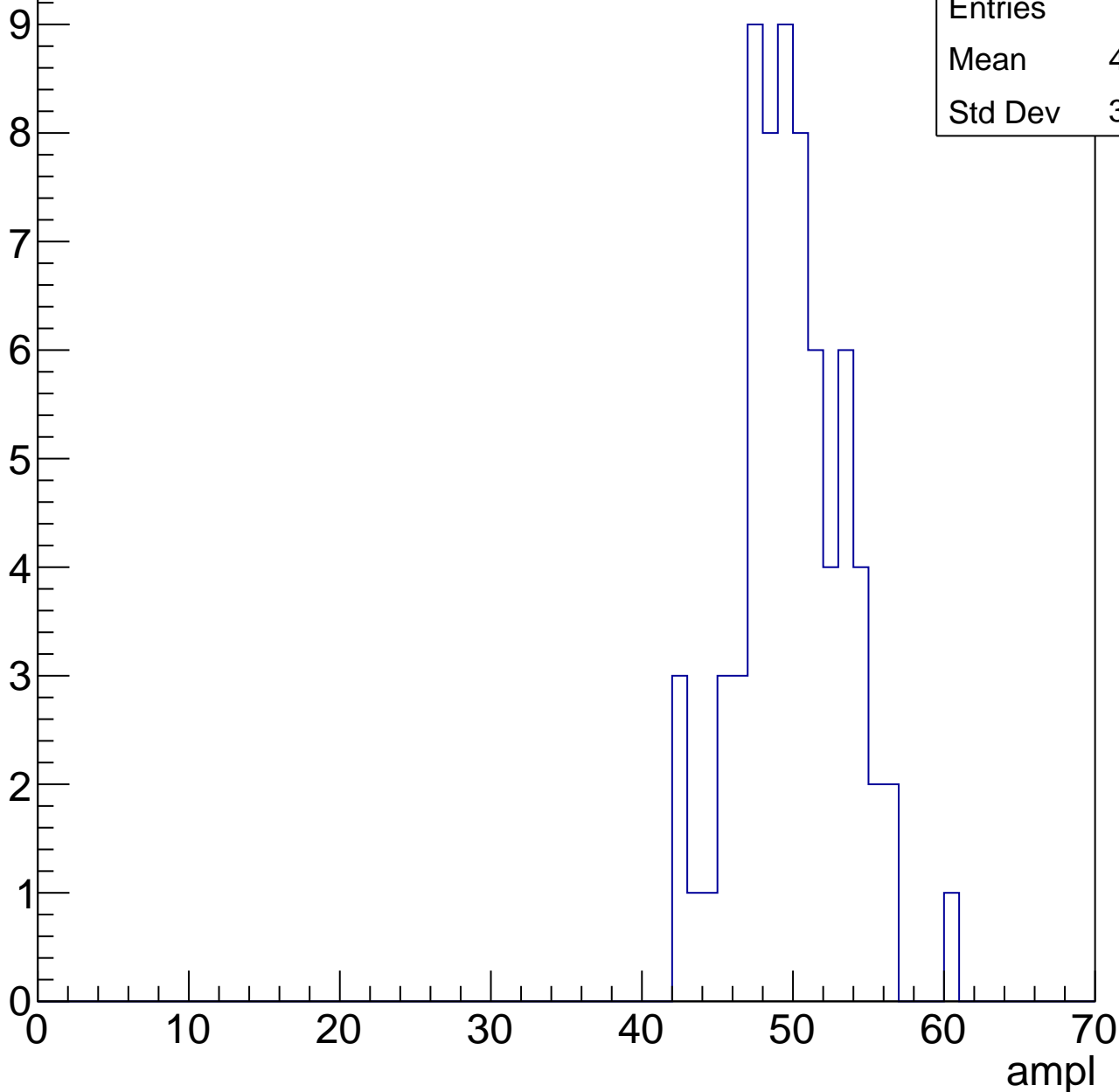


B1L101S, U15-ch7, adc3

calib_packv5_042523_0143.root, FC#0, port D2

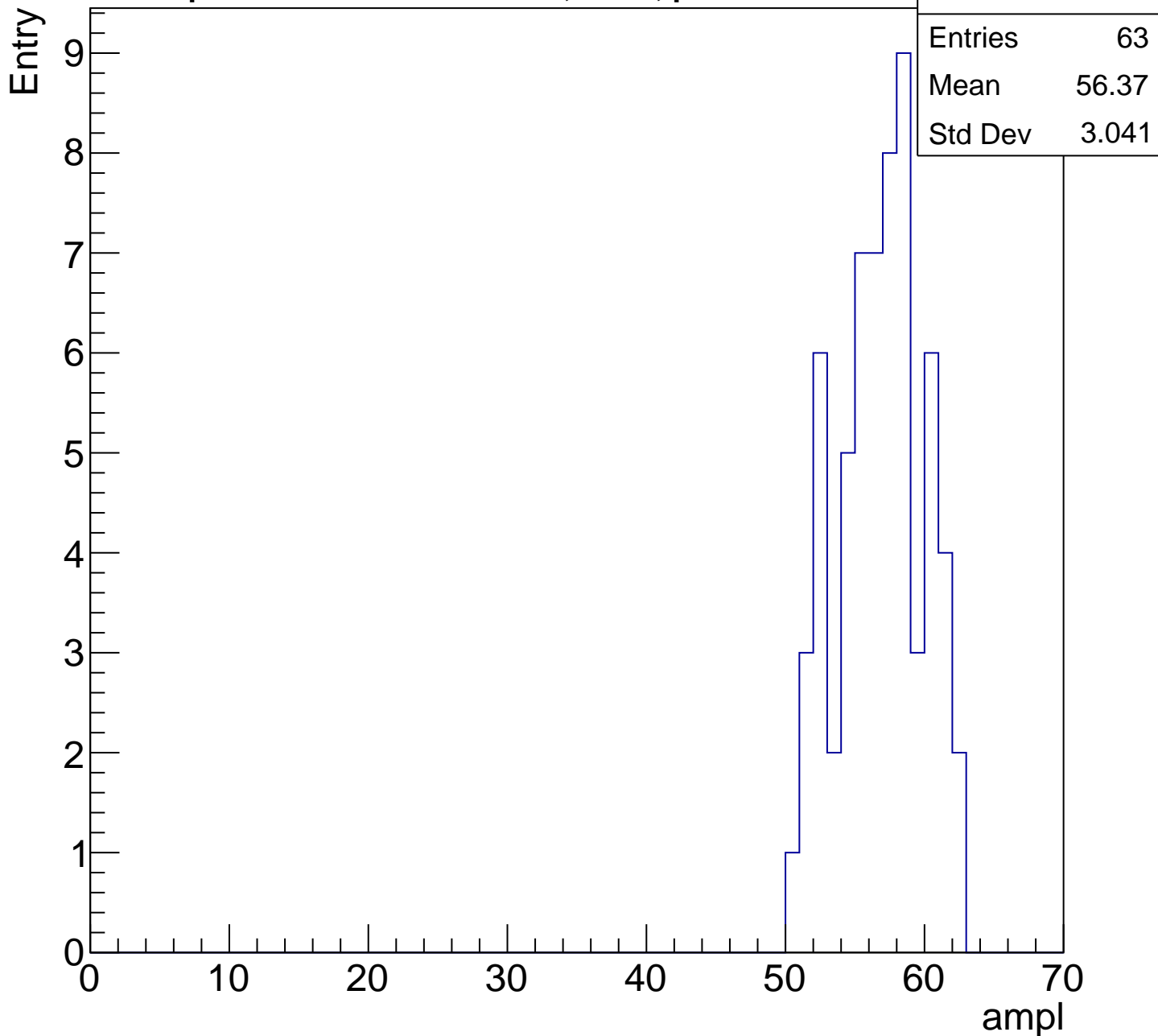
Entry

Entries	70
Mean	49.49
Std Dev	3.516



B1L101S, U15-ch7, adc4

calib_packv5_042523_0143.root, FC#0, port D2

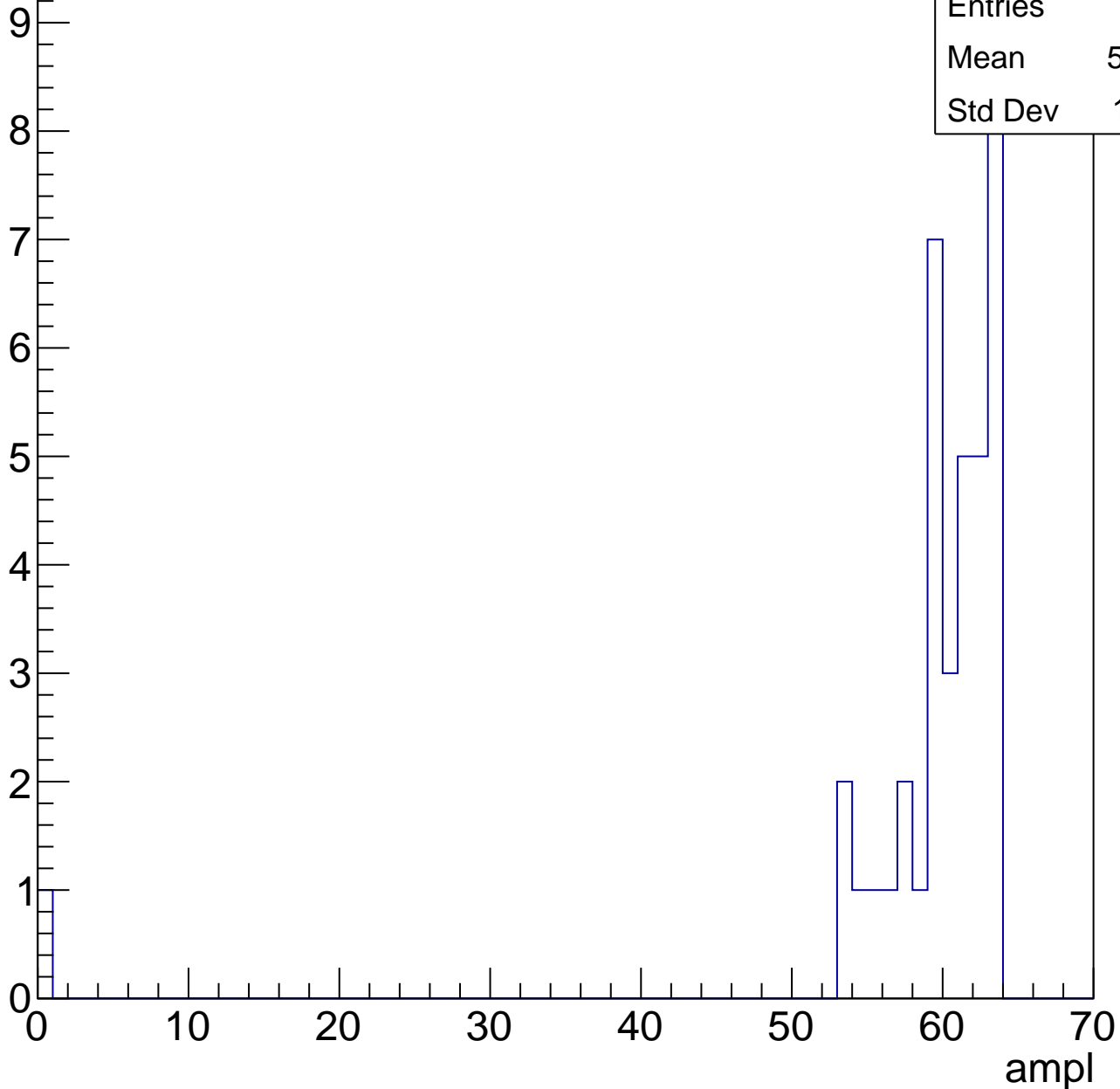


B1L101S, U15-ch7, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

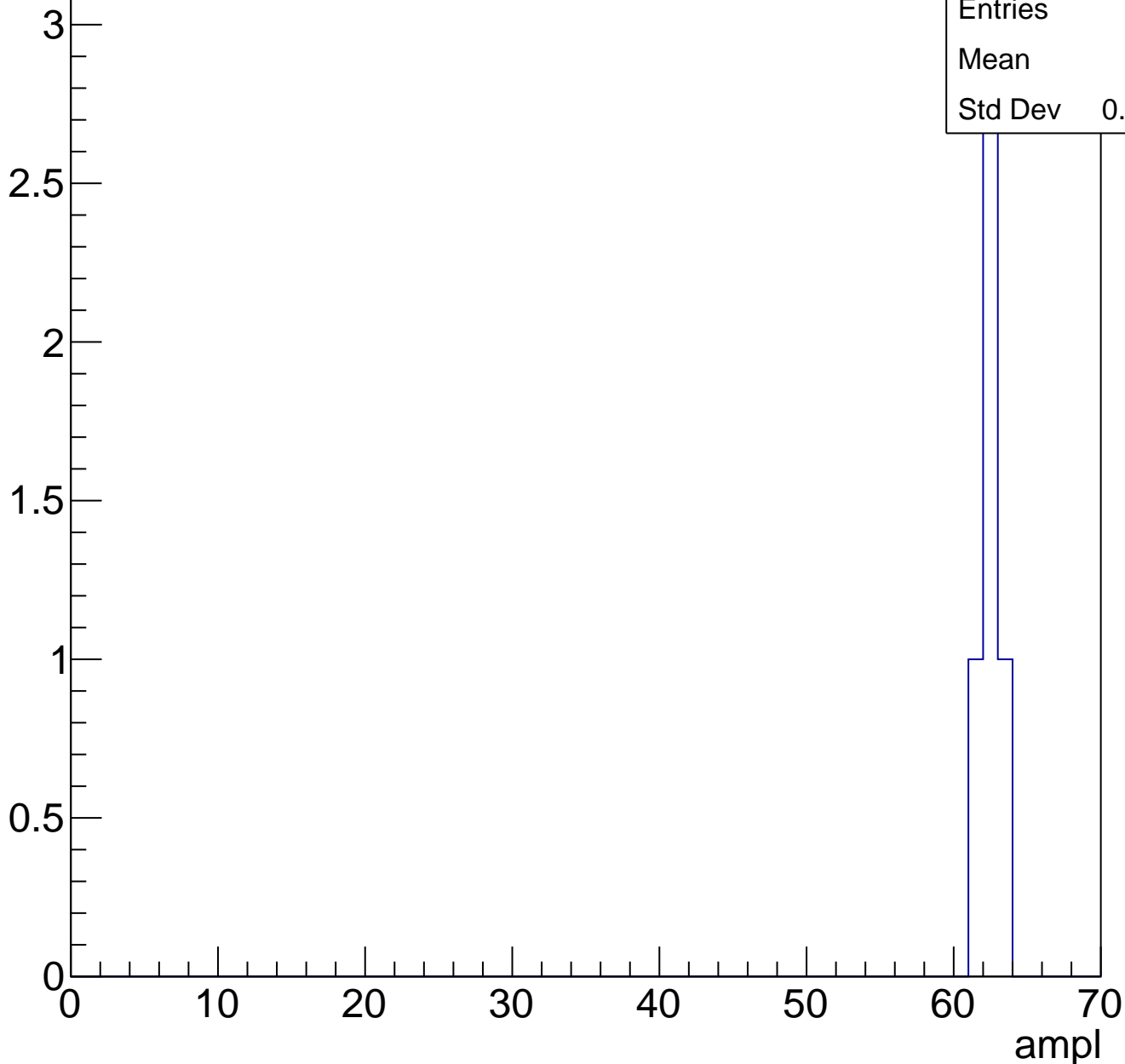
Entries	38
Mean	58.37
Std Dev	10.01



B1L101S, U15-ch7, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch7, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch8, adc0

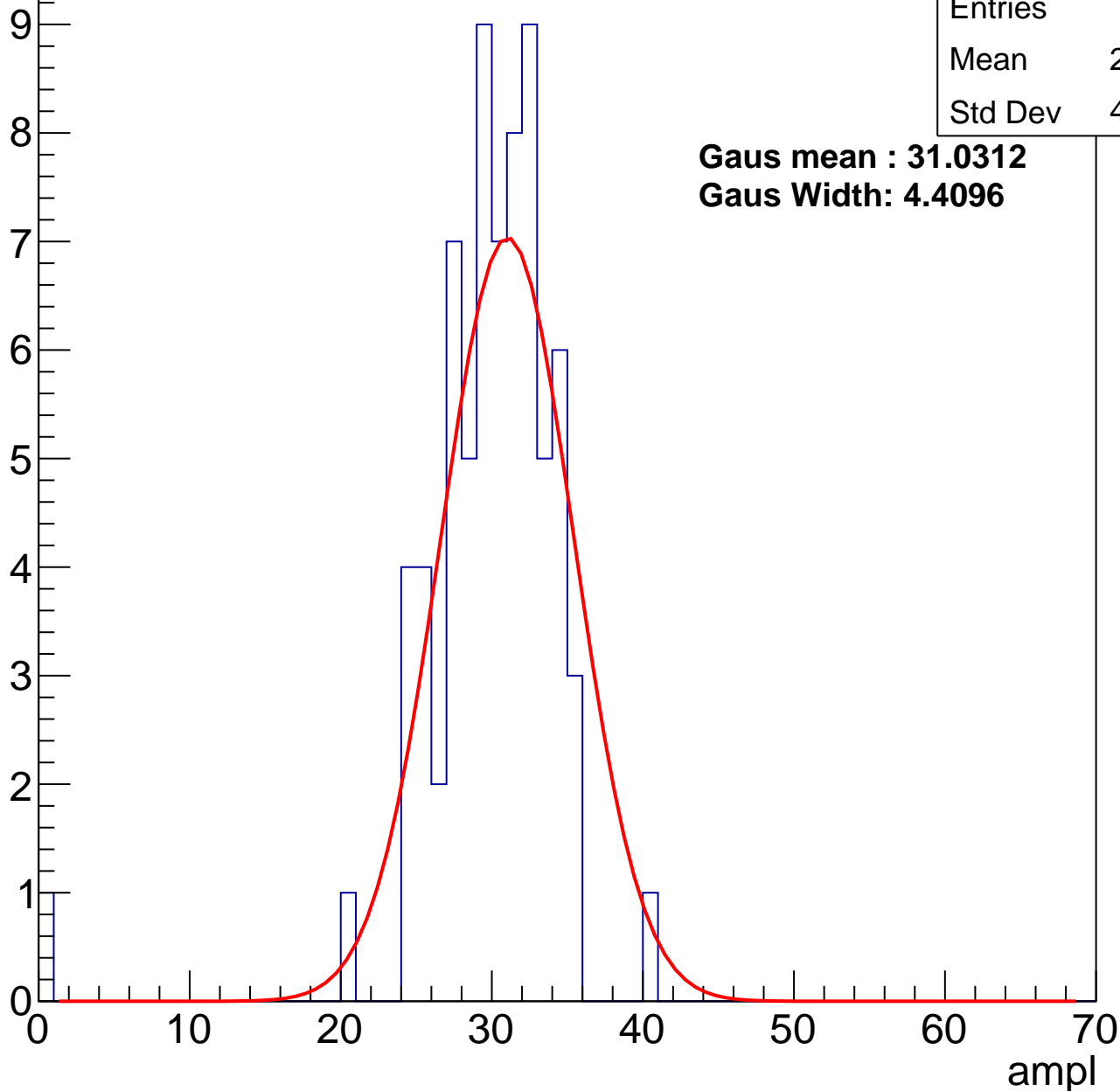
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.42
Std Dev	4.864

Gaus mean : 31.0312

Gaus Width: 4.4096



B1L101S, U15-ch8, adc1

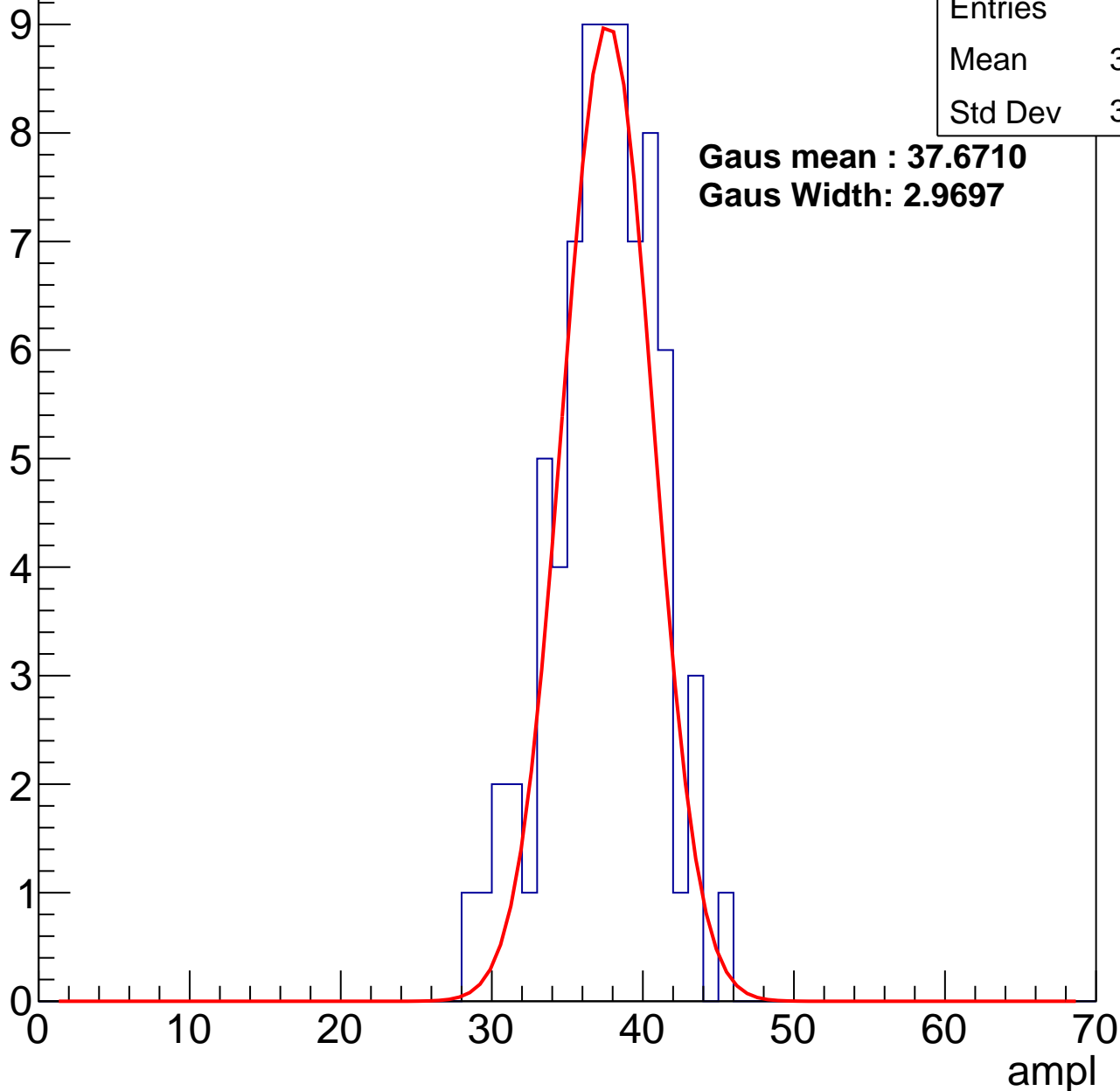
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	36.99
Std Dev	3.432

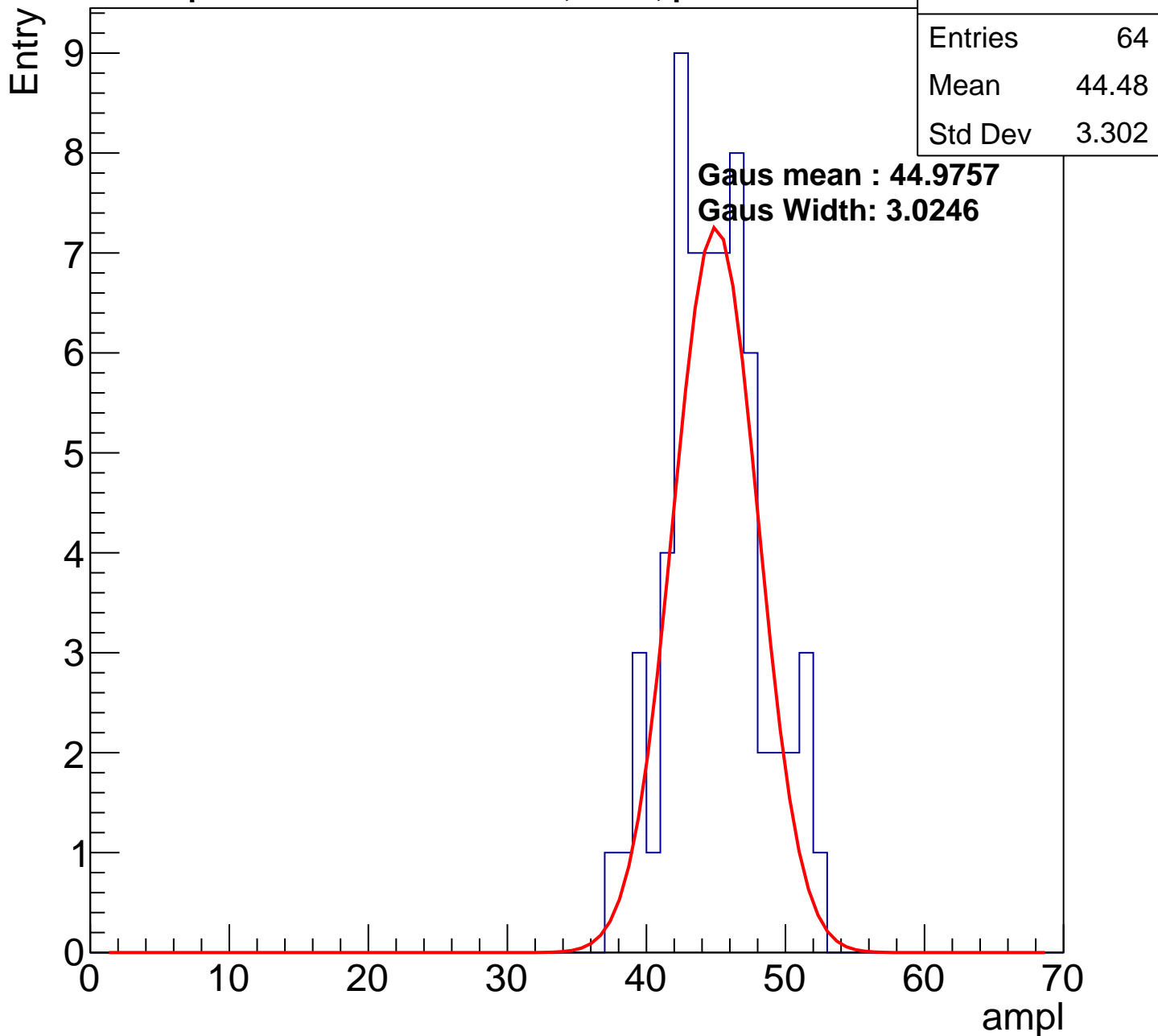
Gaus mean : 37.6710

Gaus Width: 2.9697



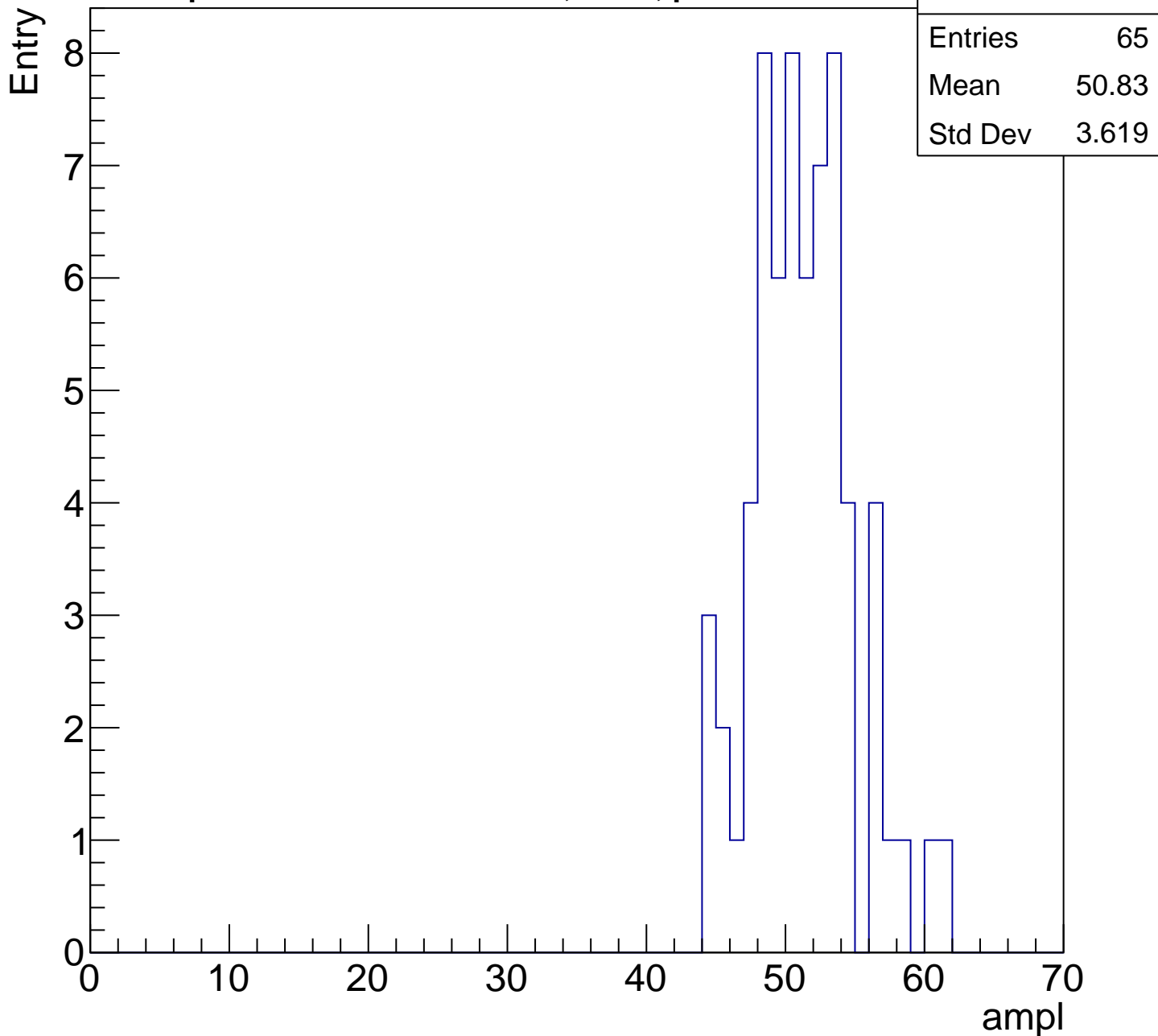
B1L101S, U15-ch8, adc2

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch8, adc3

calib_packv5_042523_0143.root, FC#0, port D2

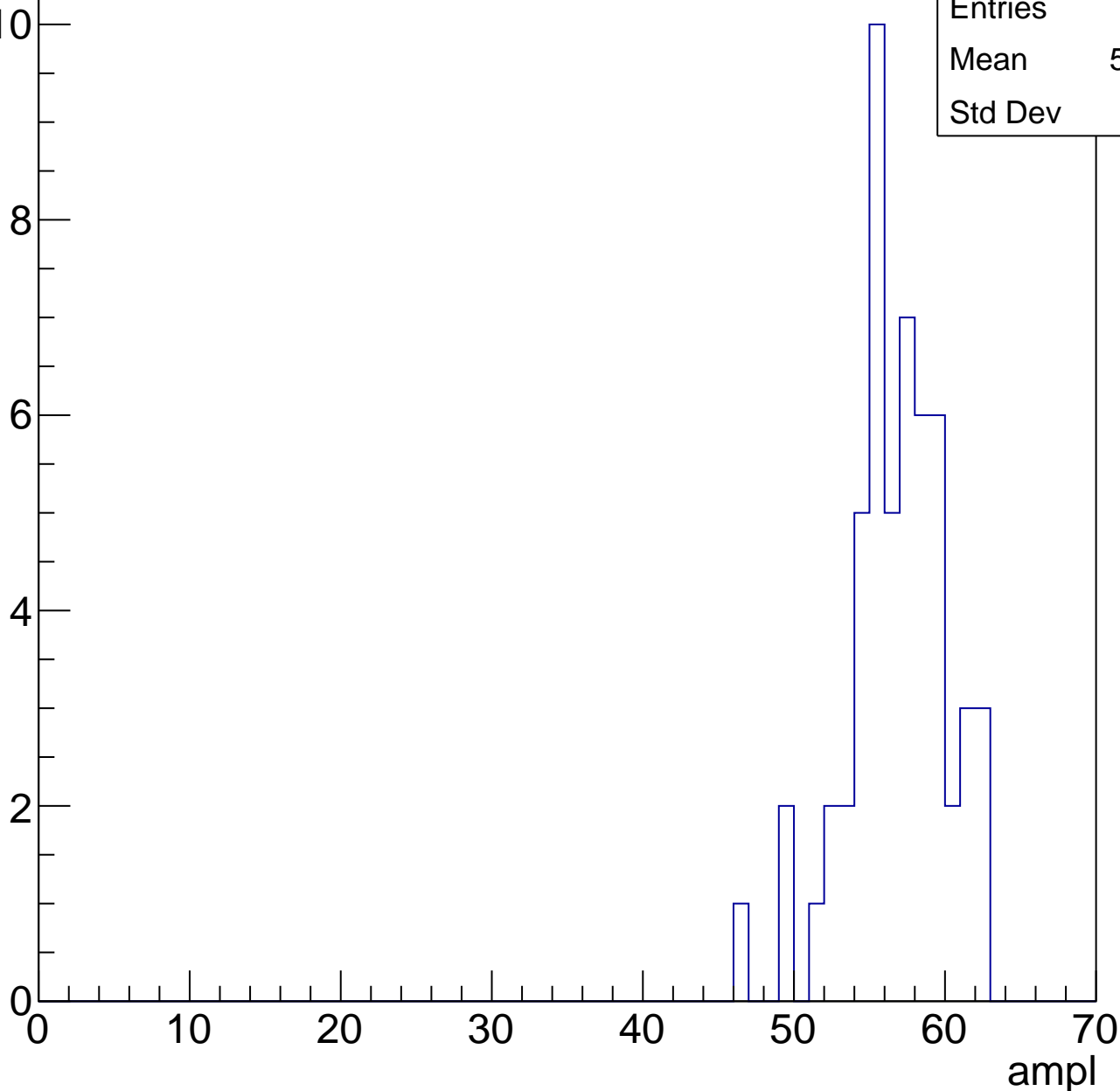


B1L101S, U15-ch8, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	56.27
Std Dev	3.3

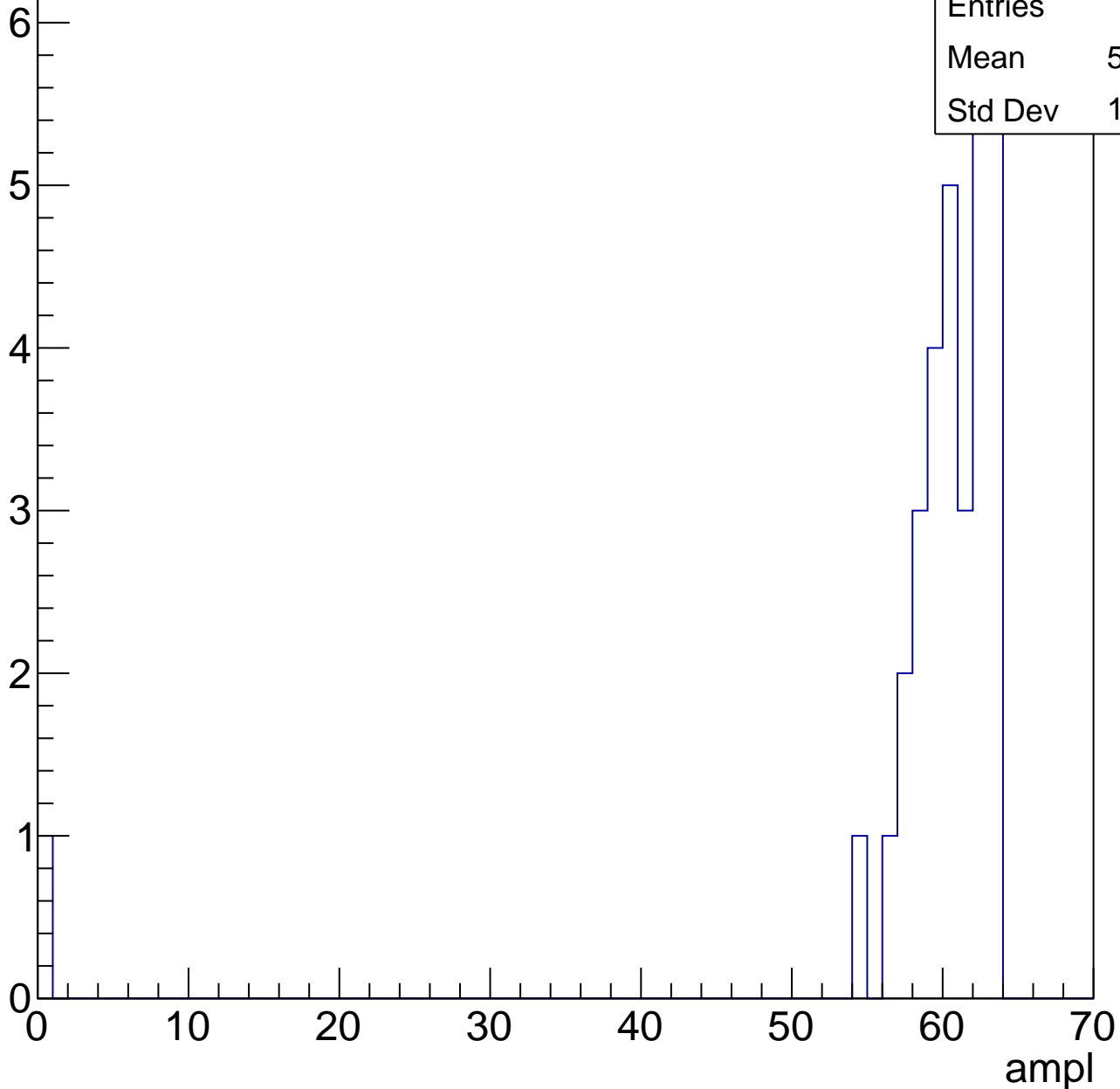


B1L101S, U15-ch8, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

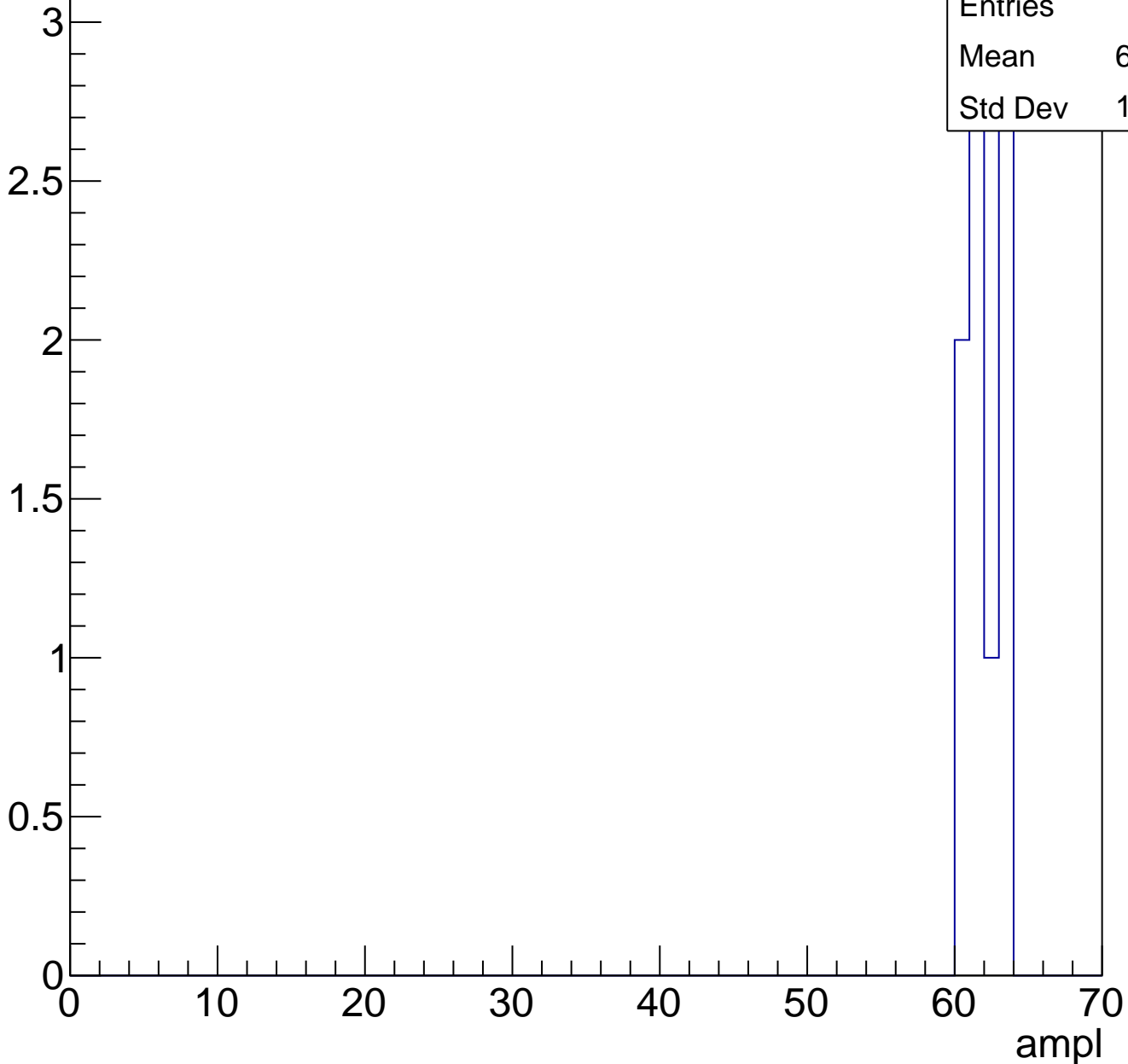
Entries	32
Mean	58.34
Std Dev	10.72



B1L101S, U15-ch8, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch8, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch9, adc0

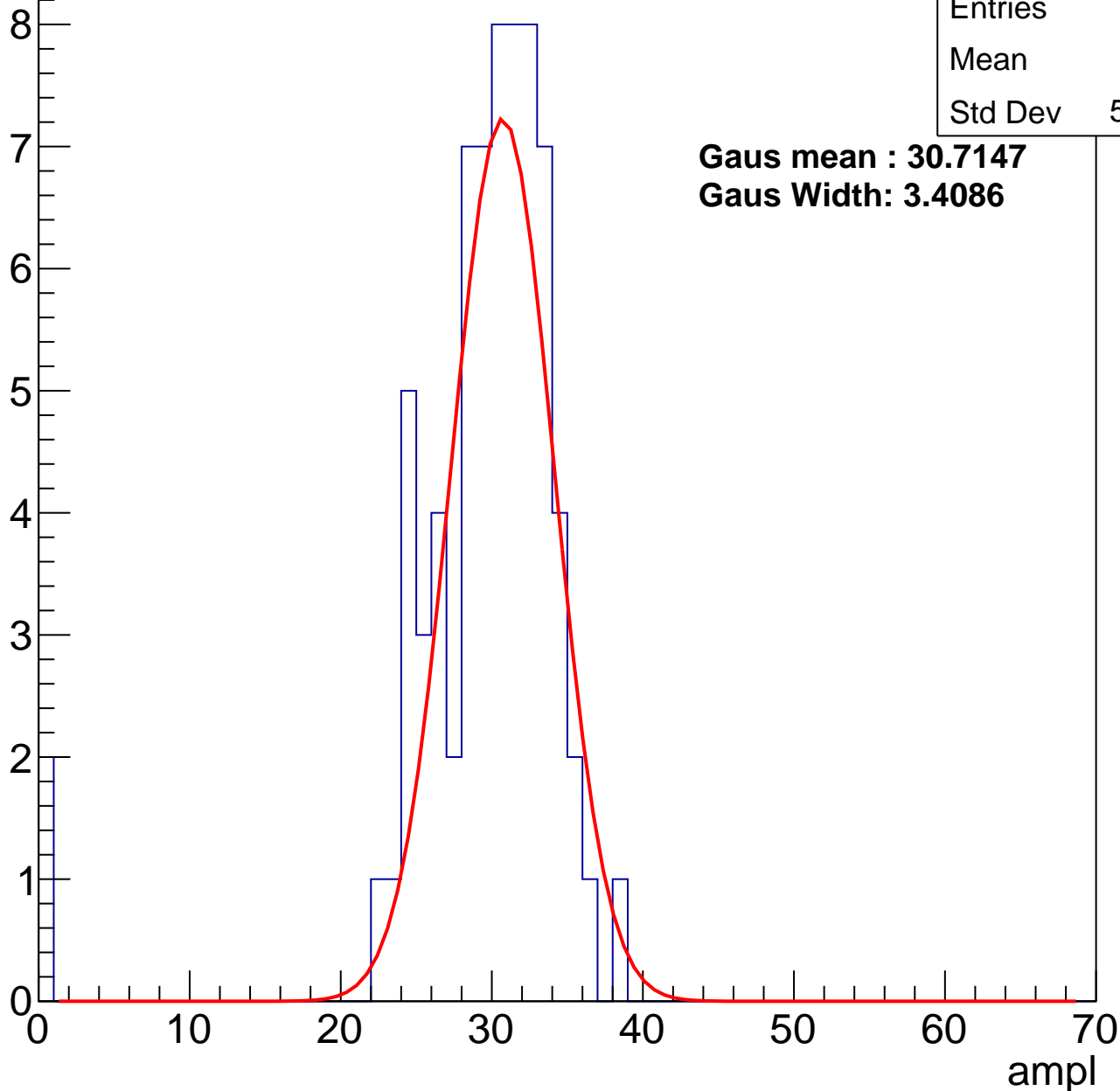
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	28.9
Std Dev	5.953

Gaus mean : 30.7147

Gaus Width: 3.4086



B1L101S, U15-ch9, adc1

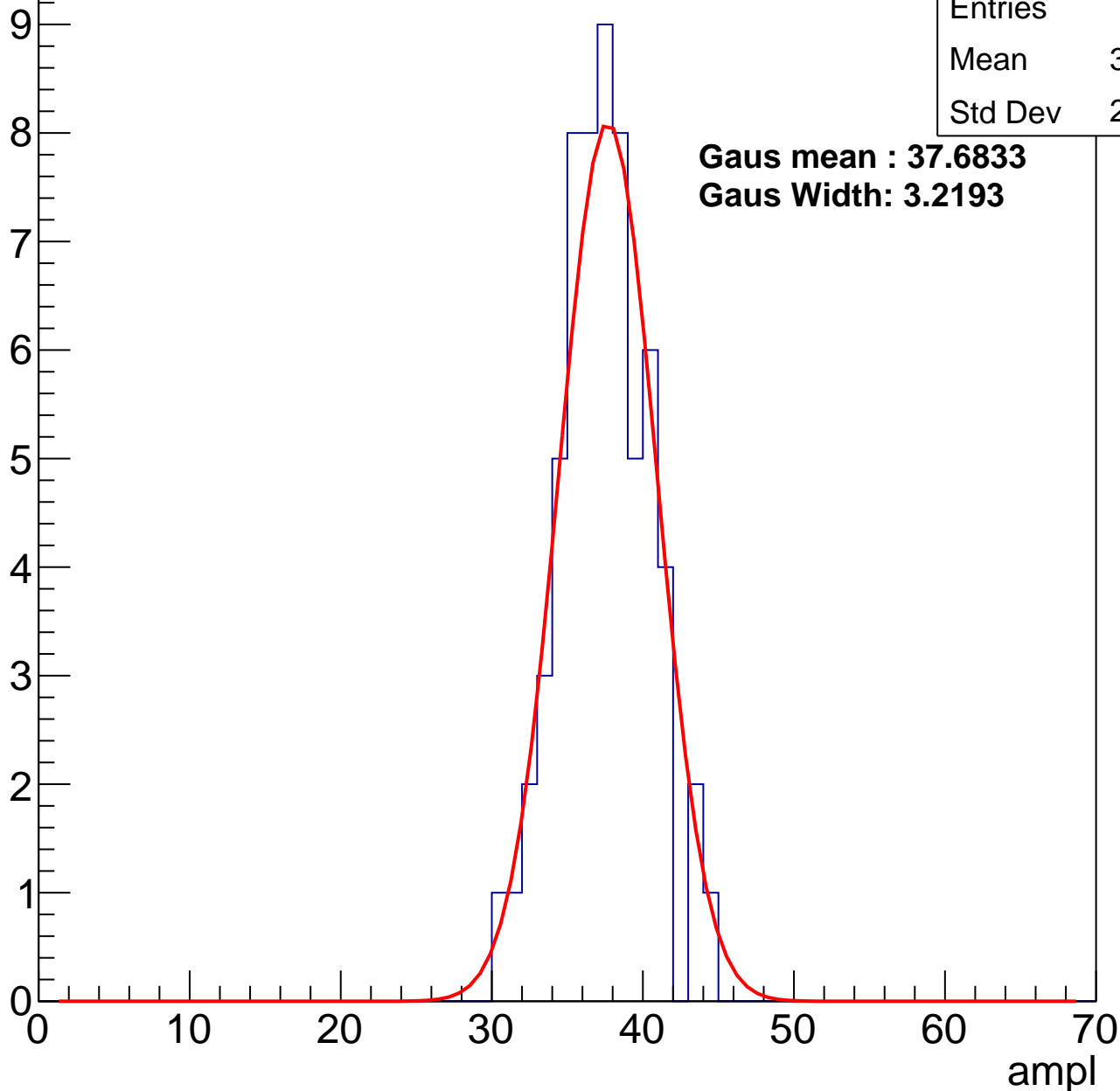
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.95
Std Dev	2.914

Gaus mean : 37.6833

Gaus Width: 3.2193



B1L101S, U15-ch9, adc2

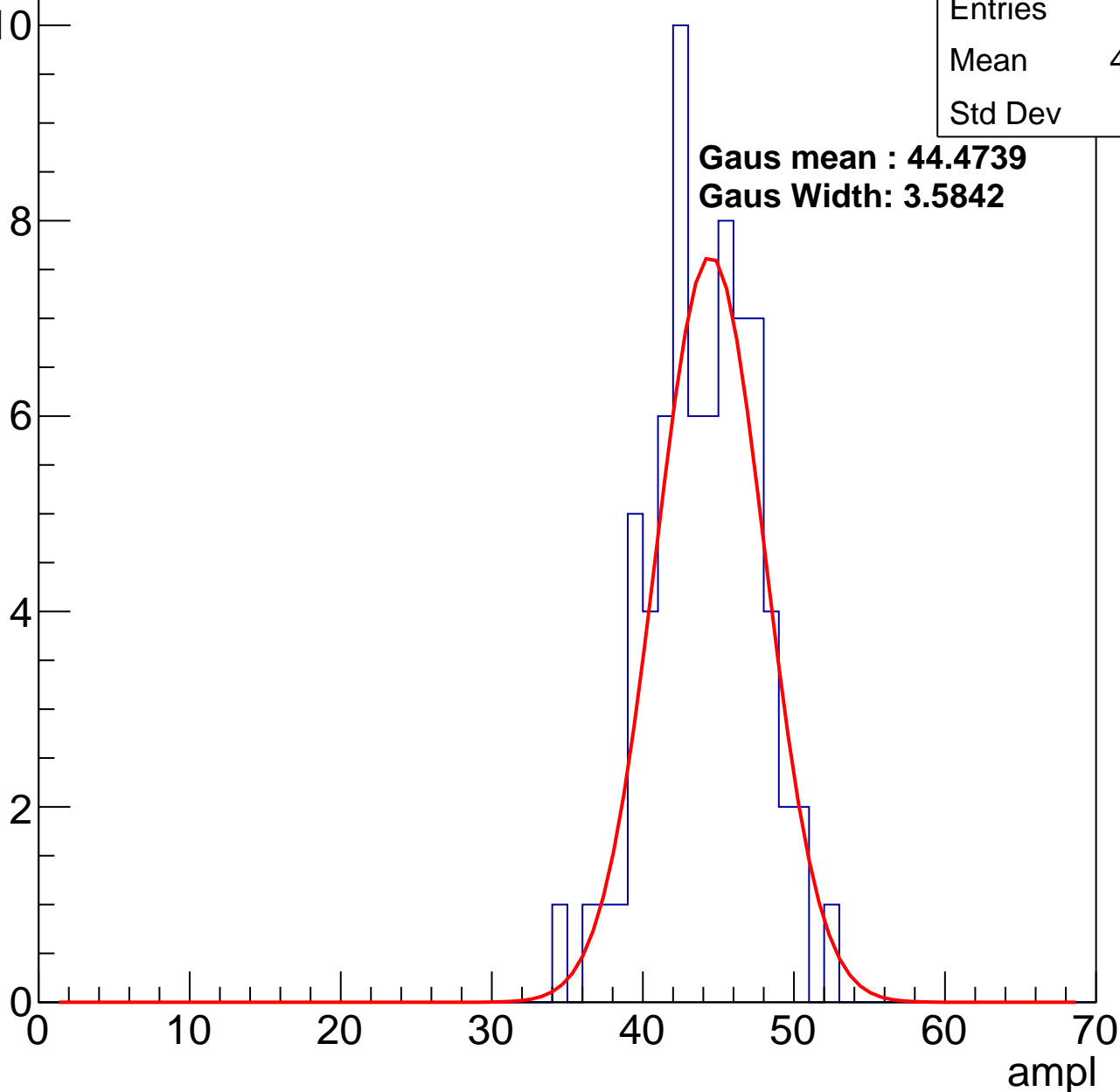
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	43.62
Std Dev	3.49

Gaus mean : 44.4739

Gaus Width: 3.5842

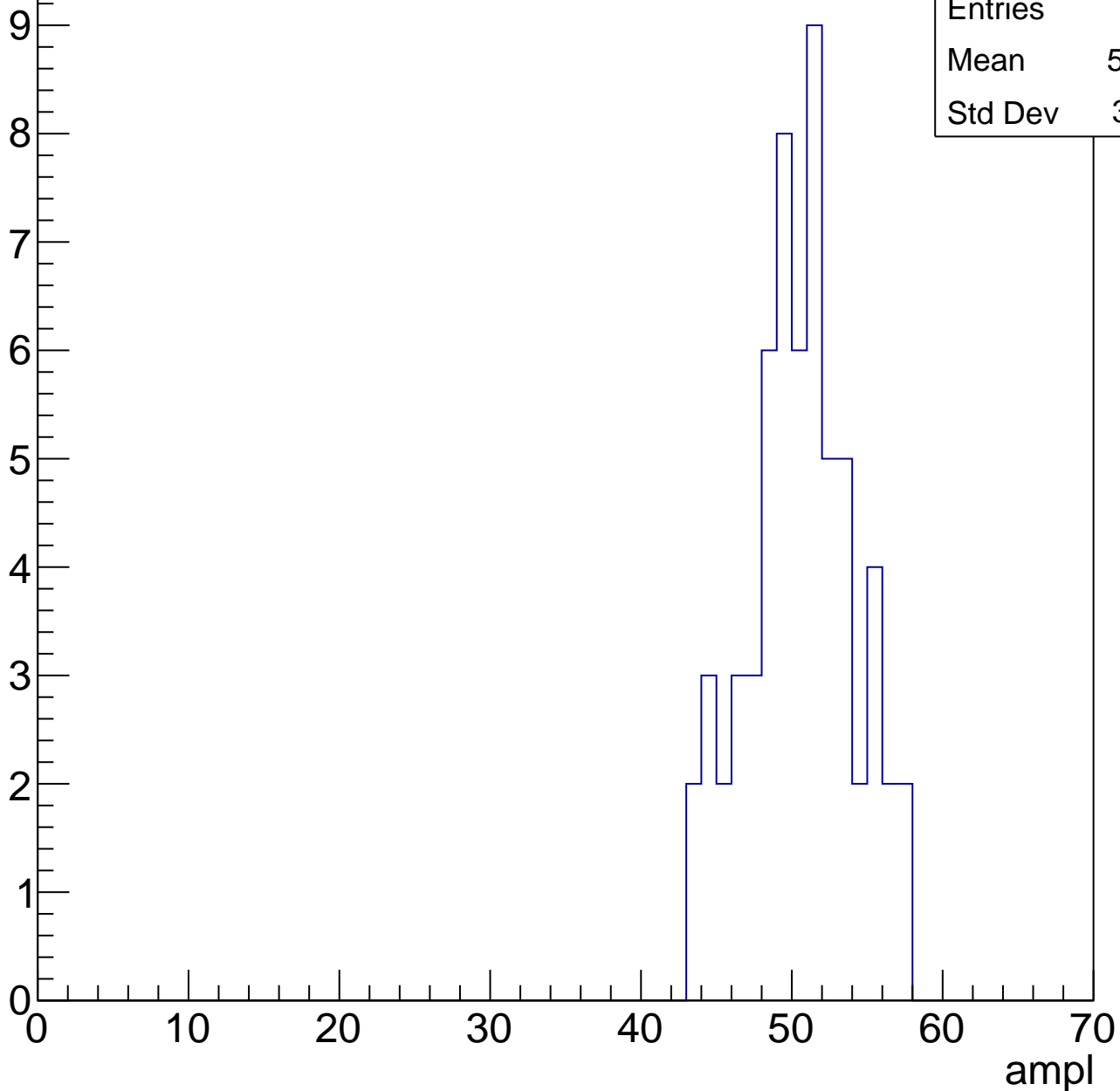


B1L101S, U15-ch9, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	50.08
Std Dev	3.451

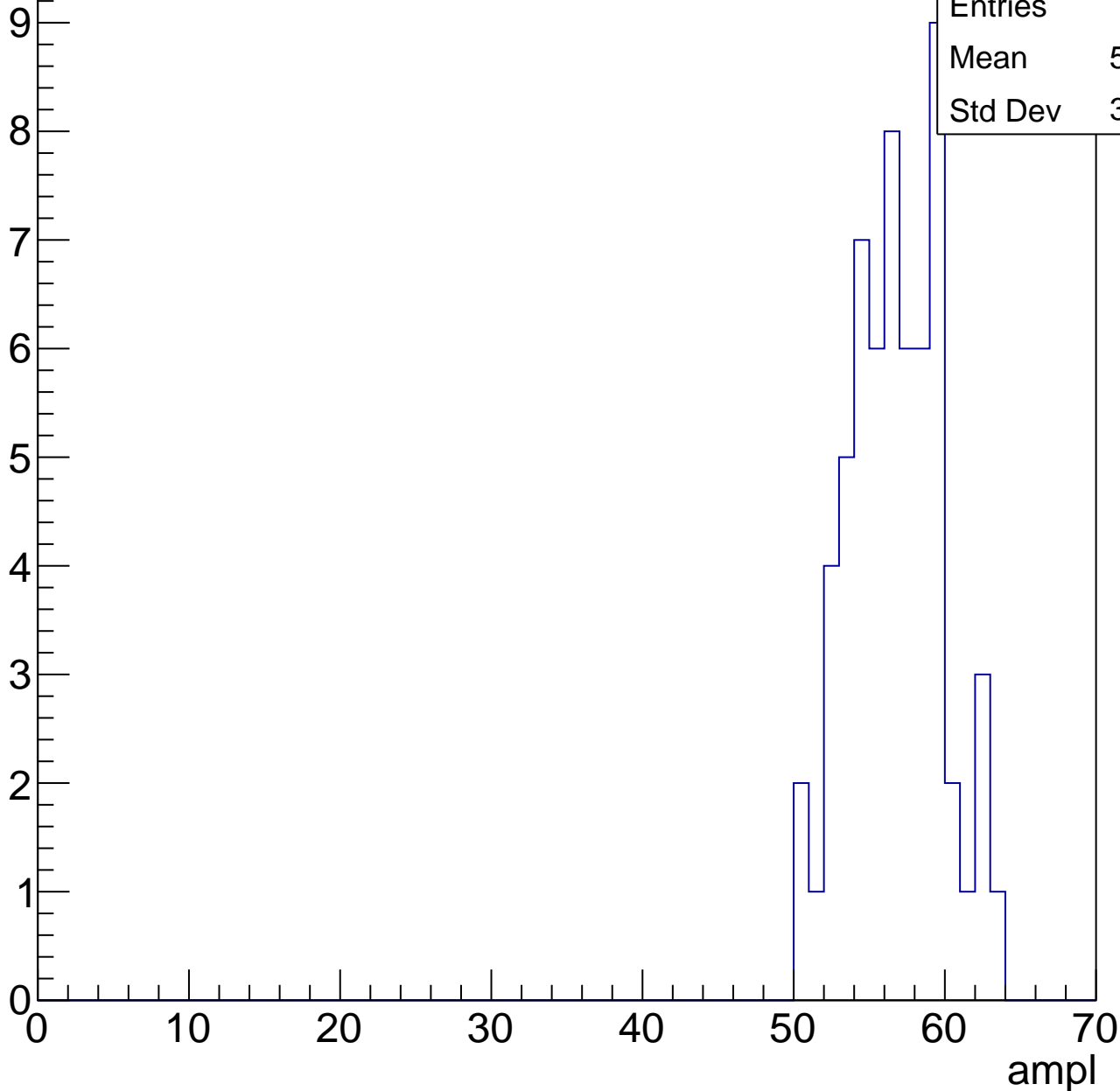


B1L101S, U15-ch9, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	56.25
Std Dev	3.033

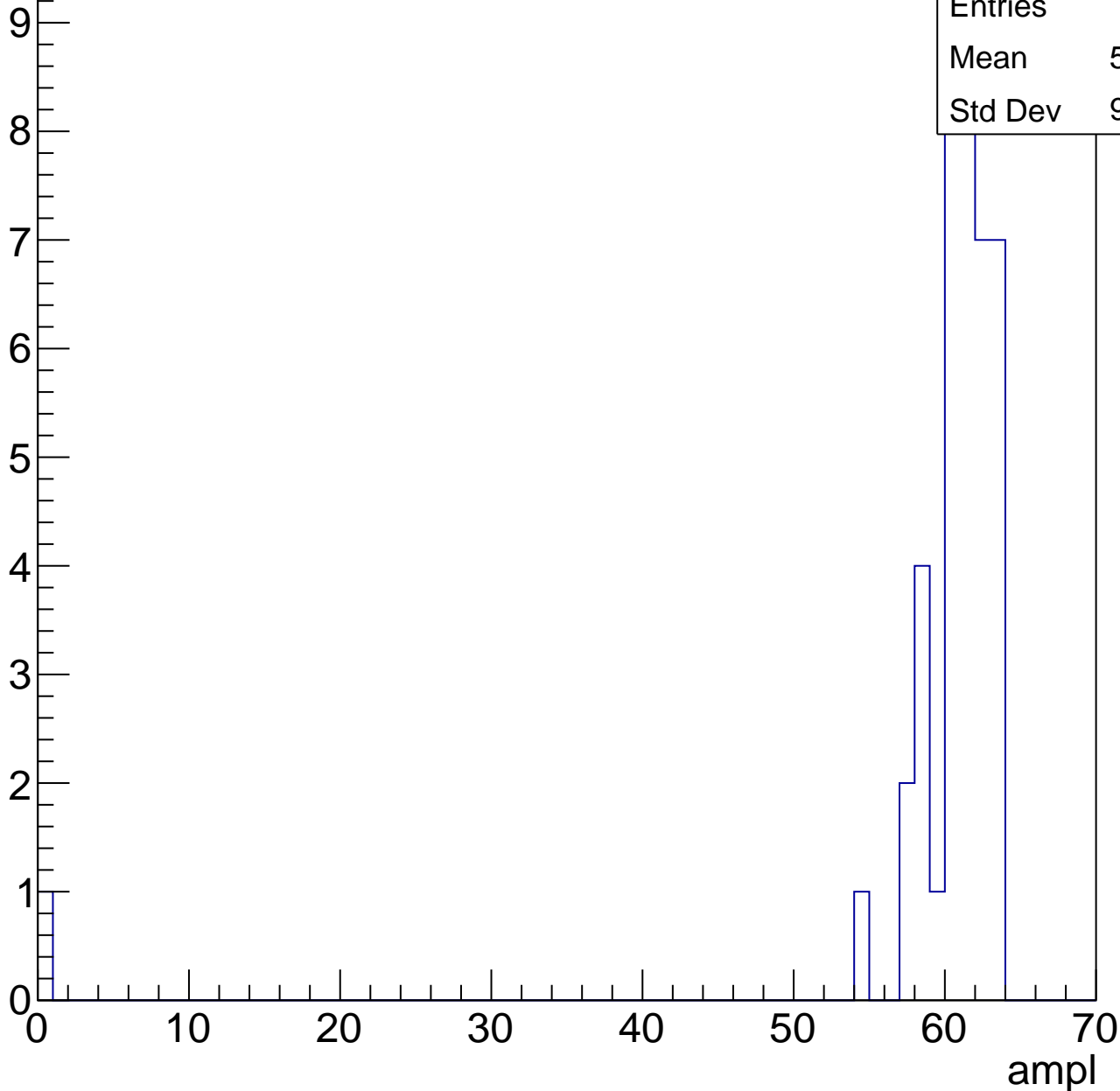


B1L101S, U15-ch9, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	59.08
Std Dev	9.665



B1L101S, U15-ch9, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L101S, U15-ch9, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch10, adc0

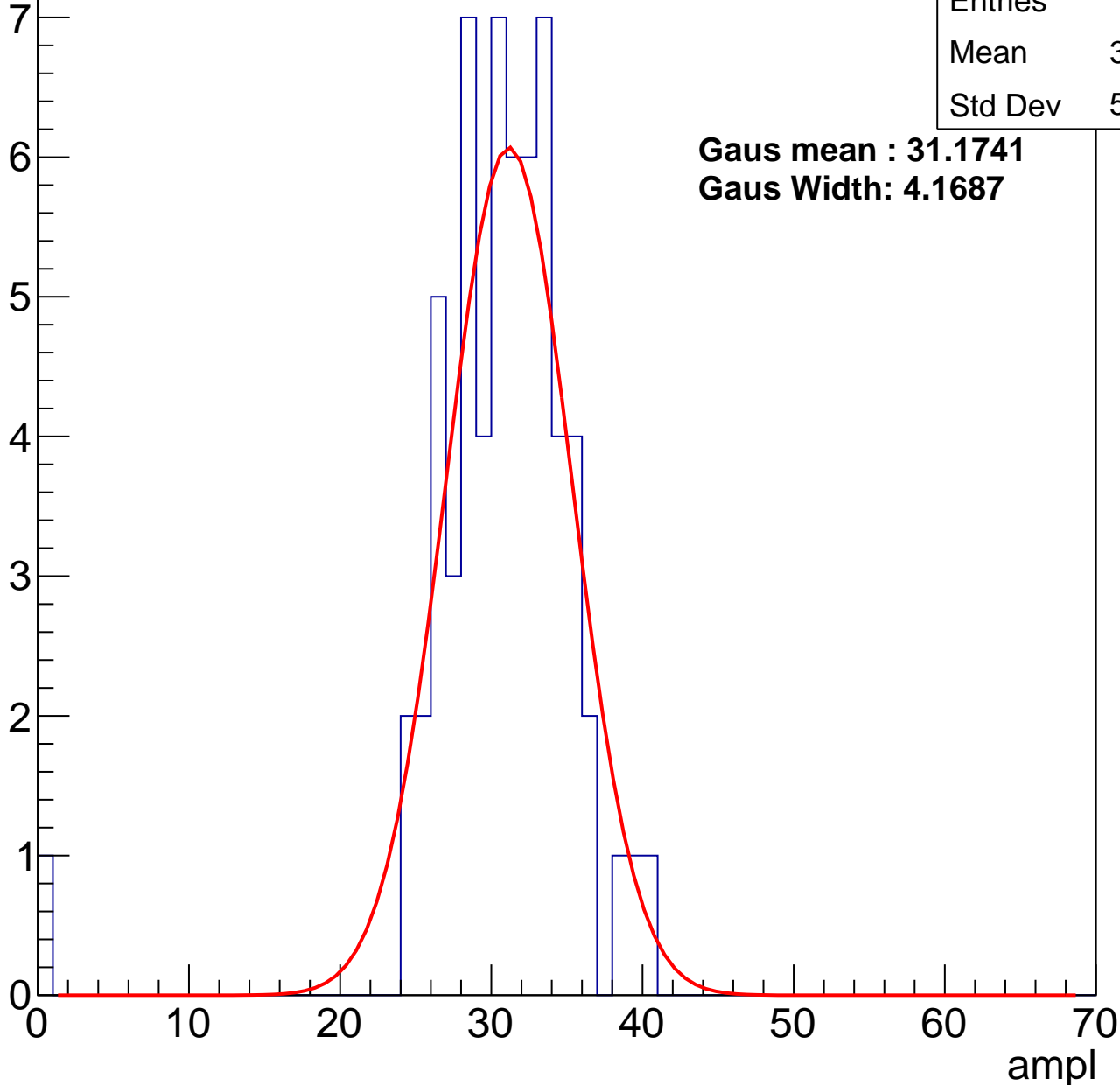
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.24
Std Dev	5.242

Gaus mean : 31.1741

Gaus Width: 4.1687



B1L101S, U15-ch10, adc1

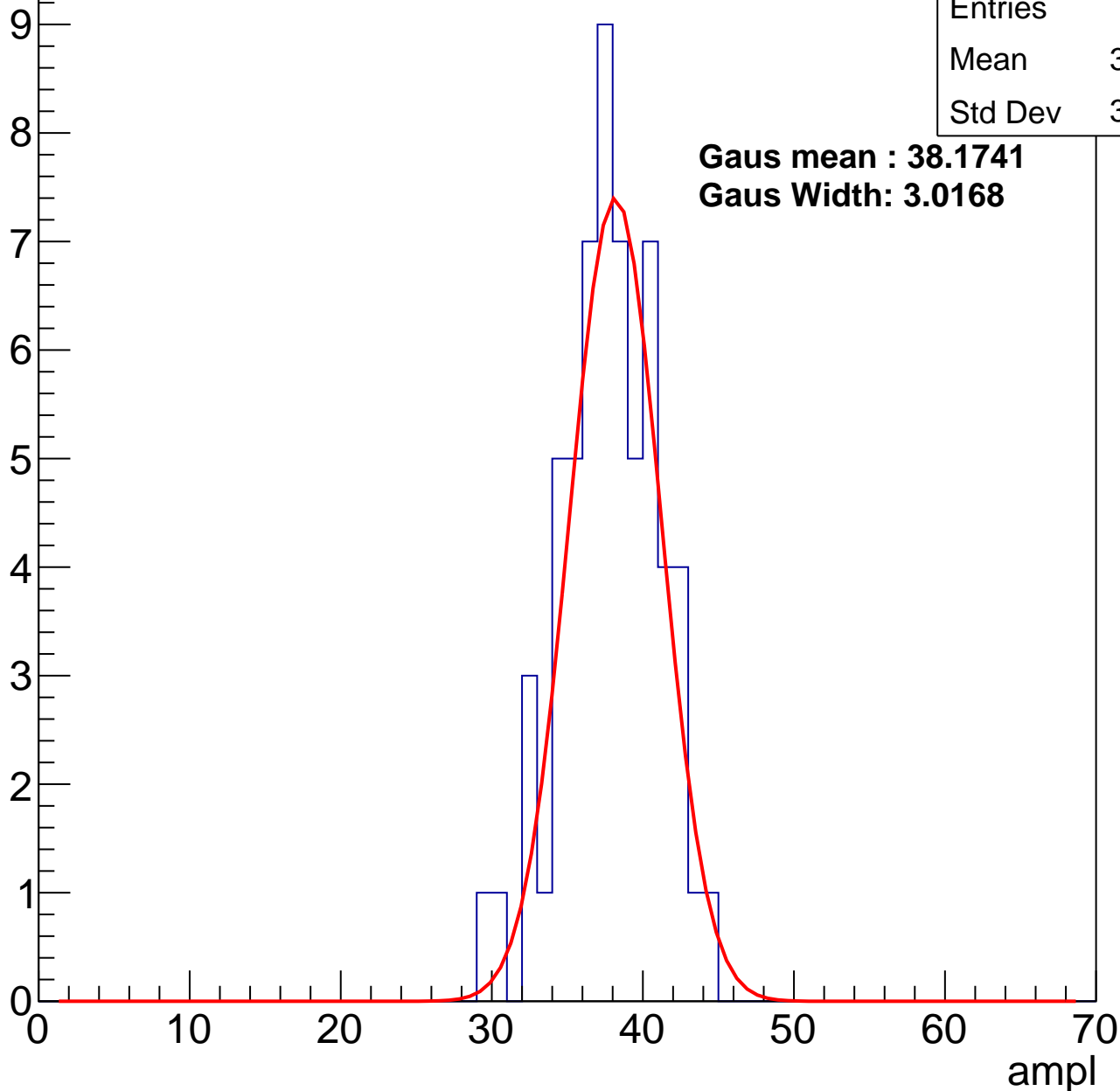
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.34
Std Dev	3.156

Gaus mean : 38.1741

Gaus Width: 3.0168



B1L101S, U15-ch10, adc2

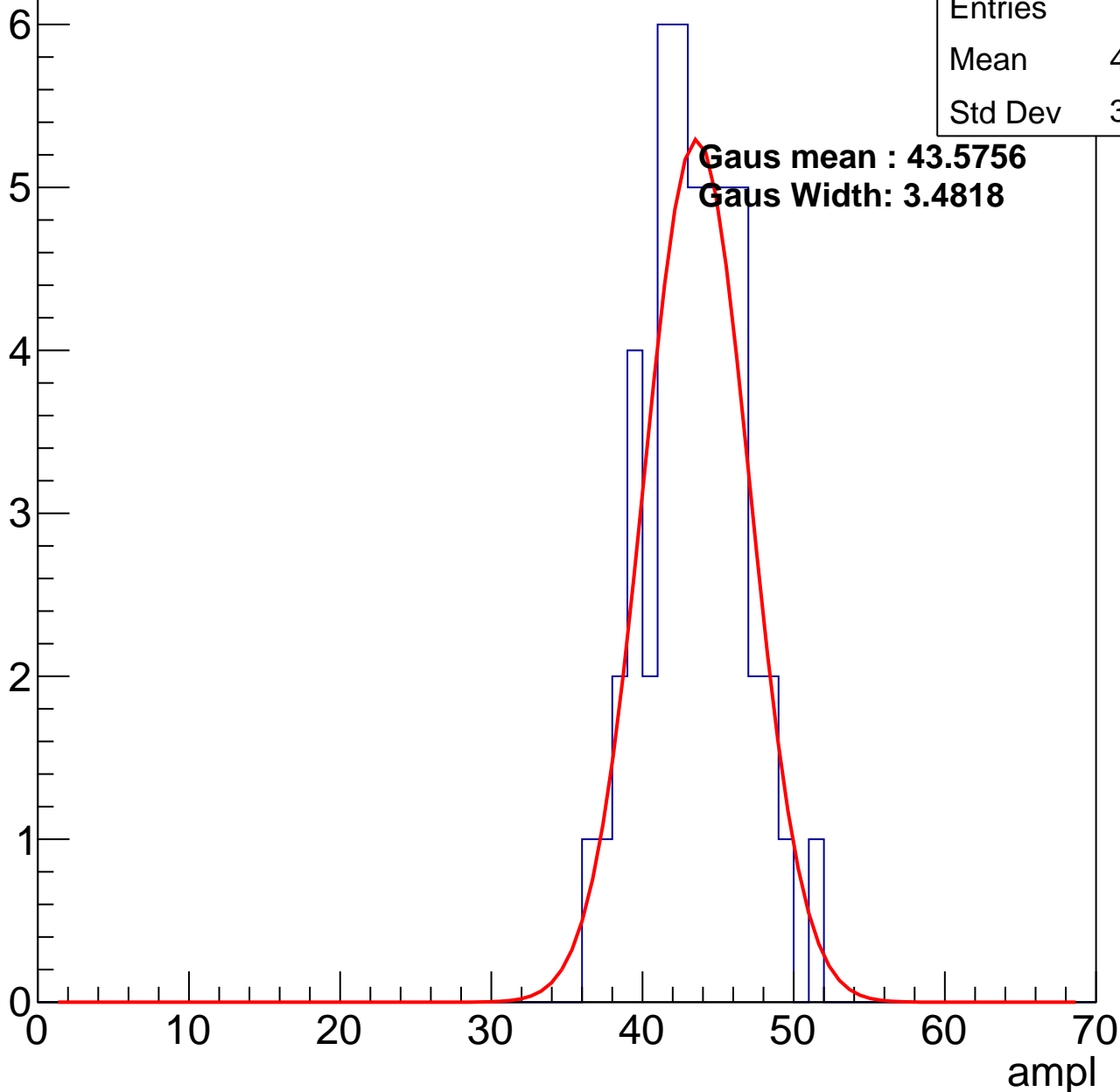
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	42.98
Std Dev	3.224

Gaus mean : 43.5756

Gaus Width: 3.4818



B1L101S, U15-ch10, adc3

calib_packv5_042523_0143.root, FC#0, port D2

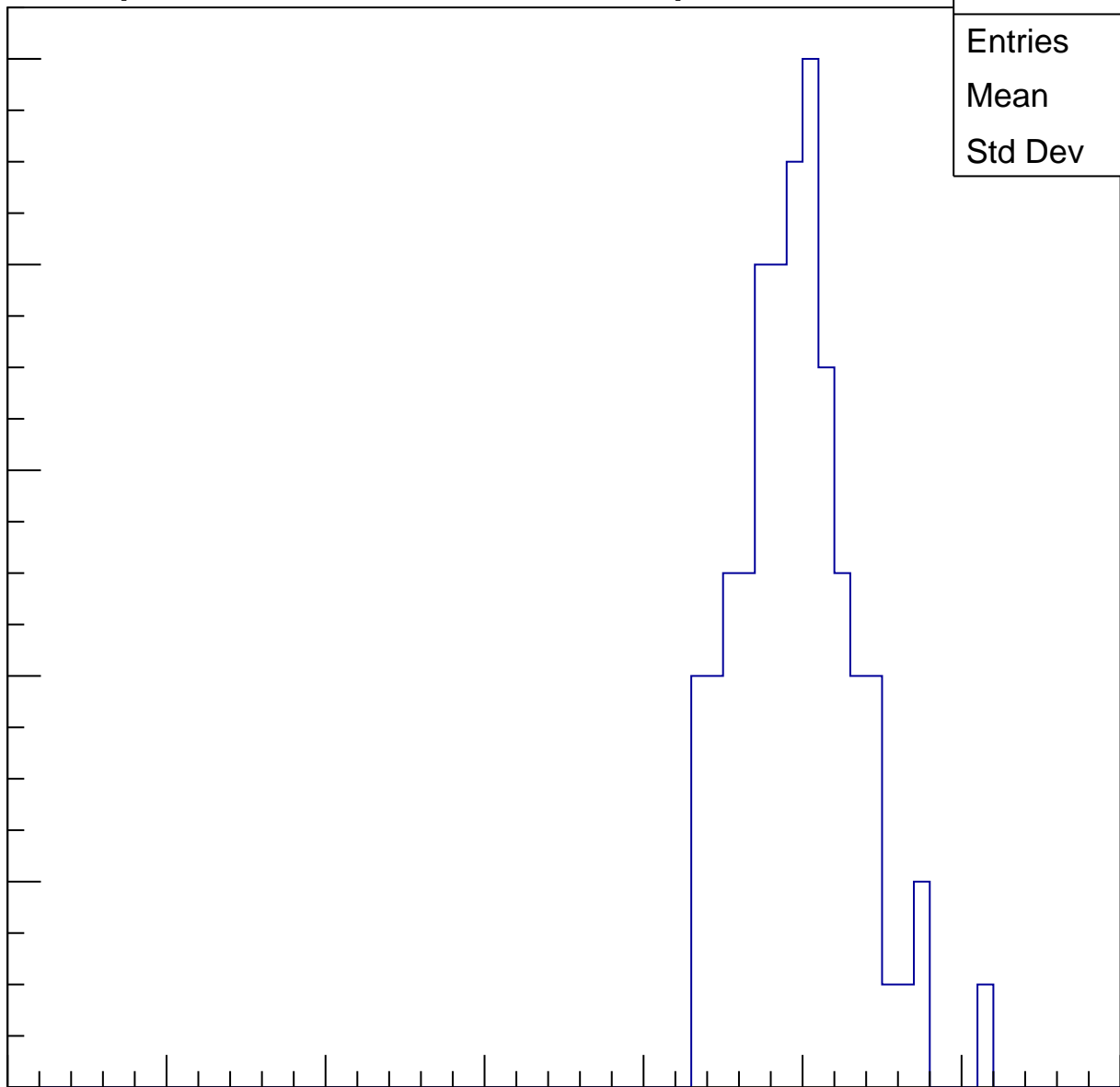
Entries	78
Mean	49.17
Std Dev	3.6

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

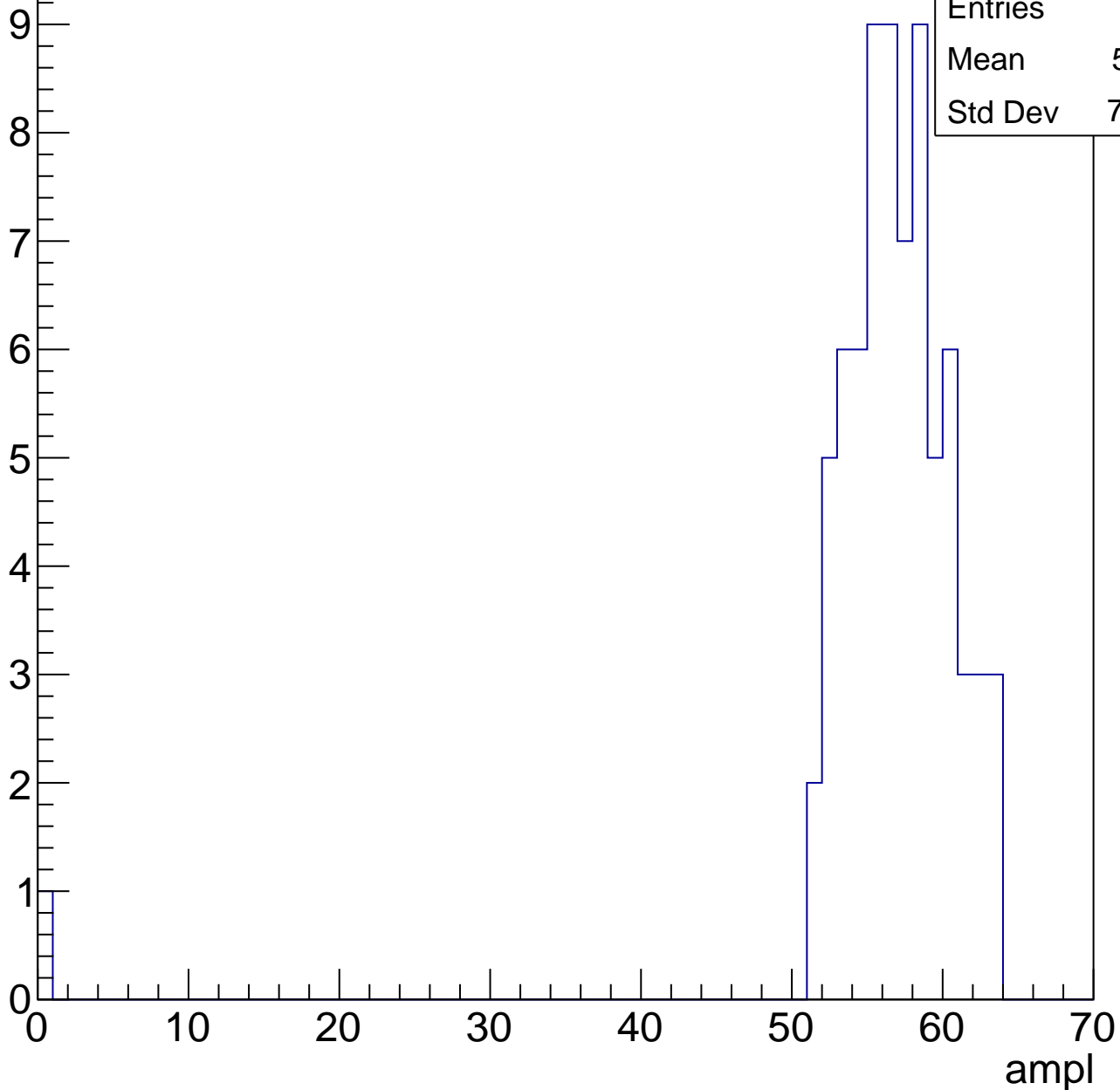


B1L101S, U15-ch10, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

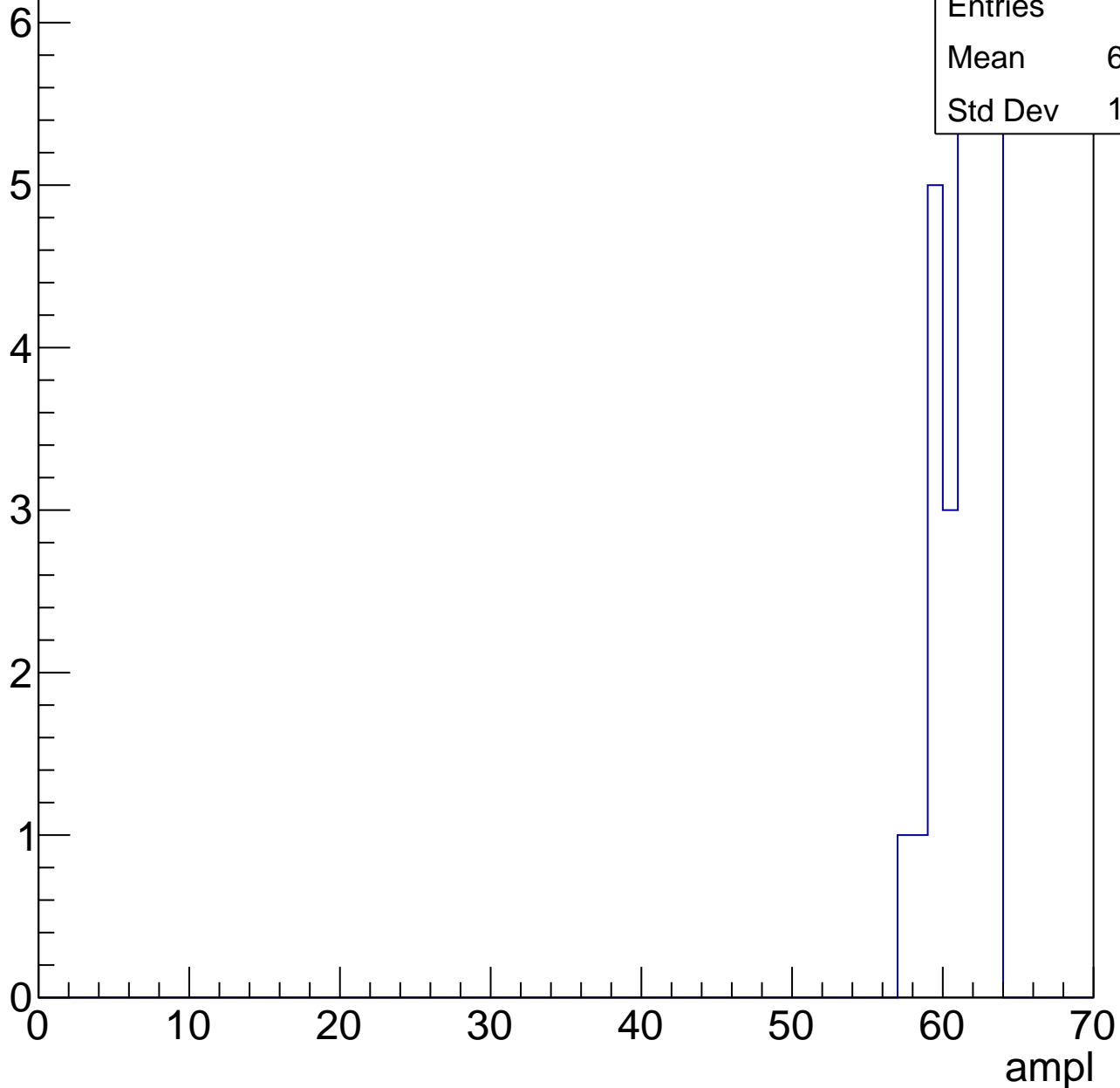
Entries	74
Mean	55.91
Std Dev	7.228



B1L101S, U15-ch10, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch10, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch10, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch11, adc0

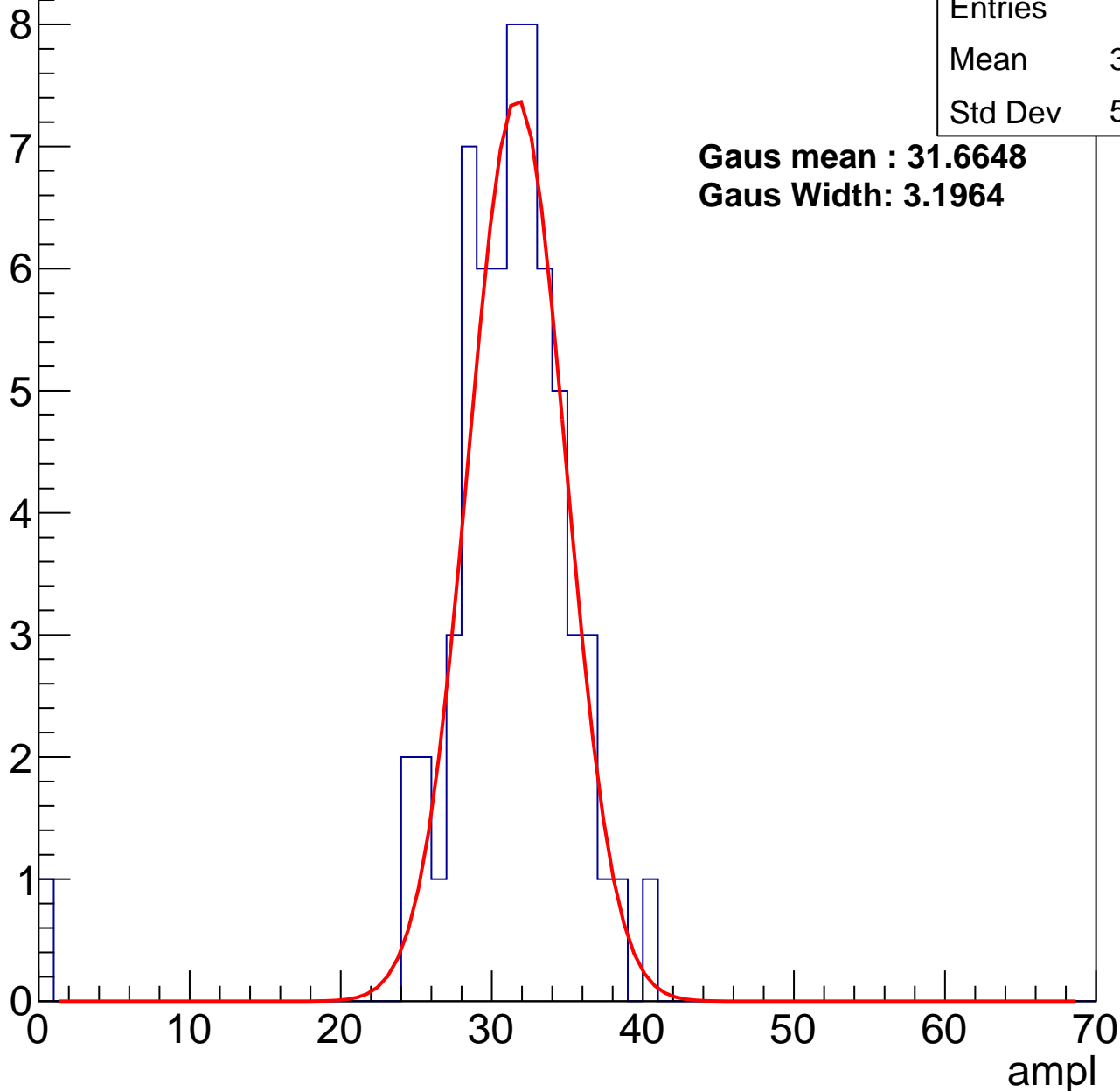
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	30.55
Std Dev	5.077

Gaus mean : 31.6648

Gaus Width: 3.1964



B1L101S, U15-ch11, adc1

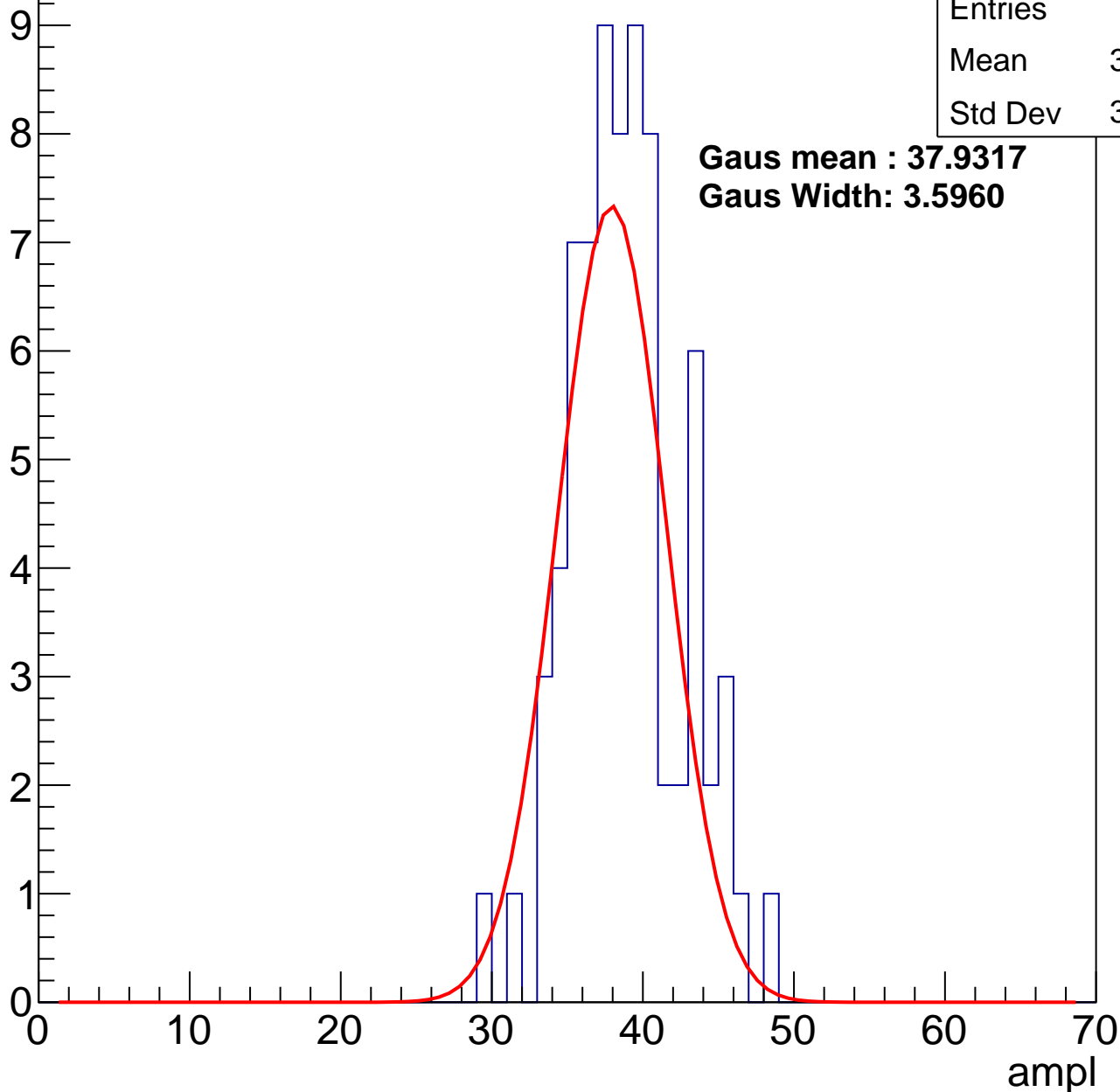
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	38.39
Std Dev	3.642

Gaus mean : 37.9317

Gaus Width: 3.5960



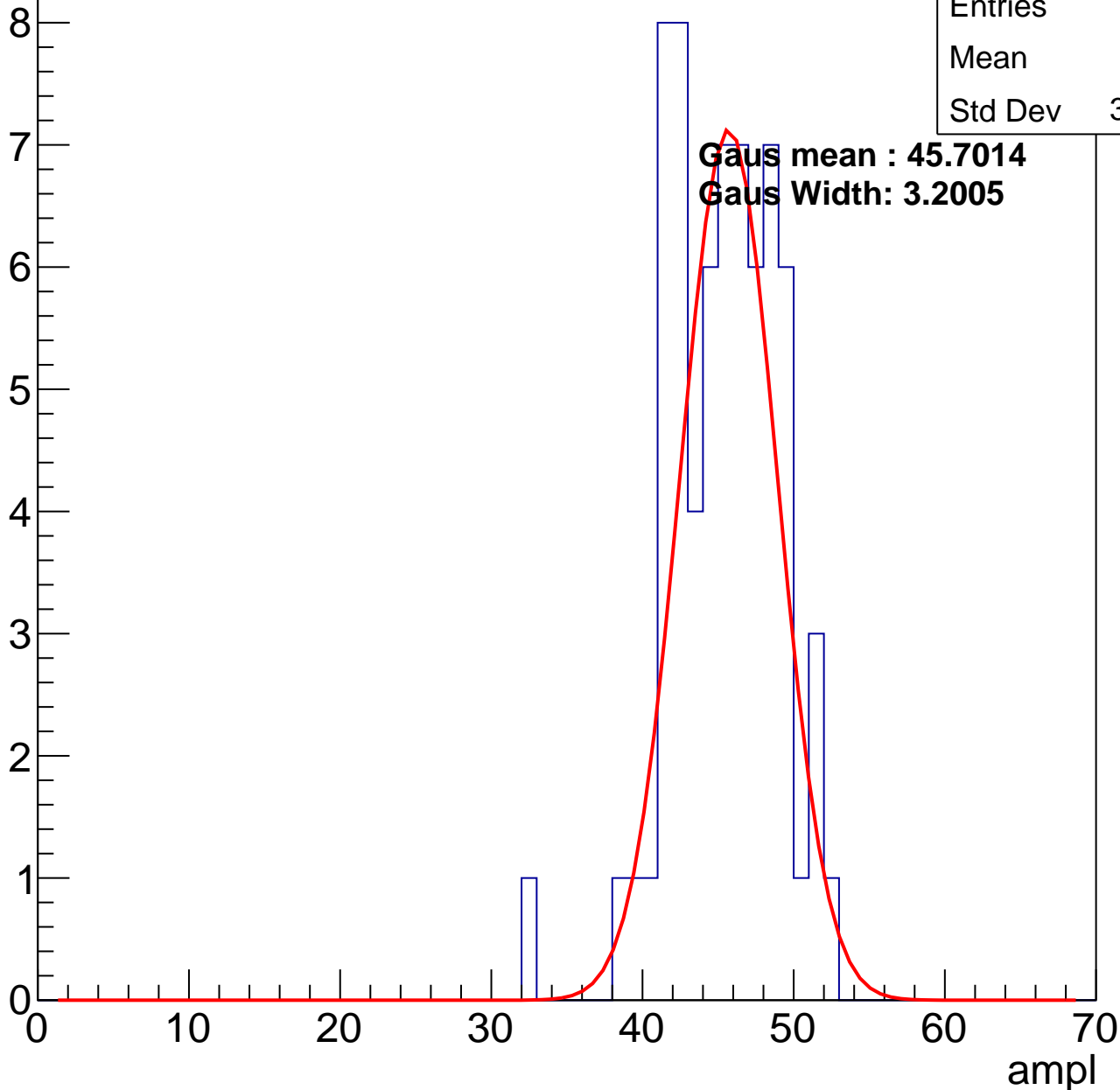
B1L101S, U15-ch11, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.9
Std Dev	3.582

Gaus mean : 45.7014
Gaus Width: 3.2005

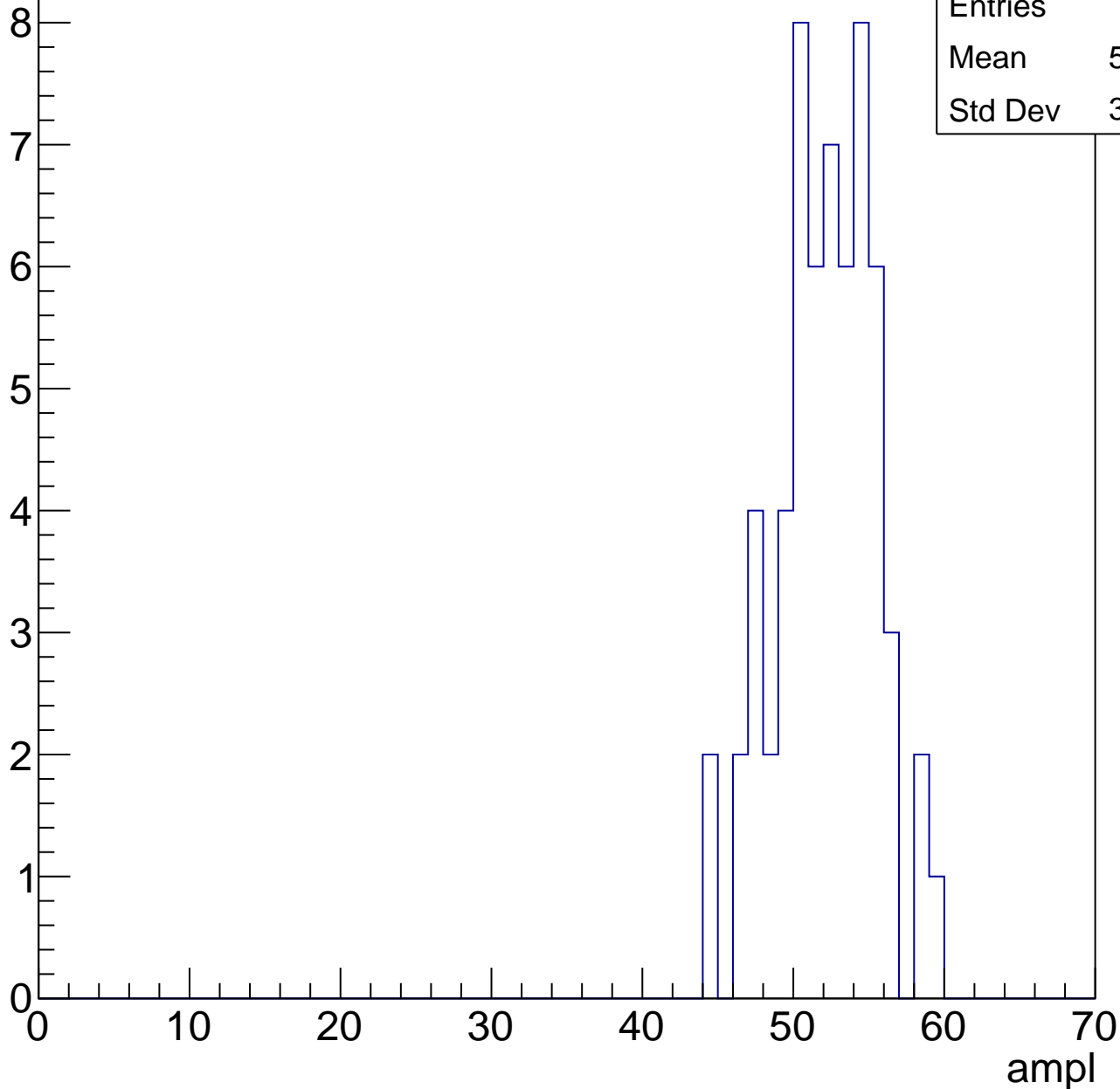


B1L101S, U15-ch11, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

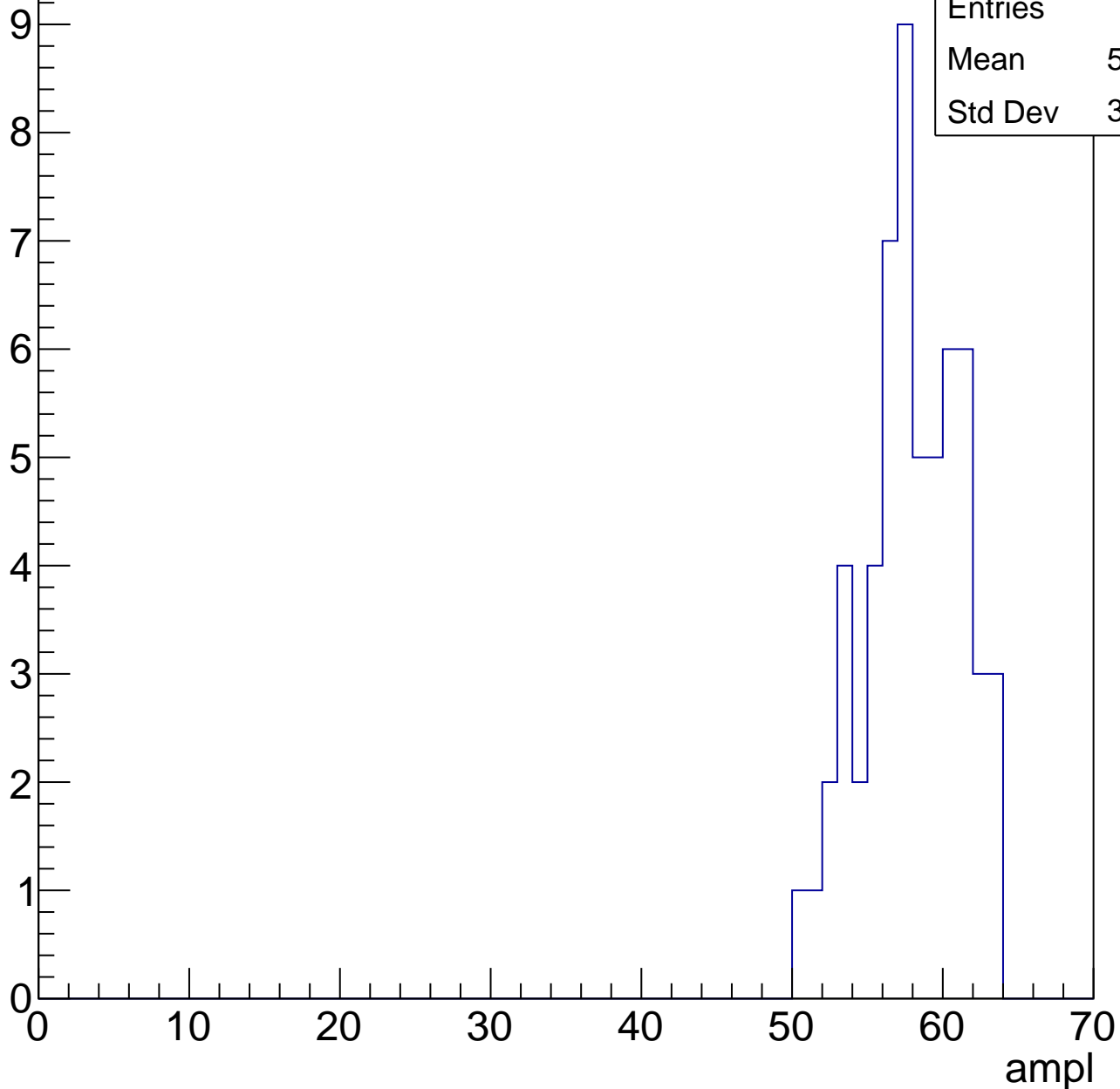
Entries	61
Mean	51.69
Std Dev	3.292



B1L101S, U15-ch11, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

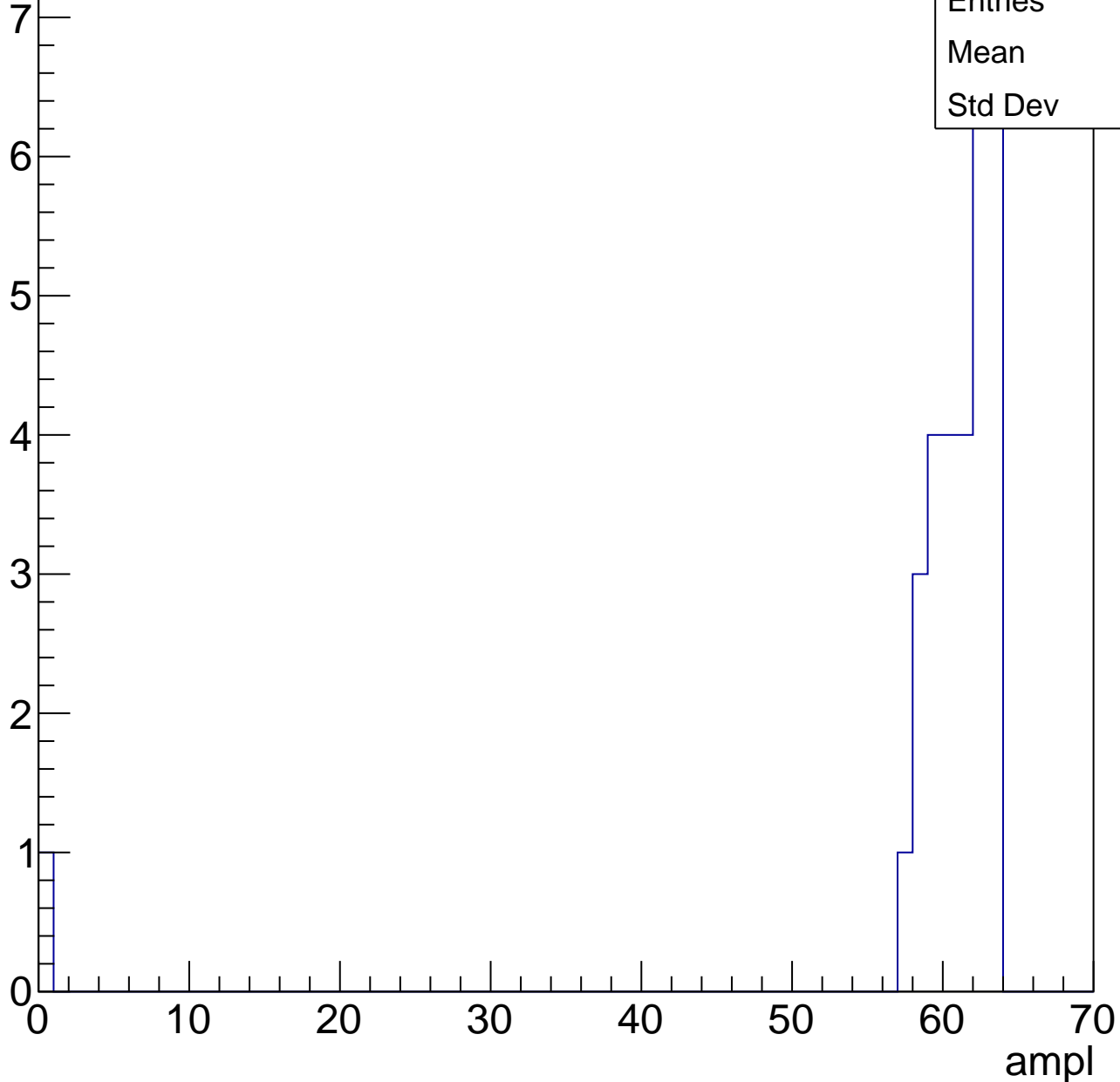


Entries	58
Mean	57.52
Std Dev	3.169

B1L101S, U15-ch11, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	31
Mean	58.9
Std Dev	10.9

B1L101S, U15-ch11, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch11, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch12, adc0

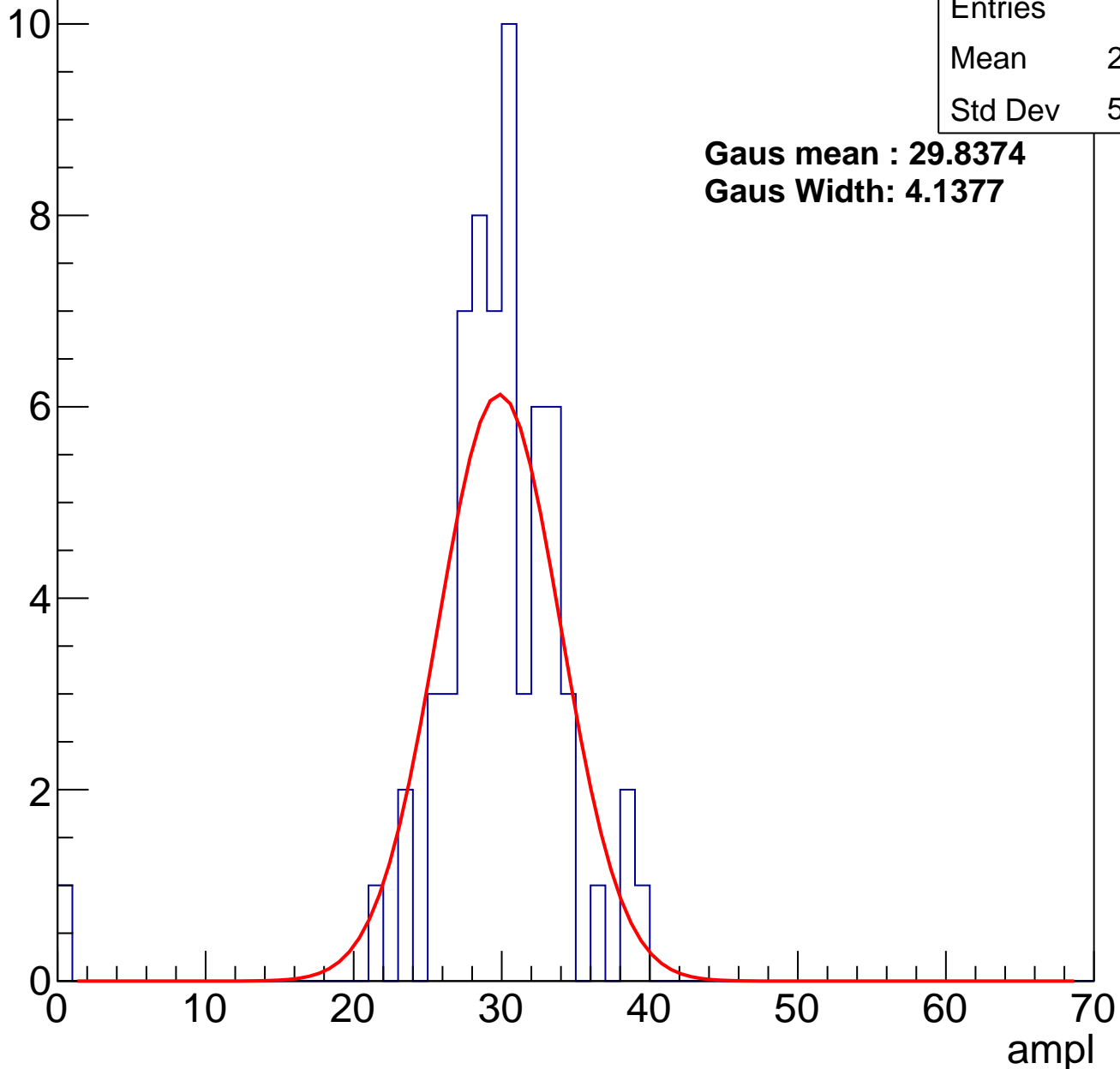
calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	29.25
Std Dev	5.065

Gaus mean : 29.8374

Gaus Width: 4.1377

Entry



B1L101S, U15-ch12, adc1

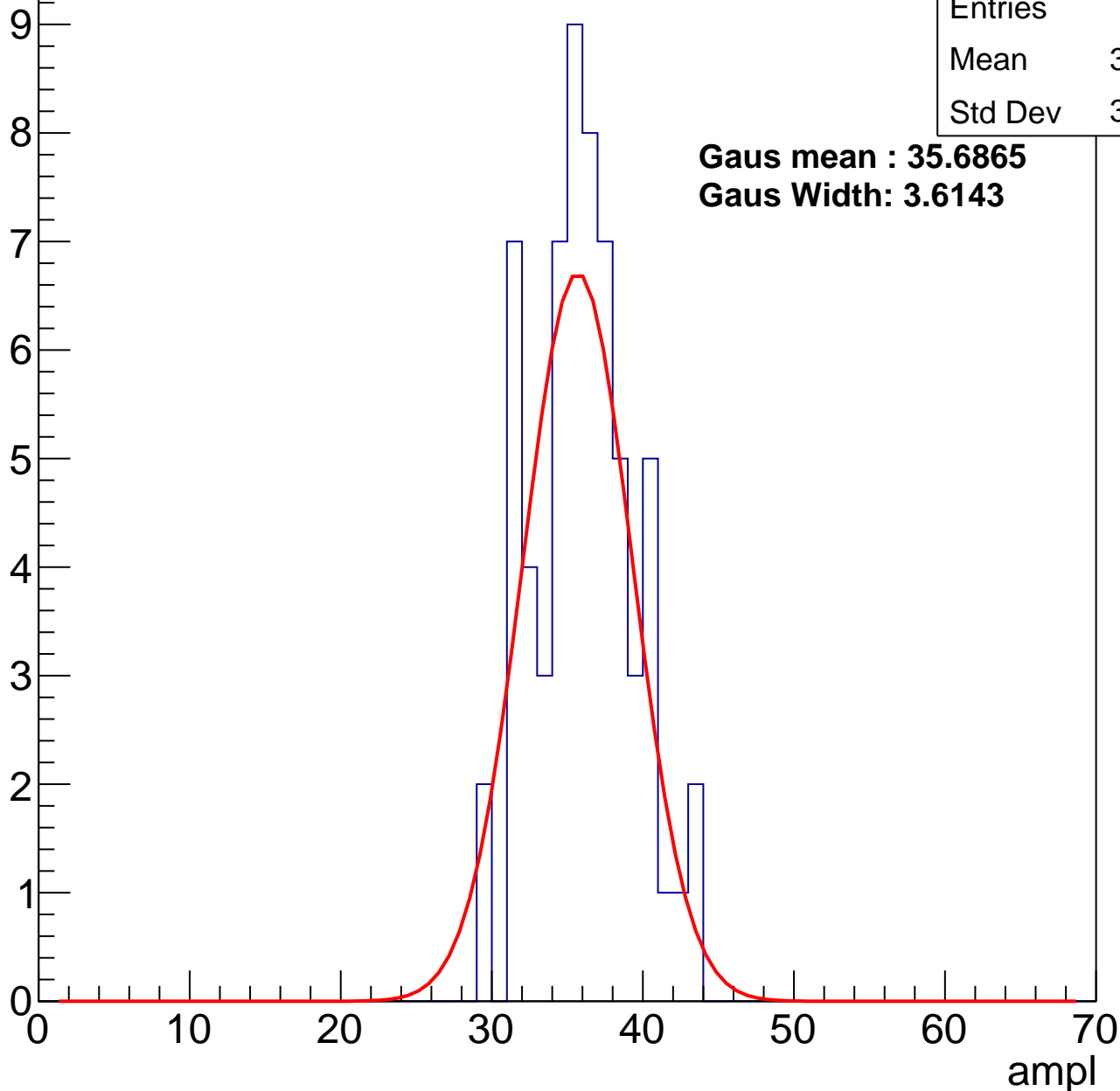
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	35.59
Std Dev	3.268

Gaus mean : 35.6865

Gaus Width: 3.6143



B1L101S, U15-ch12, adc2

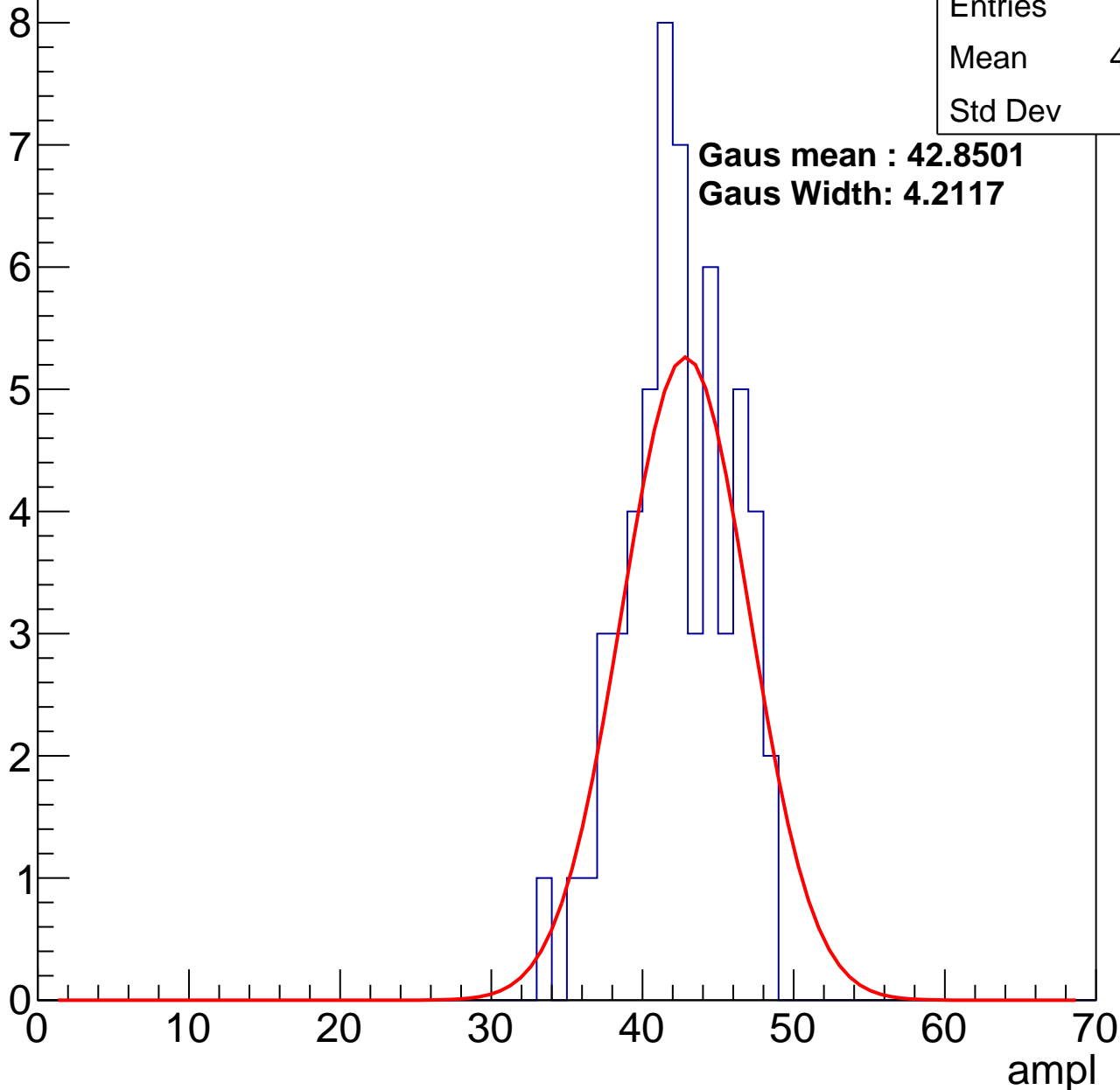
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.95
Std Dev	3.43

Gaus mean : 42.8501

Gaus Width: 4.2117

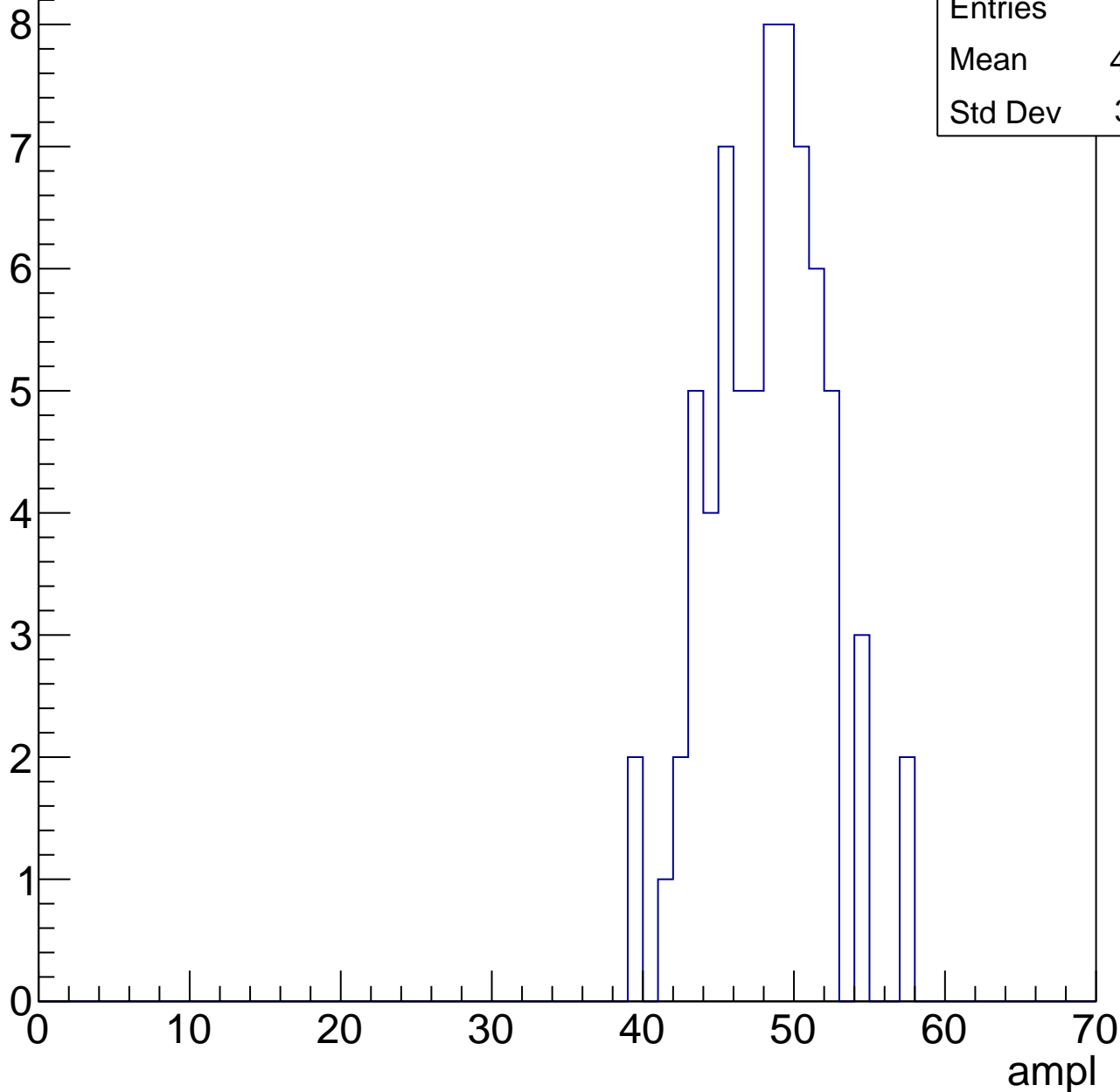


B1L101S, U15-ch12, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	47.74
Std Dev	3.771

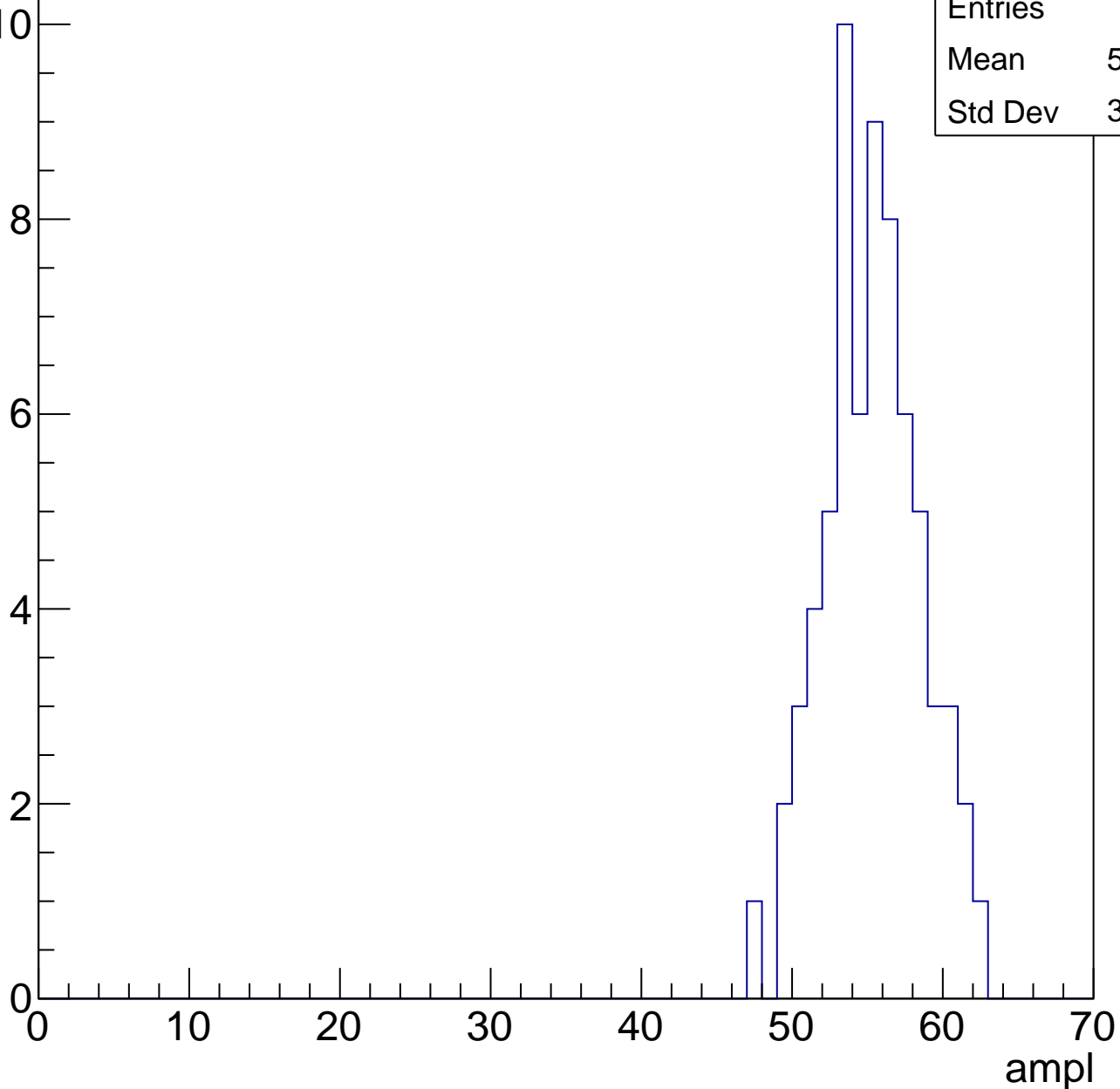


B1L101S, U15-ch12, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	54.84
Std Dev	3.174

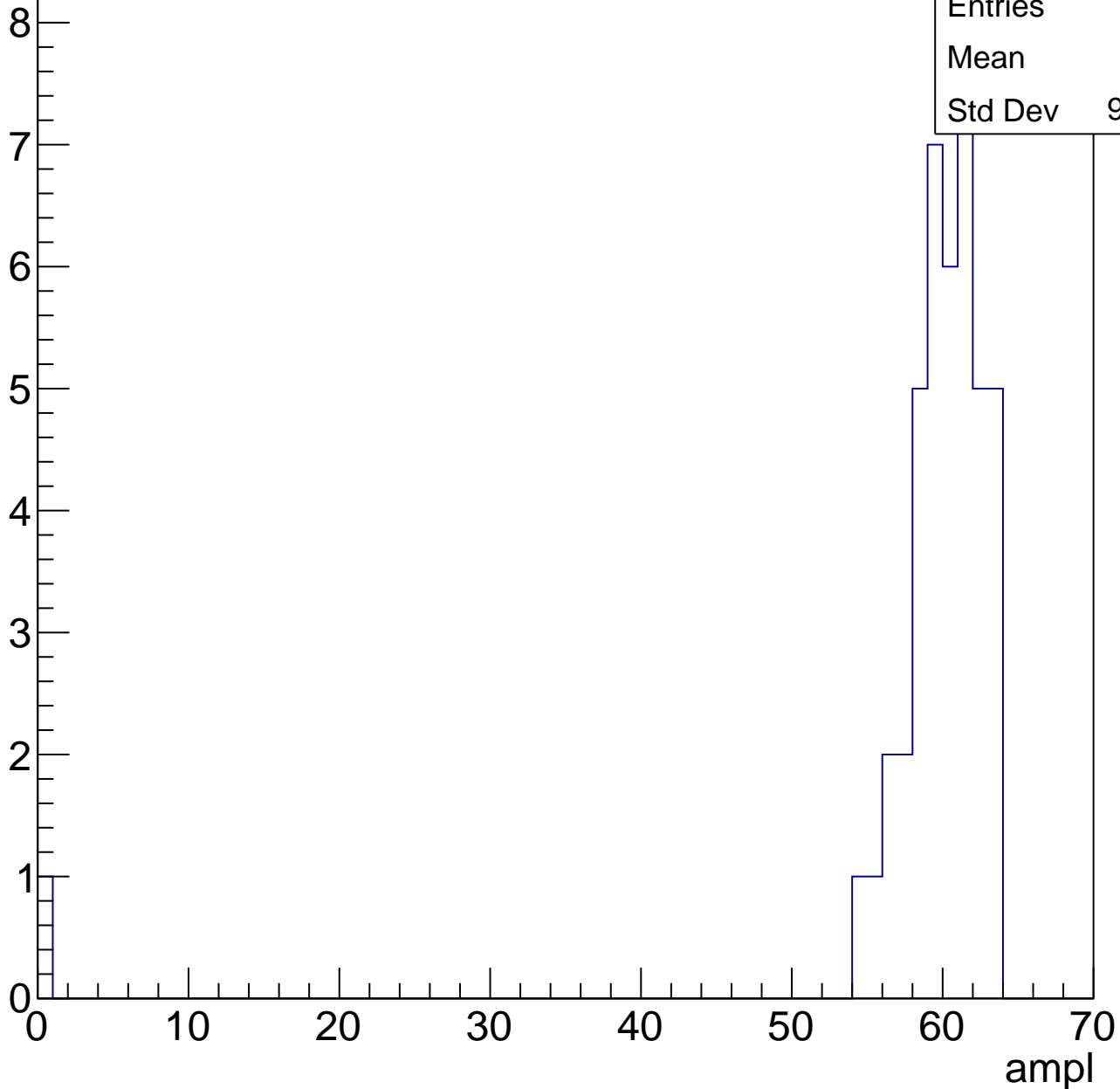


B1L101S, U15-ch12, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

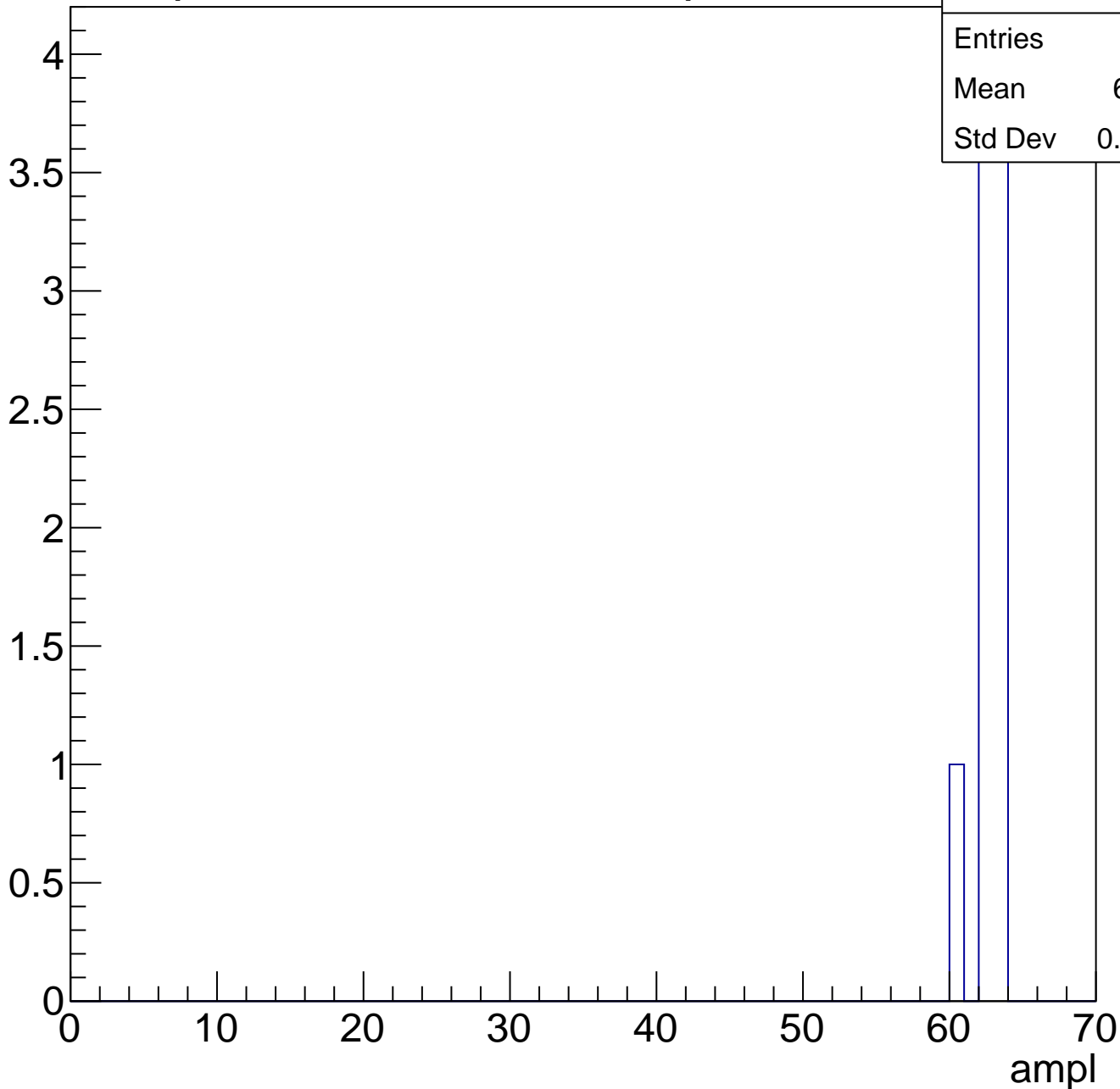
Entries	43
Mean	58.4
Std Dev	9.276



B1L101S, U15-ch12, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch12, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch13, adc0

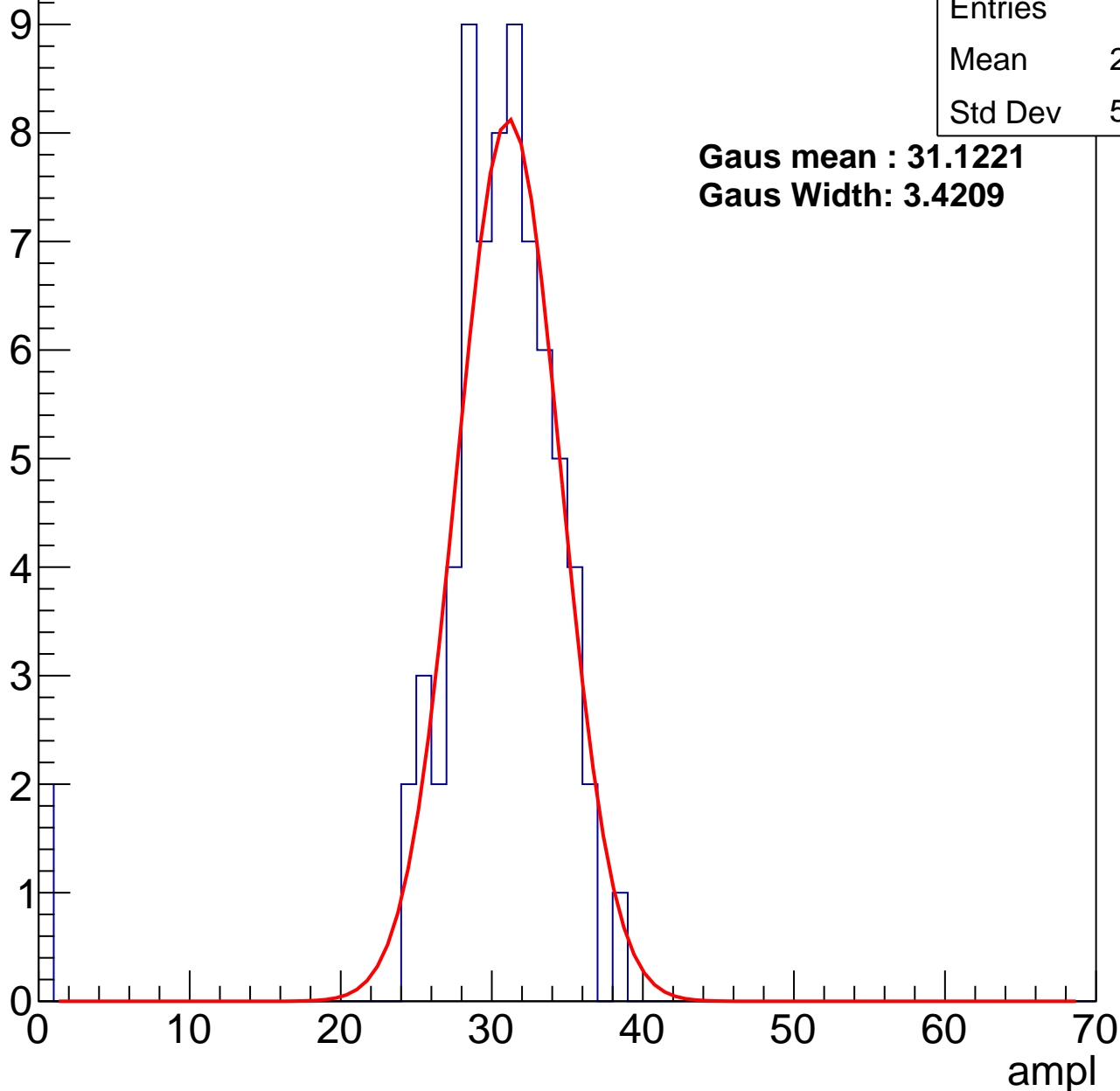
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.56
Std Dev	5.876

Gaus mean : 31.1221

Gaus Width: 3.4209



B1L101S, U15-ch13, adc1

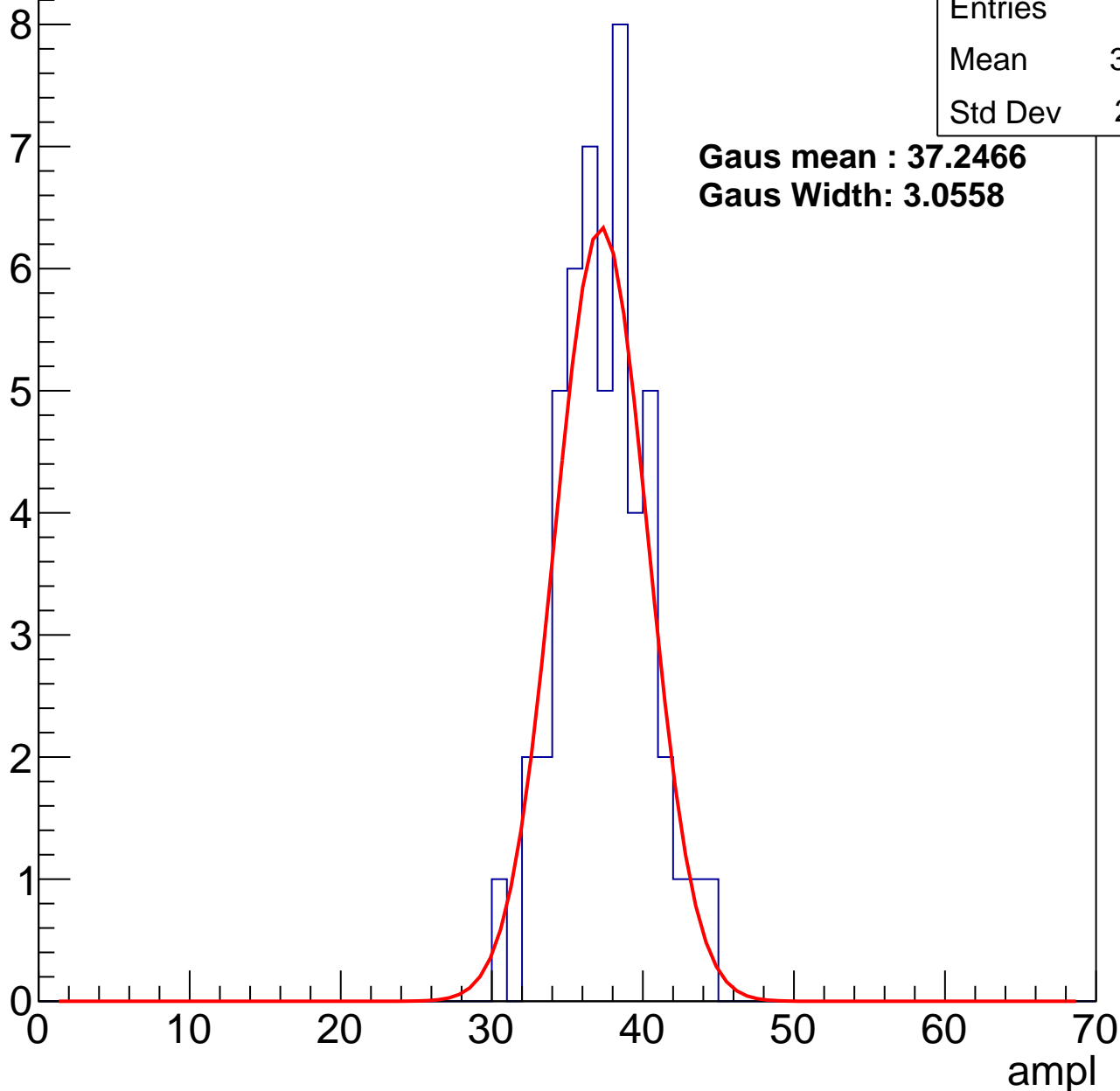
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	36.96
Std Dev	2.891

Gaus mean : 37.2466

Gaus Width: 3.0558



B1L101S, U15-ch13, adc2

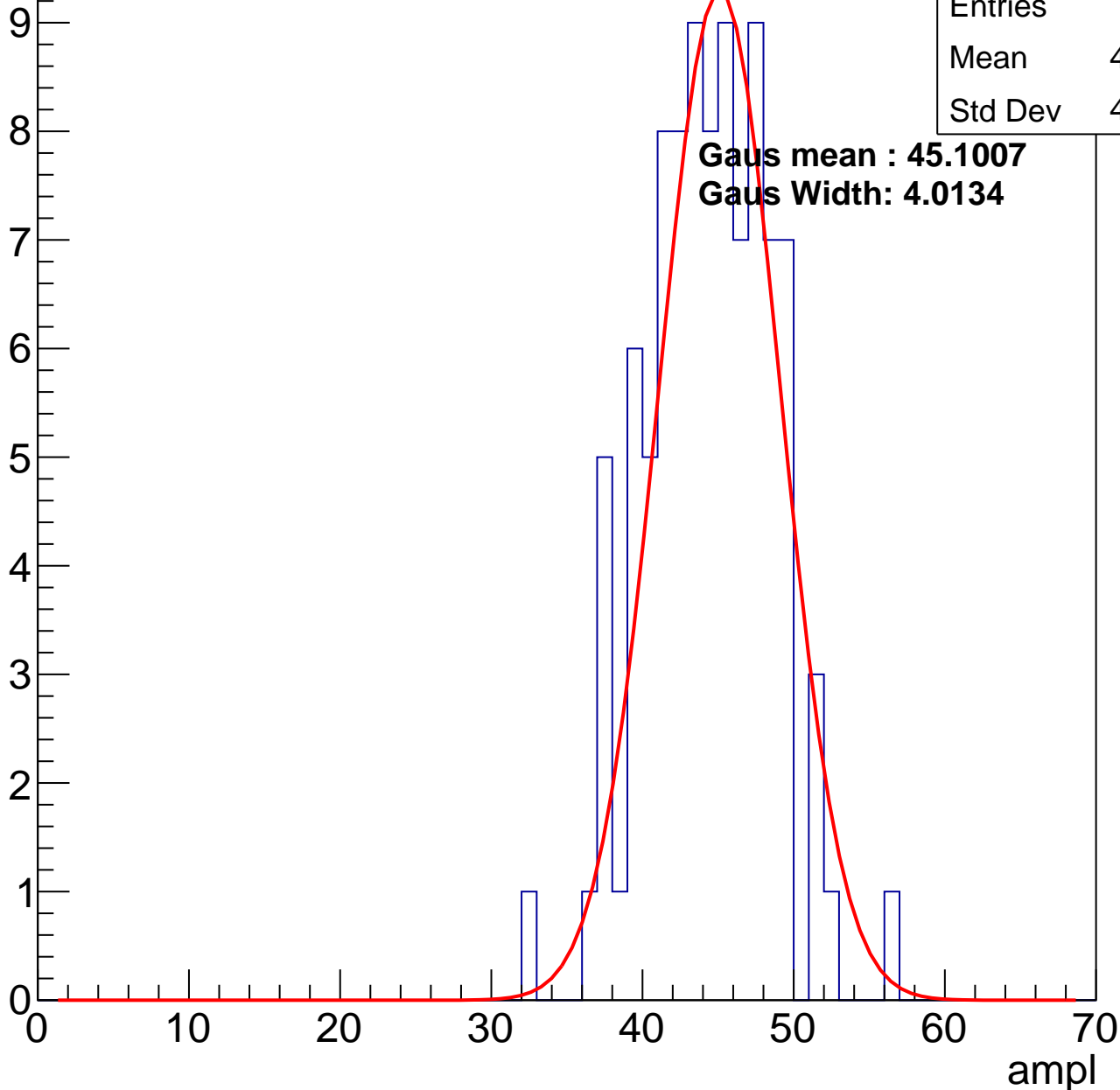
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	96
Mean	43.94
Std Dev	4.072

Gaus mean : 45.1007

Gaus Width: 4.0134

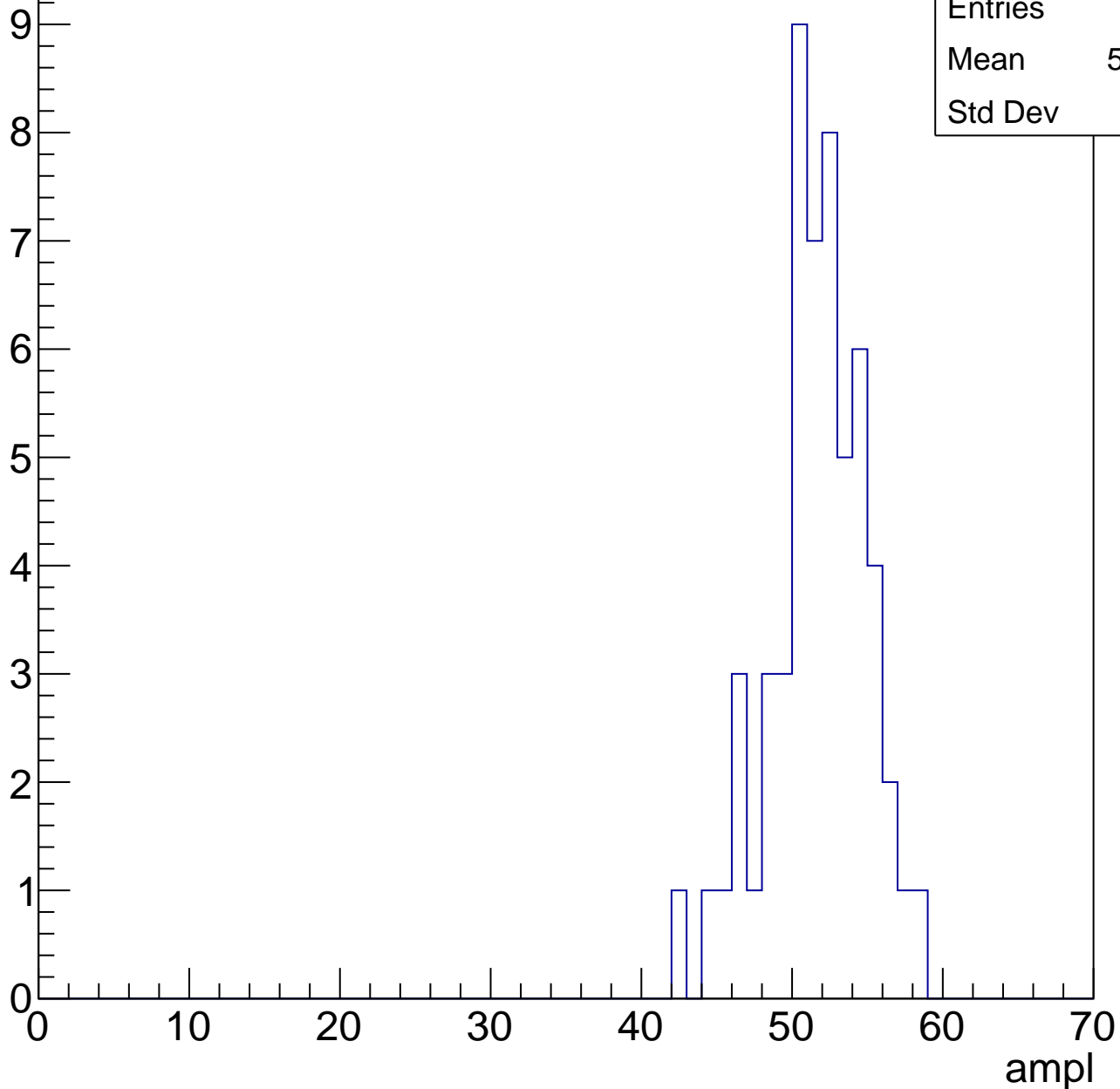


B1L101S, U15-ch13, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	51.18
Std Dev	3.23

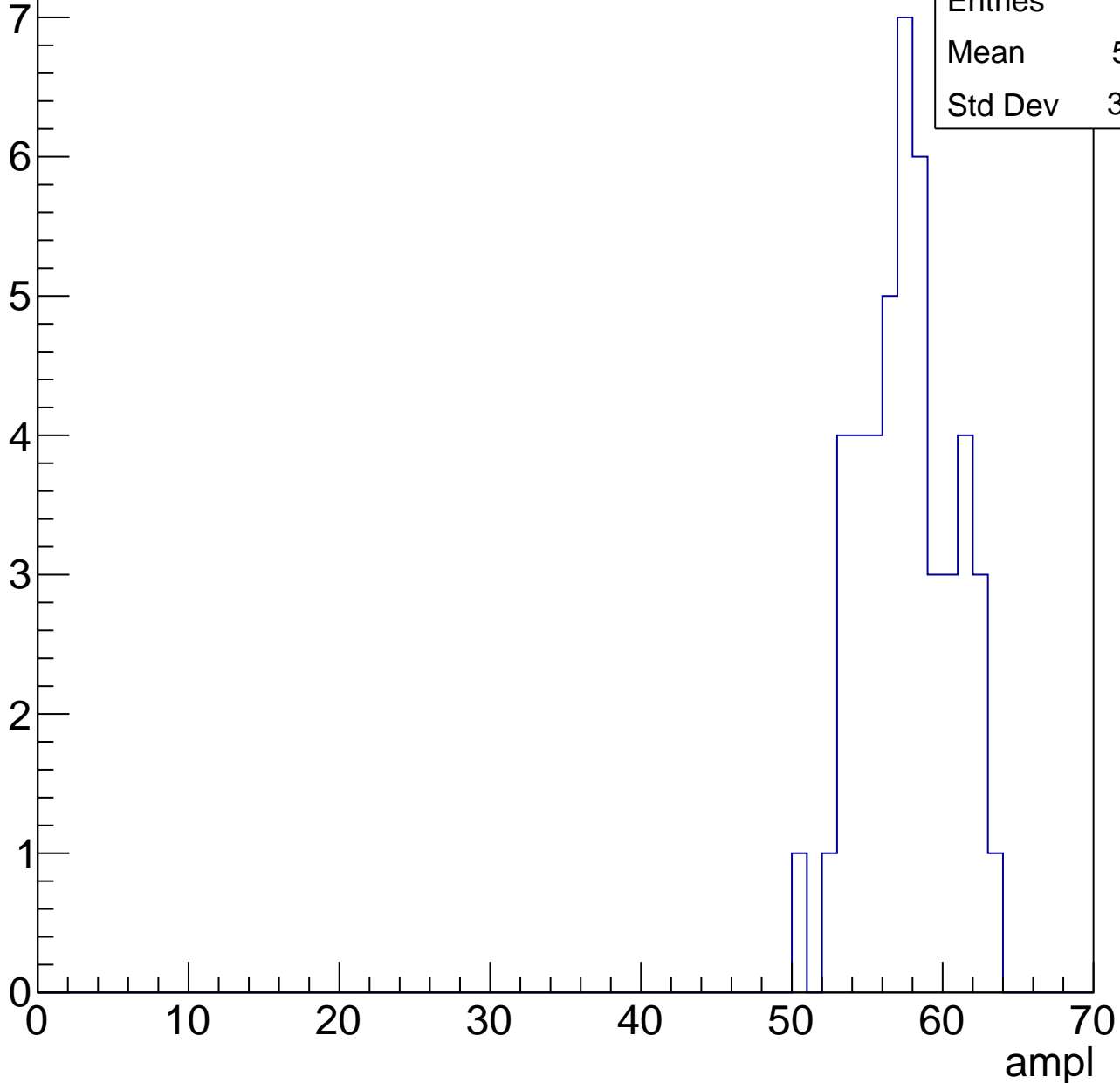


B1L101S, U15-ch13, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	57.11
Std Dev	3.002

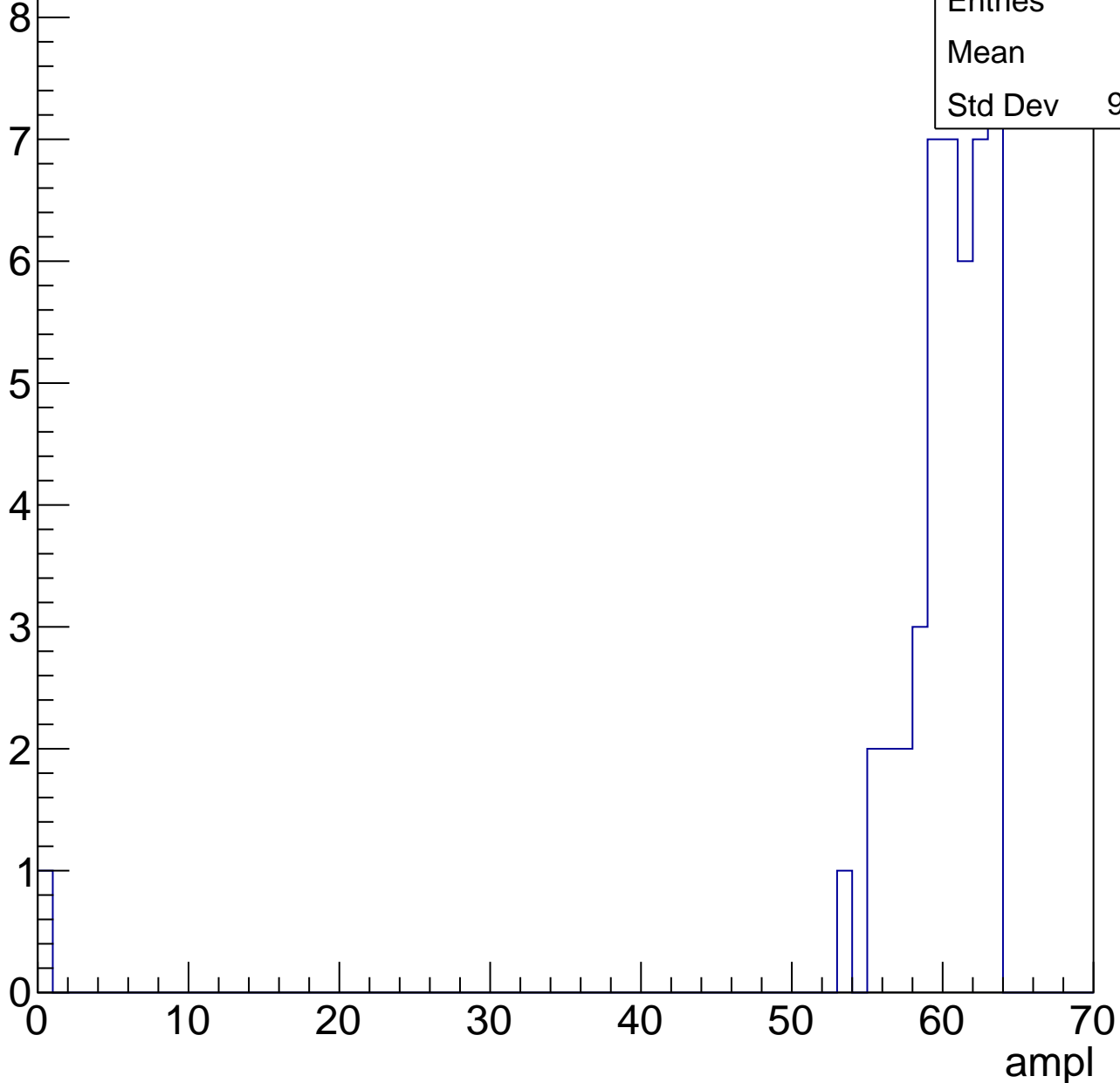


B1L101S, U15-ch13, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.7
Std Dev	9.084



B1L101S, U15-ch13, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch13, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U15-ch14, adc0

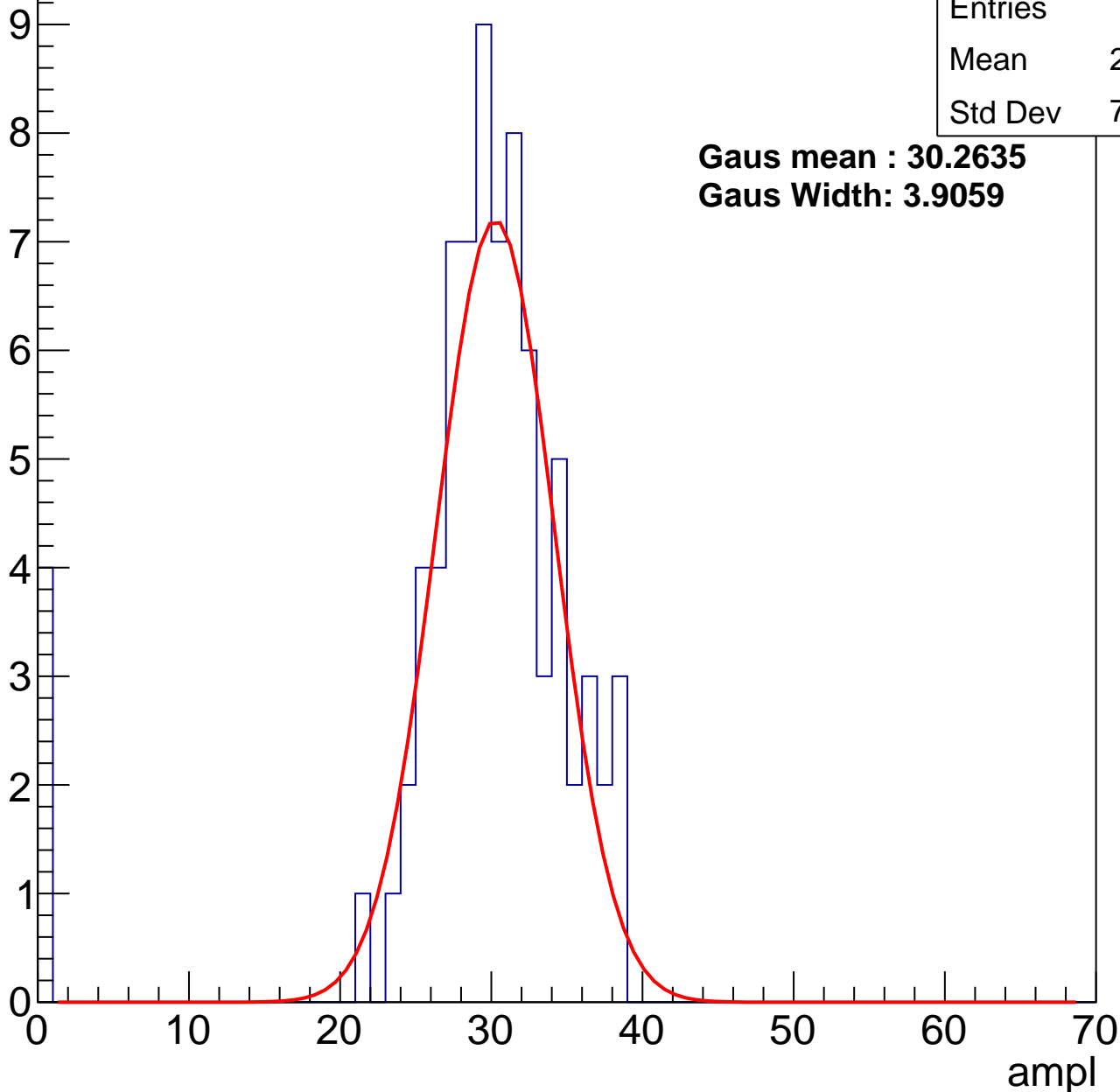
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	28.55
Std Dev	7.587

Gaus mean : 30.2635

Gaus Width: 3.9059



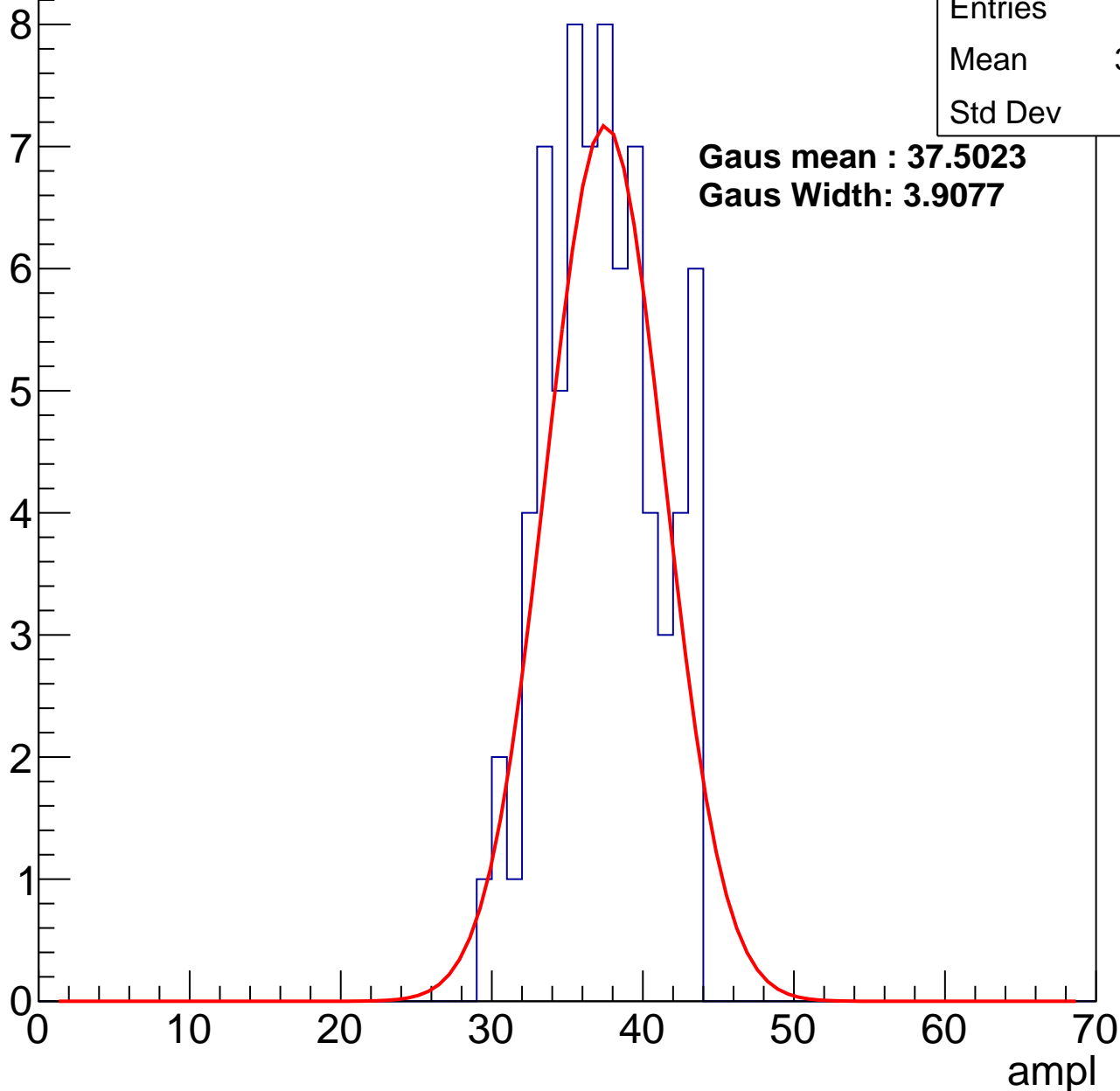
B1L101S, U15-ch14, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	36.81
Std Dev	3.56

Gaus mean : 37.5023
Gaus Width: 3.9077



B1L101S, U15-ch14, adc2

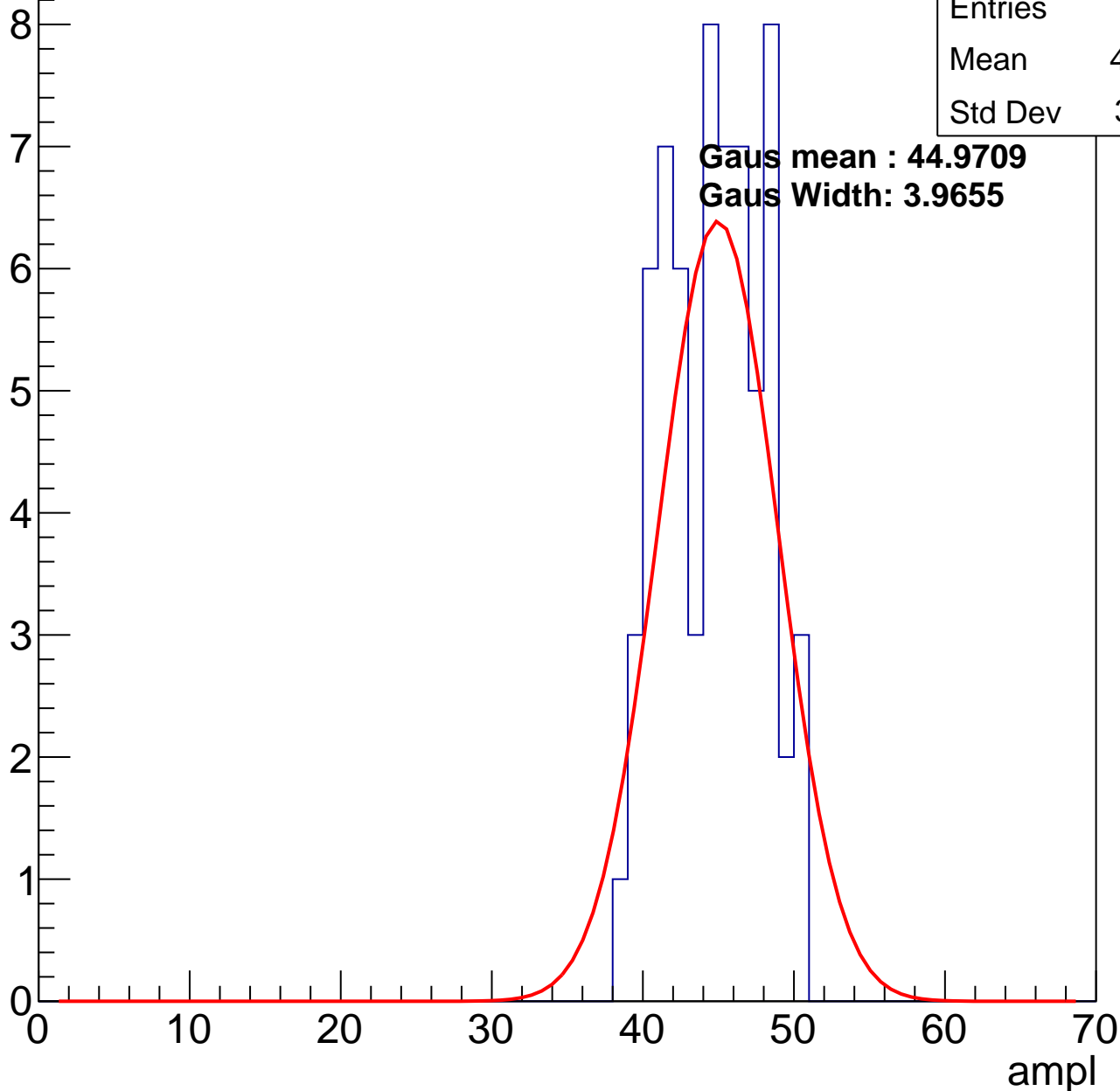
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	44.23
Std Dev	3.161

Gaus mean : 44.9709

Gaus Width: 3.9655

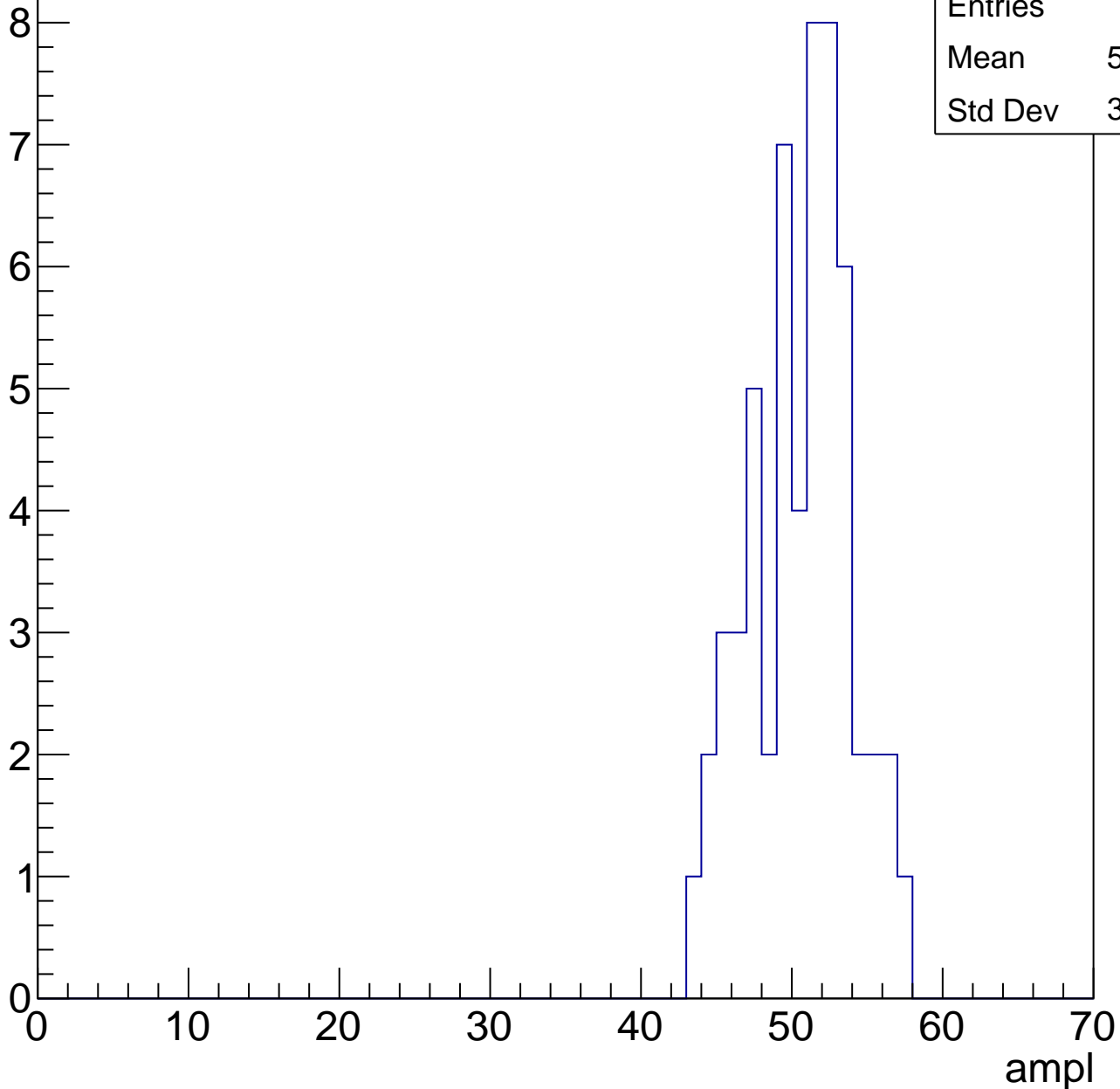


B1L101S, U15-ch14, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.12
Std Dev	3.274

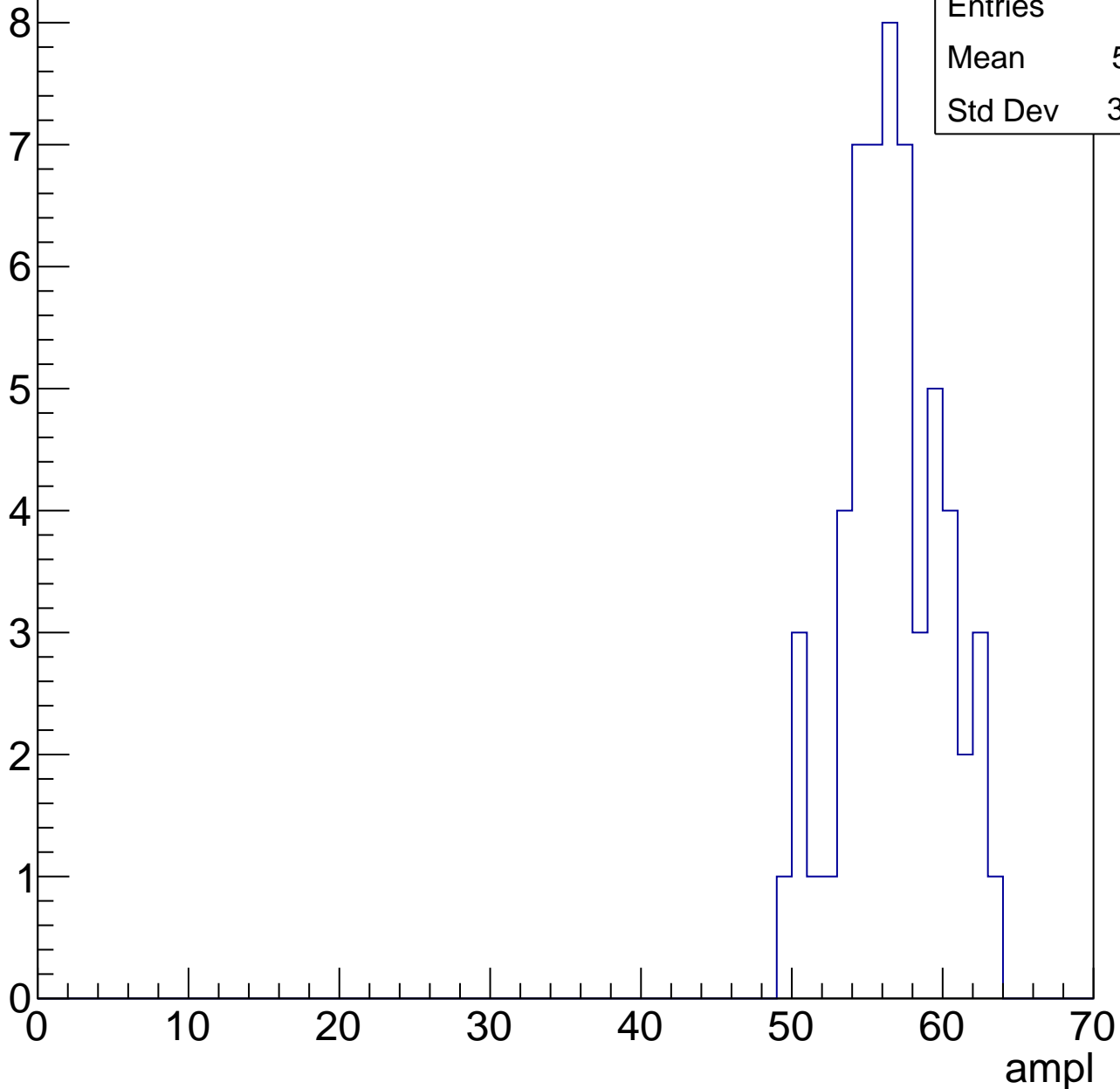


B1L101S, U15-ch14, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.21
Std Dev	3.248

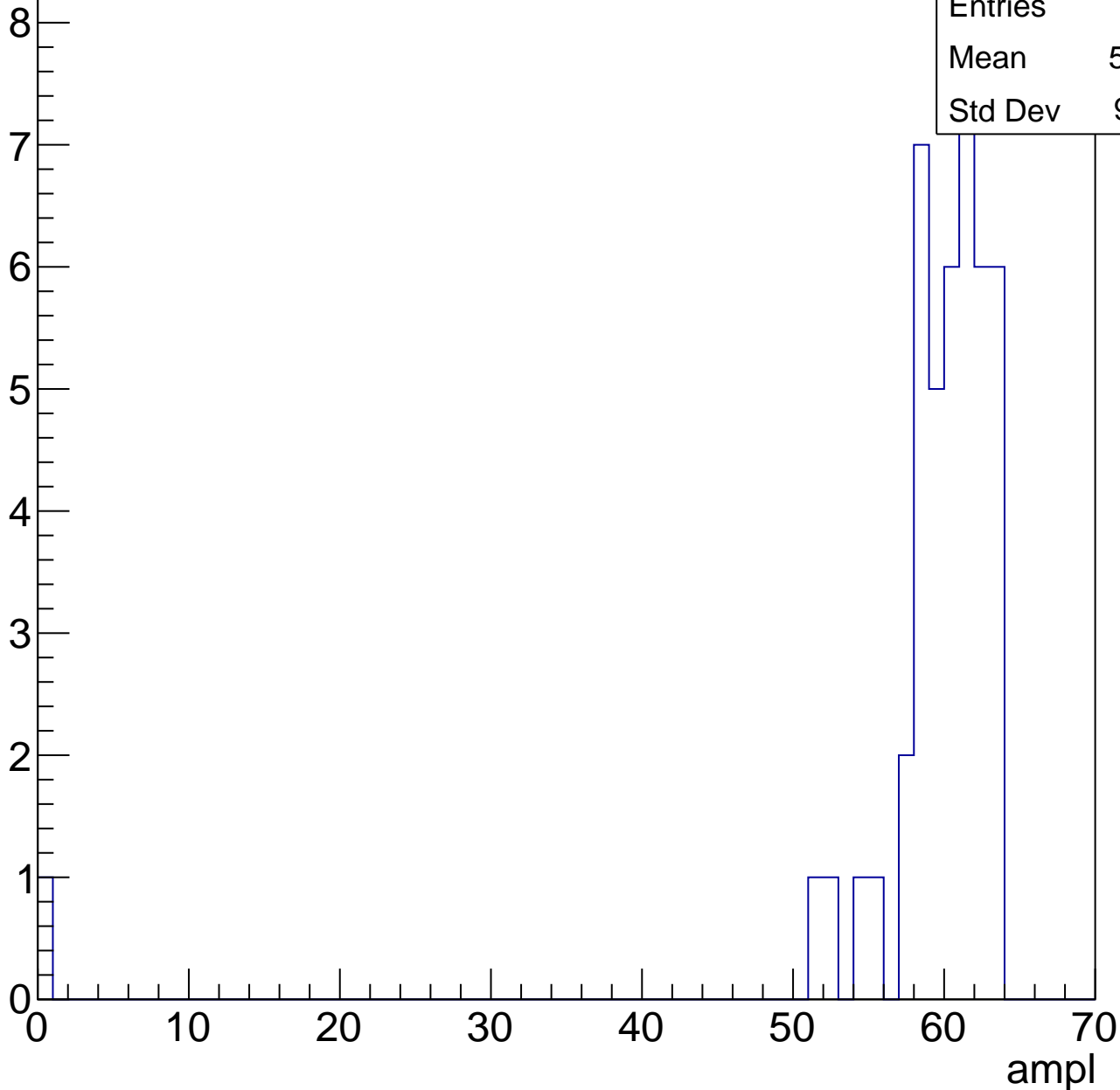


B1L101S, U15-ch14, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

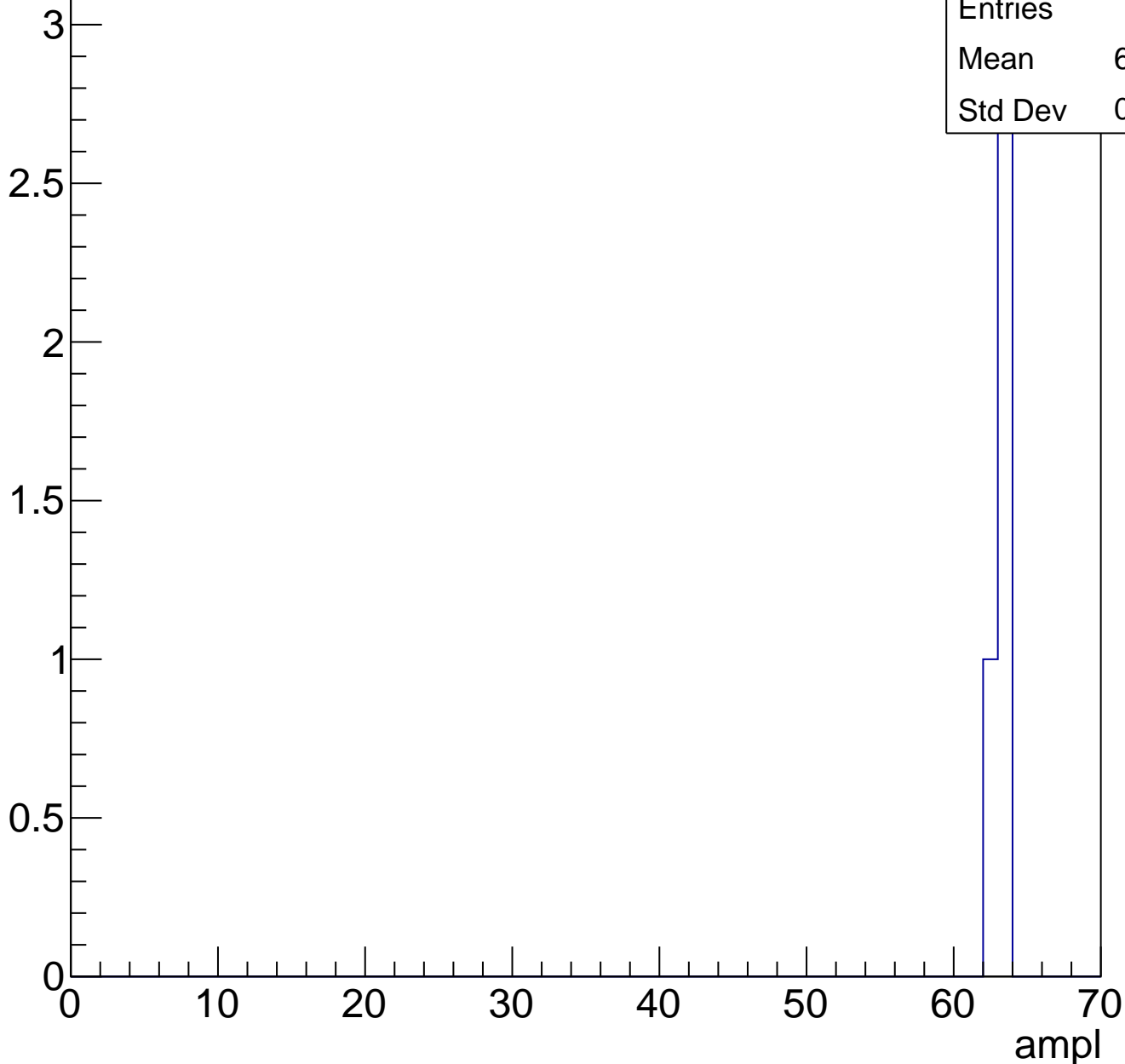
Entries	45
Mean	58.33
Std Dev	9.211



B1L101S, U15-ch14, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch14, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch15, adc0

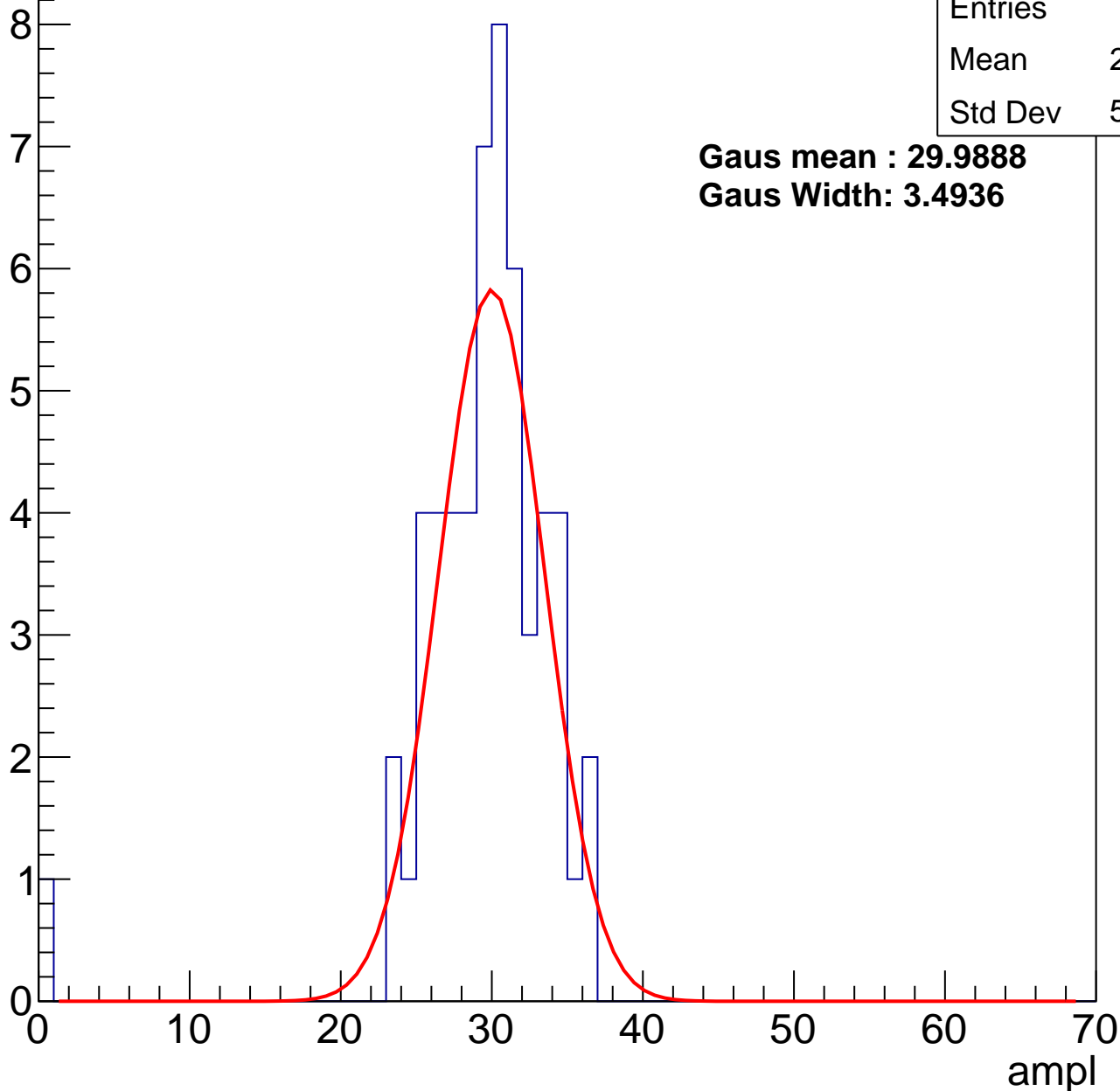
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	28.98
Std Dev	5.072

Gaus mean : 29.9888

Gaus Width: 3.4936



B1L101S, U15-ch15, adc1

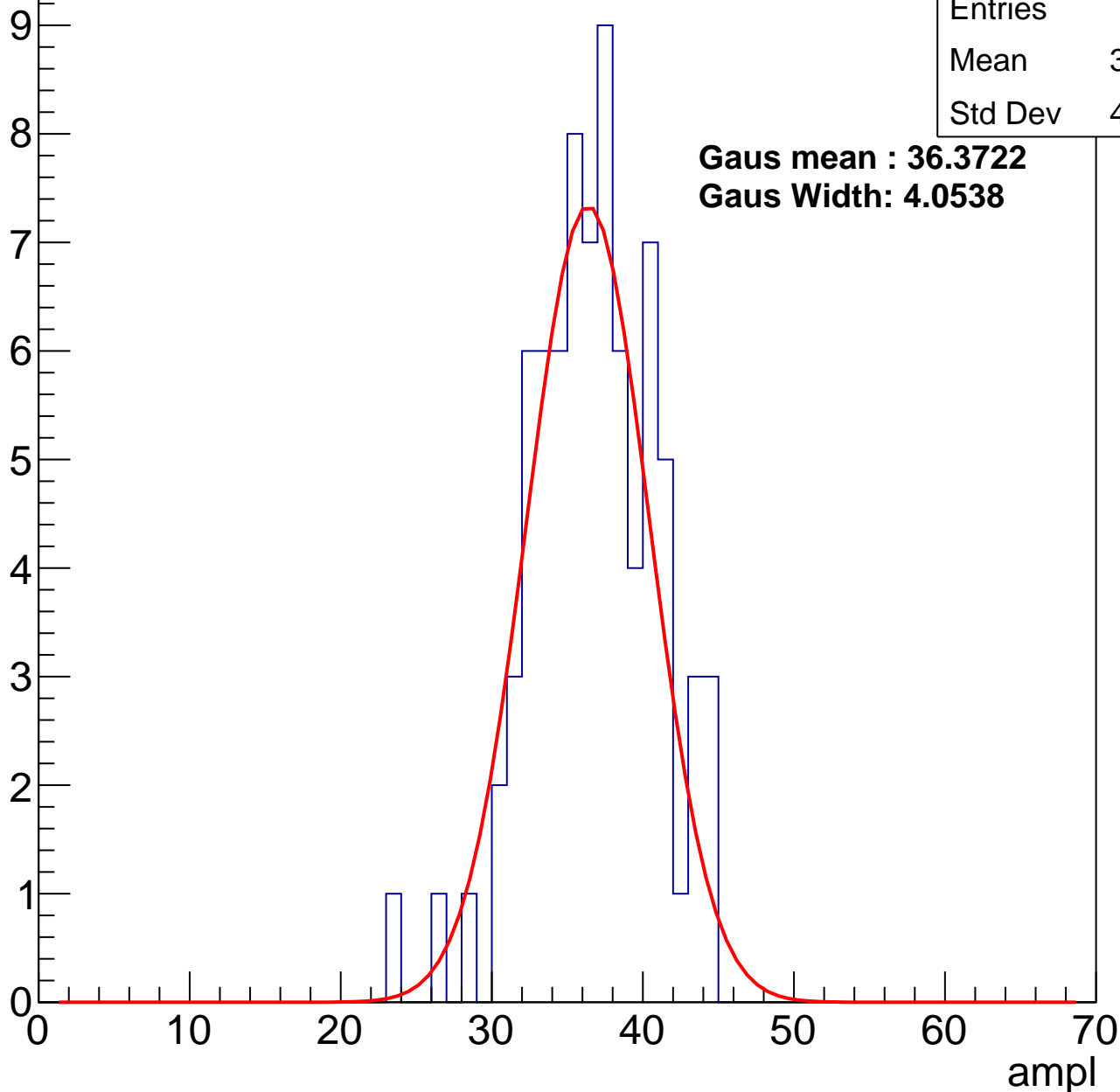
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	36.22
Std Dev	4.117

Gaus mean : 36.3722

Gaus Width: 4.0538



B1L101S, U15-ch15, adc2

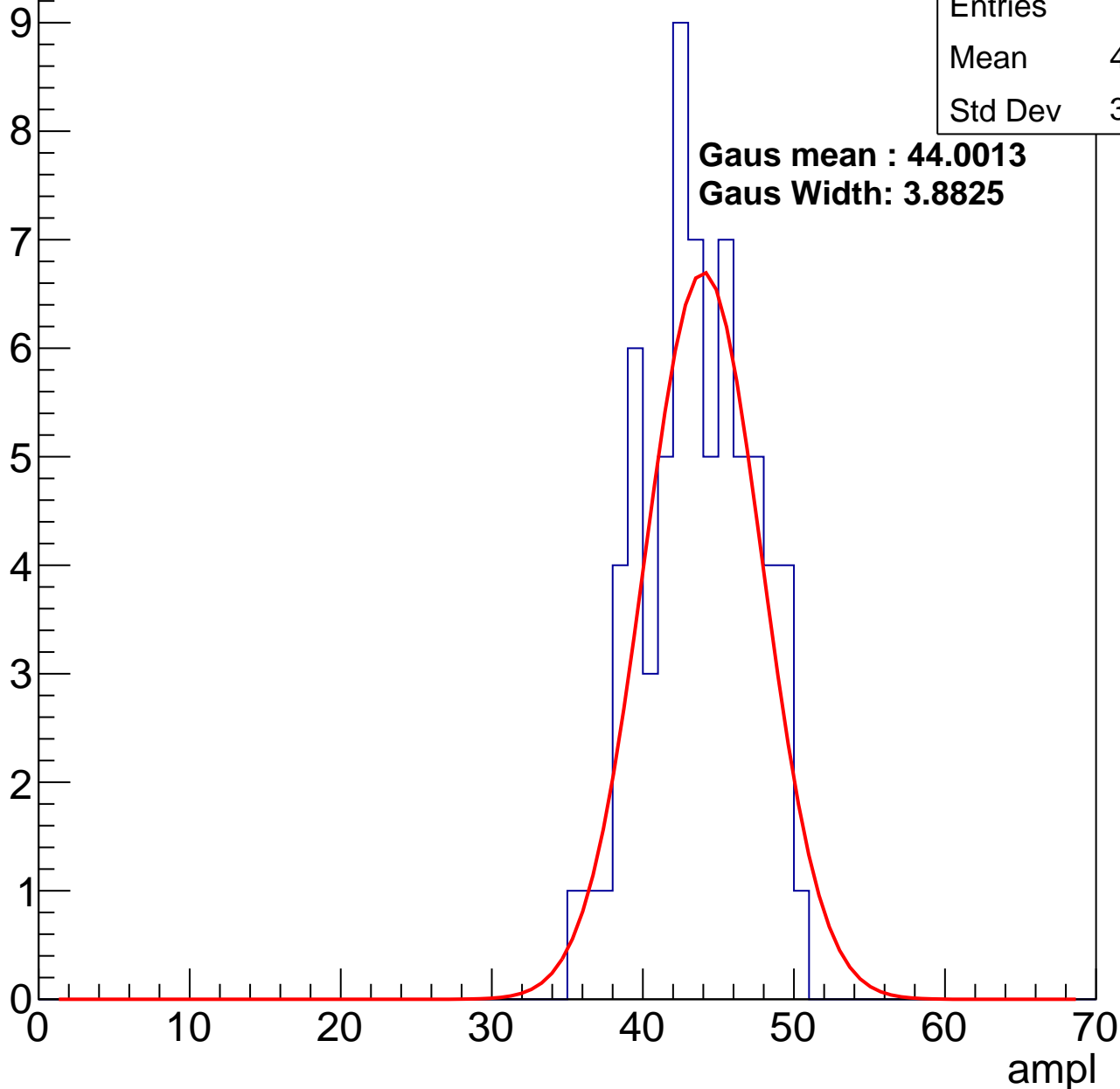
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.18
Std Dev	3.535

Gaus mean : 44.0013

Gaus Width: 3.8825

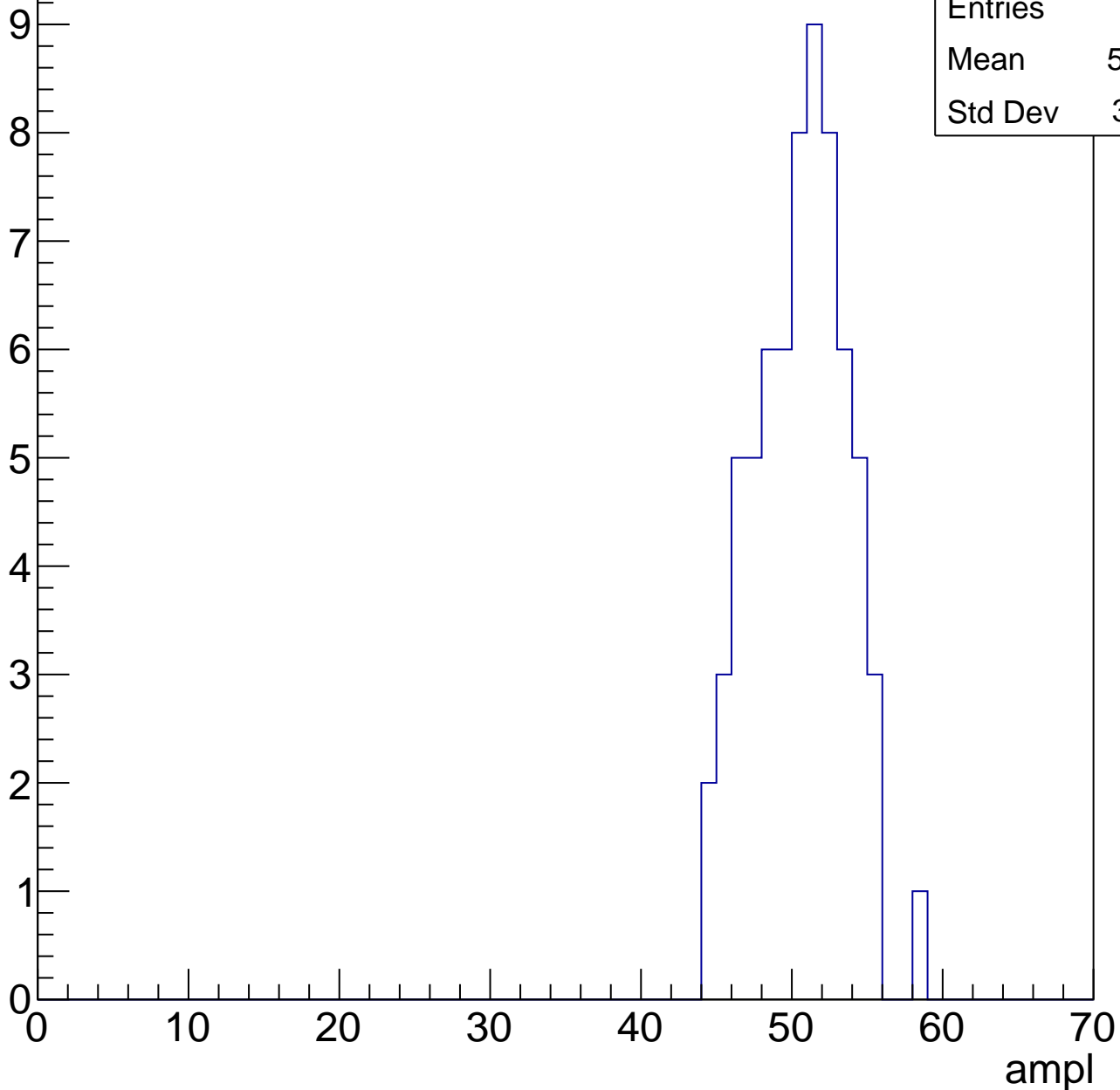


B1L101S, U15-ch15, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	50.09
Std Dev	3.031

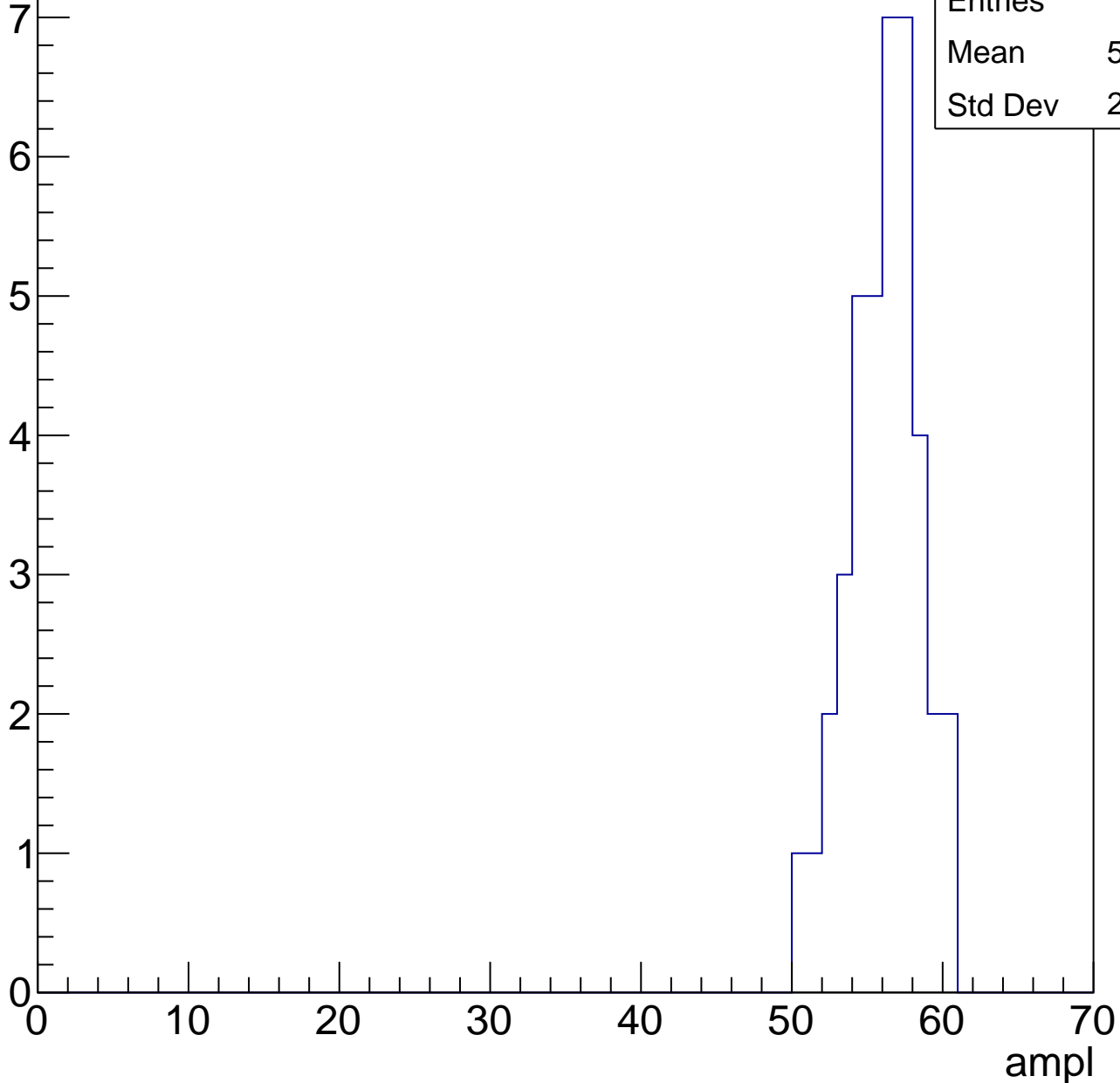


B1L101S, U15-ch15, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	55.64
Std Dev	2.337

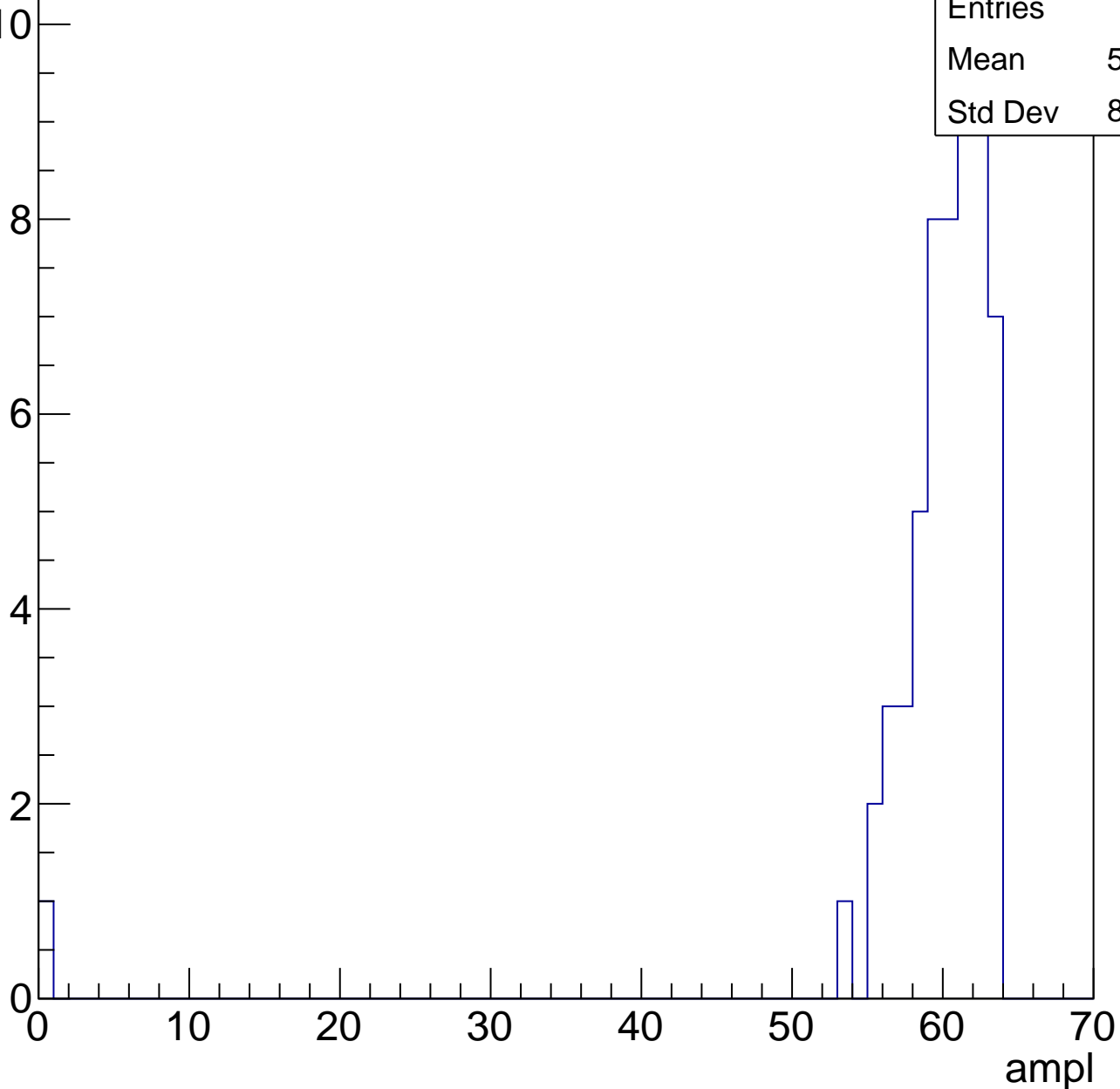


B1L101S, U15-ch15, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	58.88
Std Dev	8.139



B1L101S, U15-ch15, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch15, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch16, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	78
Mean	27.03
Std Dev	5.688

Gaus mean : 28.4169

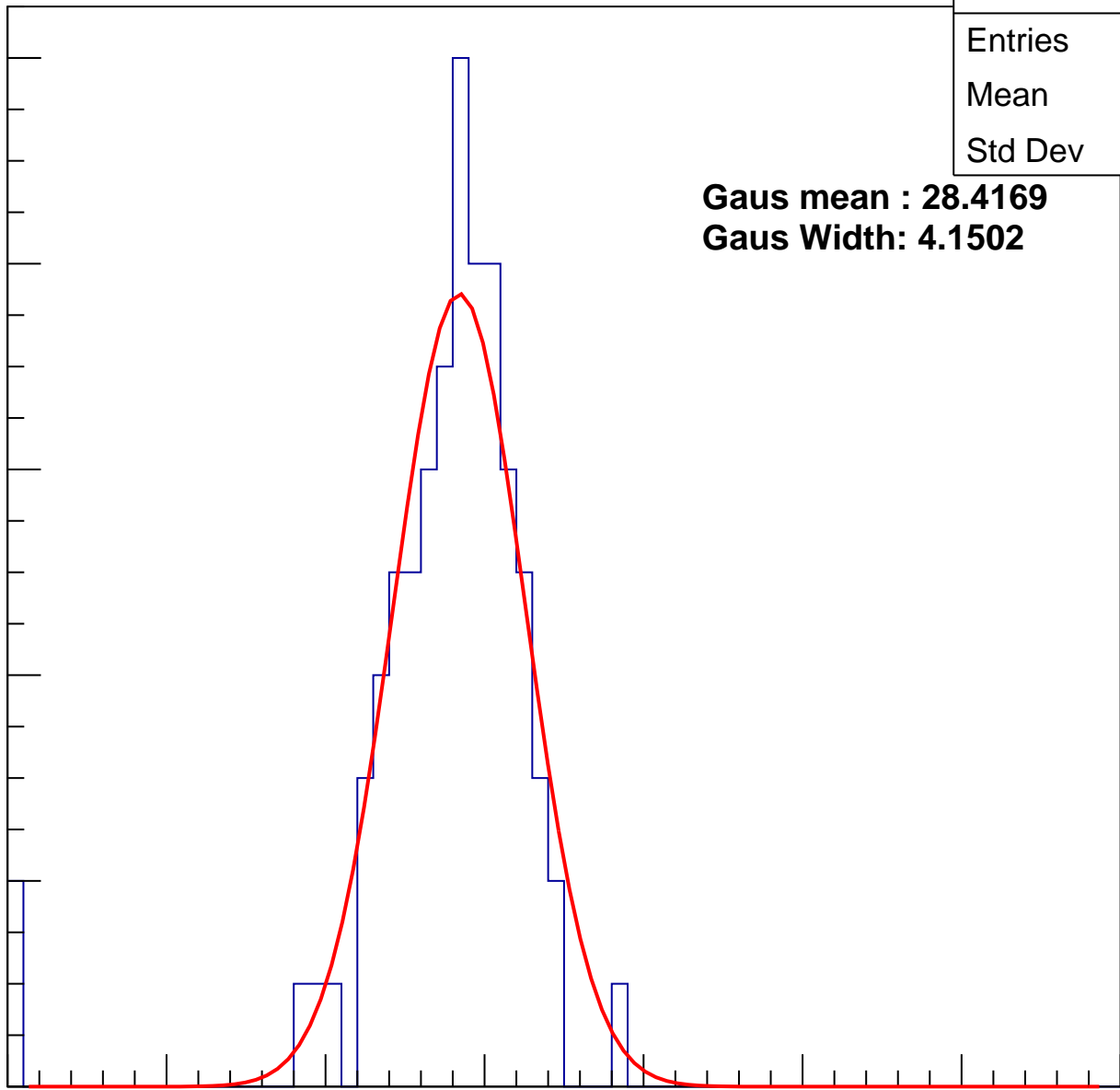
Gaus Width: 4.1502

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch16, adc1

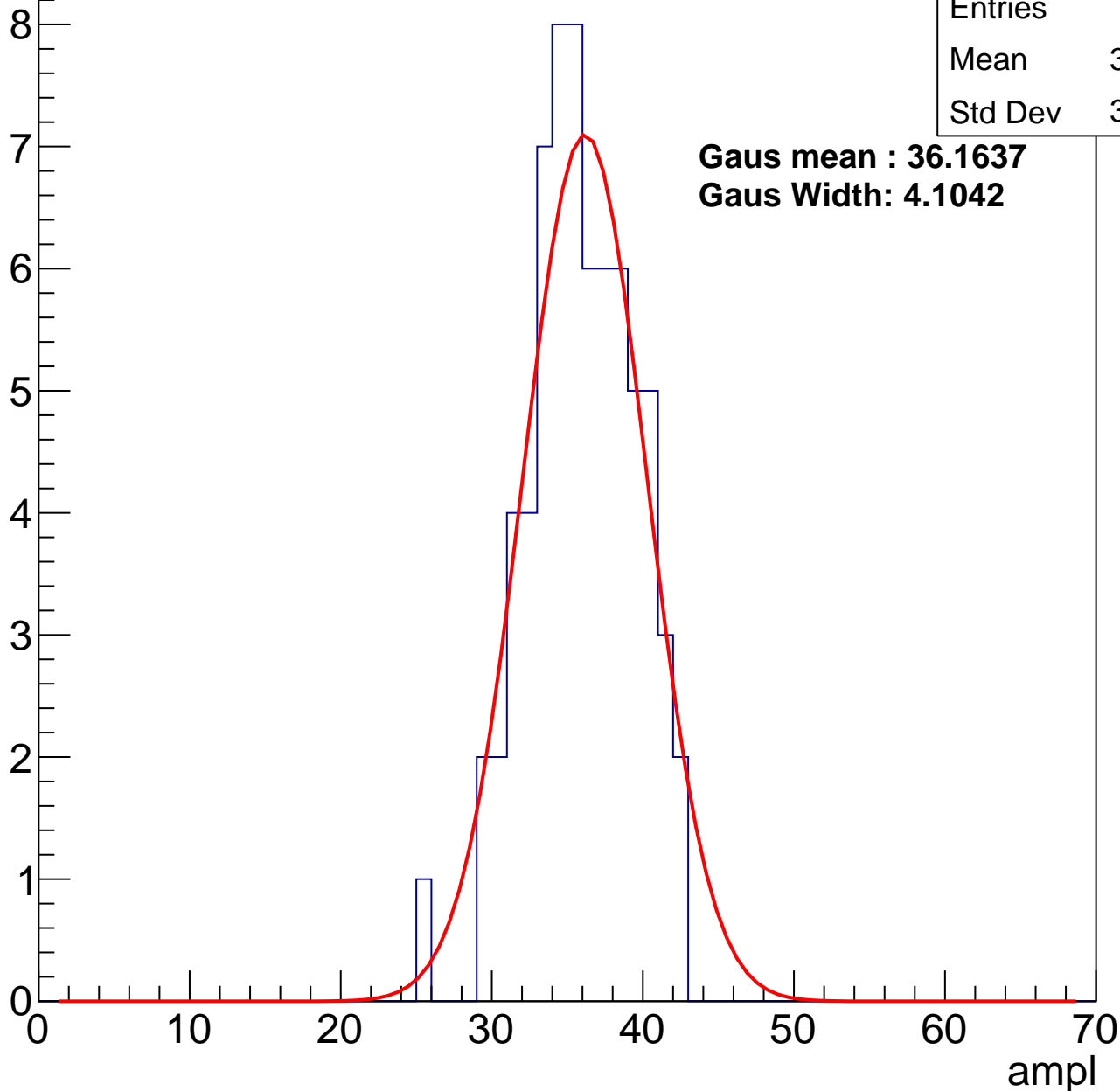
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.45
Std Dev	3.487

Gaus mean : 36.1637

Gaus Width: 4.1042



B1L101S, U15-ch16, adc2

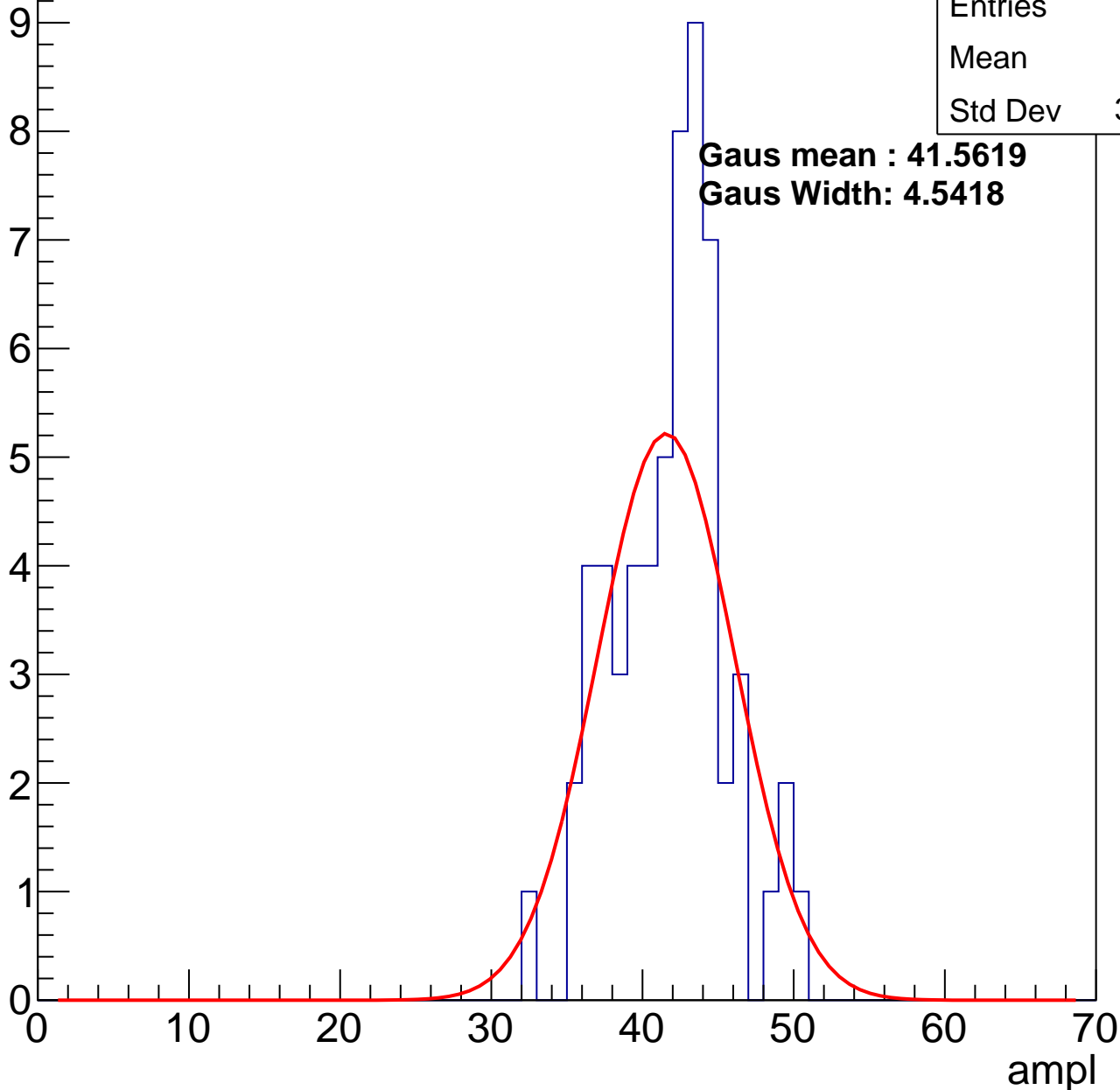
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	41.4
Std Dev	3.711

Gaus mean : 41.5619

Gaus Width: 4.5418

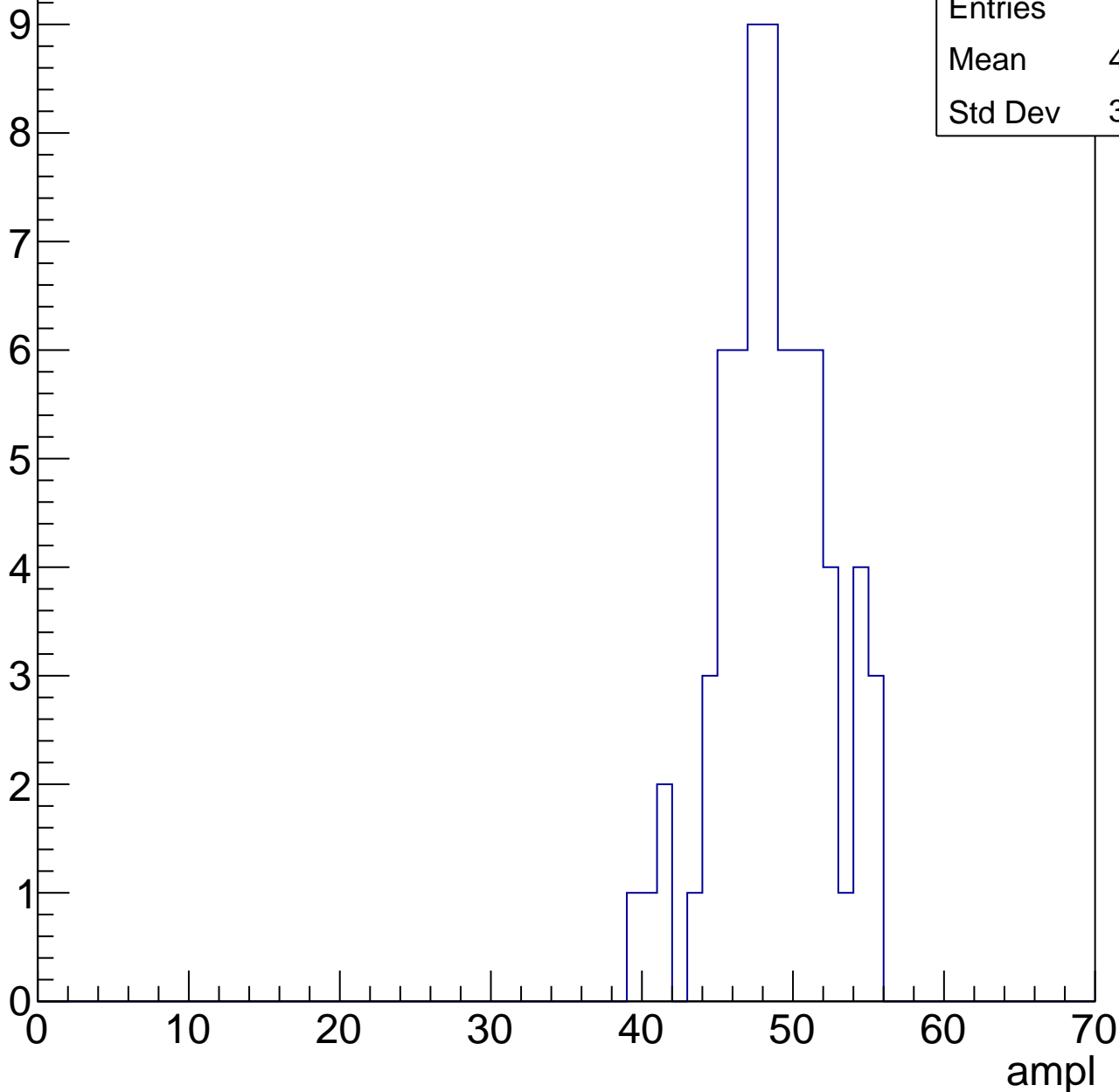


B1L101S, U15-ch16, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	48.22
Std Dev	3.564

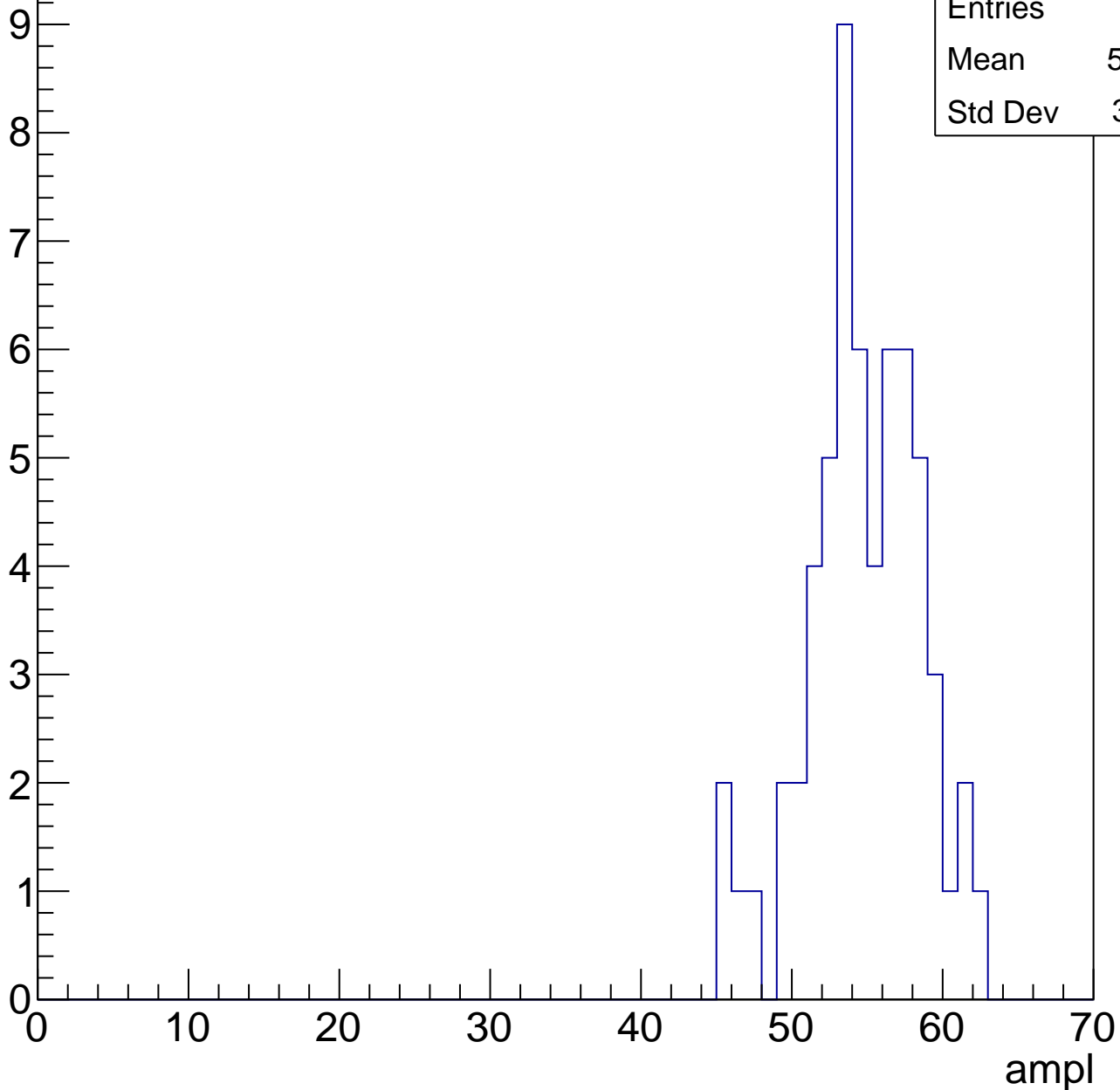


B1L101S, U15-ch16, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54.25
Std Dev	3.771

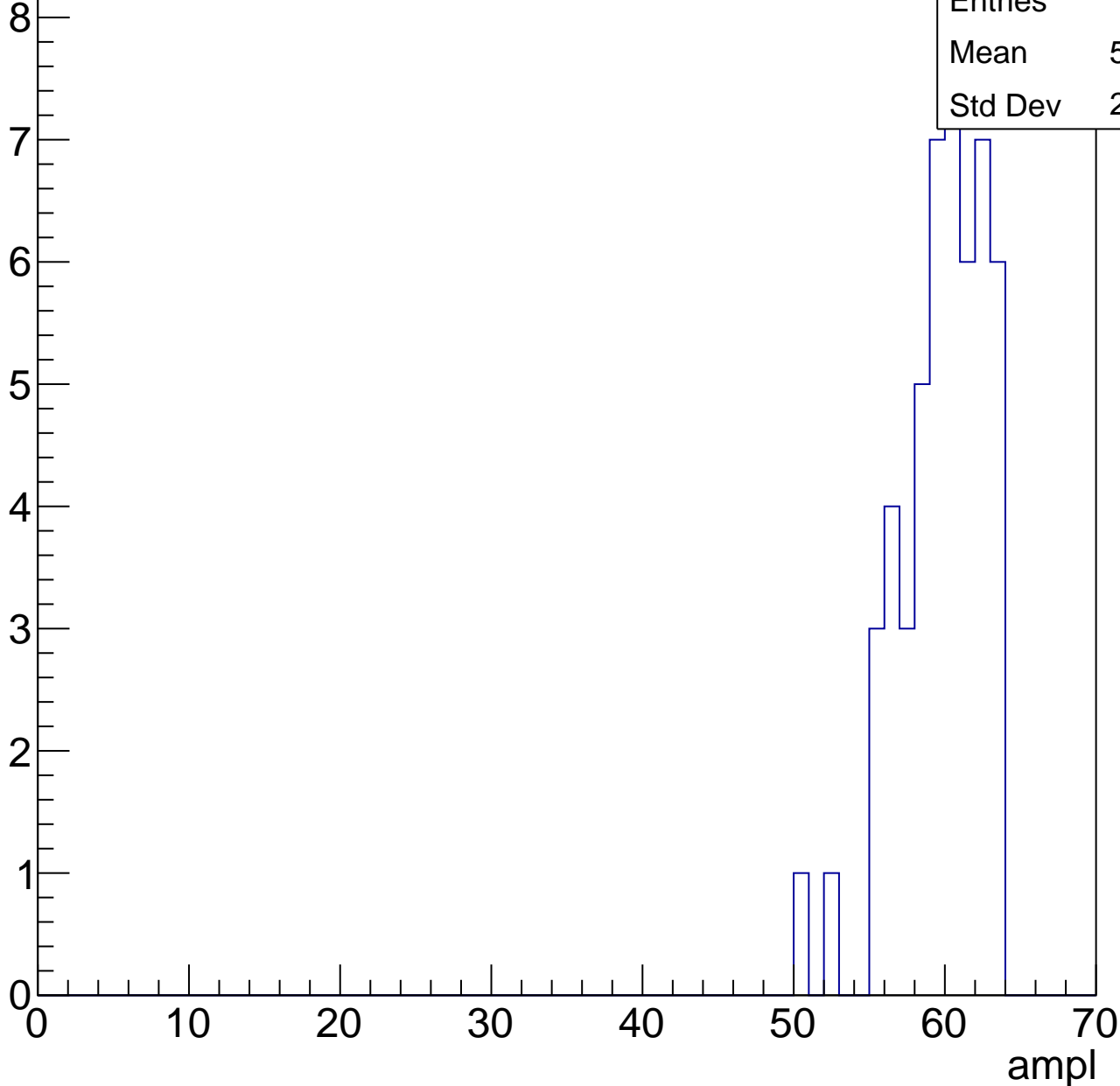


B1L101S, U15-ch16, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	59.27
Std Dev	2.863



B1L101S, U15-ch16, adc6

calib_packv5_042523_0143.root, FC#0, port D2

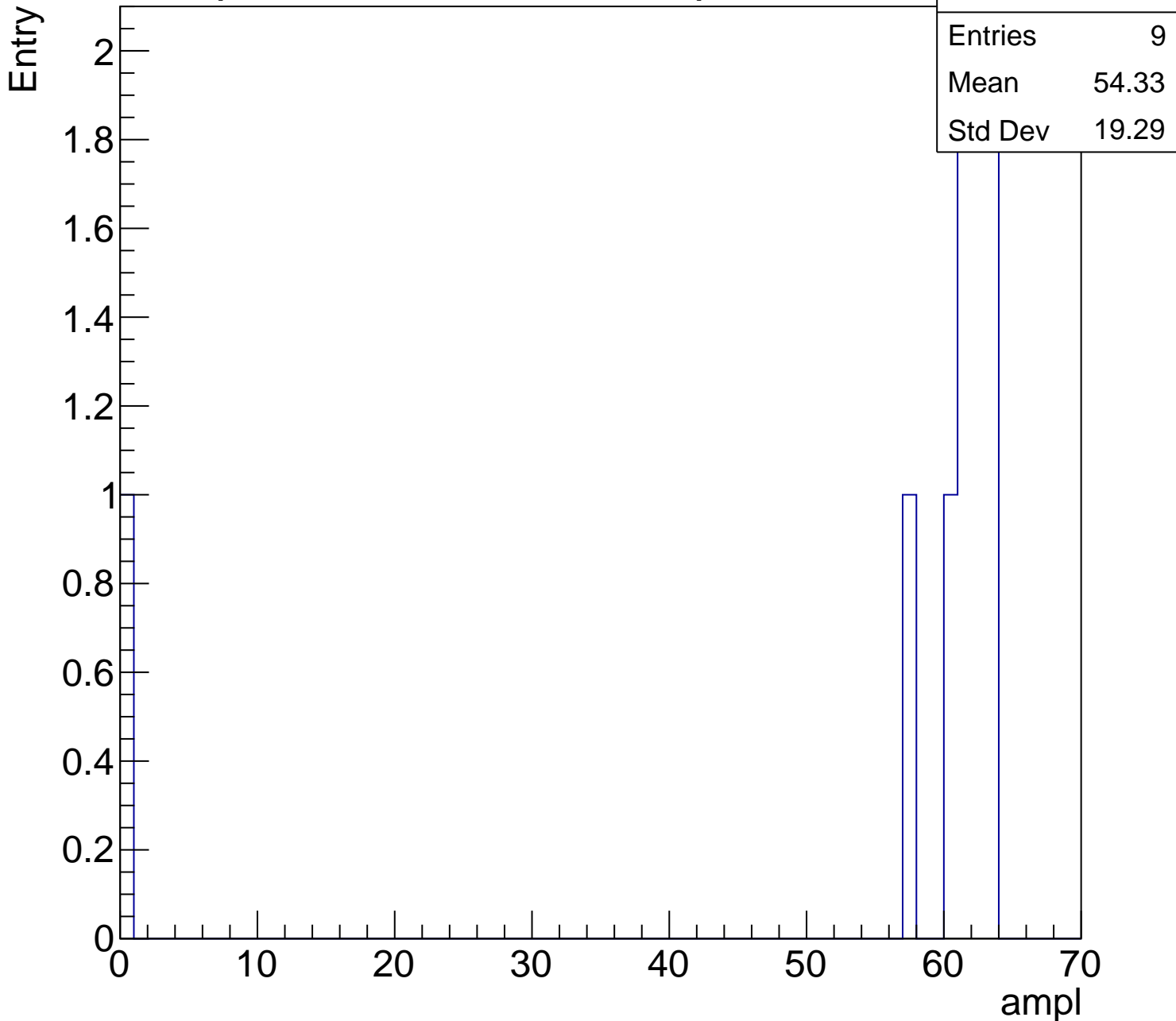
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.33
Std Dev	19.29

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch16, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch17, adc0

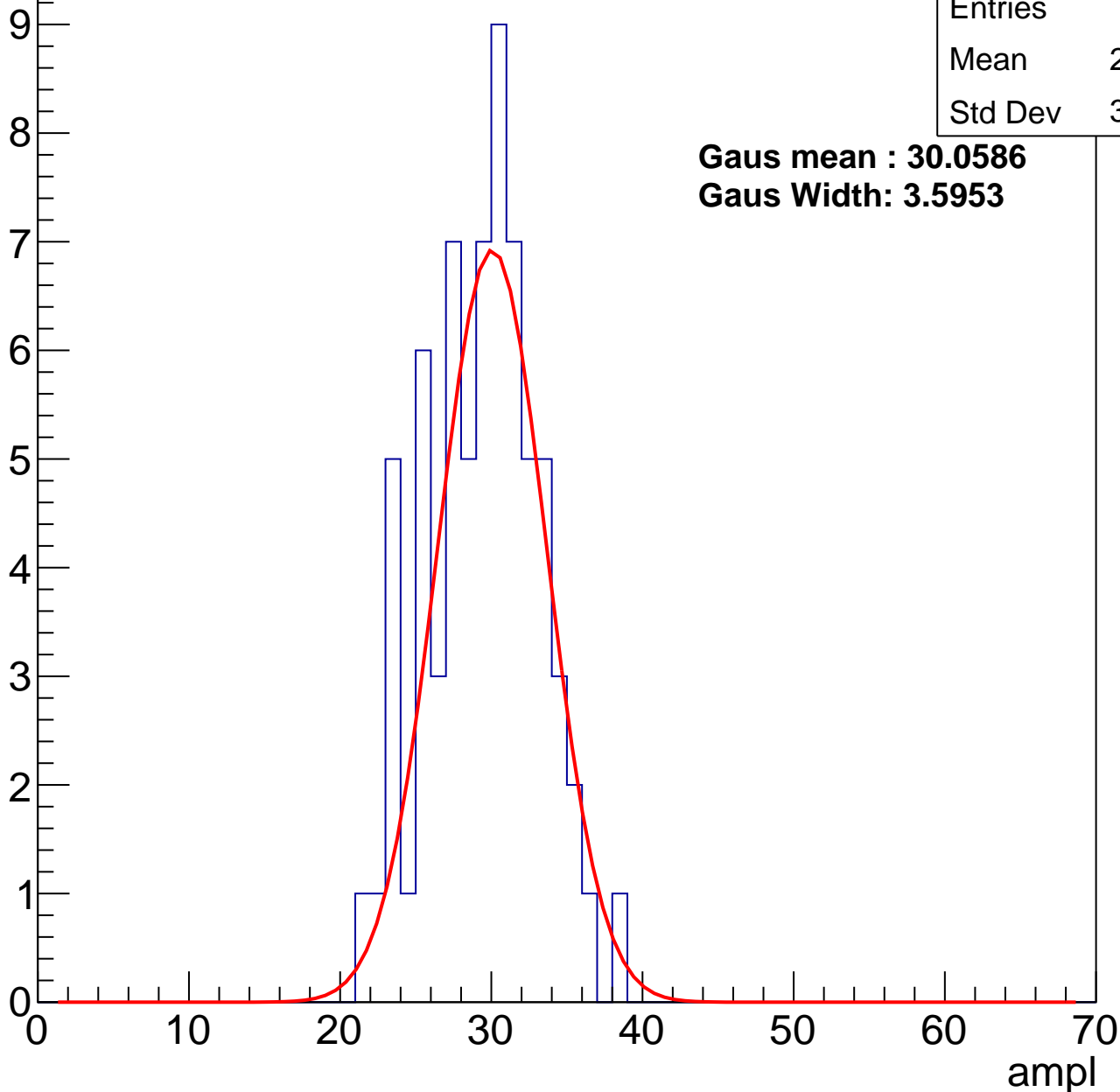
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.99
Std Dev	3.638

Gaus mean : 30.0586

Gaus Width: 3.5953



B1L101S, U15-ch17, adc1

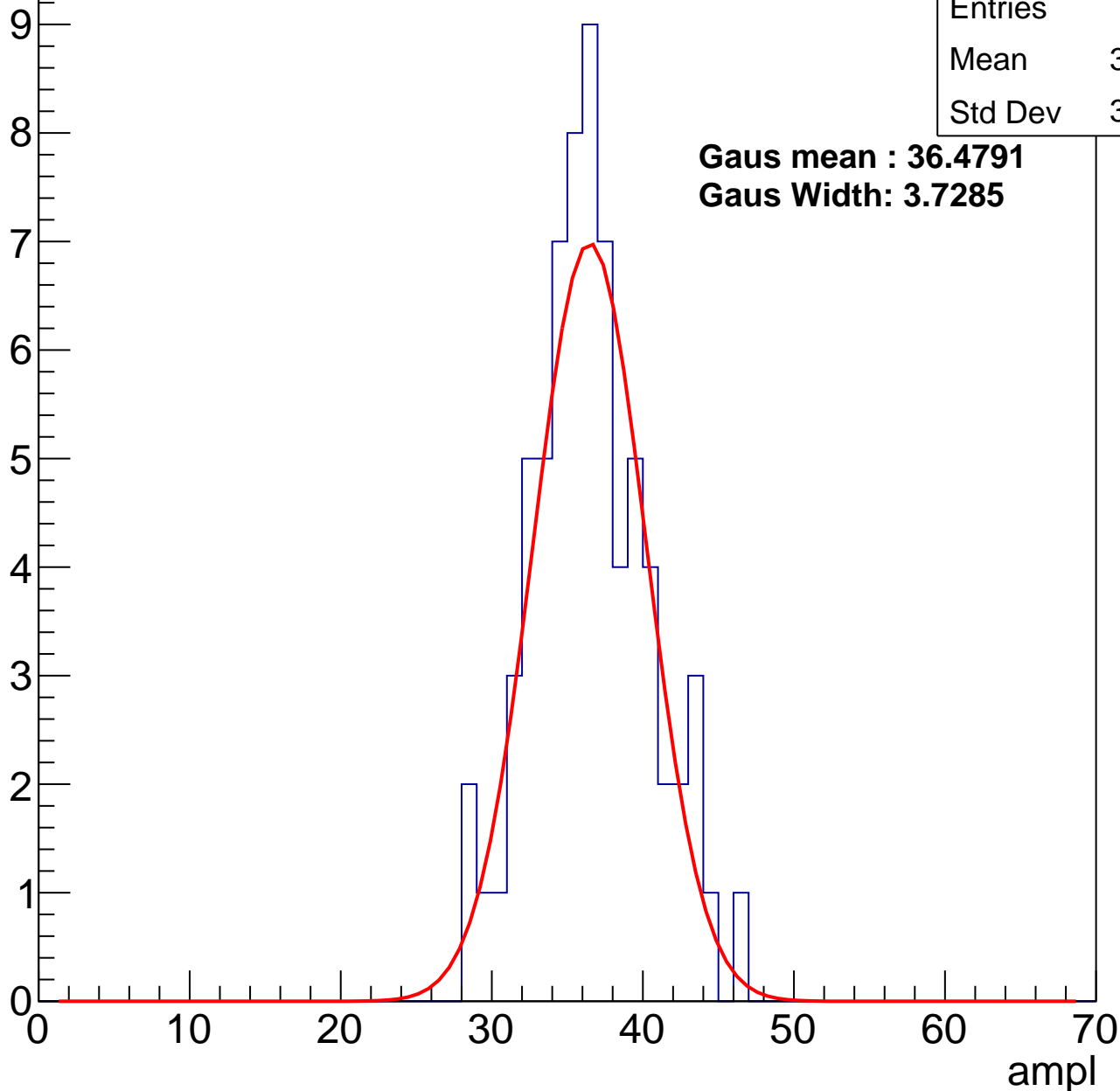
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	36.09
Std Dev	3.805

Gaus mean : 36.4791

Gaus Width: 3.7285



B1L101S, U15-ch17, adc2

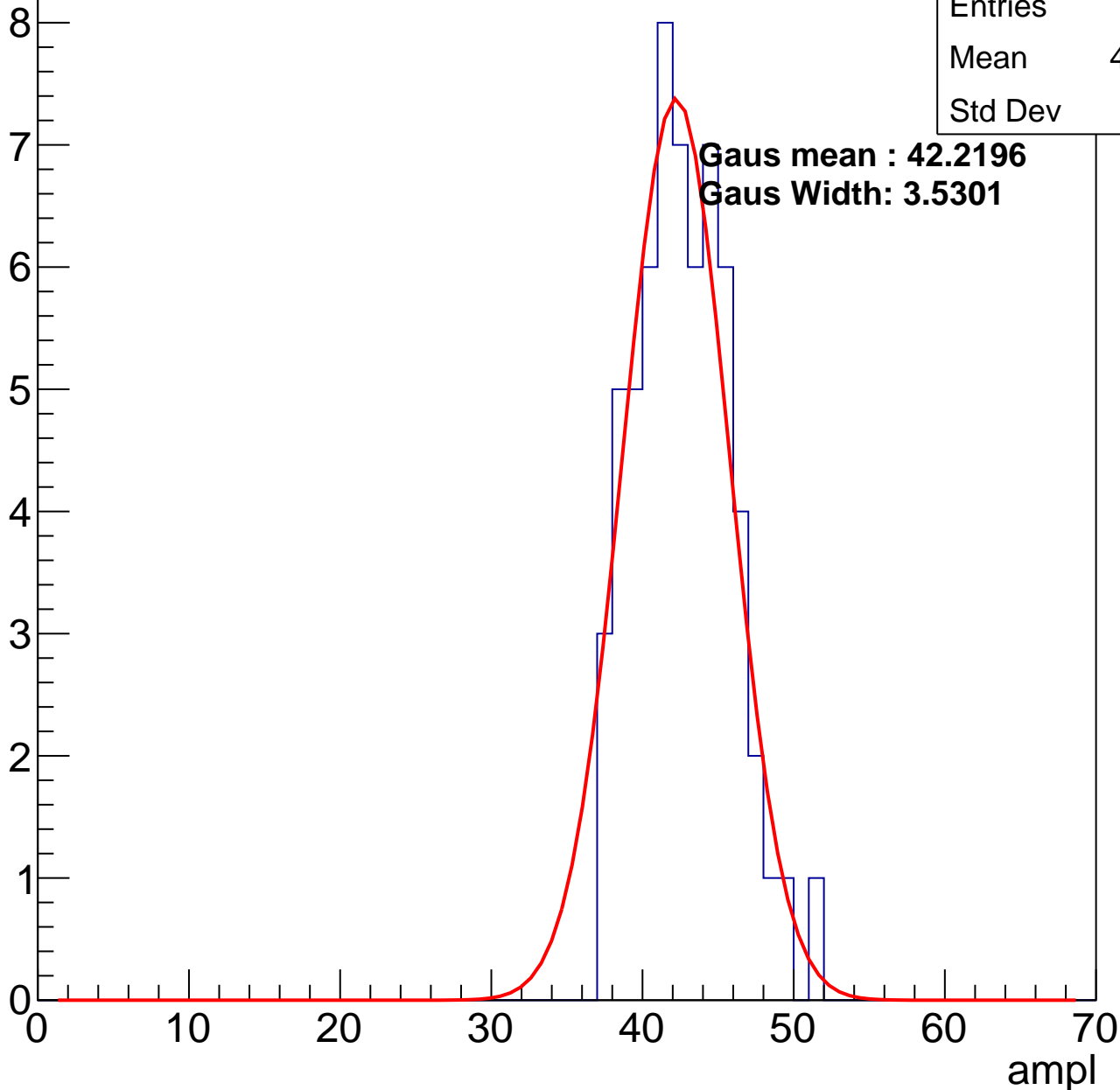
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.26
Std Dev	3.1

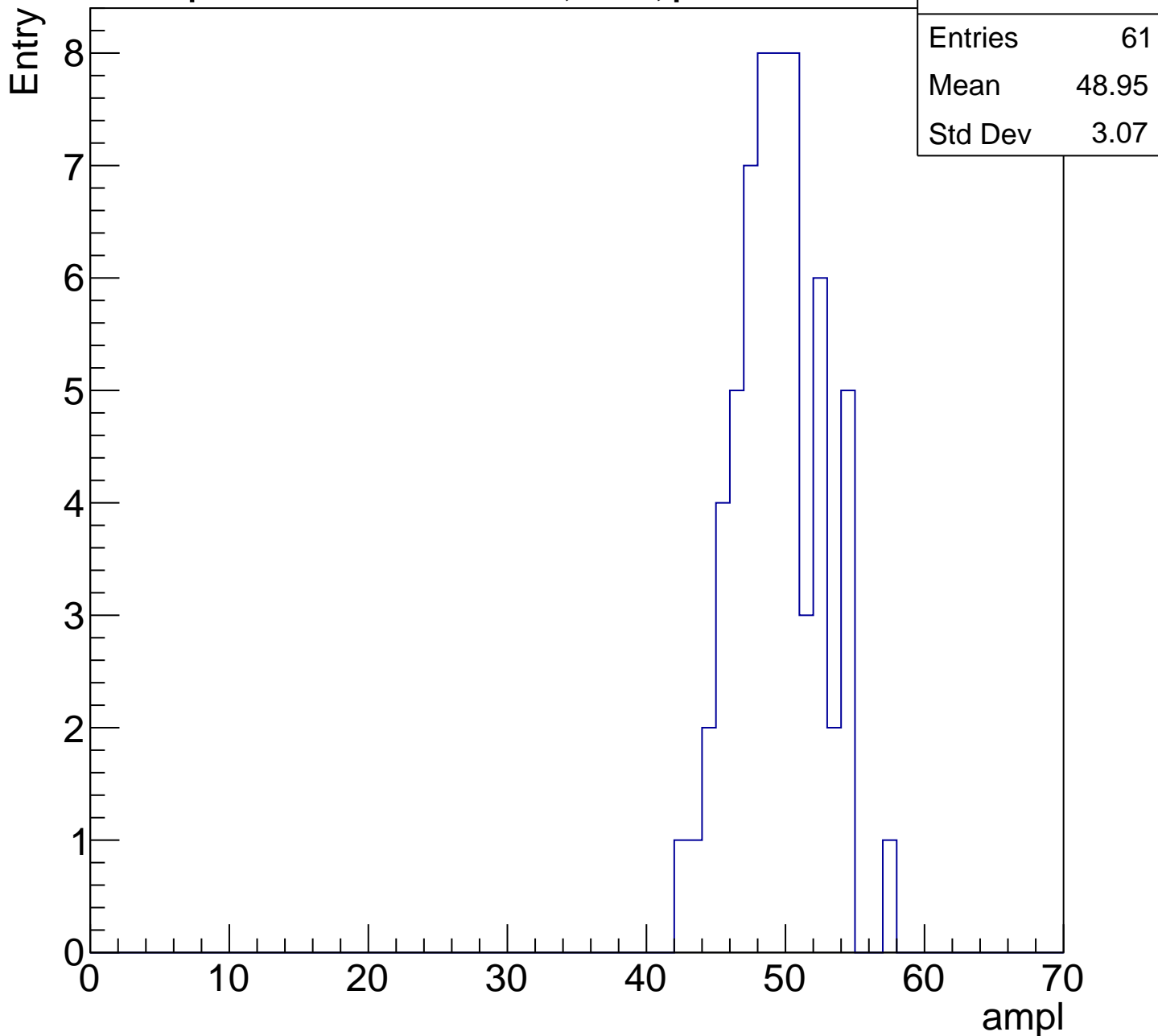
Gaus mean : 42.2196

Gaus Width: 3.5301



B1L101S, U15-ch17, adc3

calib_packv5_042523_0143.root, FC#0, port D2

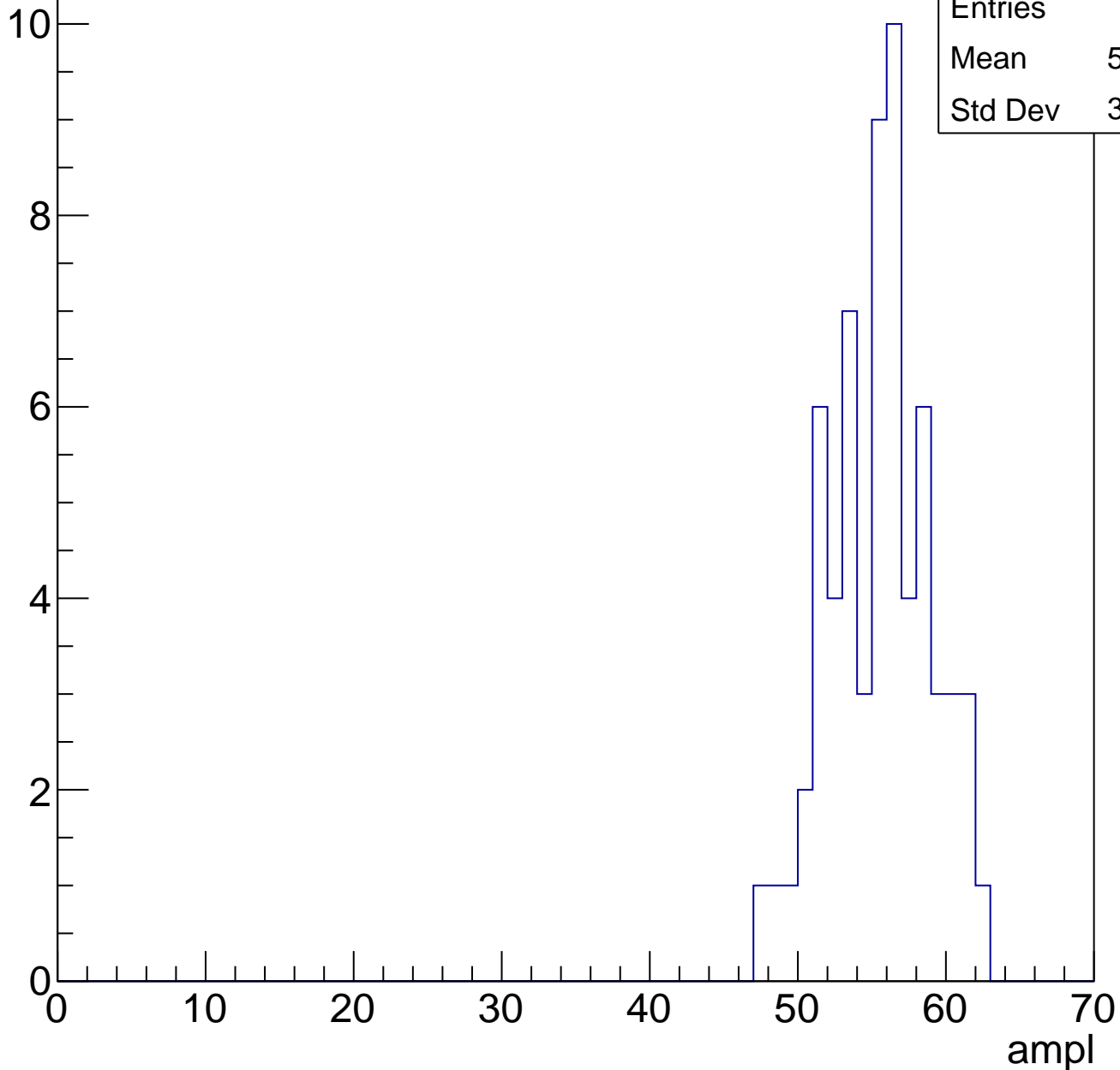


B1L101S, U15-ch17, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	64
Mean	55.06
Std Dev	3.358

Entry

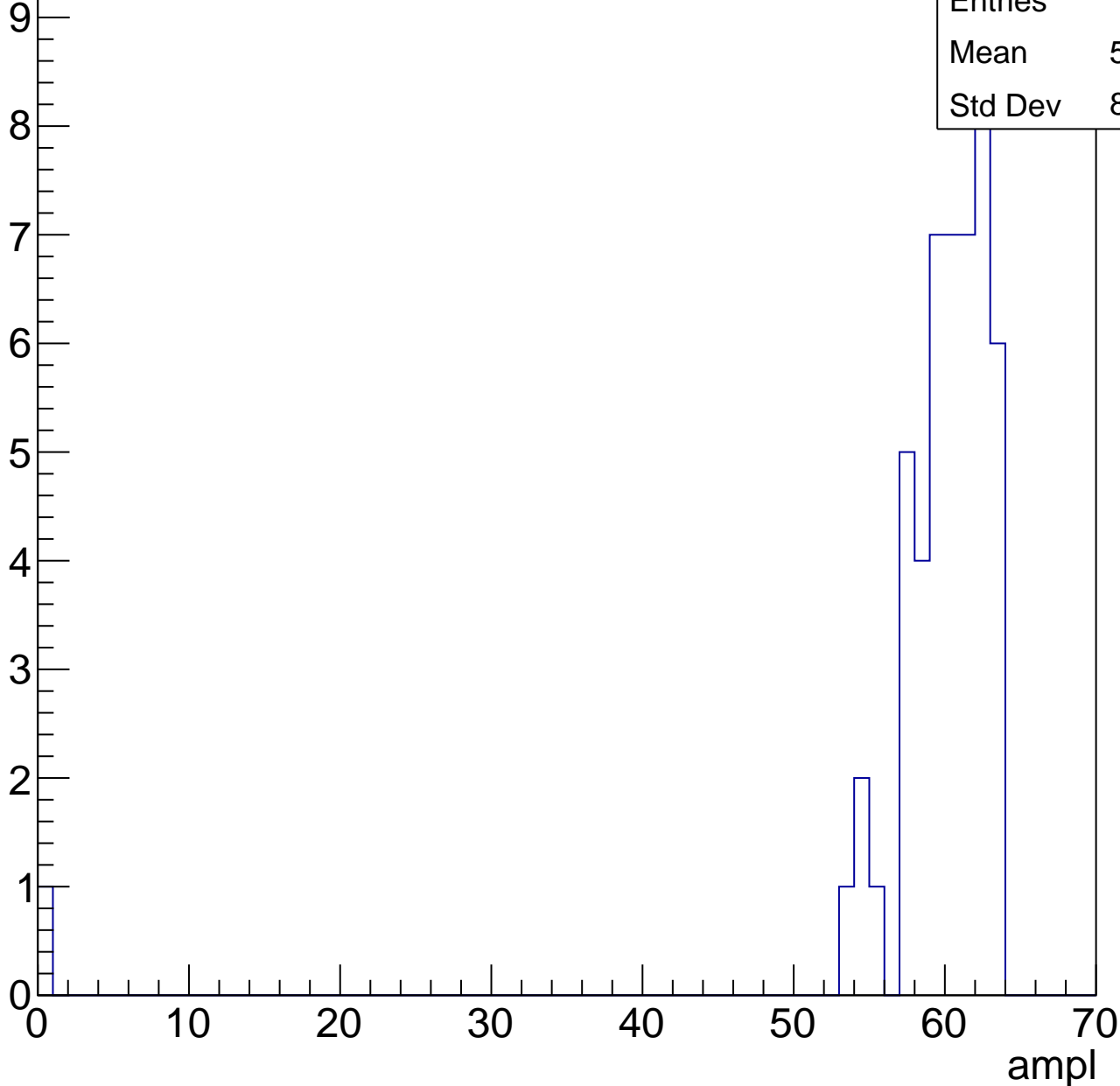


B1L101S, U15-ch17, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

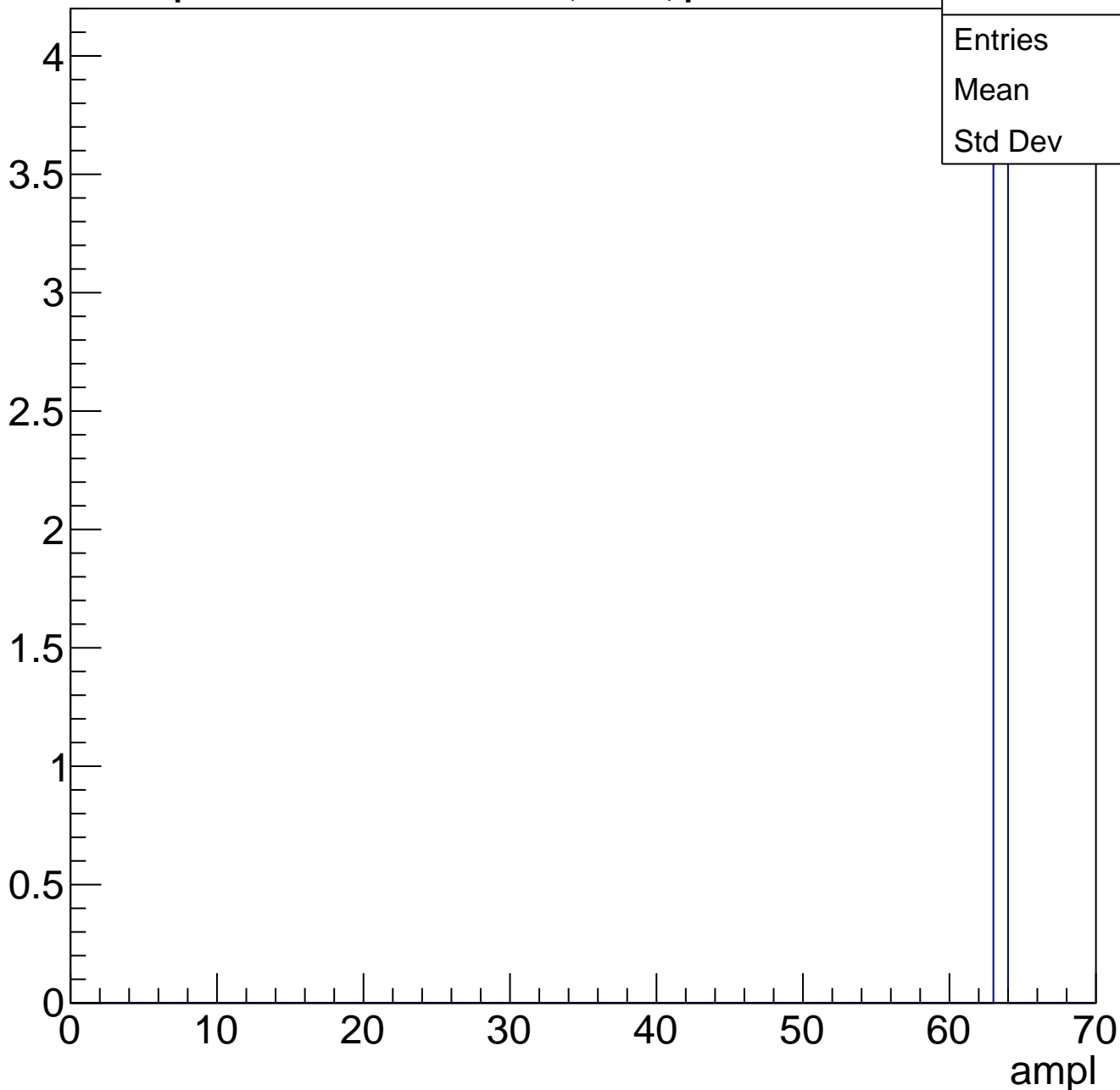
Entries	50
Mean	58.58
Std Dev	8.729



B1L101S, U15-ch17, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch17, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch18, adc0

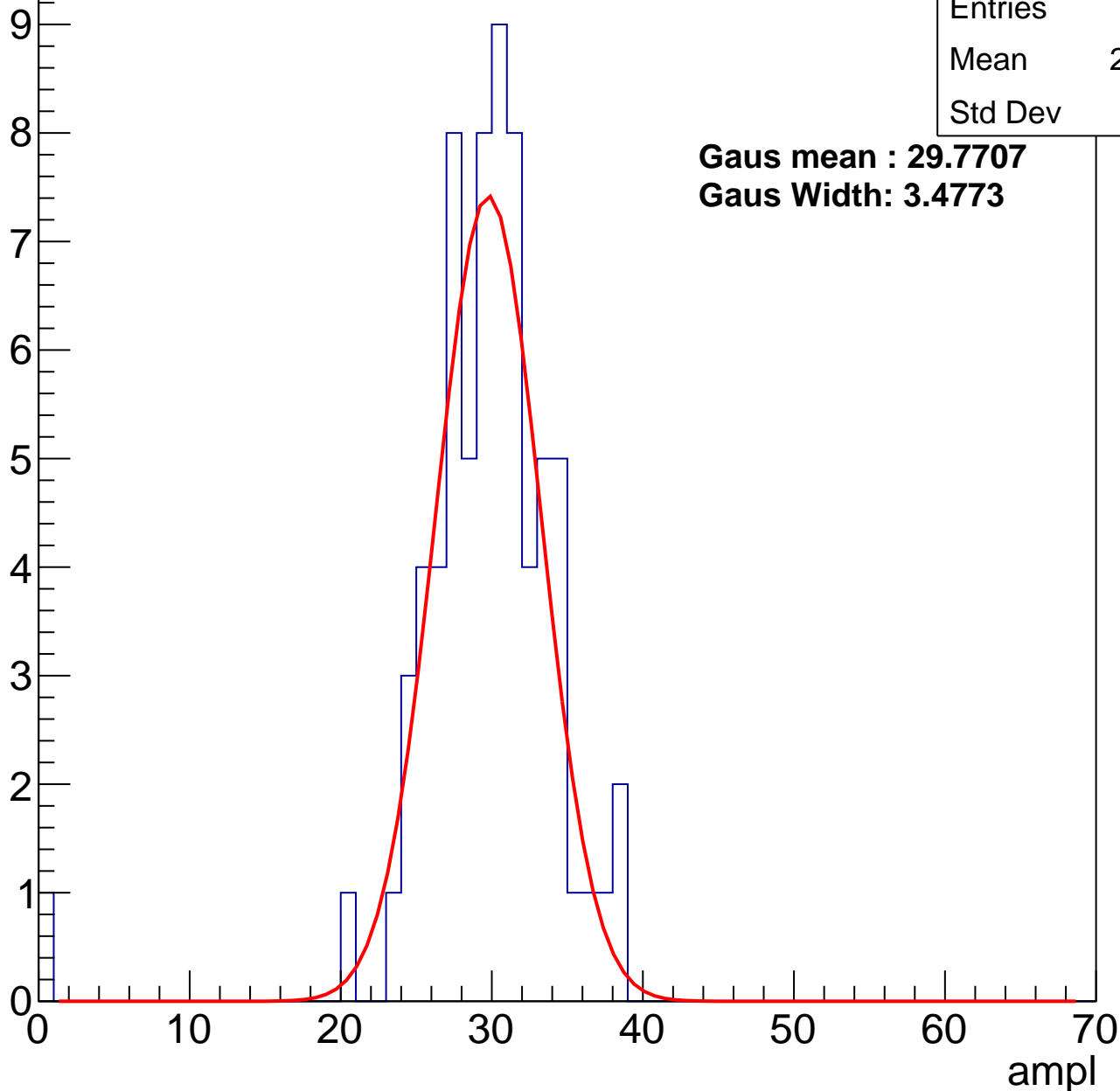
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	29.18
Std Dev	4.98

Gaus mean : 29.7707

Gaus Width: 3.4773



B1L101S, U15-ch18, adc1

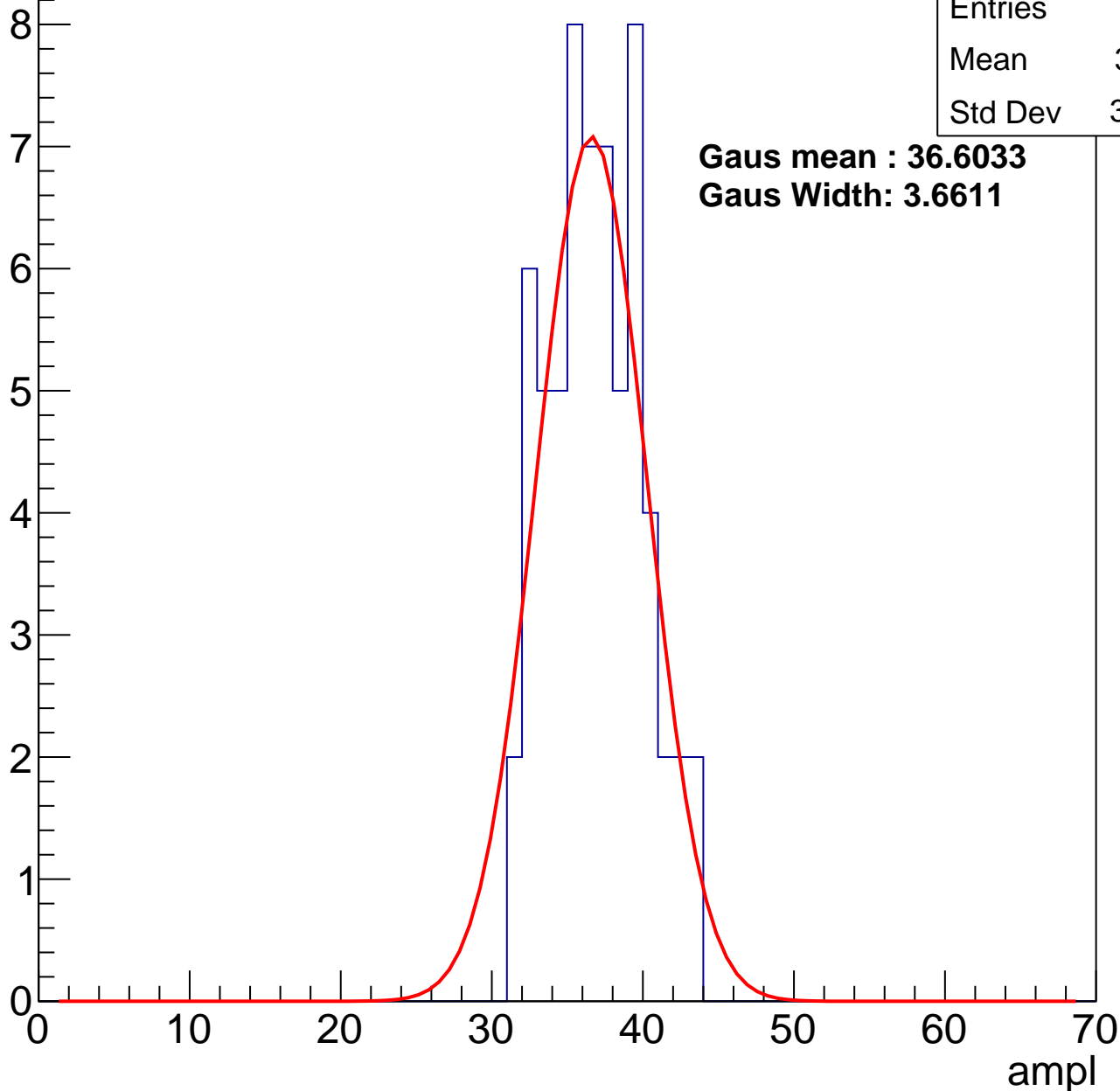
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.41
Std Dev	3.064

Gaus mean : 36.6033

Gaus Width: 3.6611



B1L101S, U15-ch18, adc2

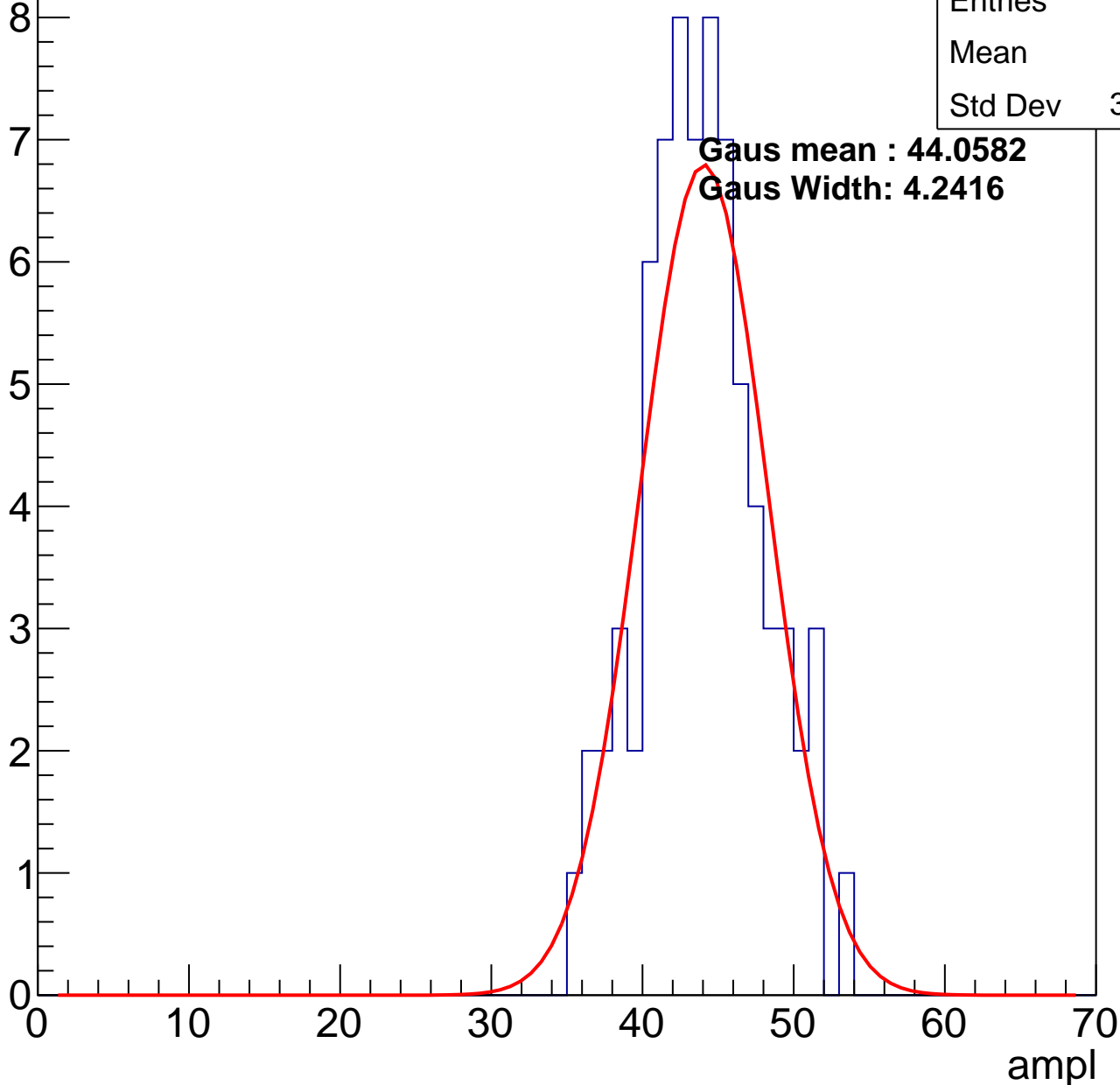
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.5
Std Dev	3.909

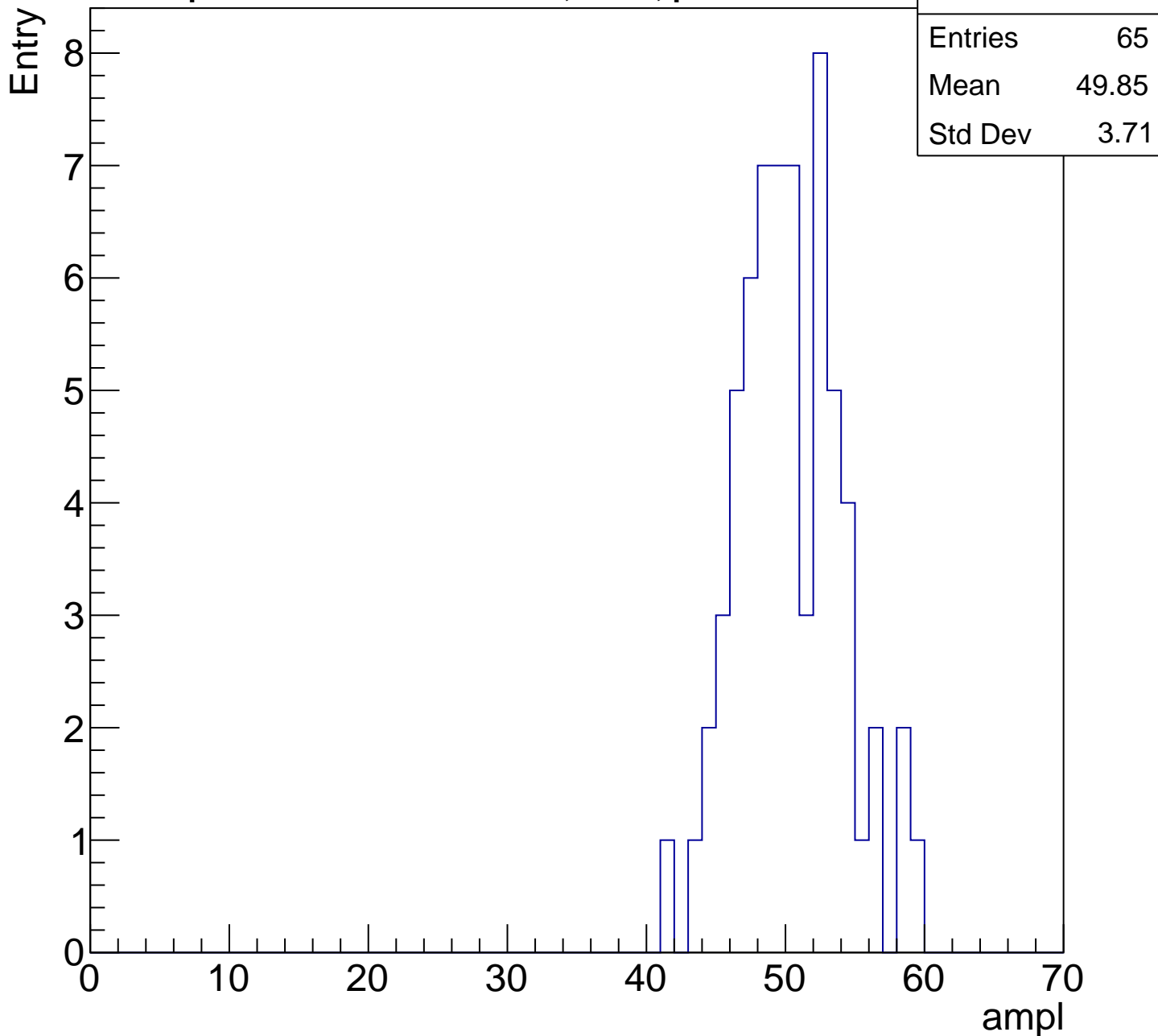
Gaus mean : 44.0582

Gaus Width: 4.2416



B1L101S, U15-ch18, adc3

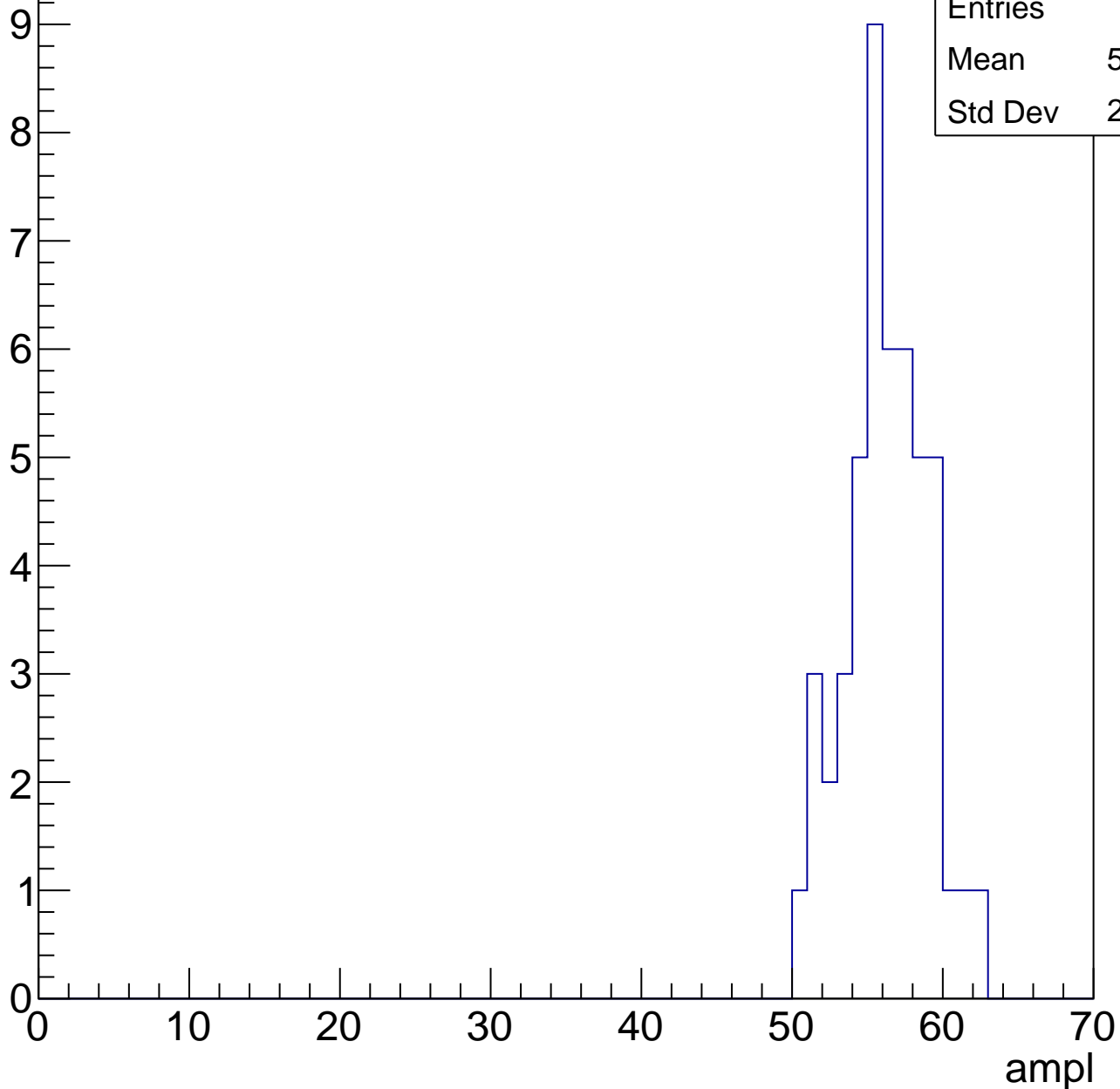
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch18, adc4

calib_packv5_042523_0143.root, FC#0, port D2

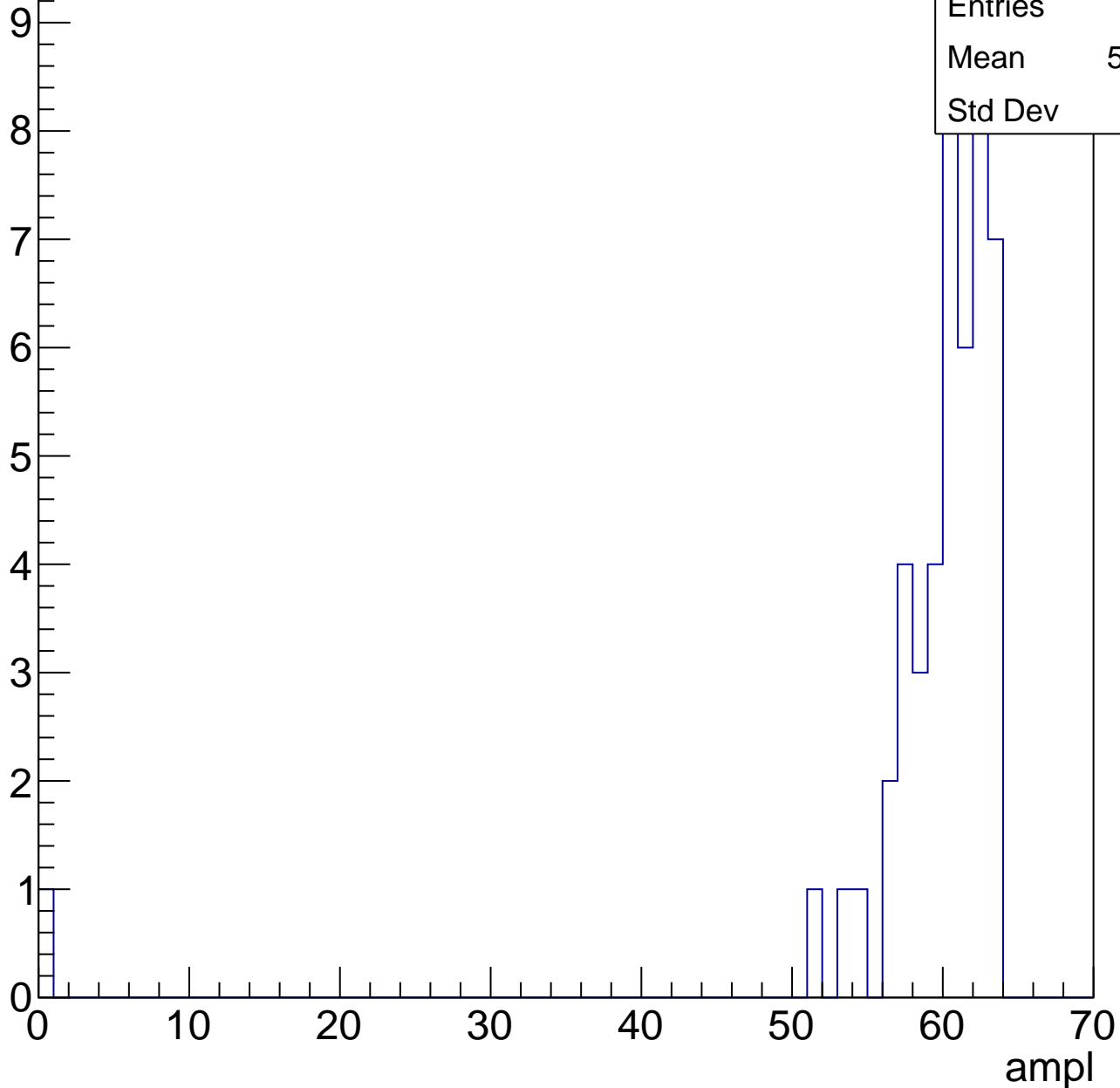
Entry



B1L101S, U15-ch18, adc5

calib_packv5_042523_0143.root, FC#0, port D2

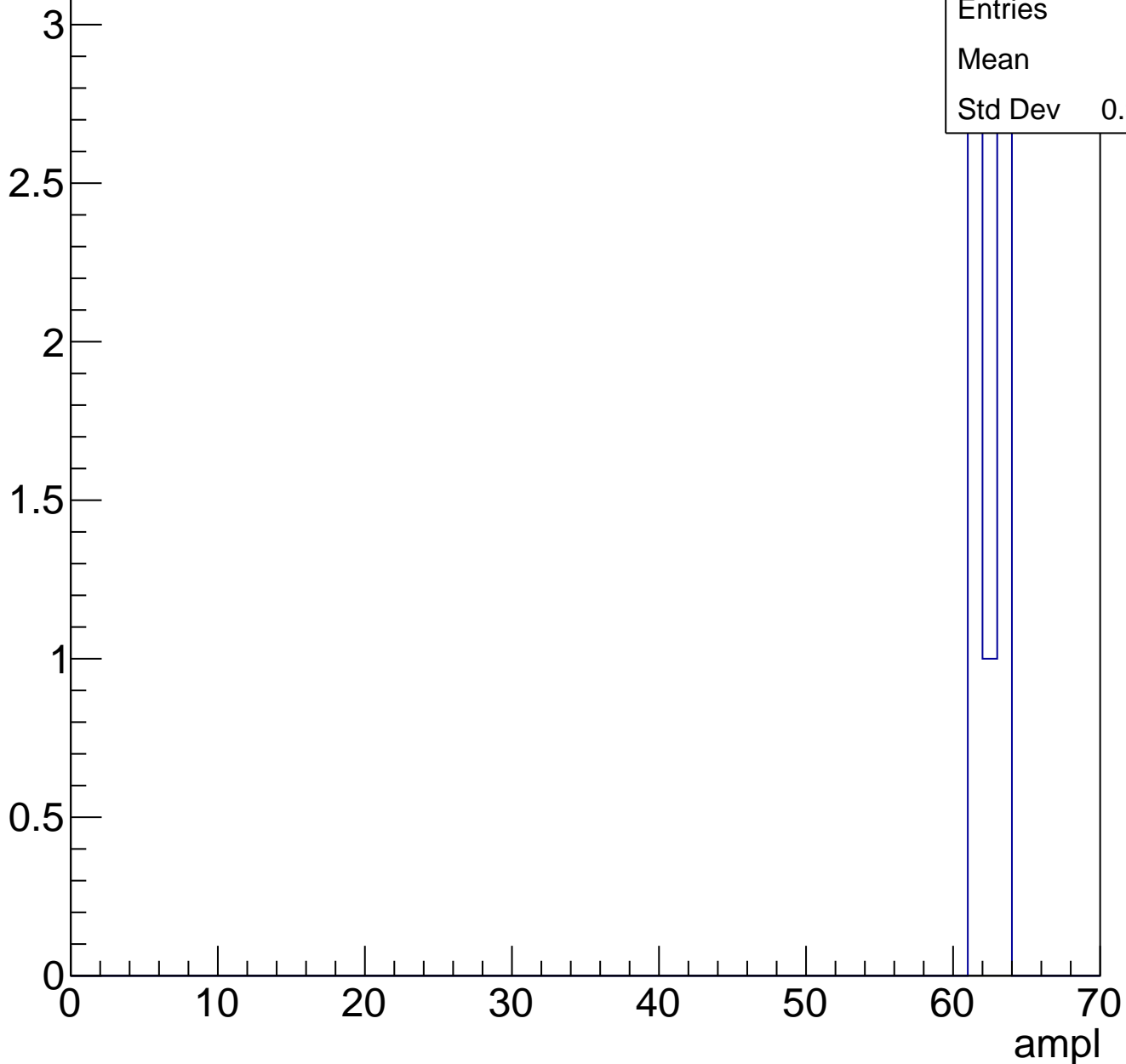
Entry



B1L101S, U15-ch18, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch18, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch19, adc0

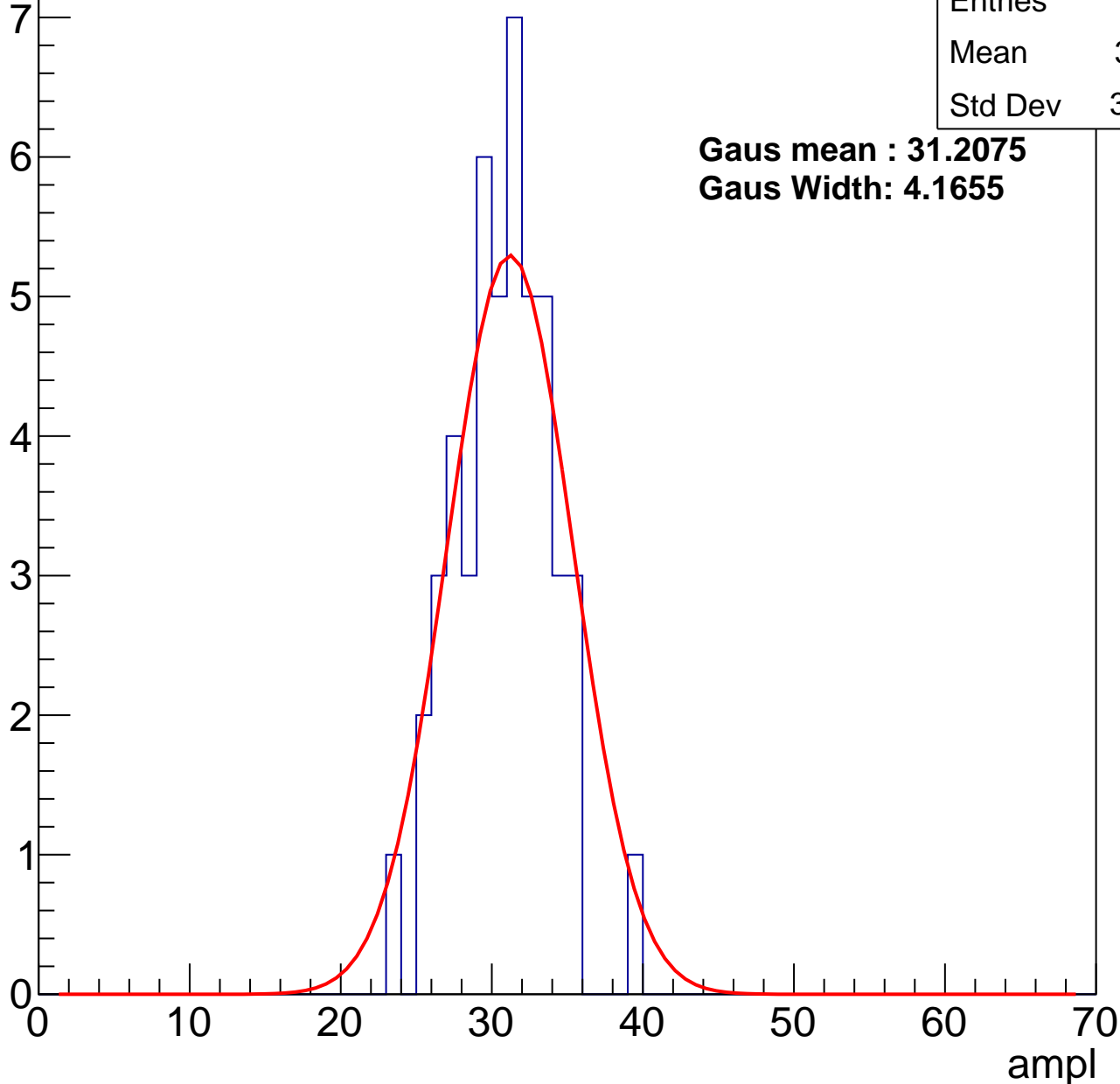
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	30.31
Std Dev	3.137

Gaus mean : 31.2075

Gaus Width: 4.1655



B1L101S, U15-ch19, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	36.17
Std Dev	5.494

Gaus mean : 37.4788

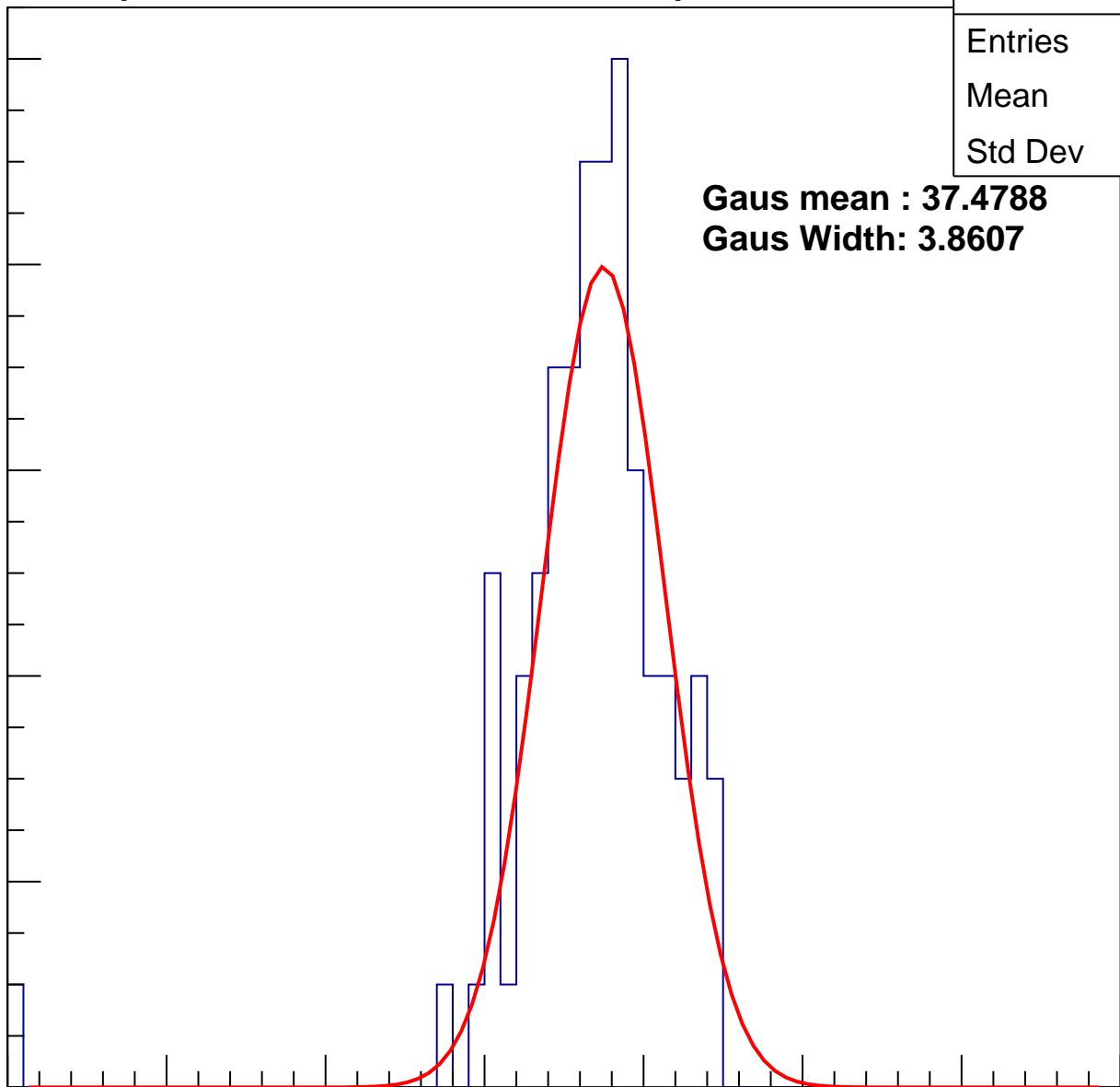
Gaus Width: 3.8607

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch19, adc2

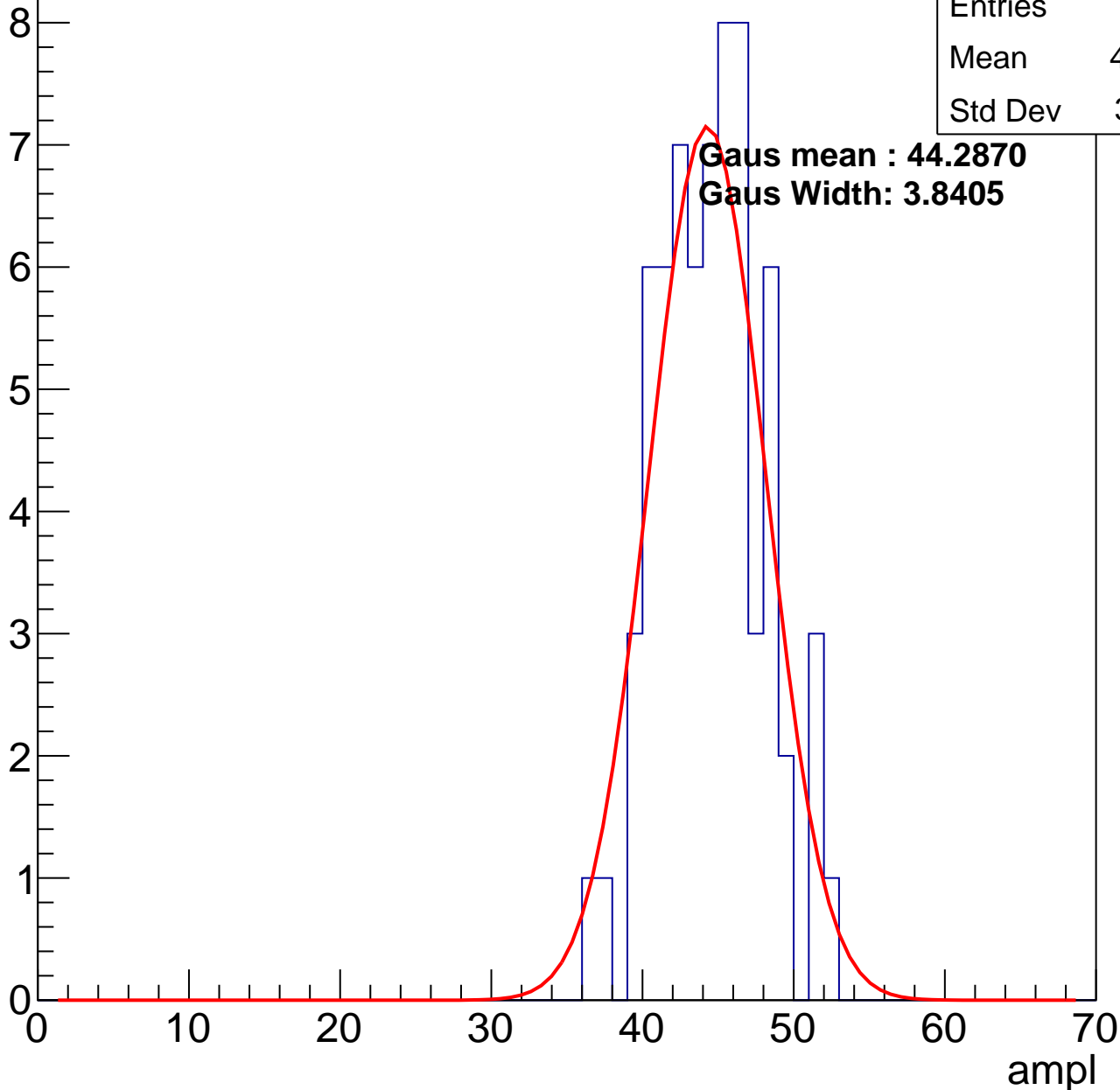
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	44.06
Std Dev	3.421

Gaus mean : 44.2870

Gaus Width: 3.8405



B1L101S, U15-ch19, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	51.37
Std Dev	3.449

Entry

10

8

6

4

2

0

0

10

20

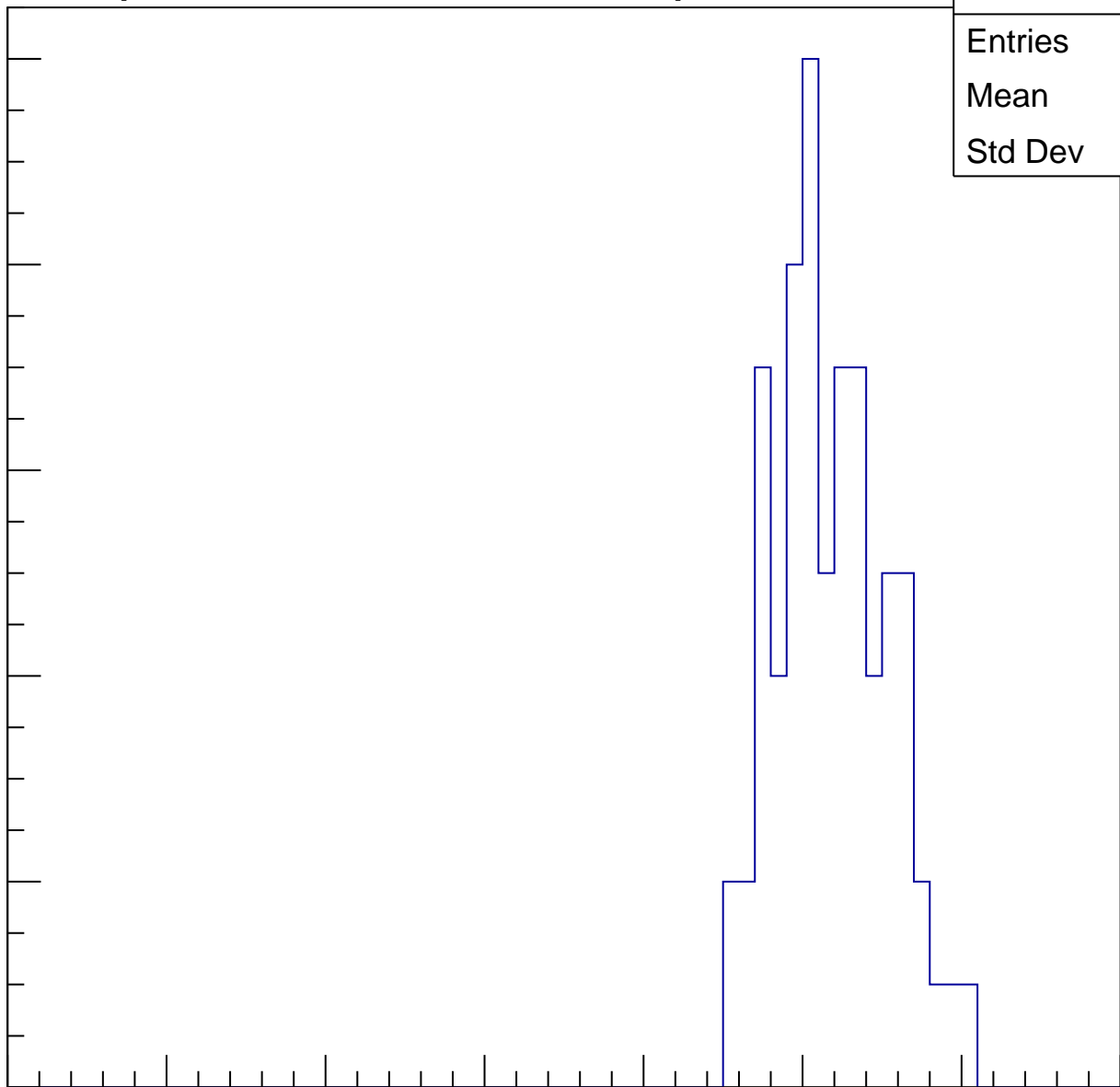
30

40

50

60

ampl

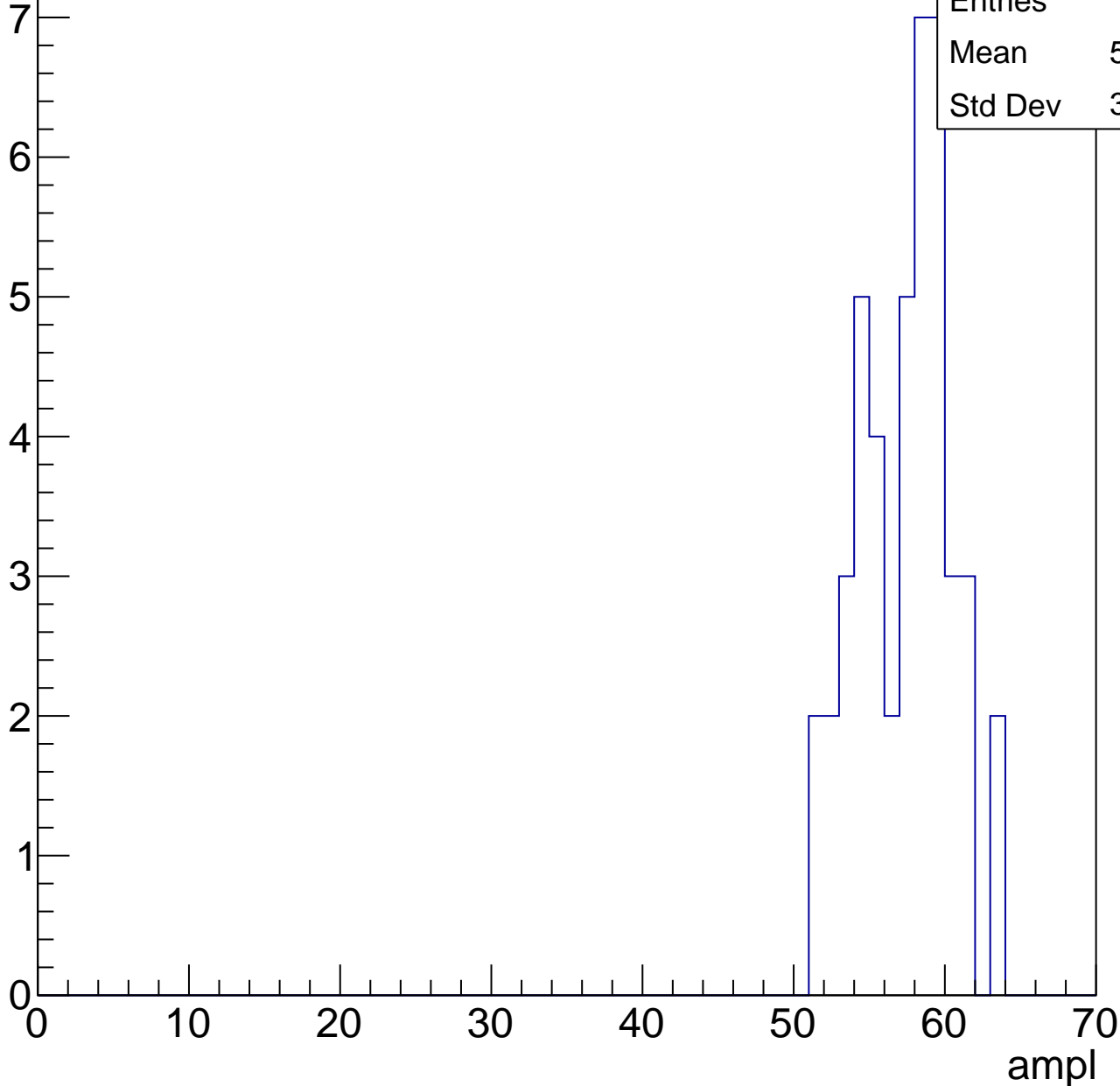


B1L101S, U15-ch19, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	56.89
Std Dev	3.035

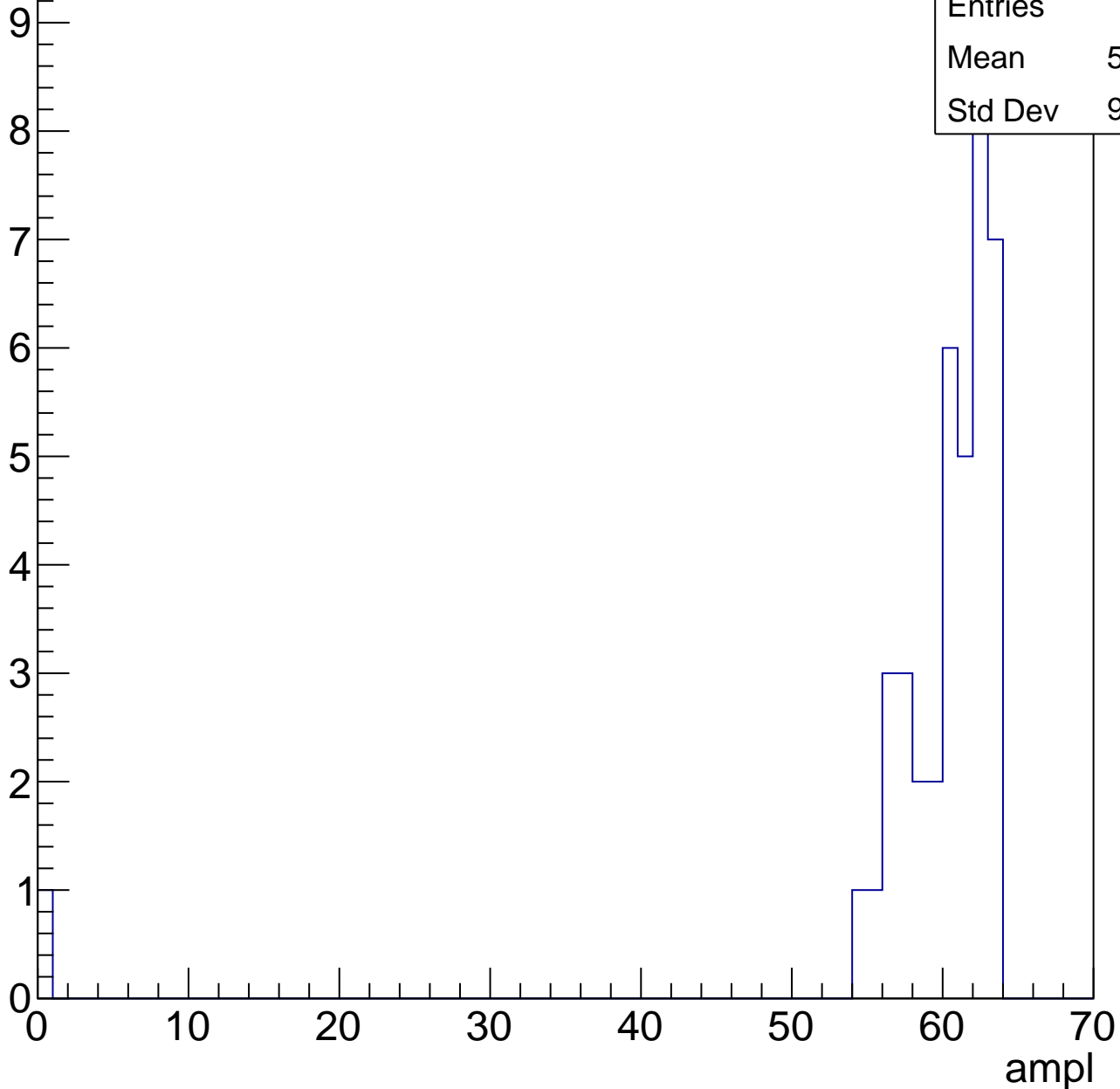


B1L101S, U15-ch19, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

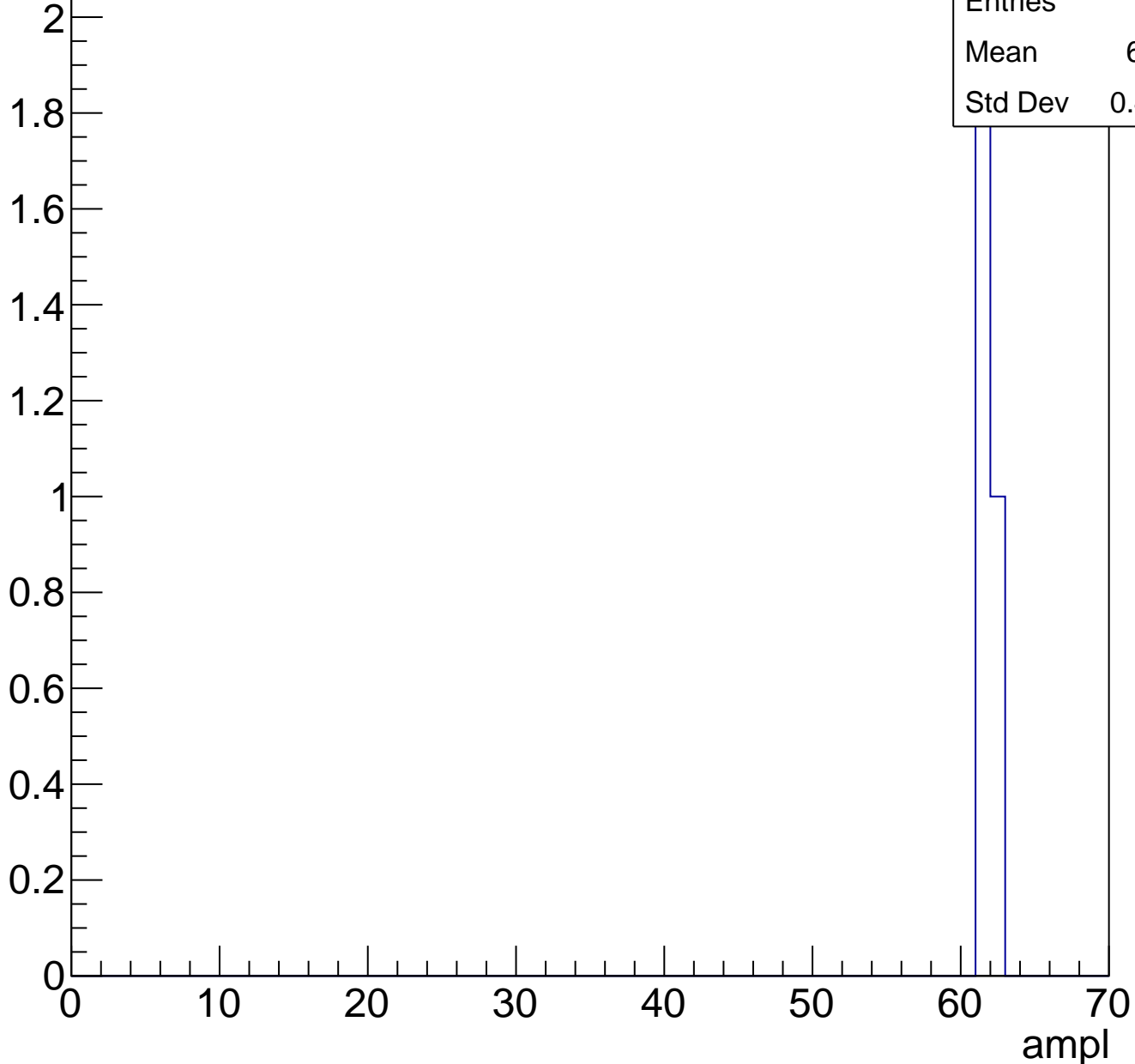
Entries	40
Mean	58.65
Std Dev	9.717



B1L101S, U15-ch19, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	61.33
Std Dev	0.4714

B1L101S, U15-ch19, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch20, adc0

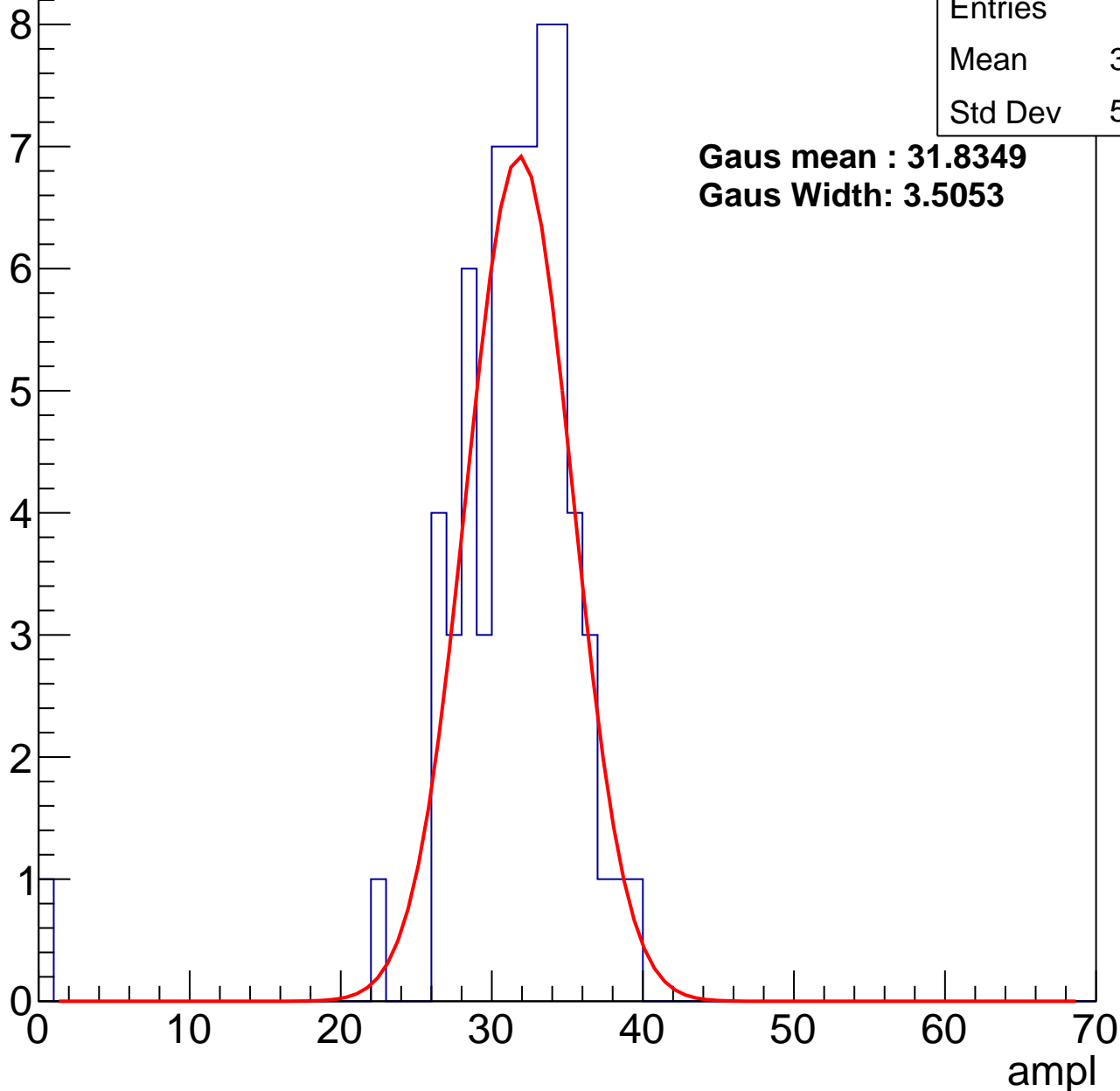
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	30.94
Std Dev	5.056

Gaus mean : 31.8349

Gaus Width: 3.5053



B1L101S, U15-ch20, adc1

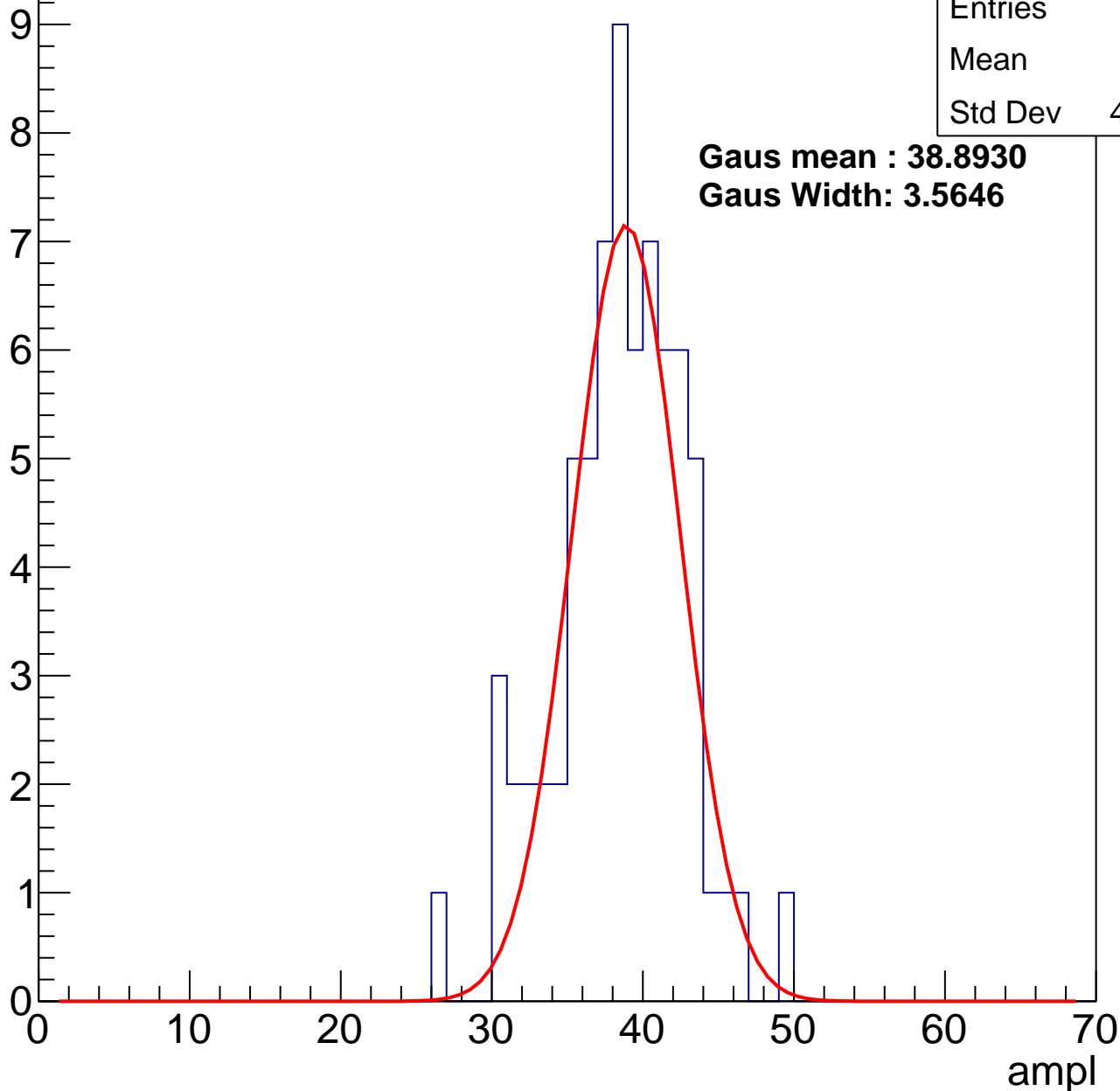
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	38.1
Std Dev	4.147

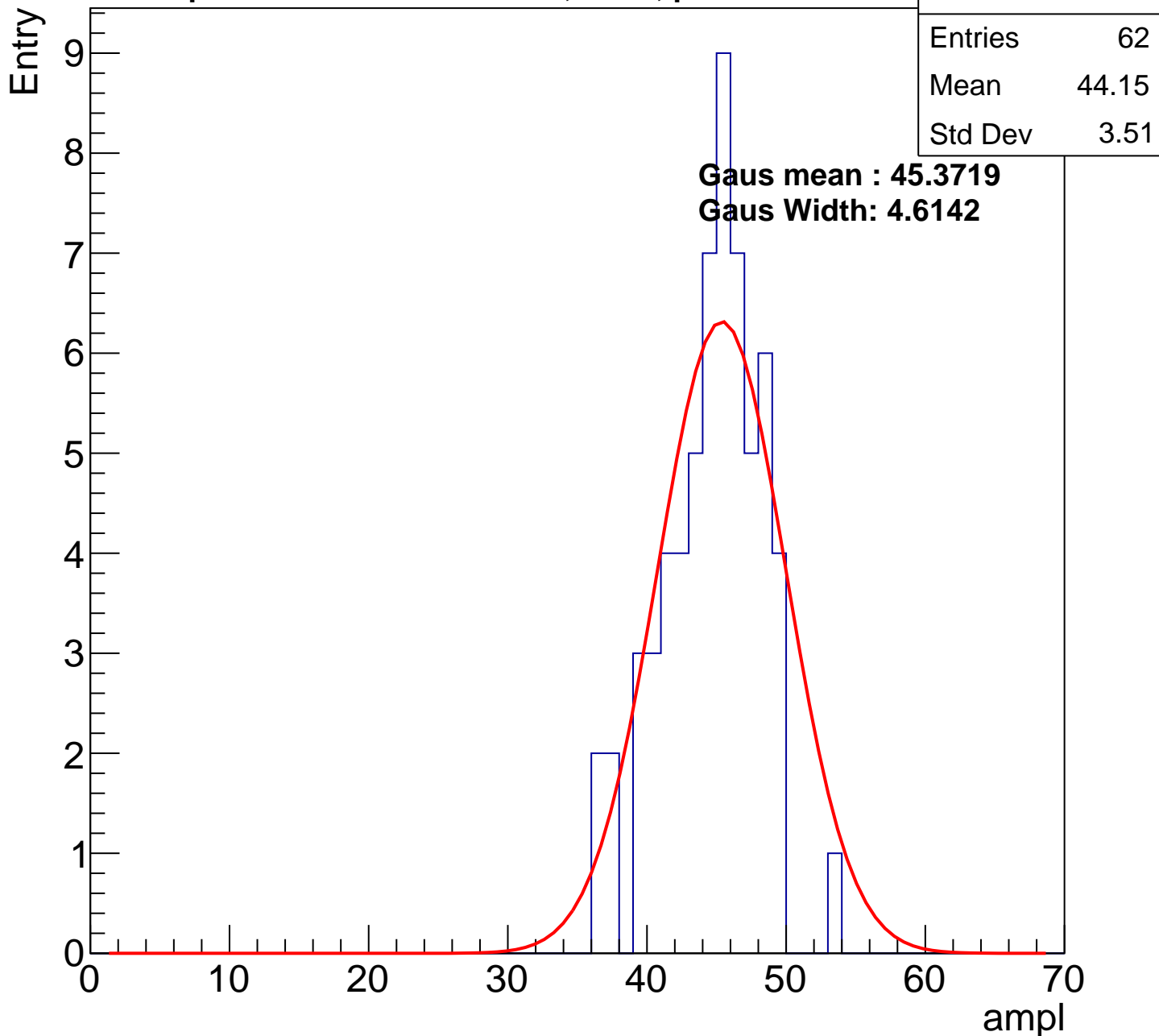
Gaus mean : 38.8930

Gaus Width: 3.5646



B1L101S, U15-ch20, adc2

calib_packv5_042523_0143.root, FC#0, port D2

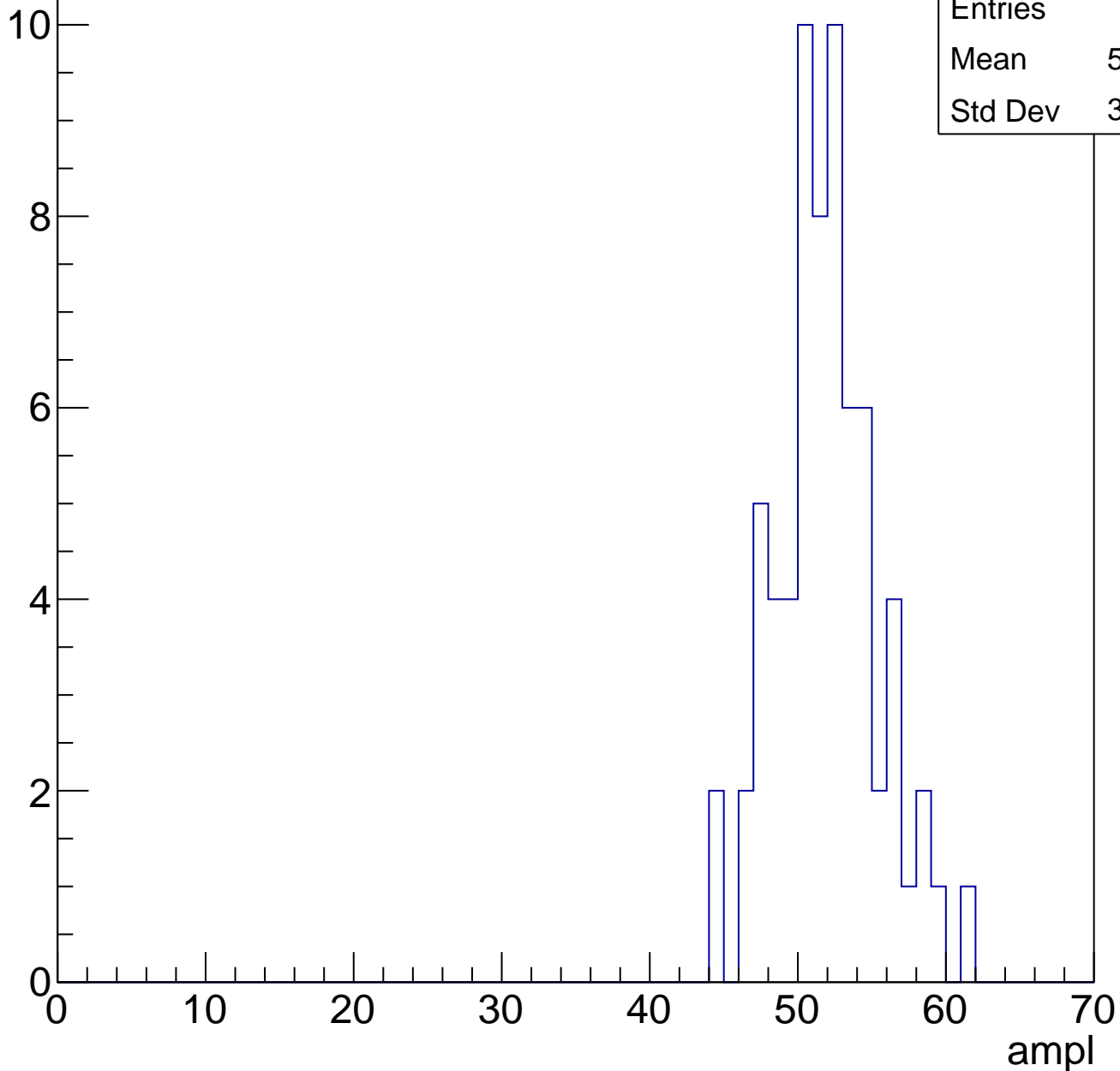


B1L101S, U15-ch20, adc3

calib_packv5_042523_0143.root, FC#0, port D2

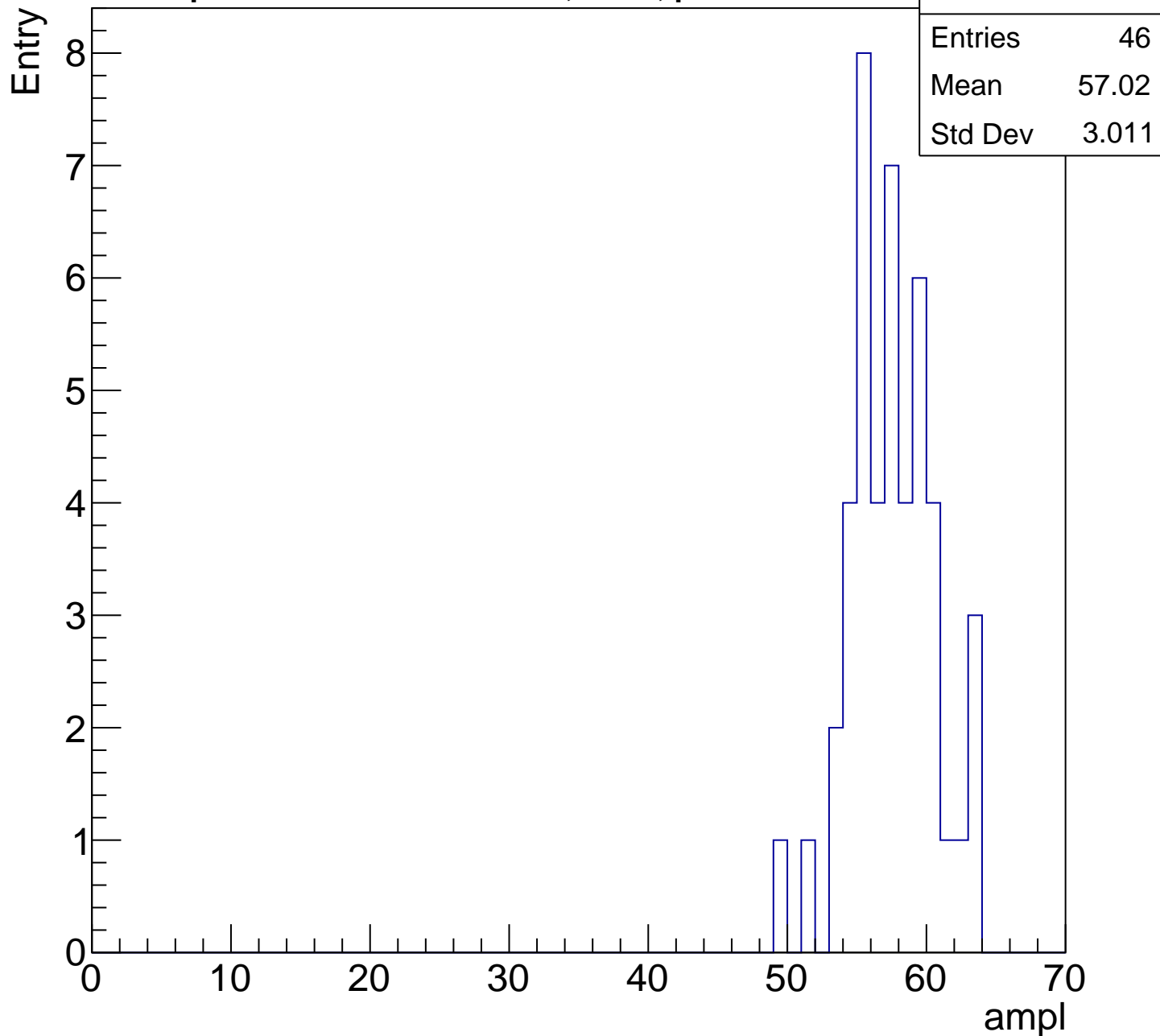
Entry

Entries	68
Mean	51.47
Std Dev	3.415



B1L101S, U15-ch20, adc4

calib_packv5_042523_0143.root, FC#0, port D2

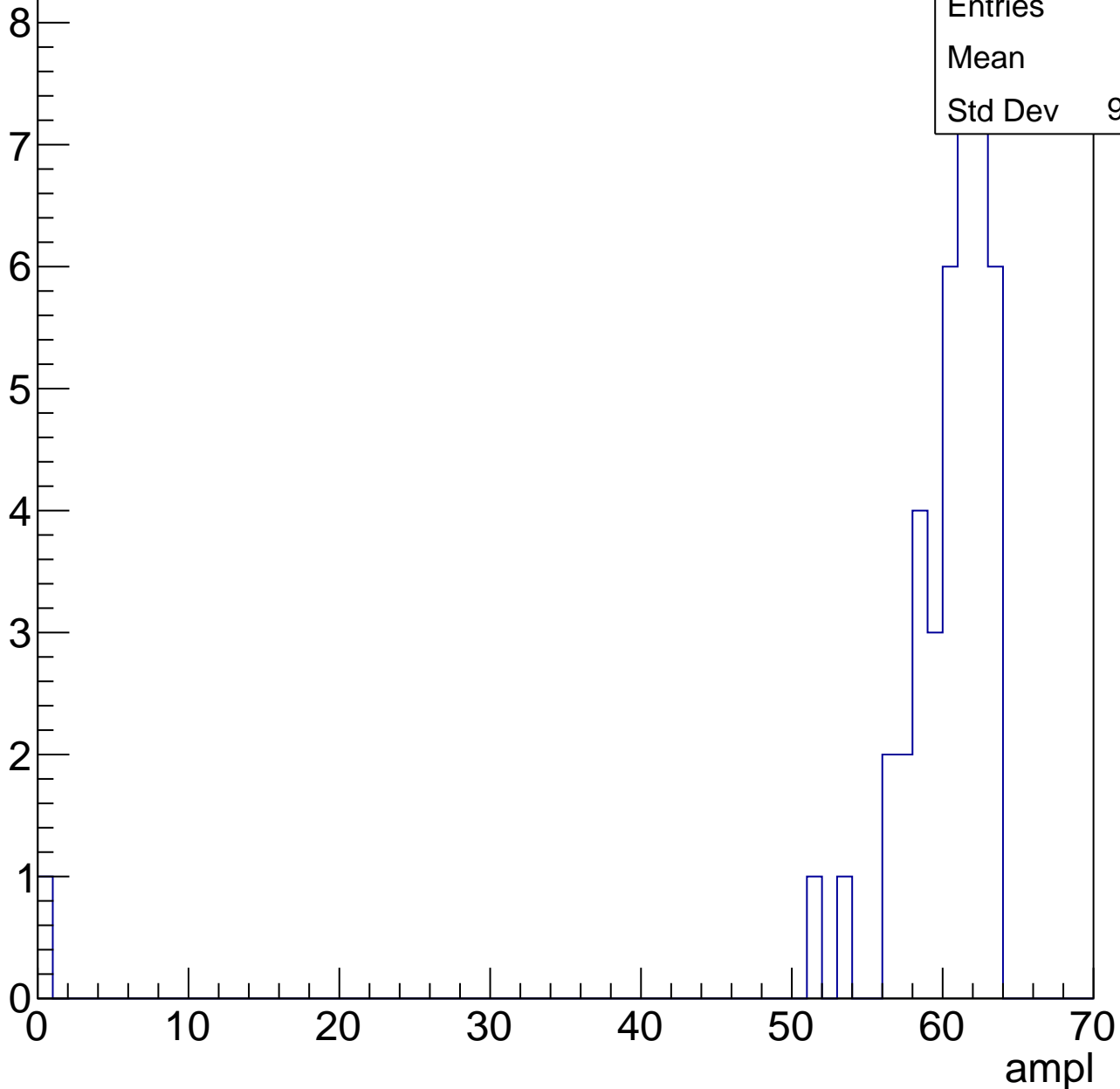


B1L101S, U15-ch20, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.6
Std Dev	9.525



B1L101S, U15-ch20, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch20, adc7

calib_packv5_042523_0143.root, FC#0, port D2

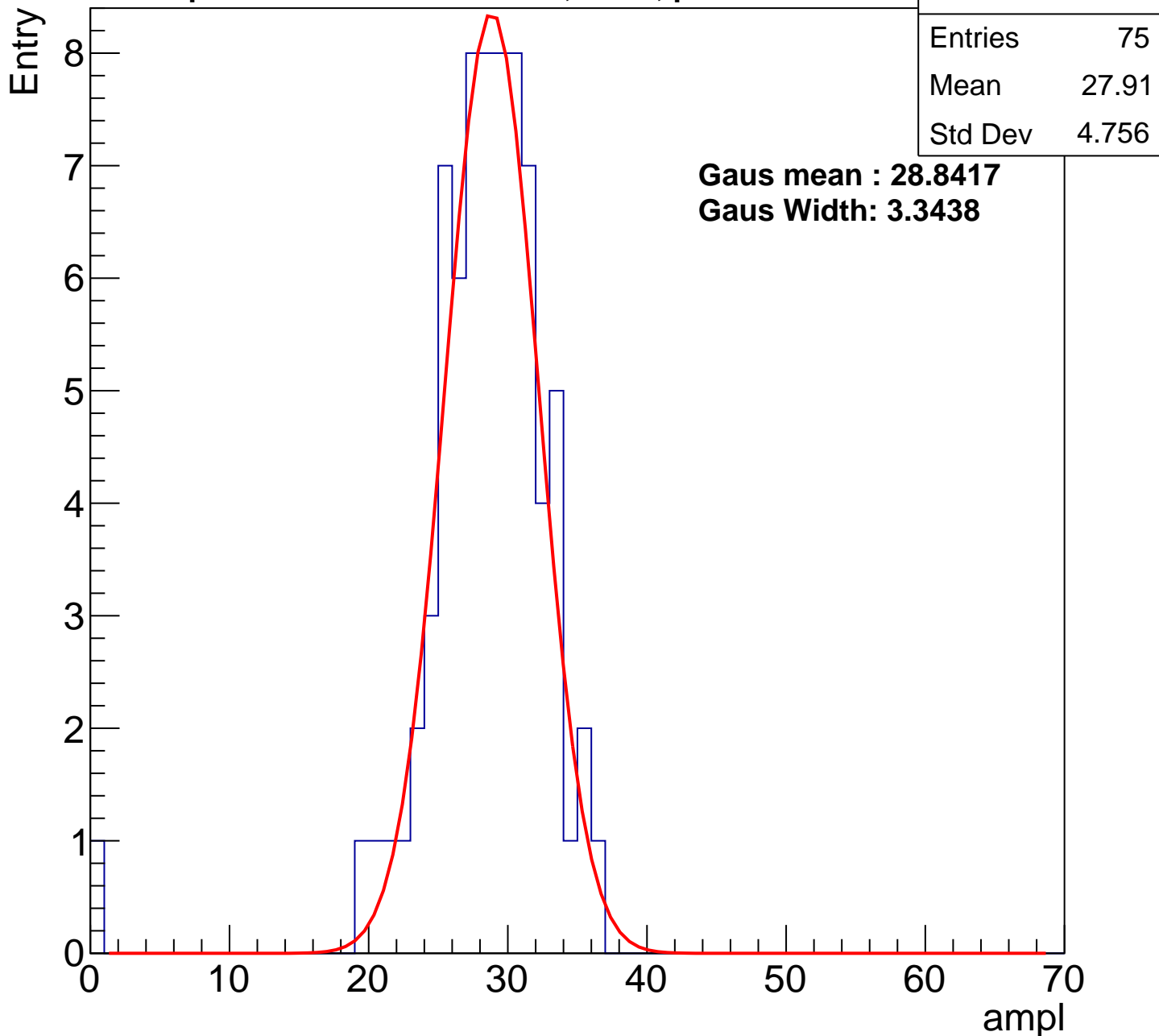
Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch21, adc0

calib_packv5_042523_0143.root, FC#0, port D2



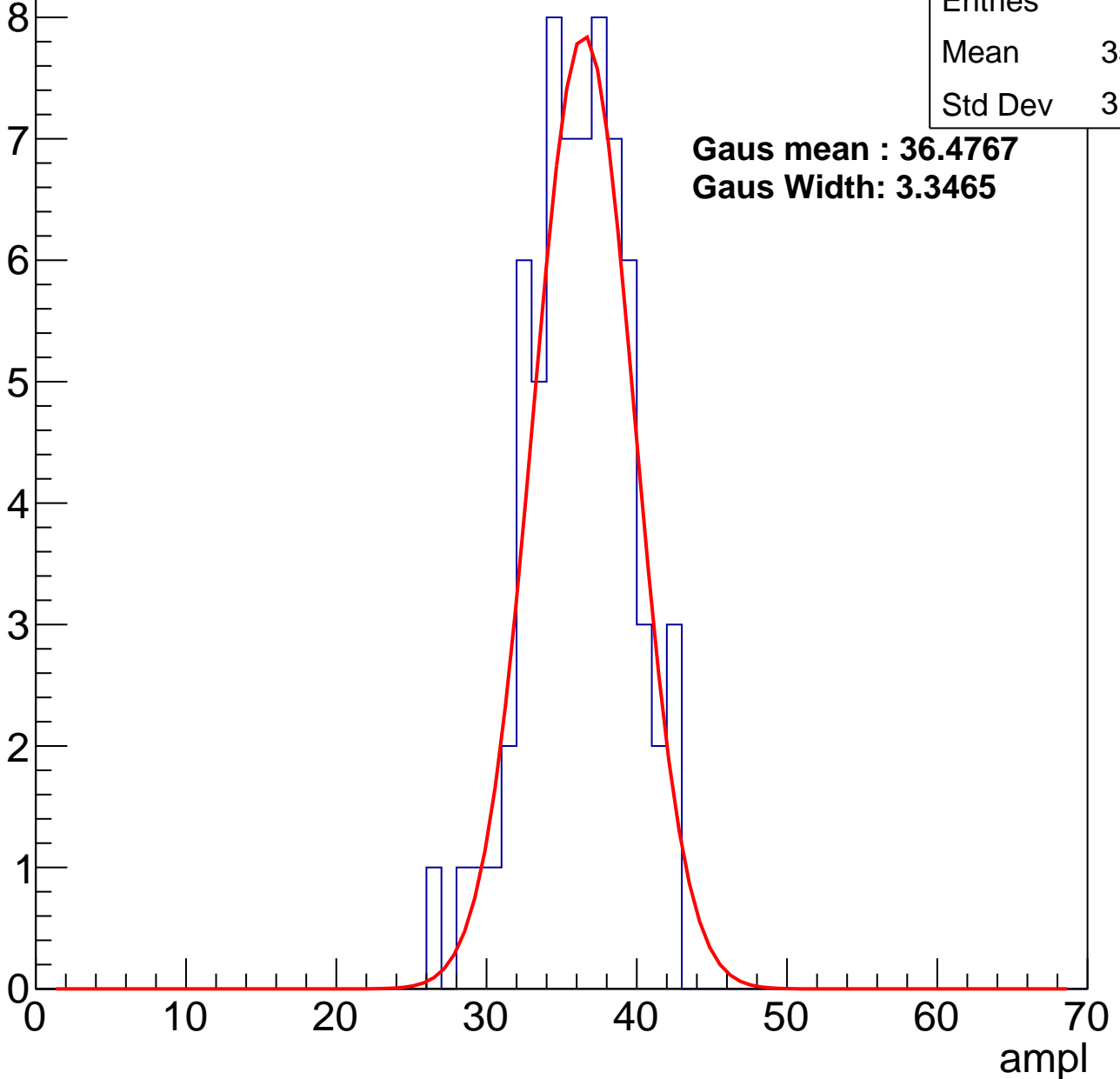
B1L101S, U15-ch21, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	35.66
Std Dev	3.359

Gaus mean : 36.4767
Gaus Width: 3.3465



B1L101S, U15-ch21, adc2

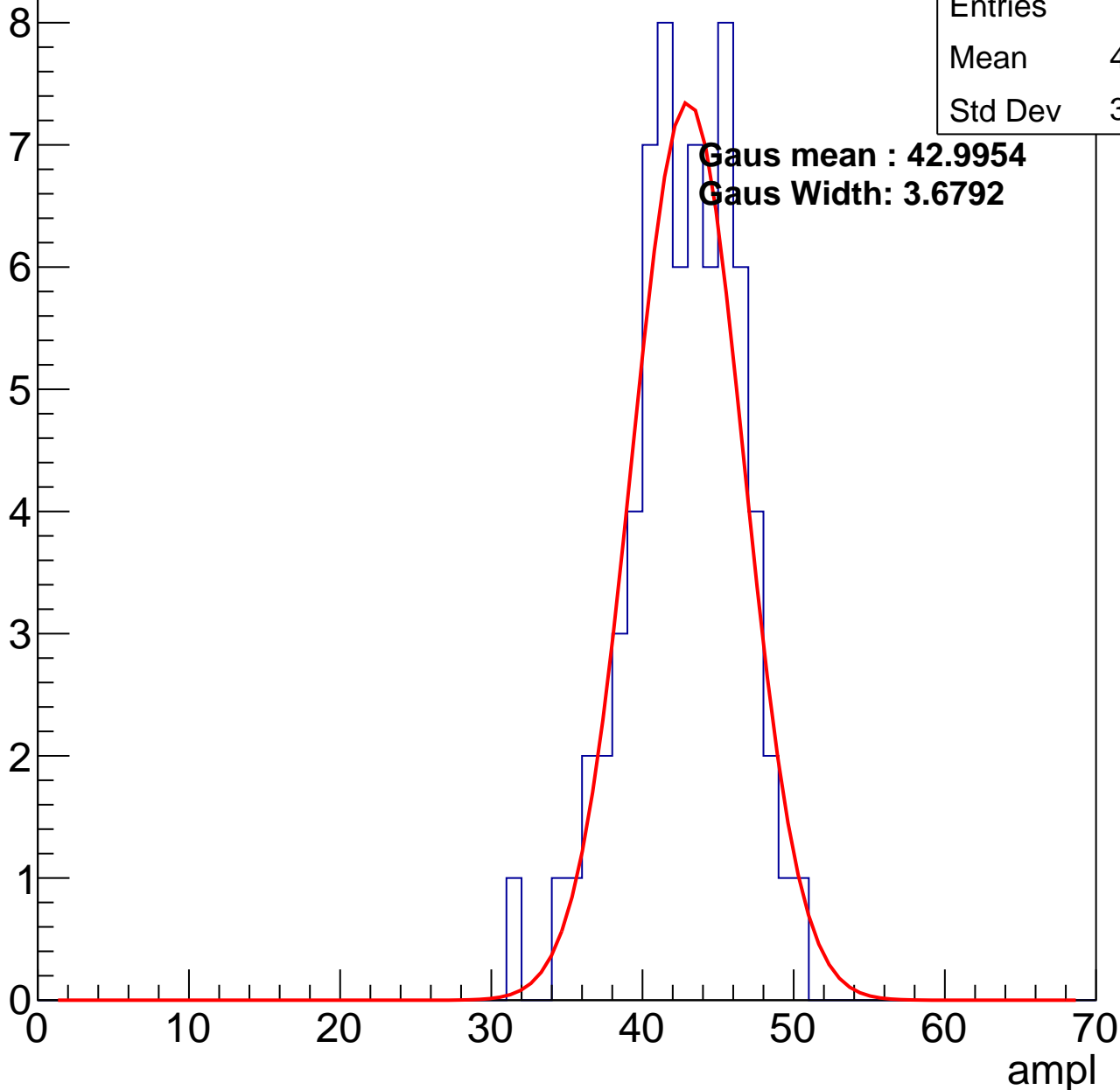
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.29
Std Dev	3.692

Gaus mean : 42.9954

Gaus Width: 3.6792

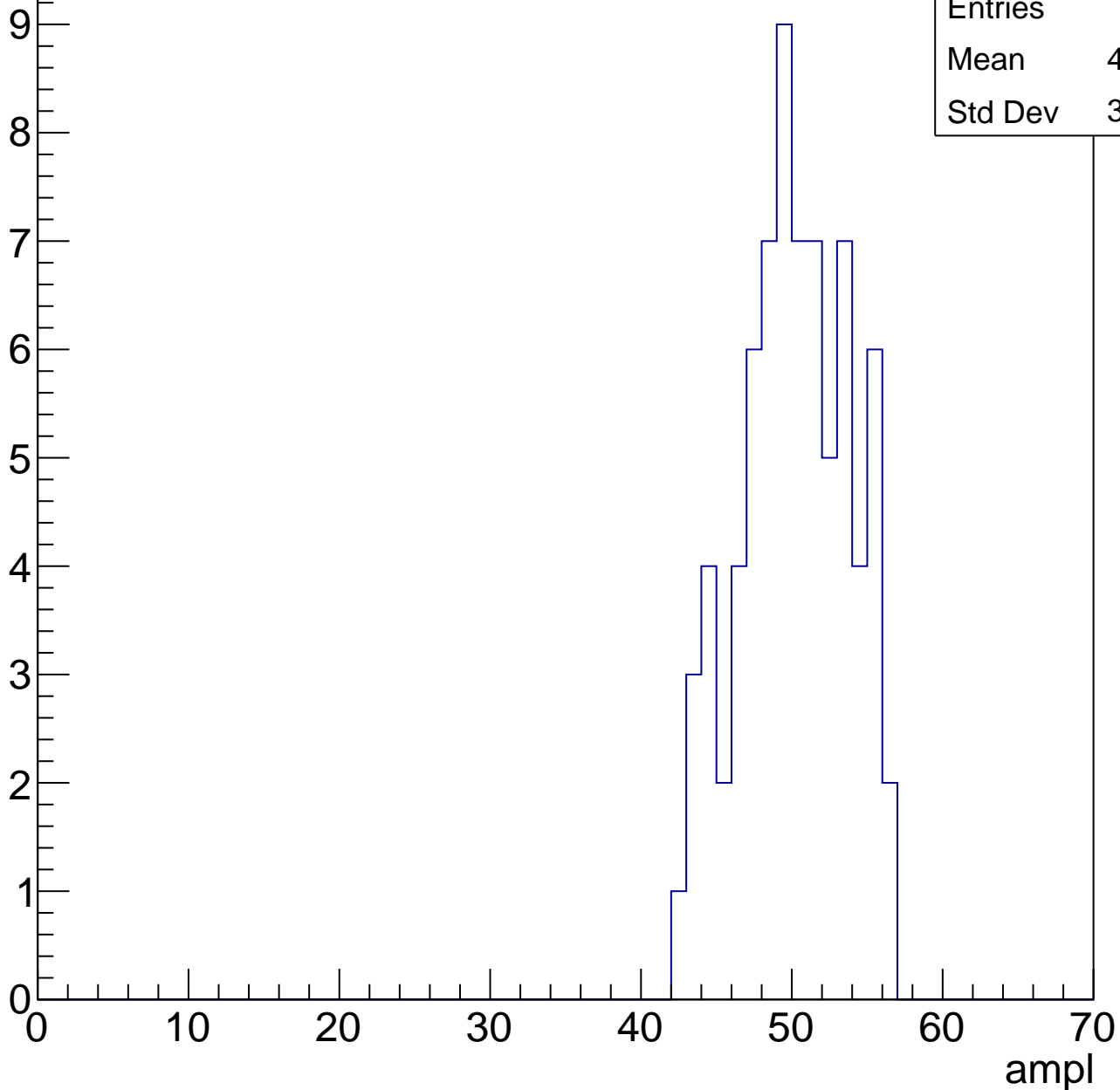


B1L101S, U15-ch21, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	49.68
Std Dev	3.542

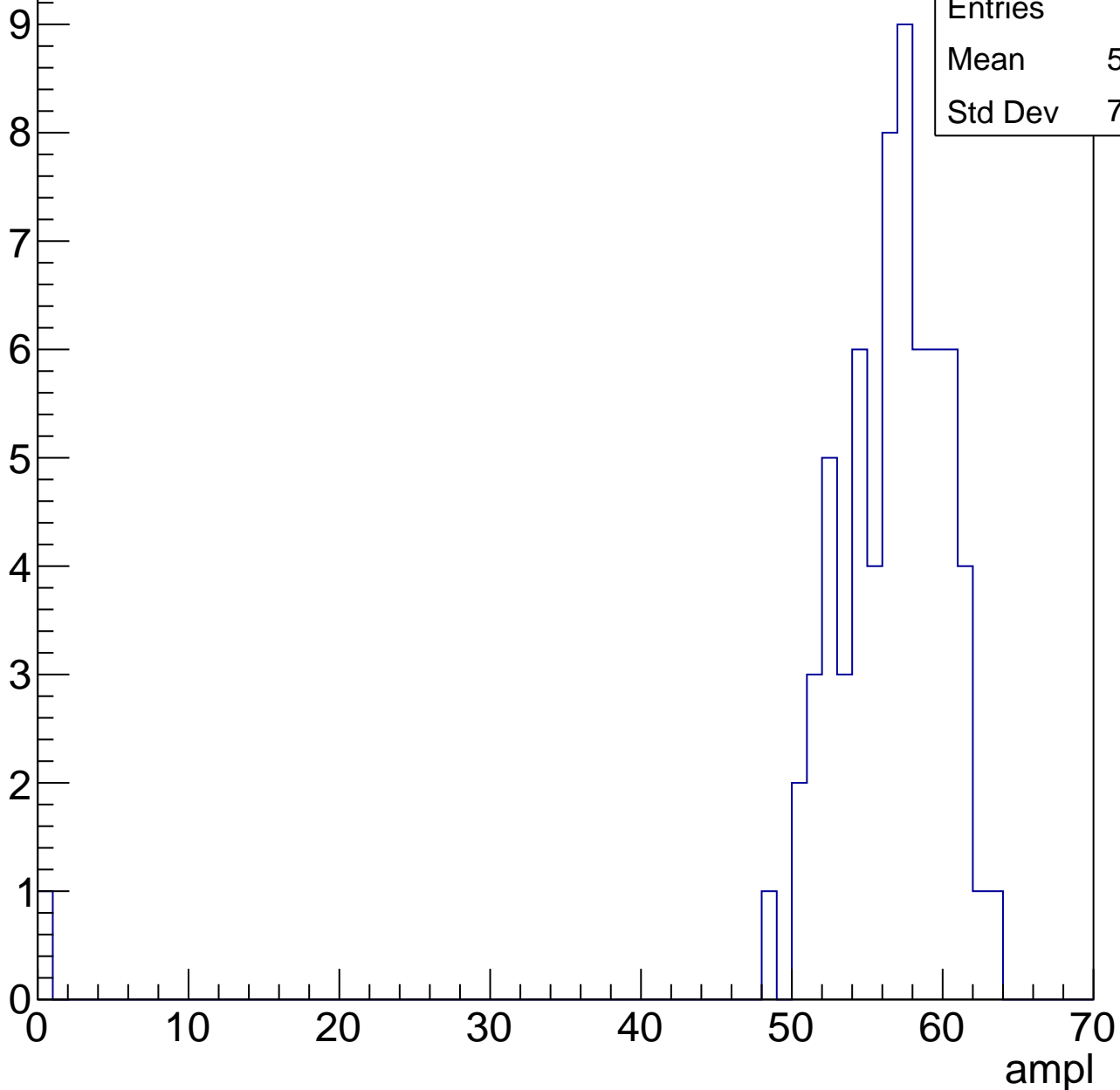


B1L101S, U15-ch21, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	55.39
Std Dev	7.614

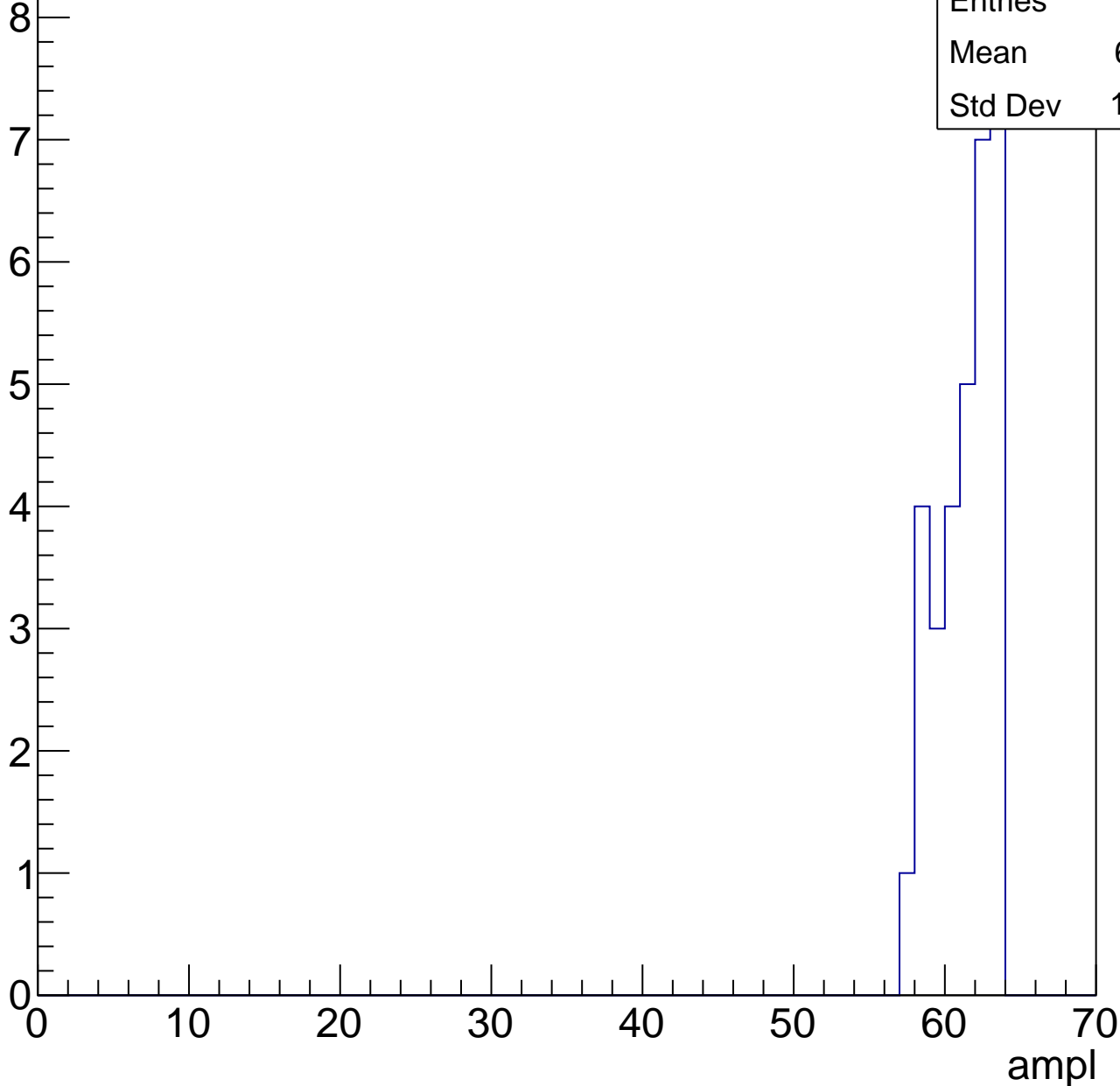


B1L101S, U15-ch21, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

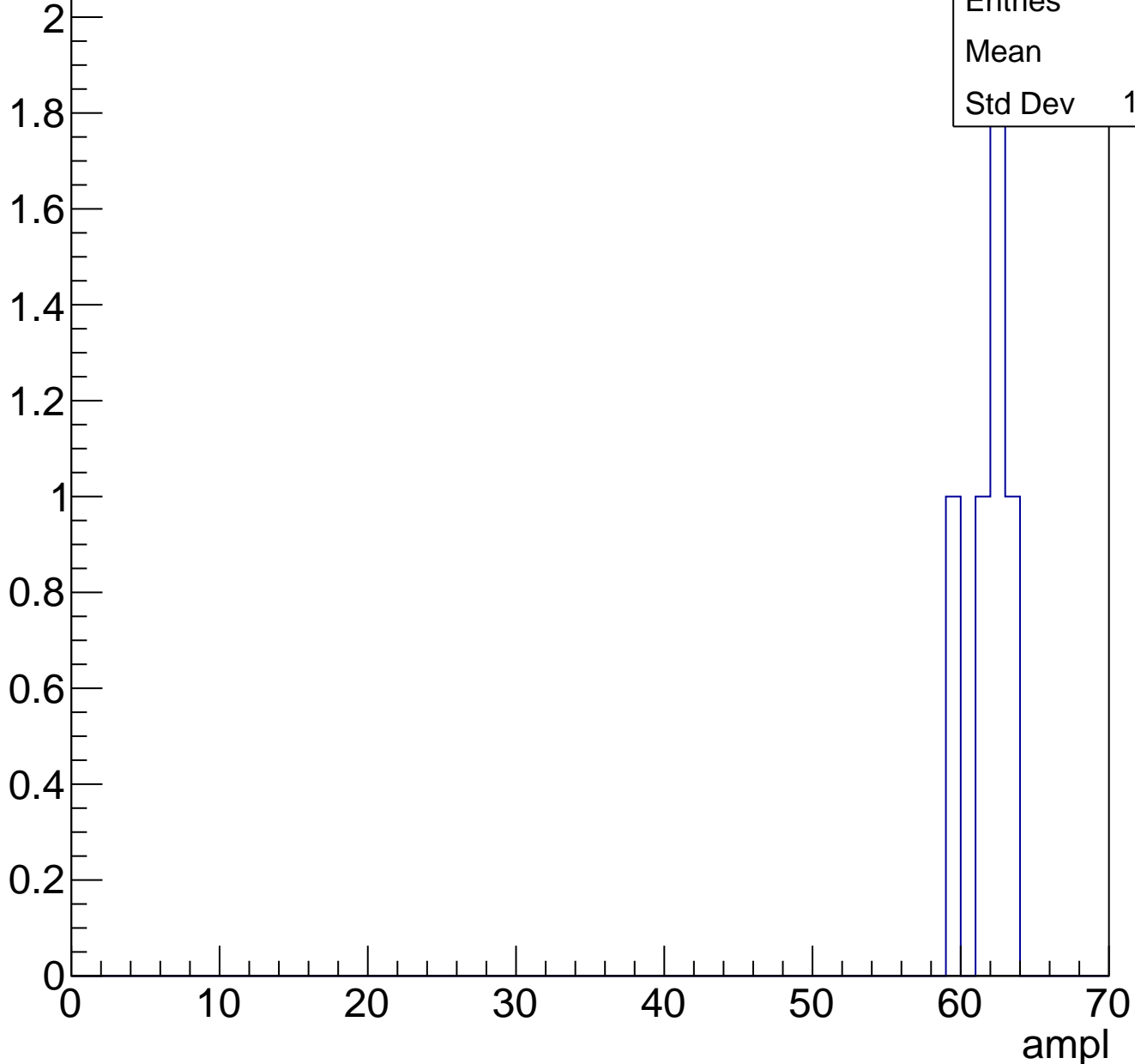
Entries	32
Mean	60.91
Std Dev	1.826



B1L101S, U15-ch21, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch21, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch22, adc0

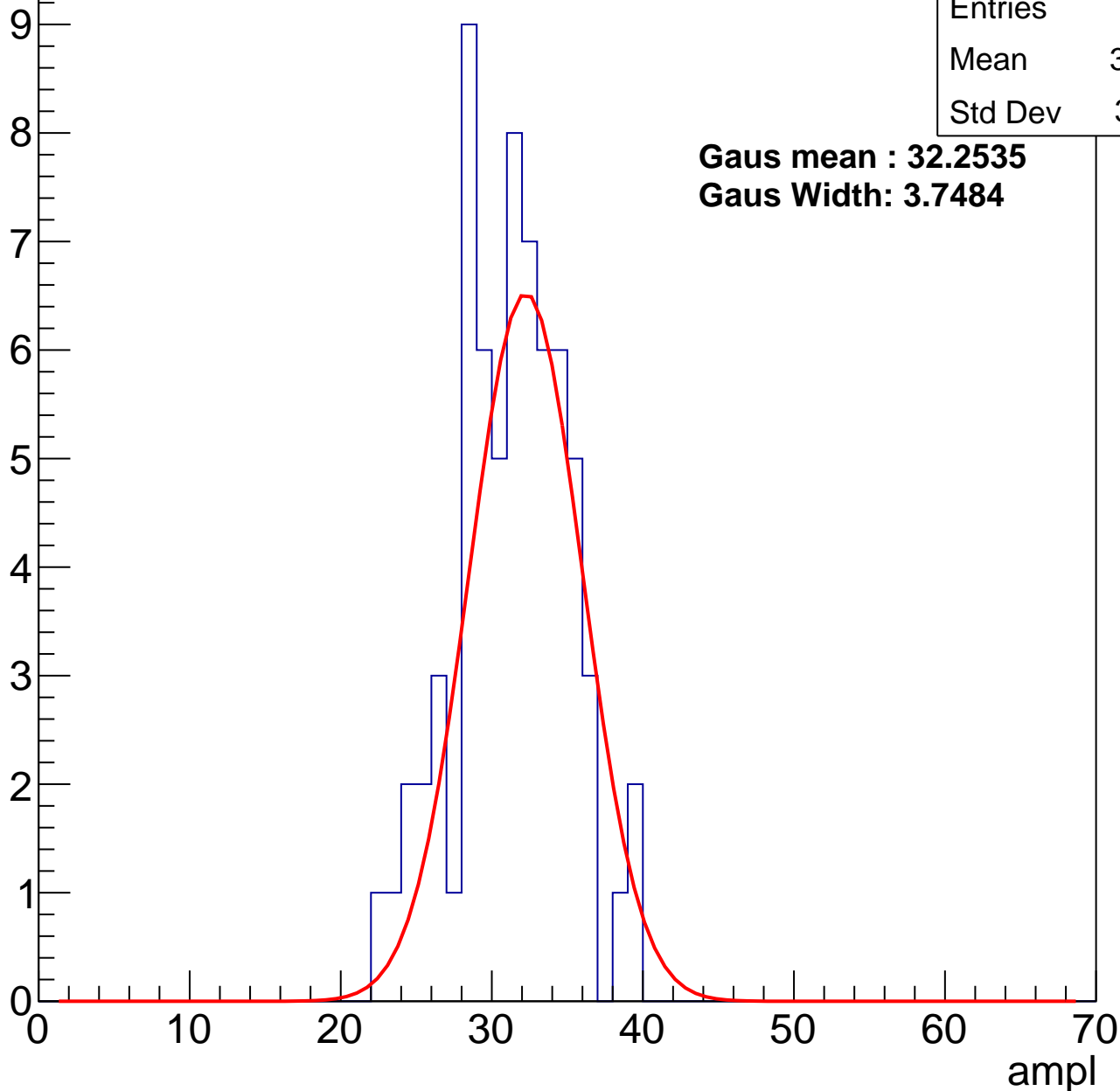
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	30.84
Std Dev	3.701

Gaus mean : 32.2535

Gaus Width: 3.7484



B1L101S, U15-ch22, adc1

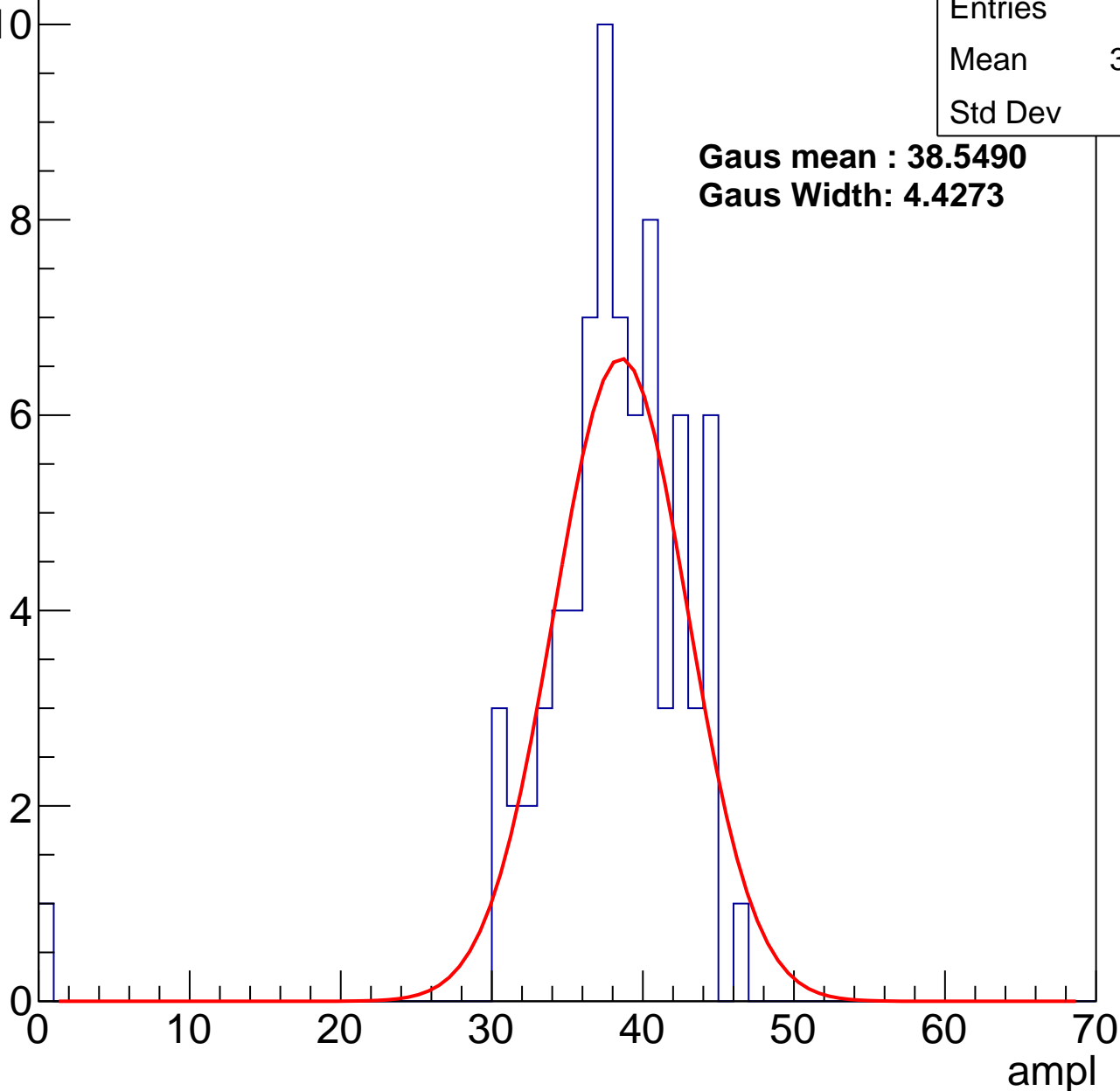
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.46
Std Dev	5.75

Gaus mean : 38.5490

Gaus Width: 4.4273



B1L101S, U15-ch22, adc2

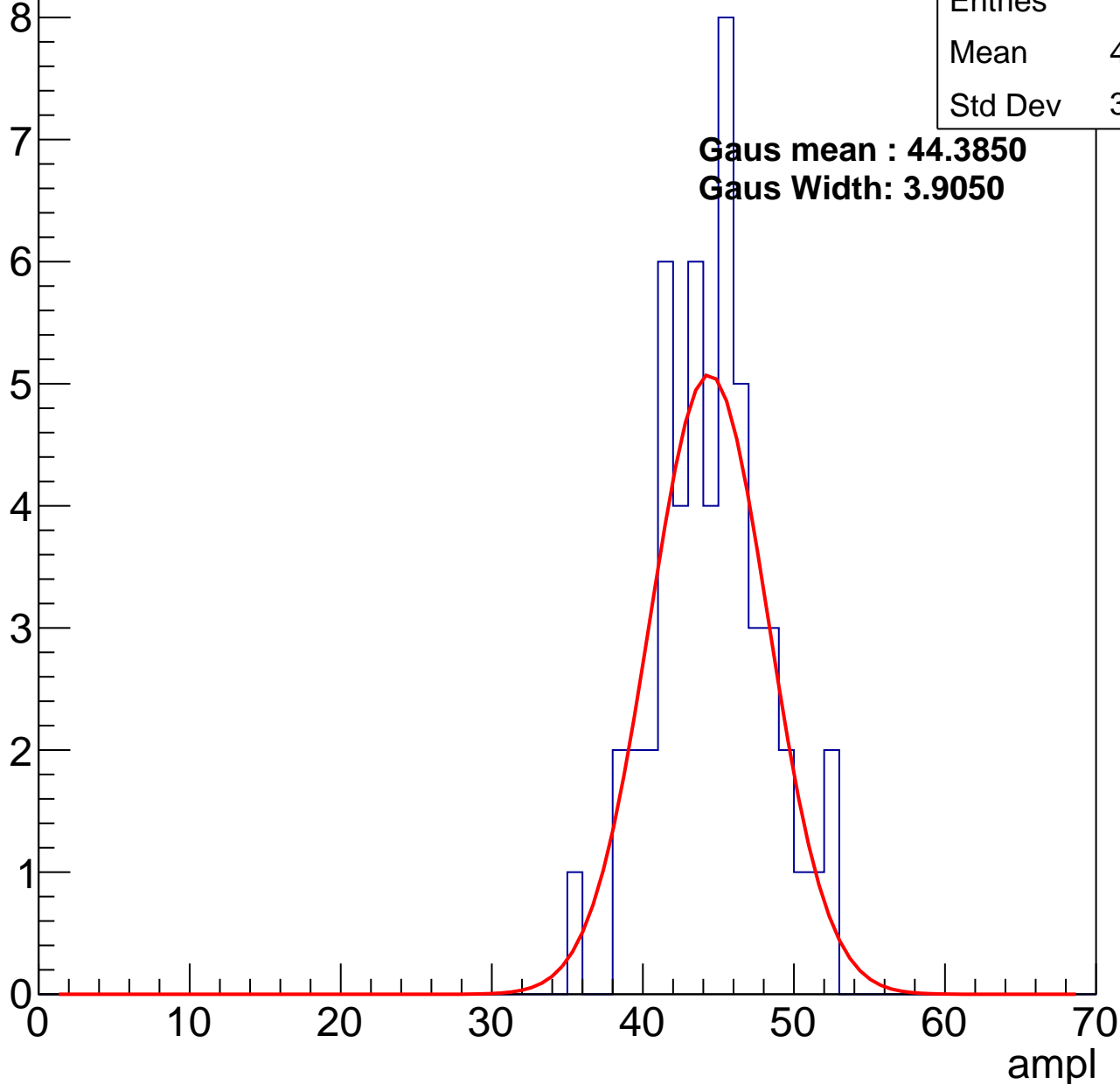
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	44.13
Std Dev	3.606

Gaus mean : 44.3850

Gaus Width: 3.9050

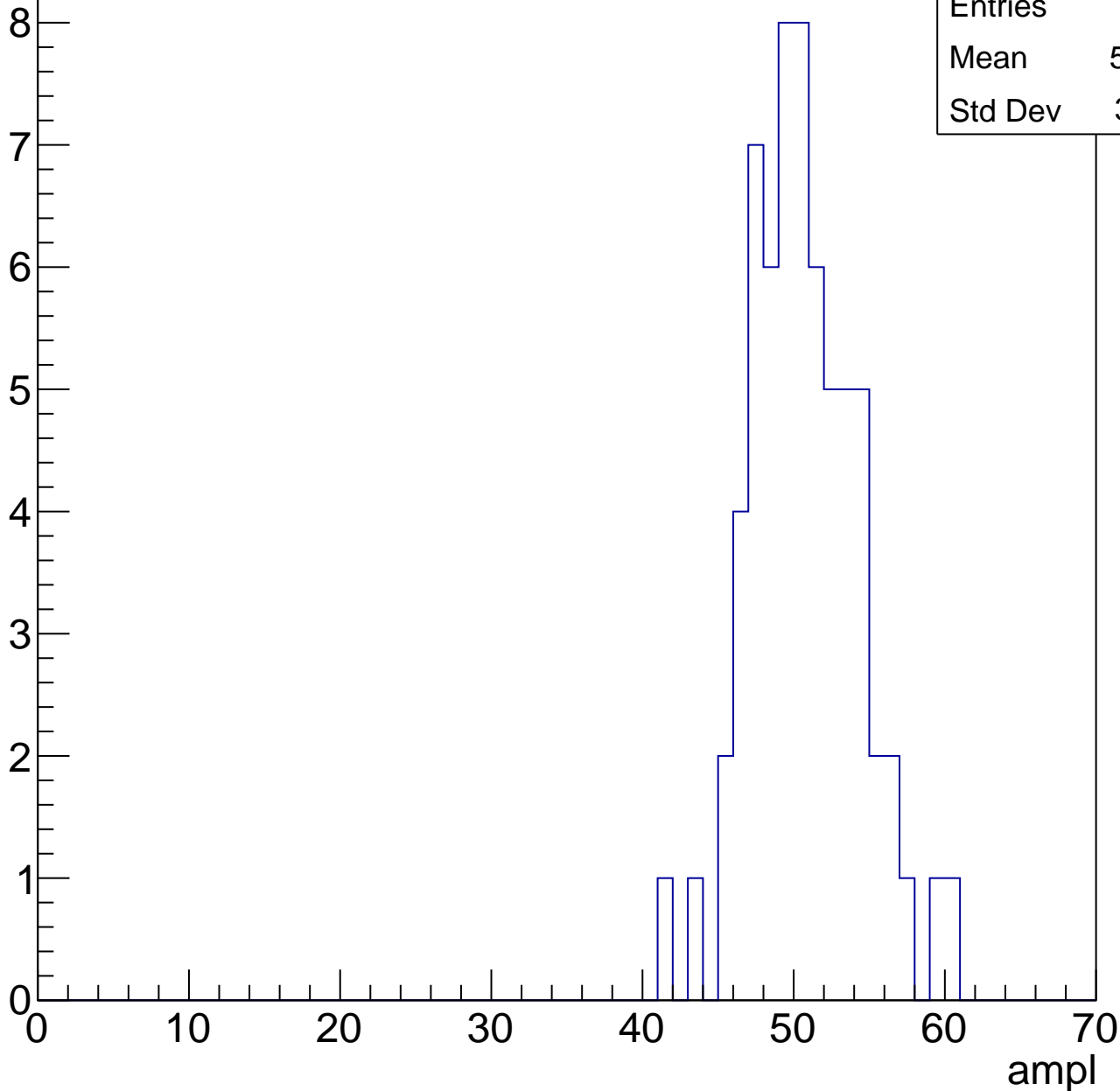


B1L101S, U15-ch22, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

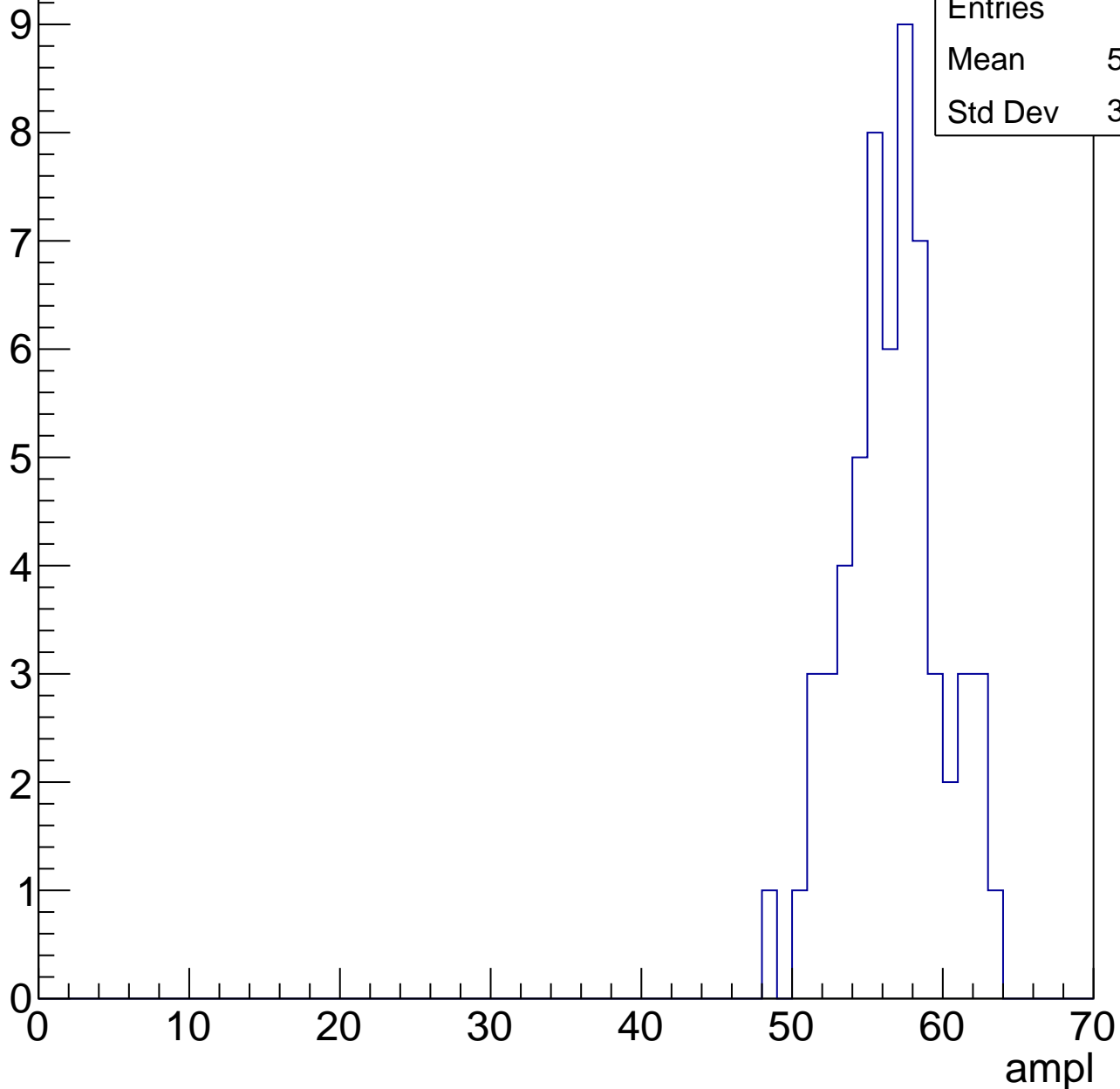
Entries	65
Mean	50.25
Std Dev	3.591



B1L101S, U15-ch22, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



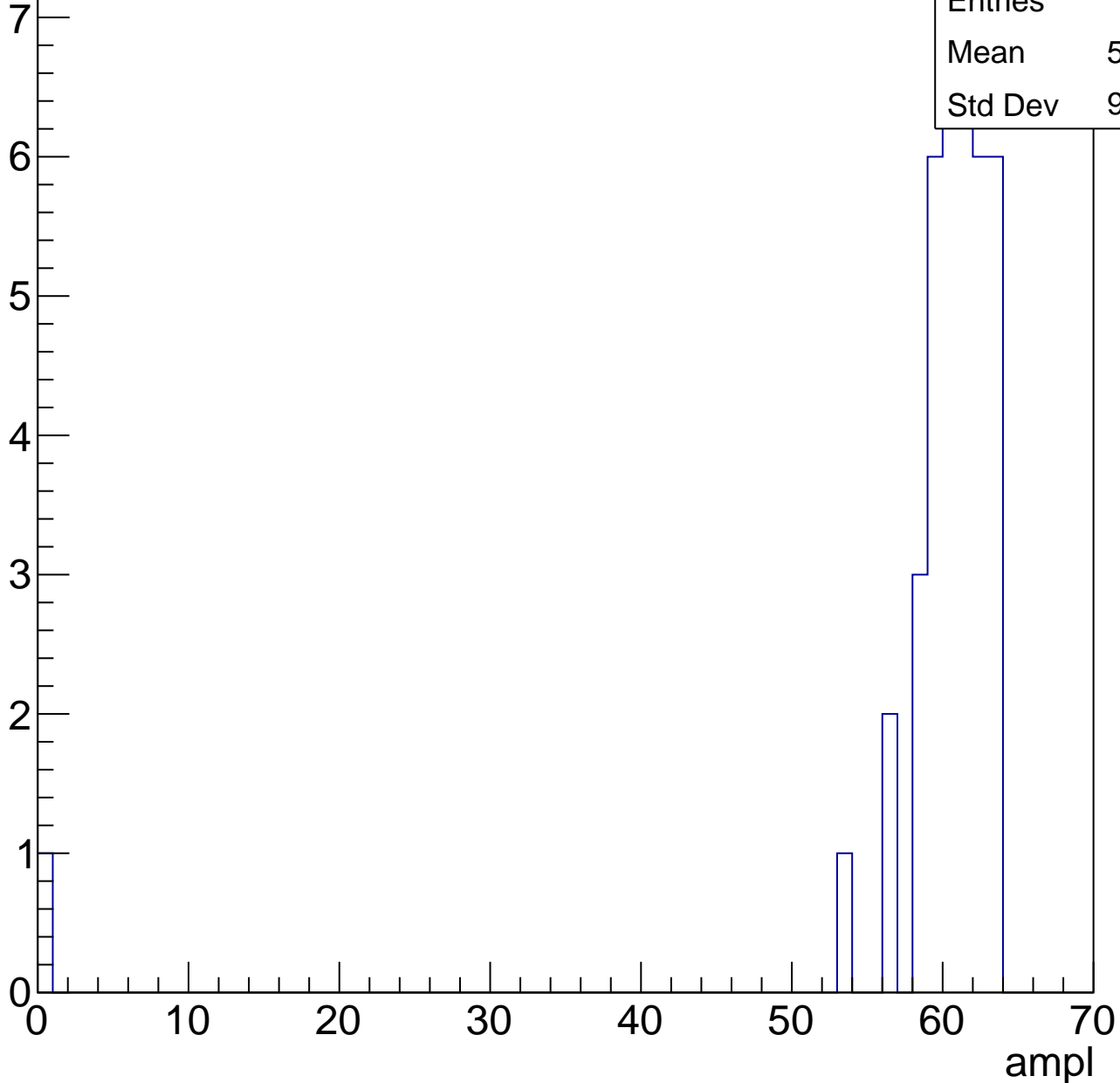
Entries	59
Mean	56.15
Std Dev	3.225

B1L101S, U15-ch22, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	58.72
Std Dev	9.766



B1L101S, U15-ch22, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L101S, U15-ch22, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch23, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	62
Mean	29.21
Std Dev	4.916

Gaus mean : 30.7404

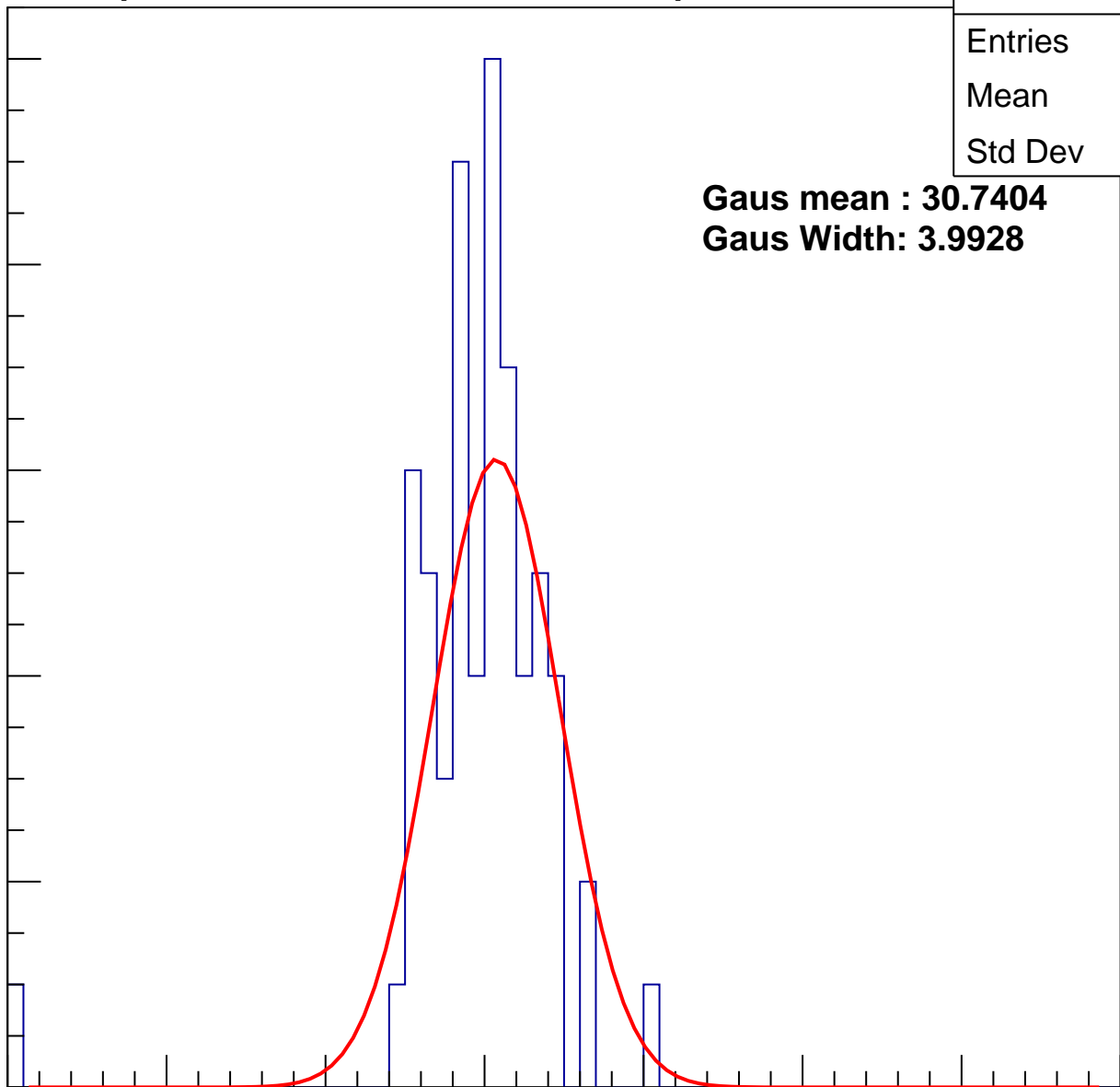
Gaus Width: 3.9928

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch23, adc1

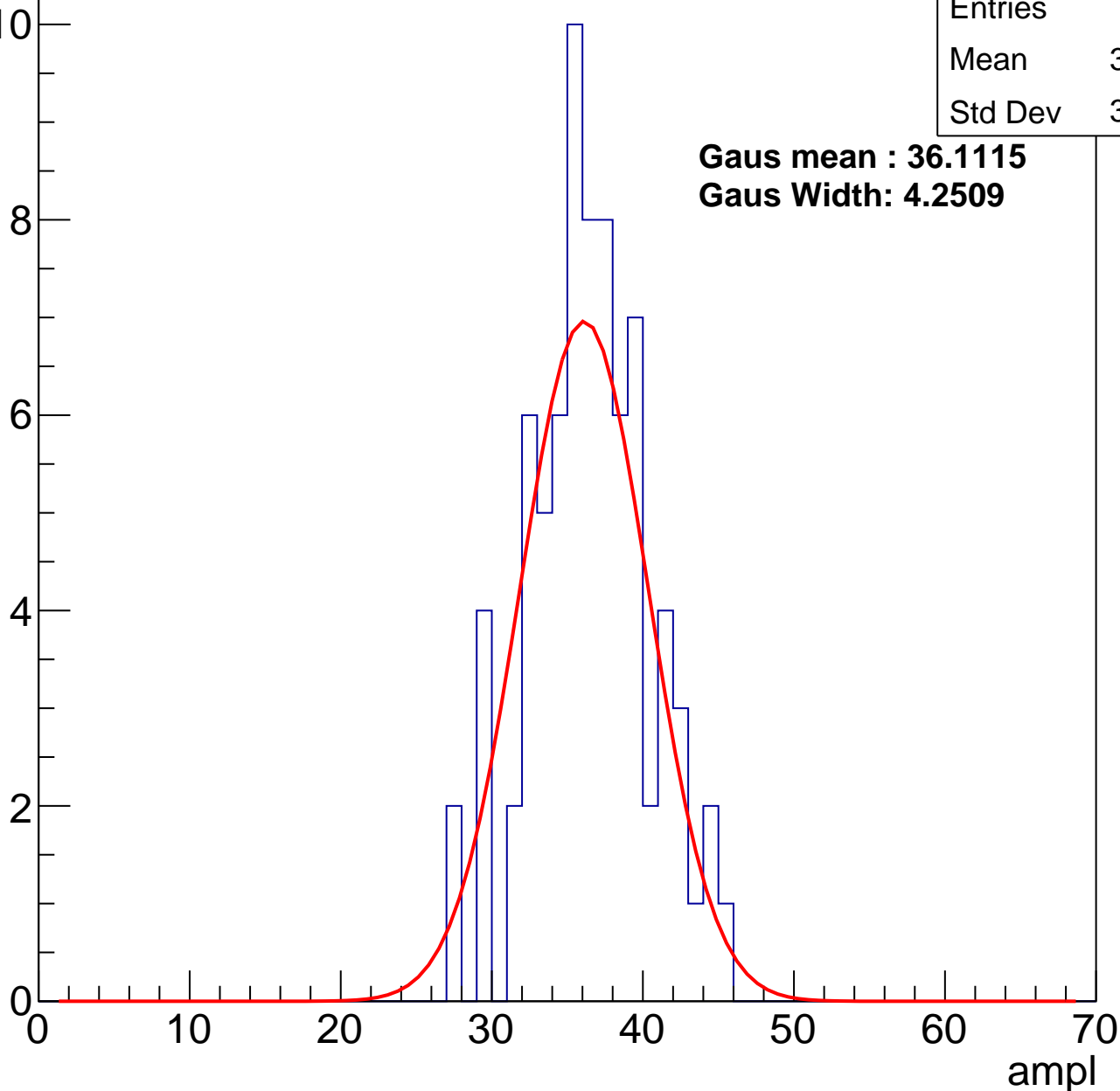
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	36.03
Std Dev	3.908

Gaus mean : 36.1115

Gaus Width: 4.2509



B1L101S, U15-ch23, adc2

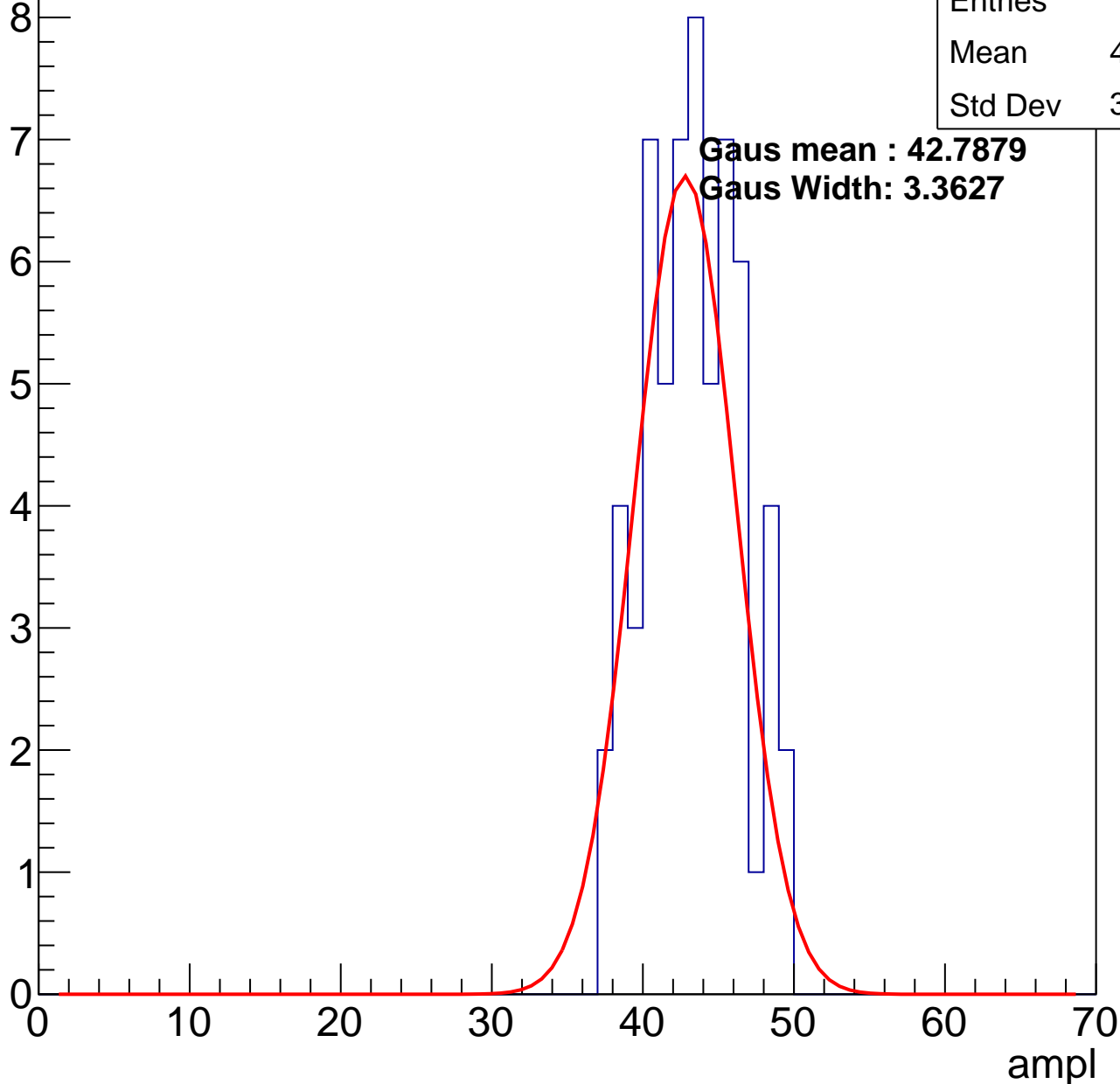
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.85
Std Dev	3.093

Gaus mean : 42.7879

Gaus Width: 3.3627

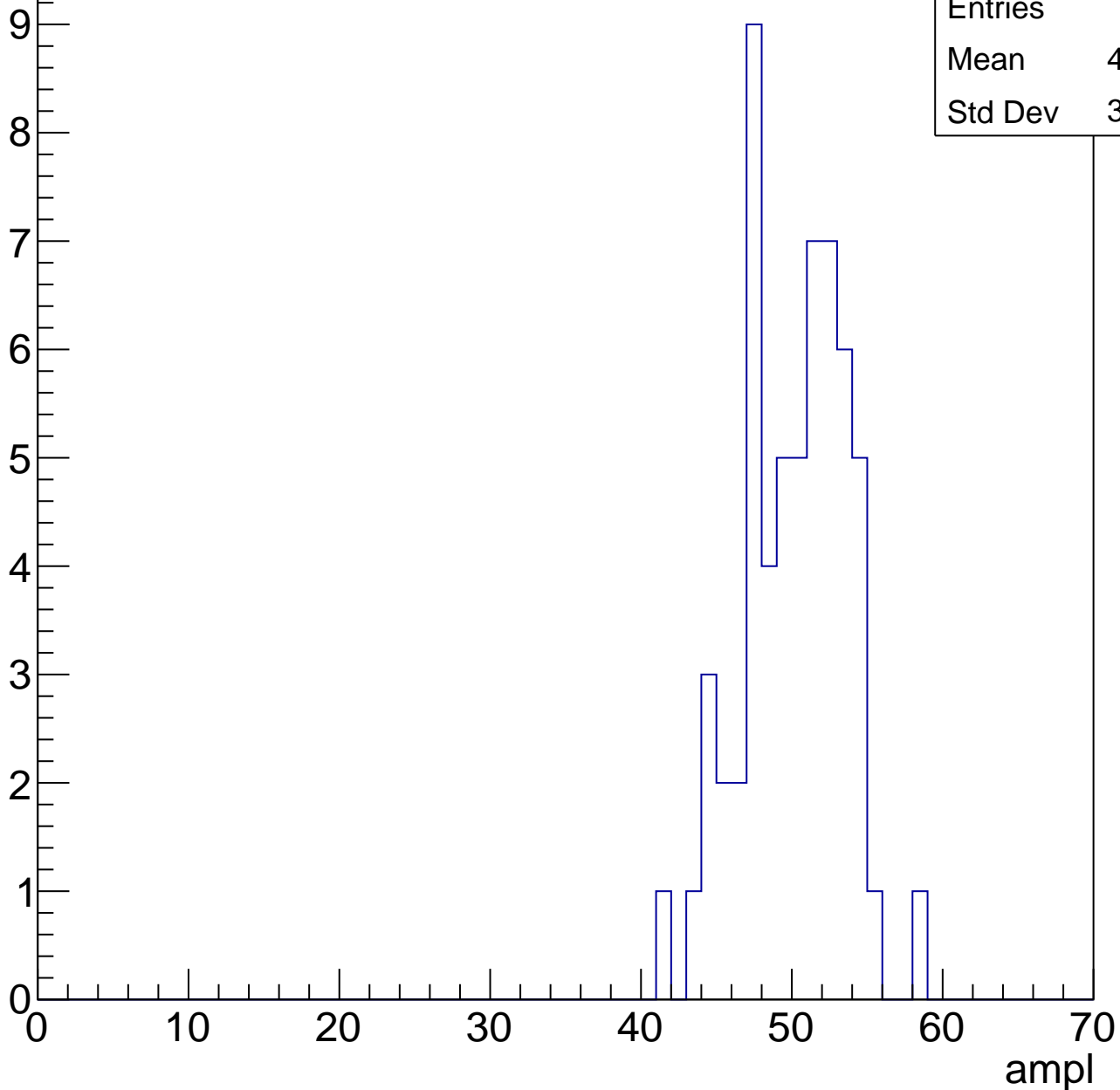


B1L101S, U15-ch23, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.66
Std Dev	3.378

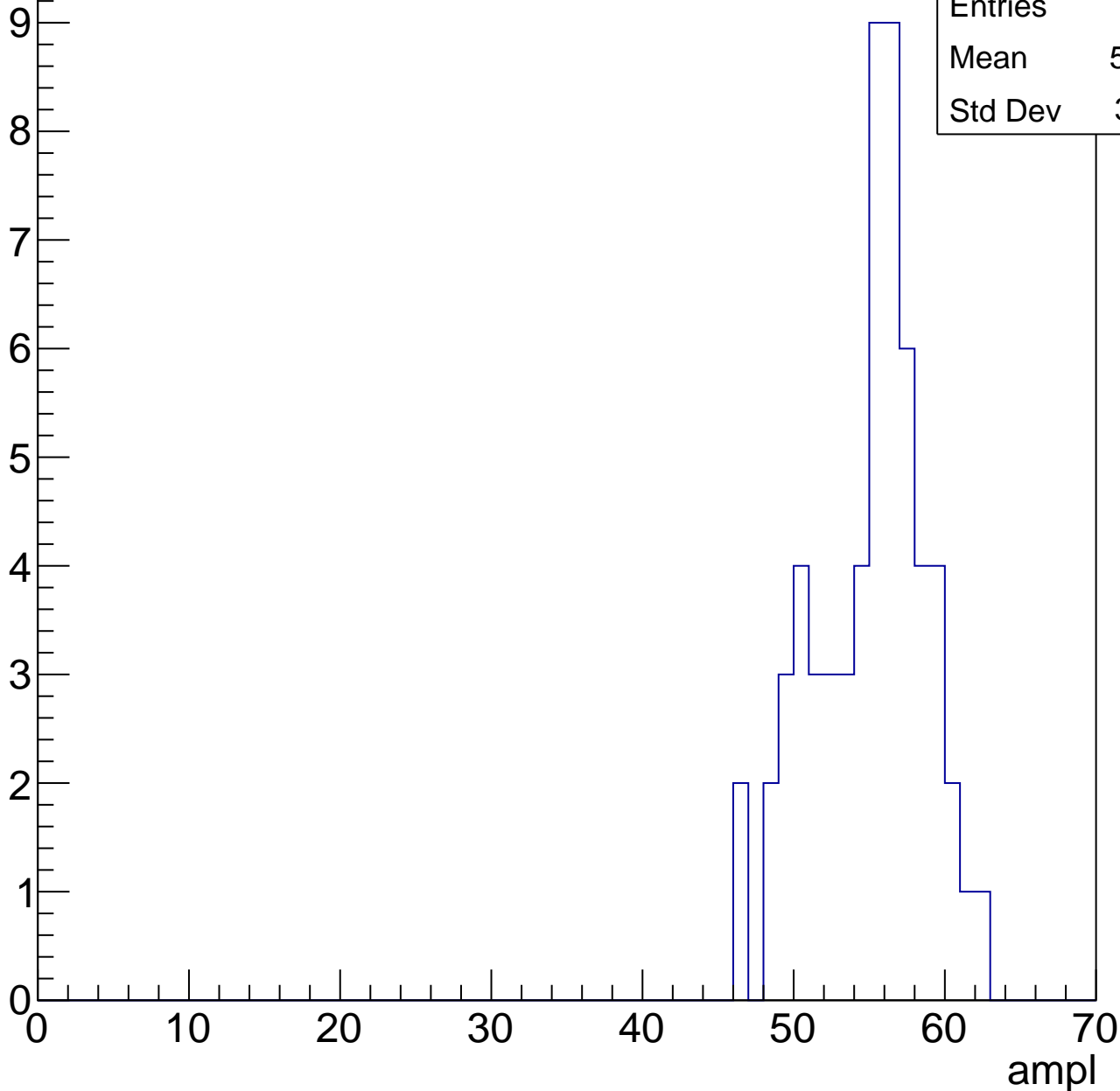


B1L101S, U15-ch23, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

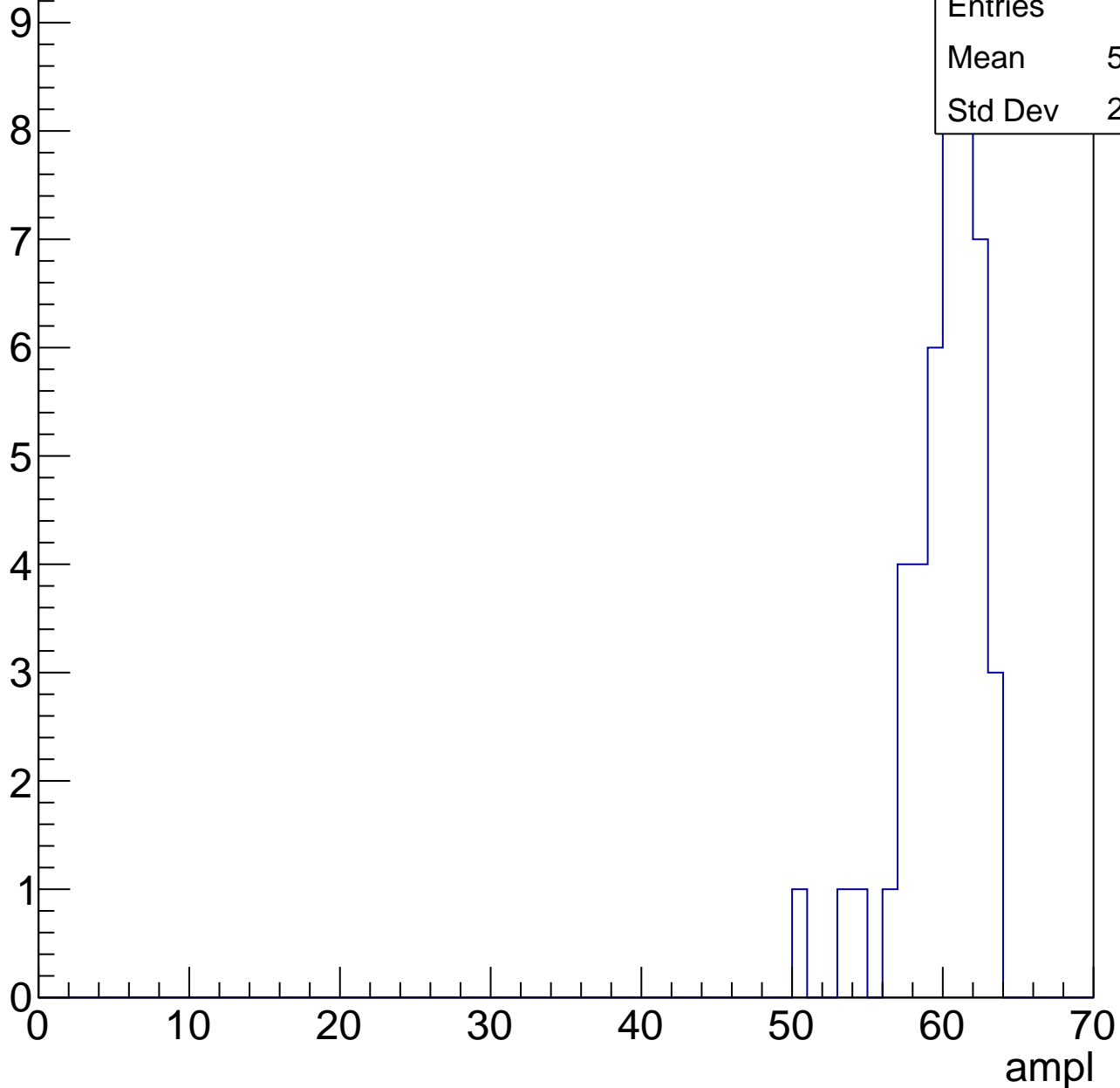
Entries	60
Mean	54.52
Std Dev	3.681



B1L101S, U15-ch23, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

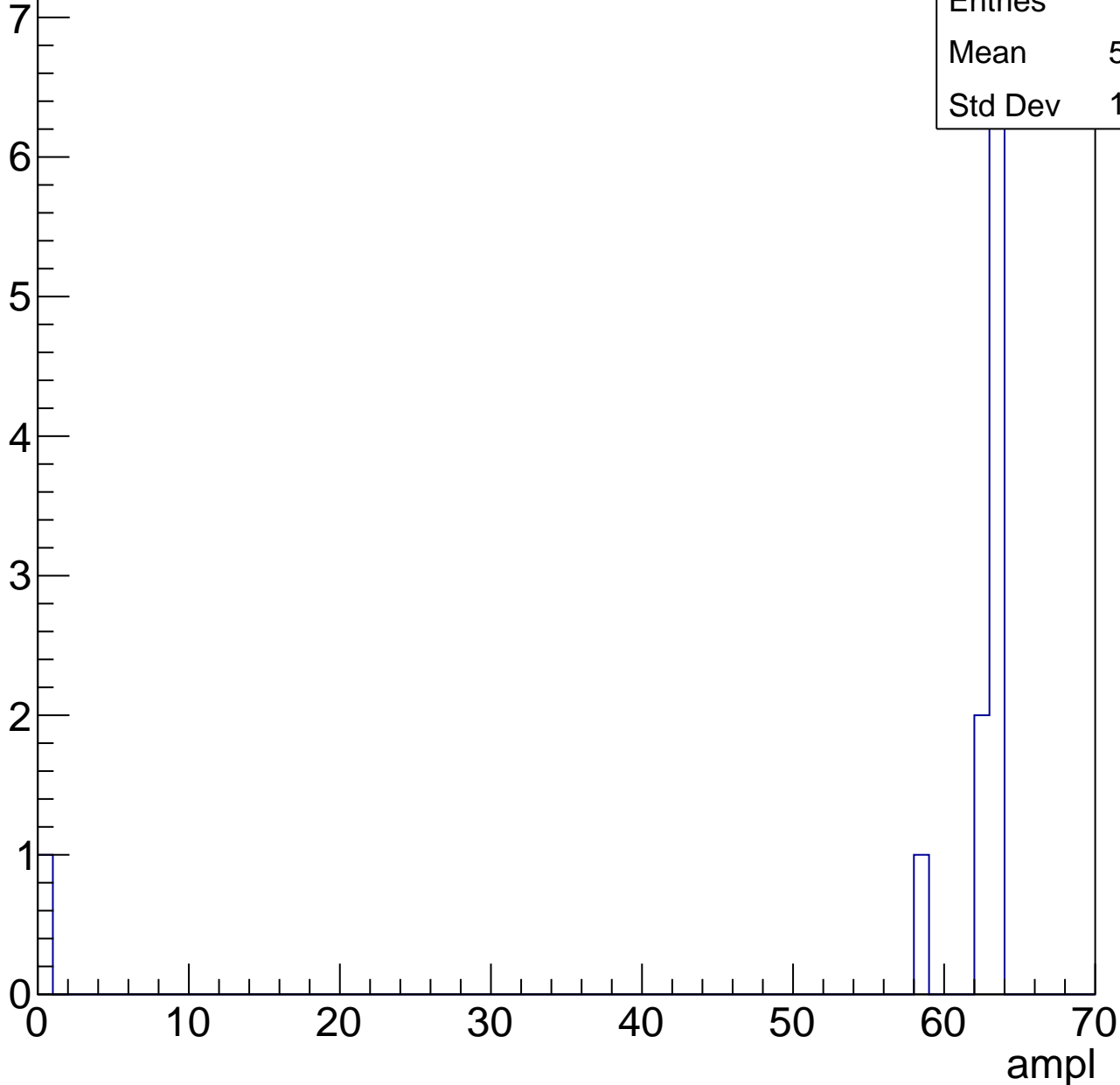


B1L101S, U15-ch23, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	11
Mean	56.64
Std Dev	17.97



B1L101S, U15-ch23, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch24, adc0

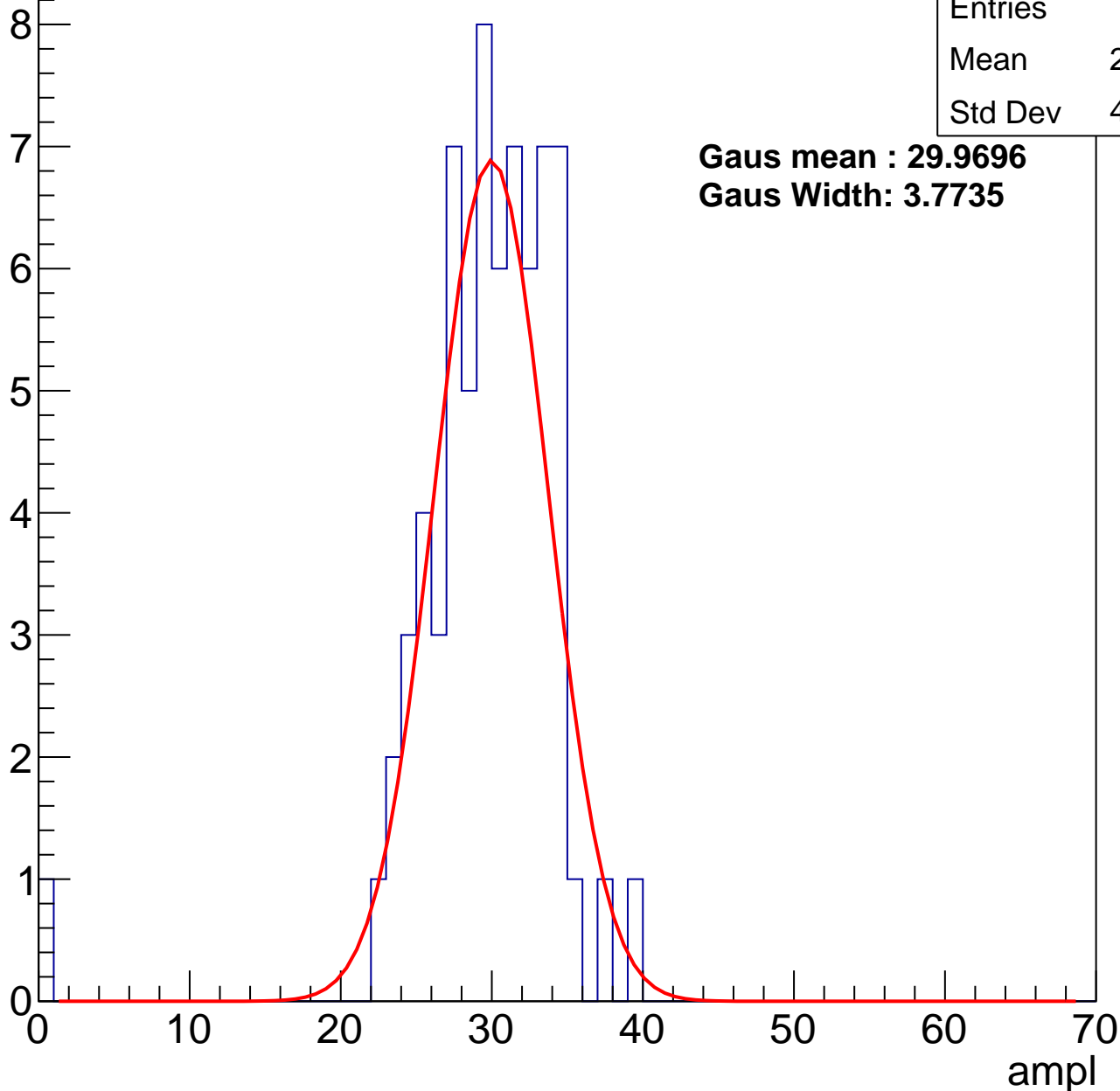
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	29.26
Std Dev	4.973

Gaus mean : 29.9696

Gaus Width: 3.7735



B1L101S, U15-ch24, adc1

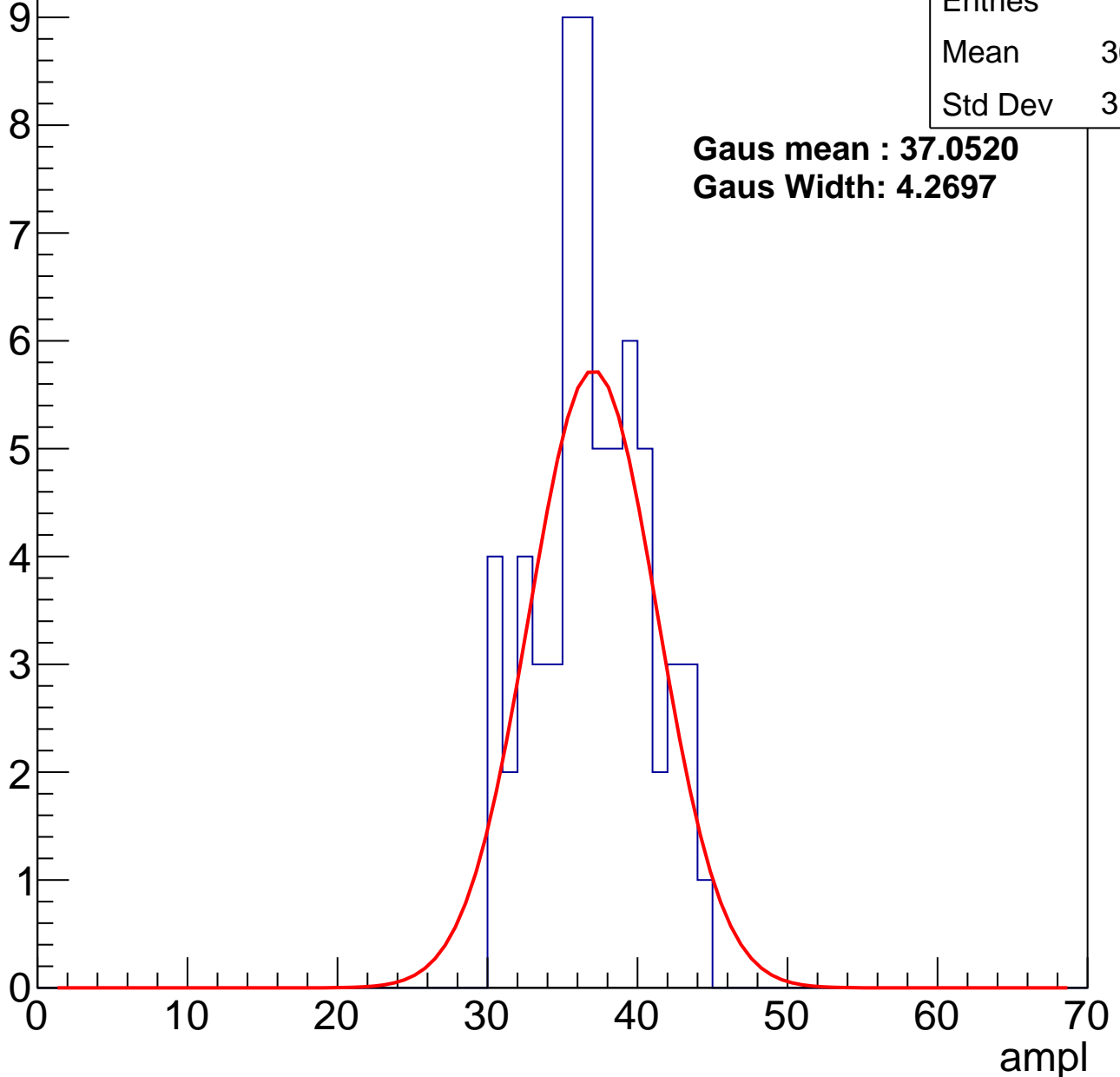
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.56
Std Dev	3.566

Gaus mean : 37.0520

Gaus Width: 4.2697



B1L101S, U15-ch24, adc2

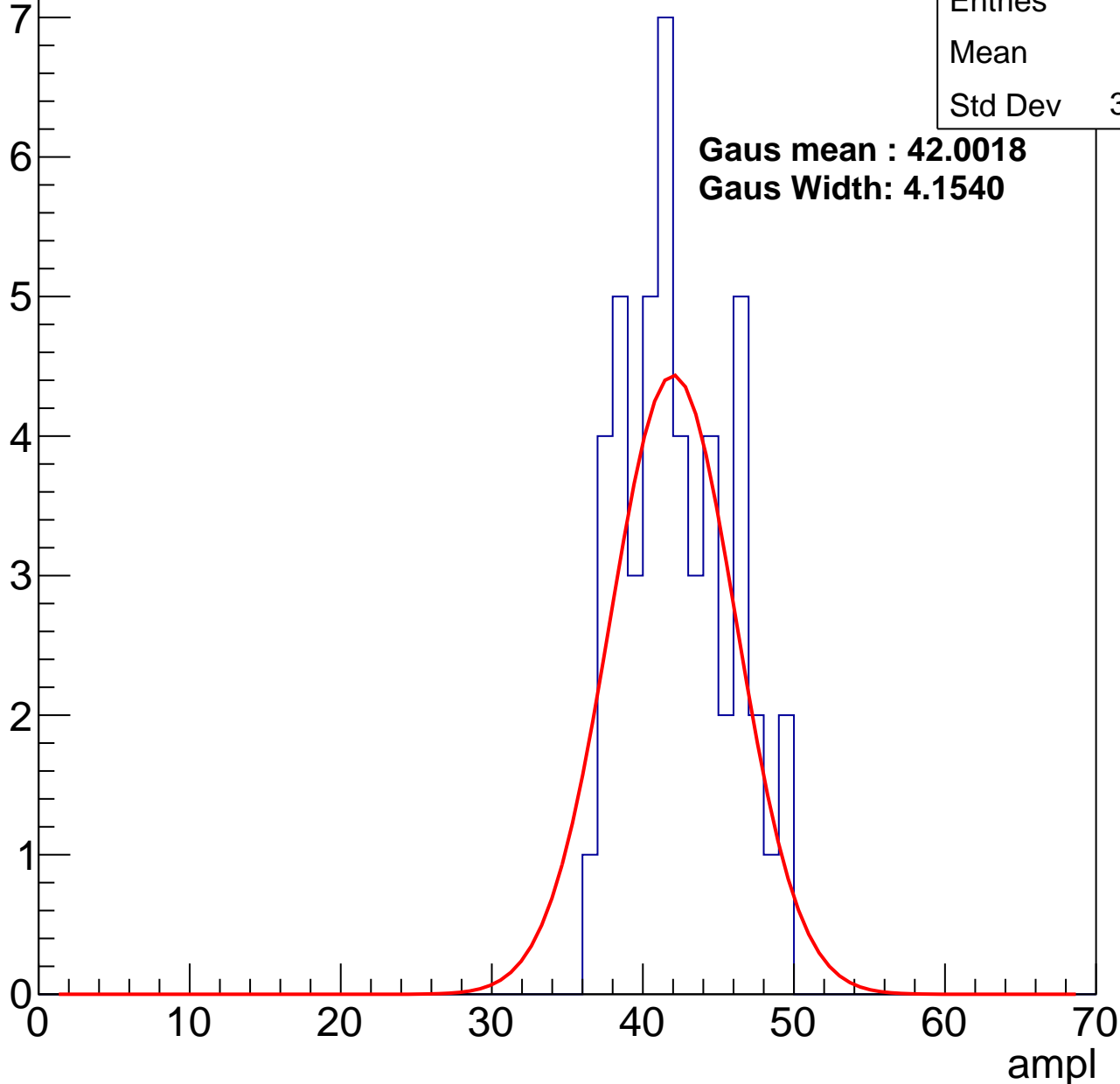
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	41.9
Std Dev	3.447

Gaus mean : 42.0018

Gaus Width: 4.1540

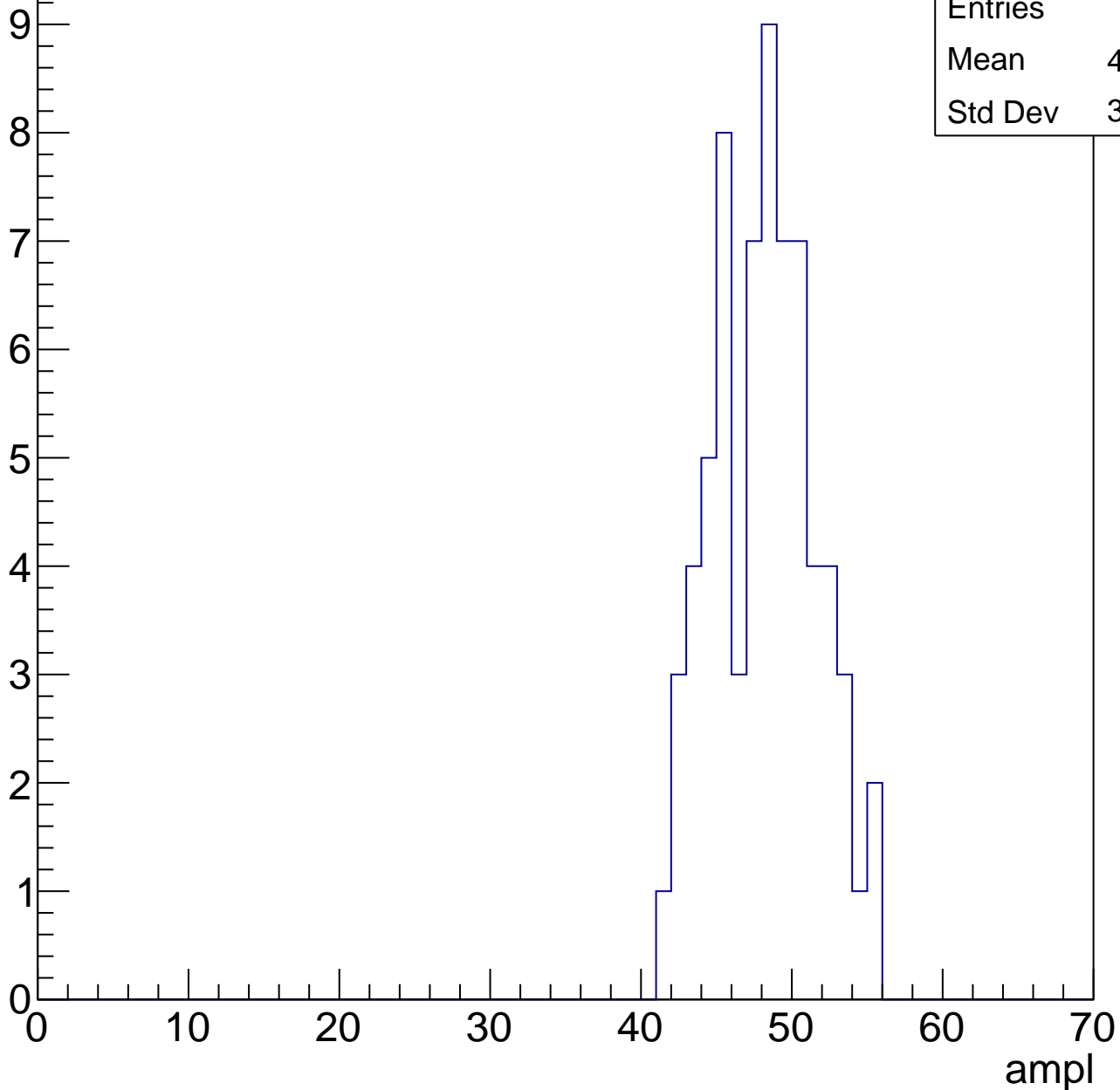


B1L101S, U15-ch24, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	47.74
Std Dev	3.359

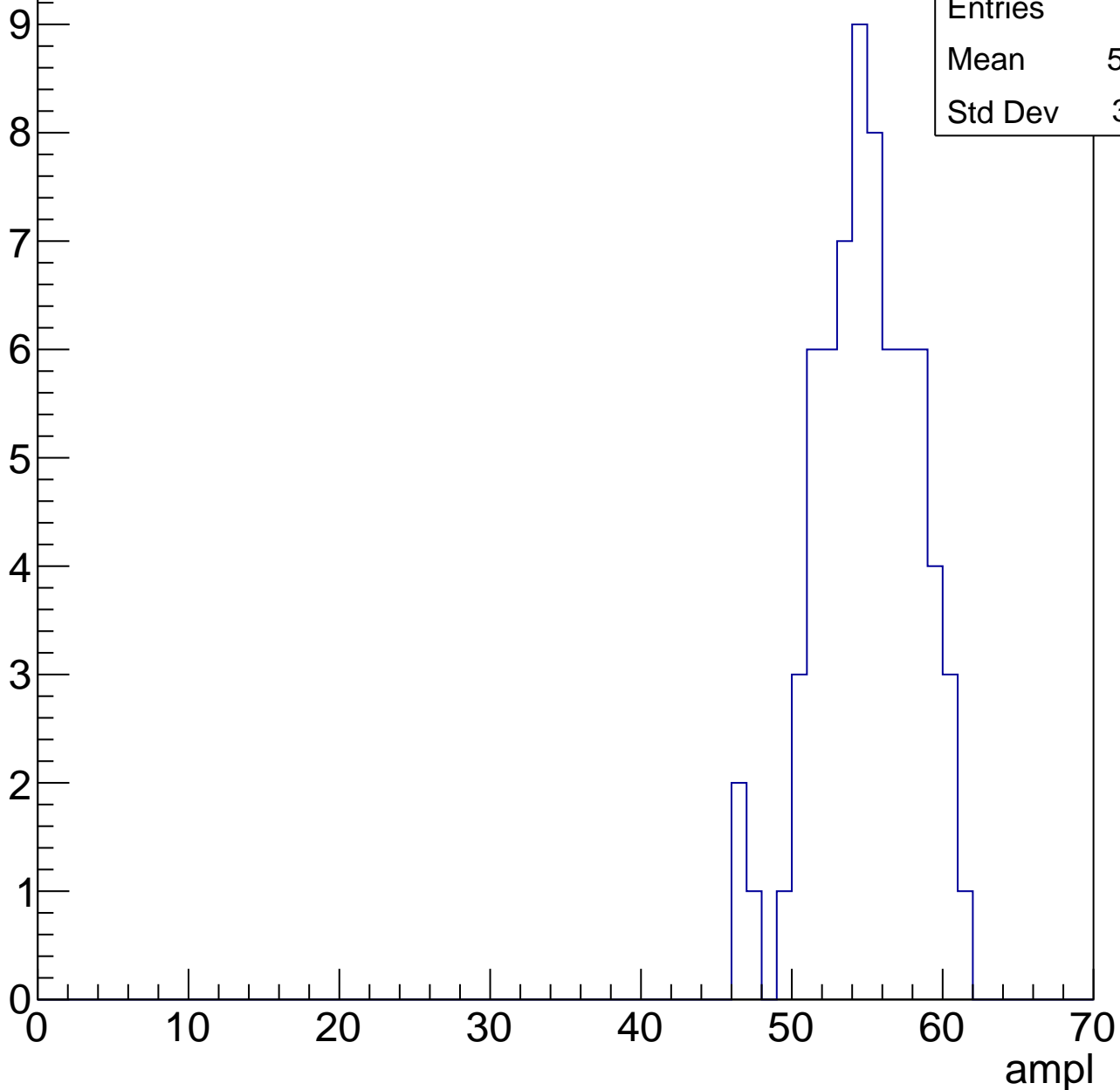


B1L101S, U15-ch24, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	54.43
Std Dev	3.321



B1L101S, U15-ch24, adc5

calib_packv5_042523_0143.root, FC#0, port D2

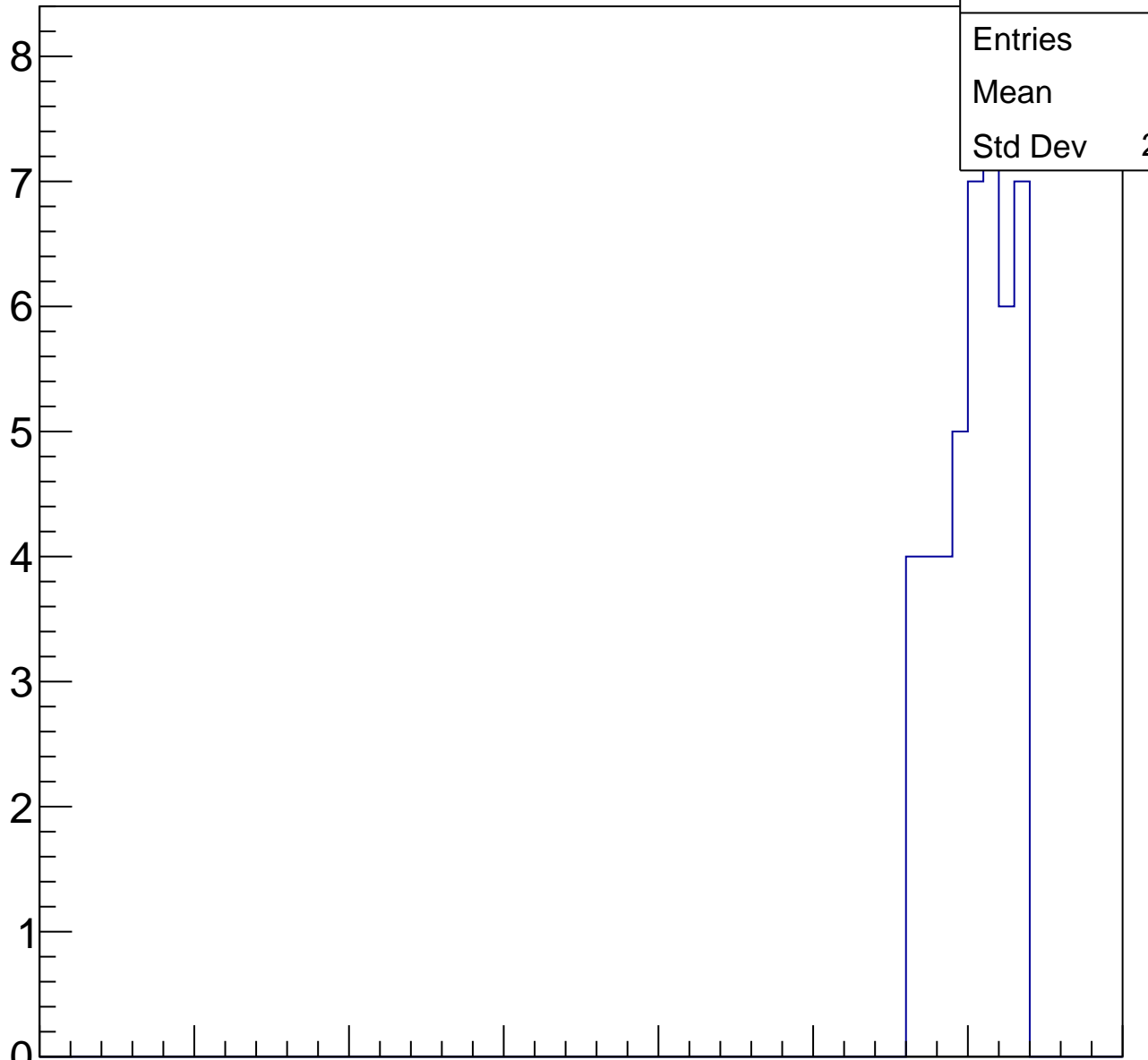
Entry

8
7
6
5
4
3
2
1
0

Entries	45
Mean	60
Std Dev	2.191

ampl

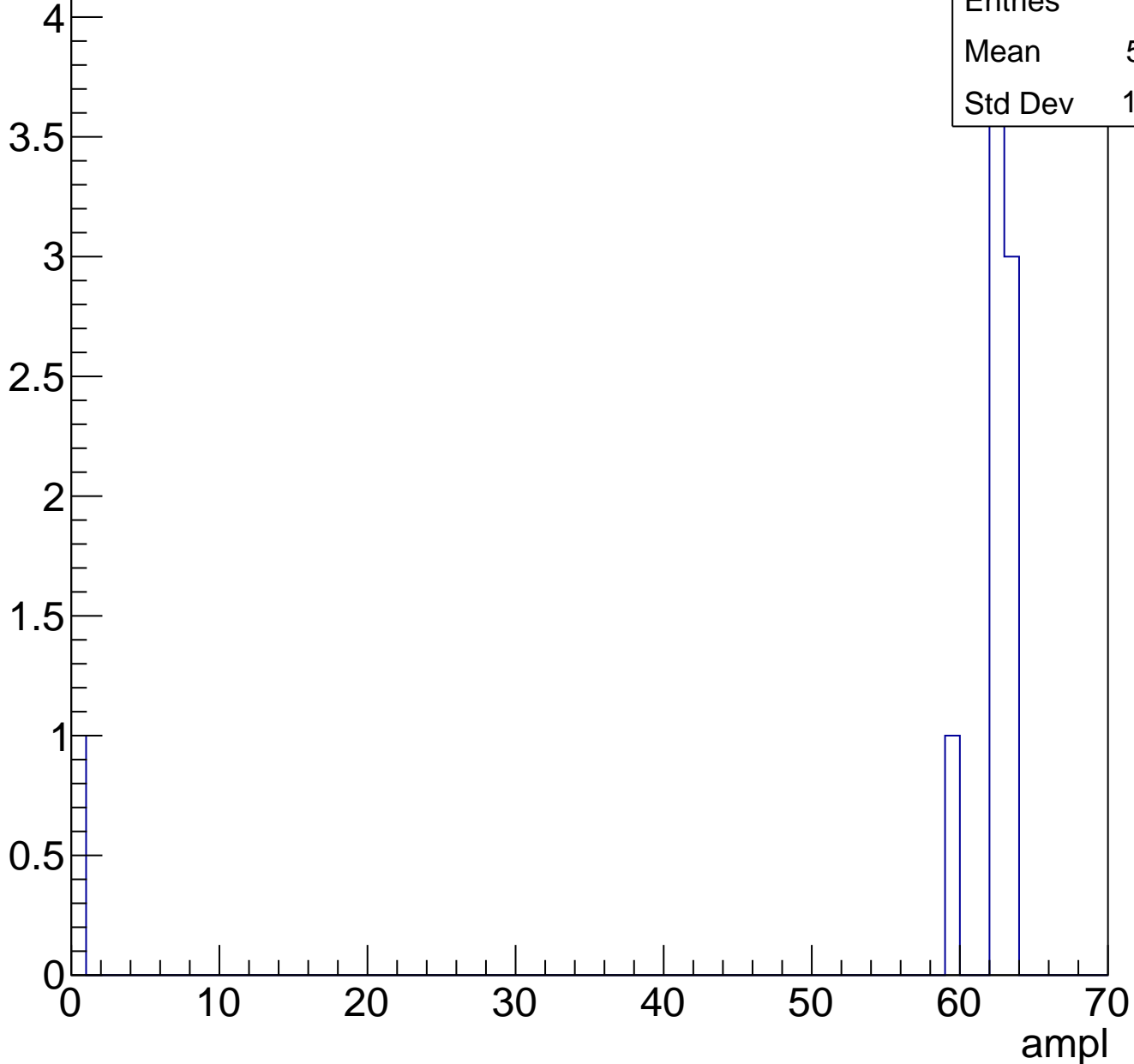
0 10 20 30 40 50 60 70



B1L101S, U15-ch24, adc6

calib_packv5_042523_0143.root, FC#0, port D2

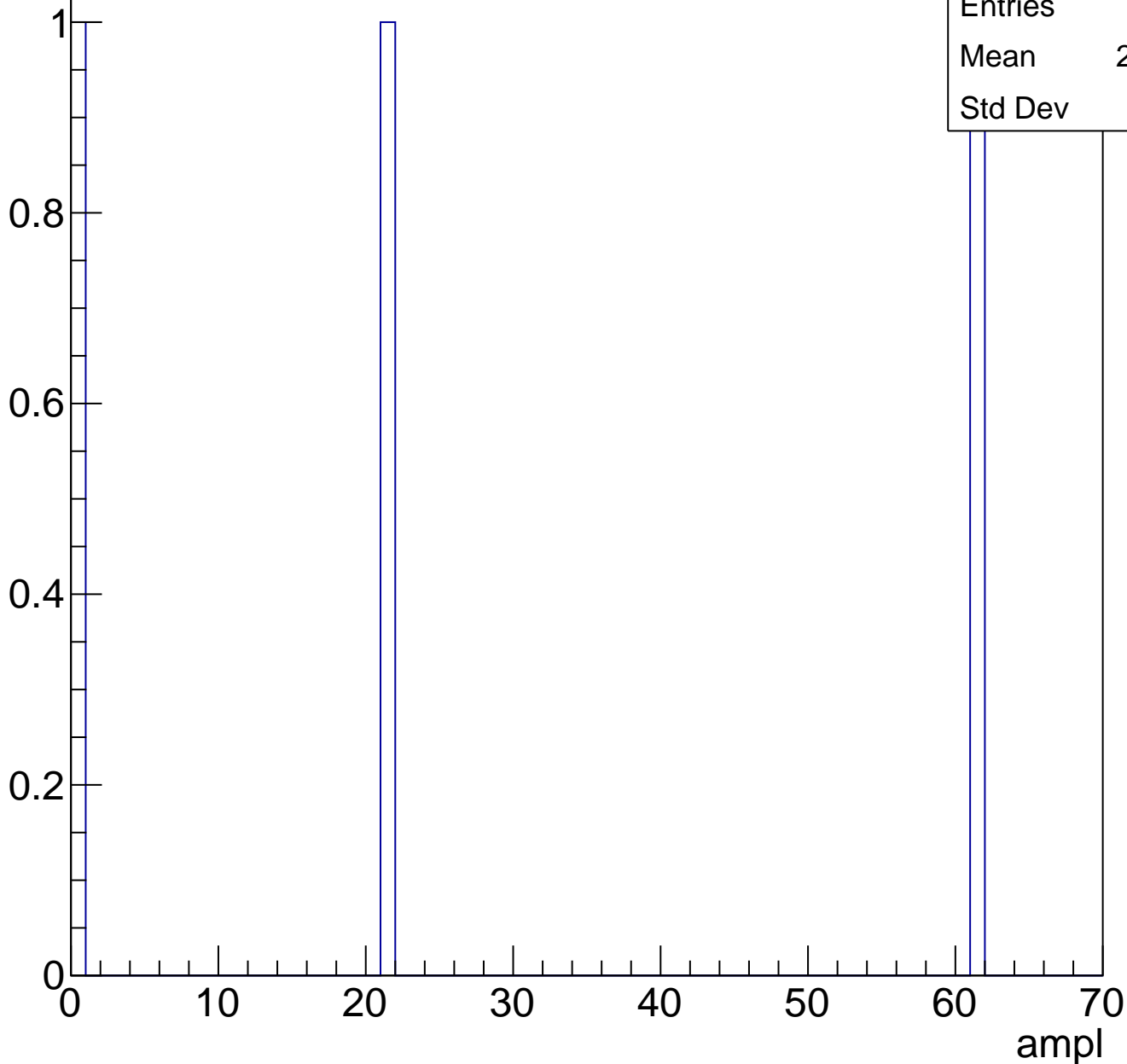
Entry



B1L101S, U15-ch24, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	27.33
Std Dev	25.3

B1L101S, U15-ch25, adc0

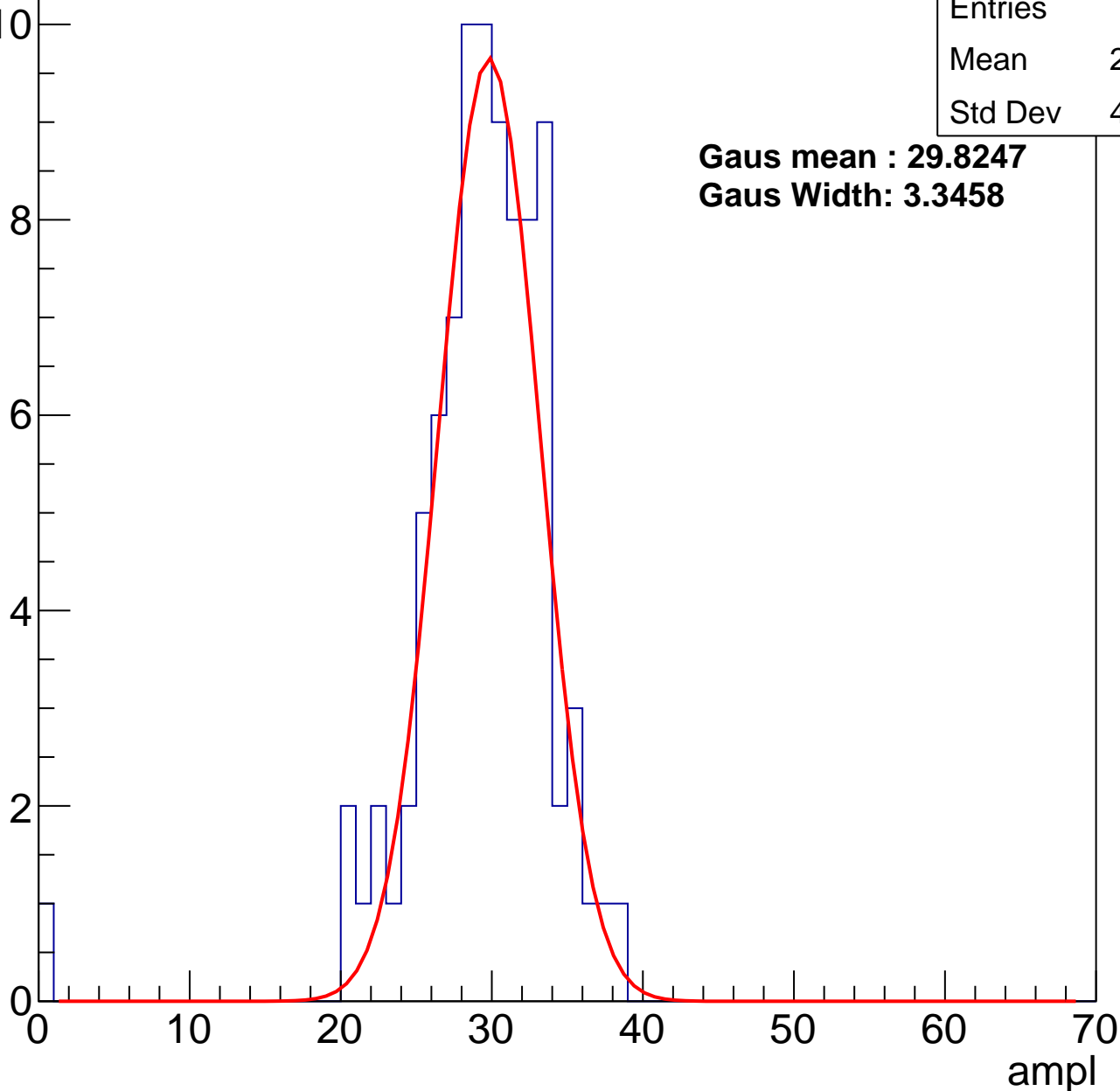
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	28.89
Std Dev	4.777

Gaus mean : 29.8247

Gaus Width: 3.3458



B1L101S, U15-ch25, adc1

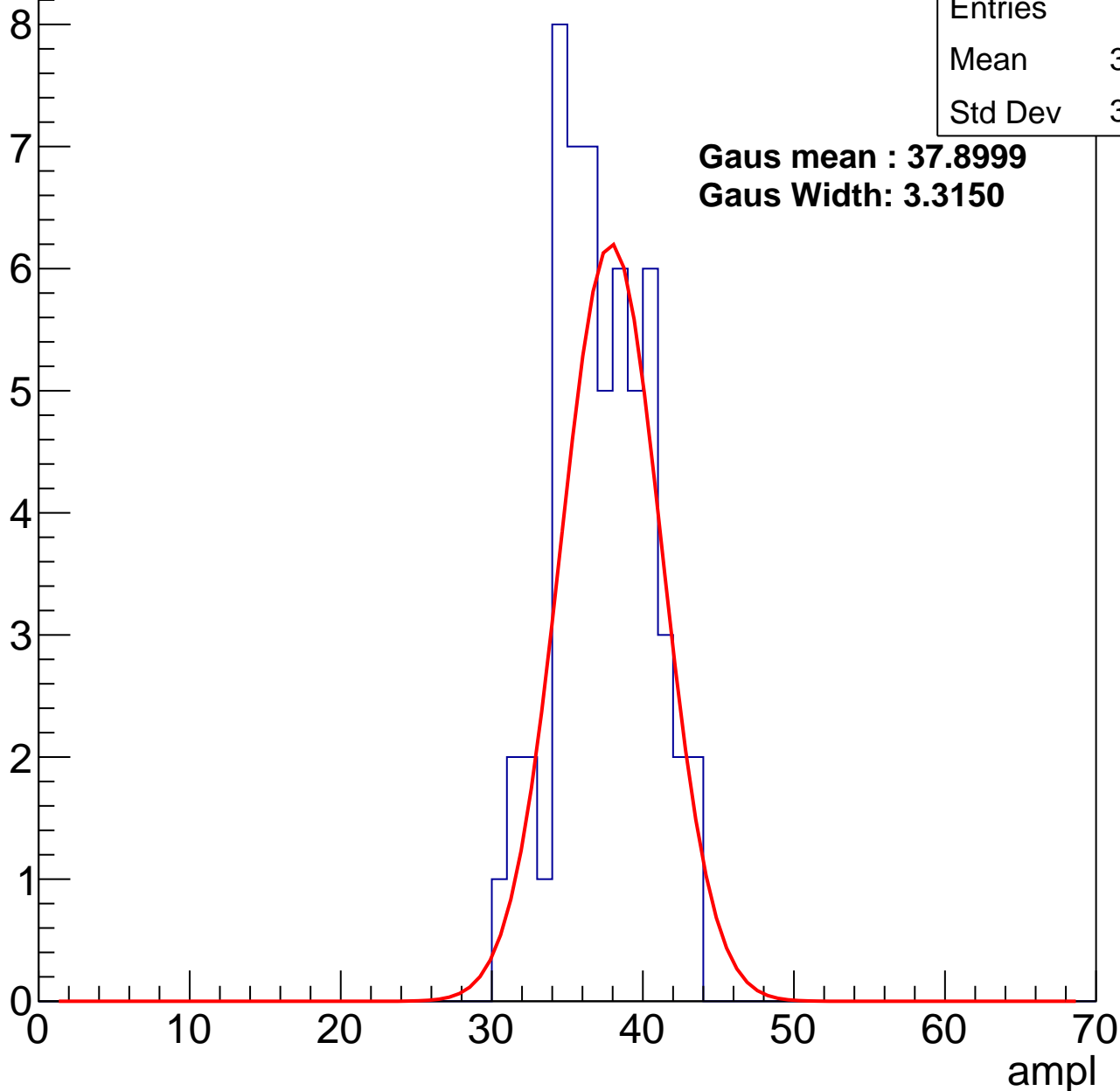
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.82
Std Dev	3.084

Gaus mean : 37.8999

Gaus Width: 3.3150



B1L101S, U15-ch25, adc2

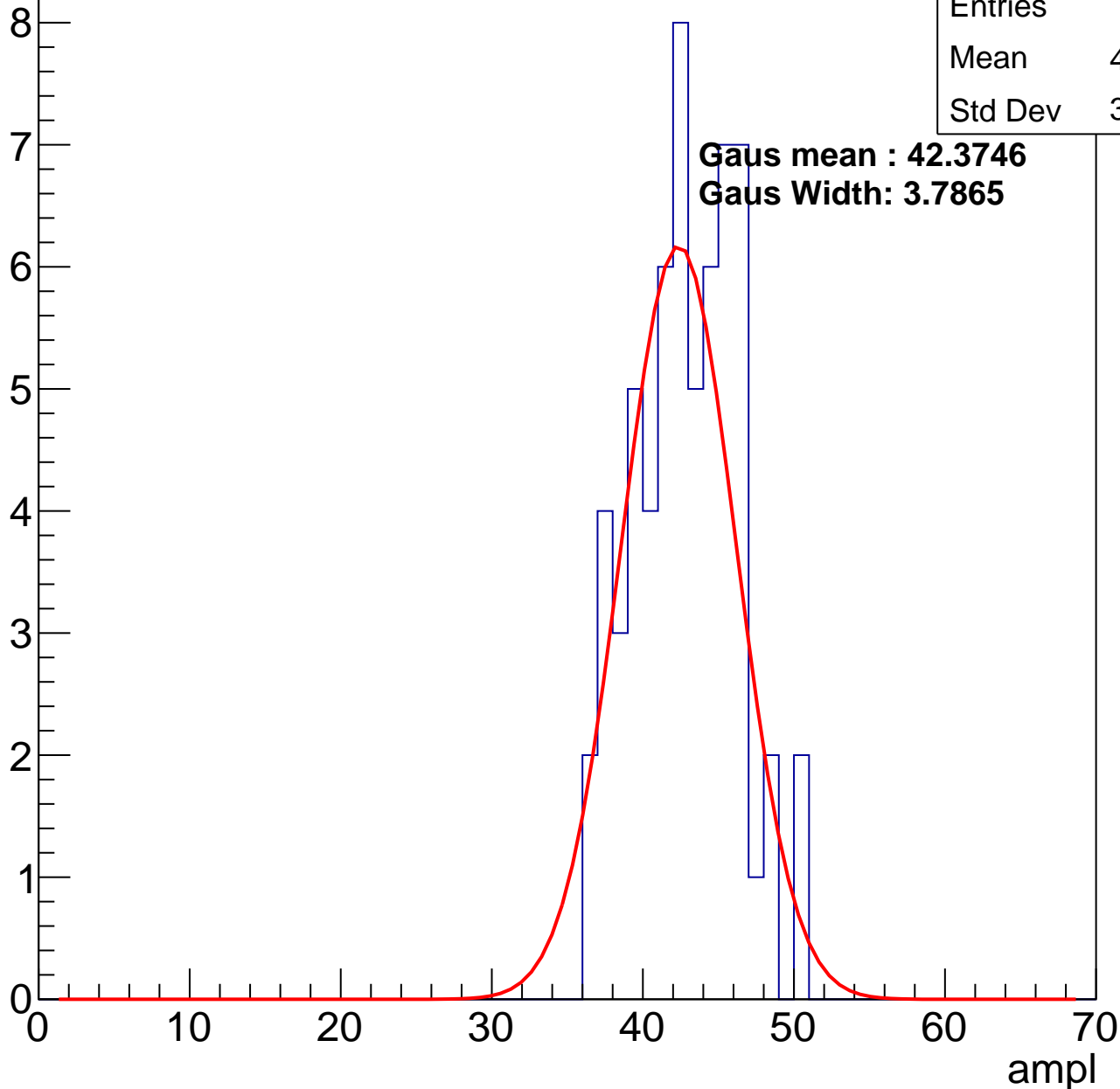
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	42.42
Std Dev	3.372

Gaus mean : 42.3746

Gaus Width: 3.7865

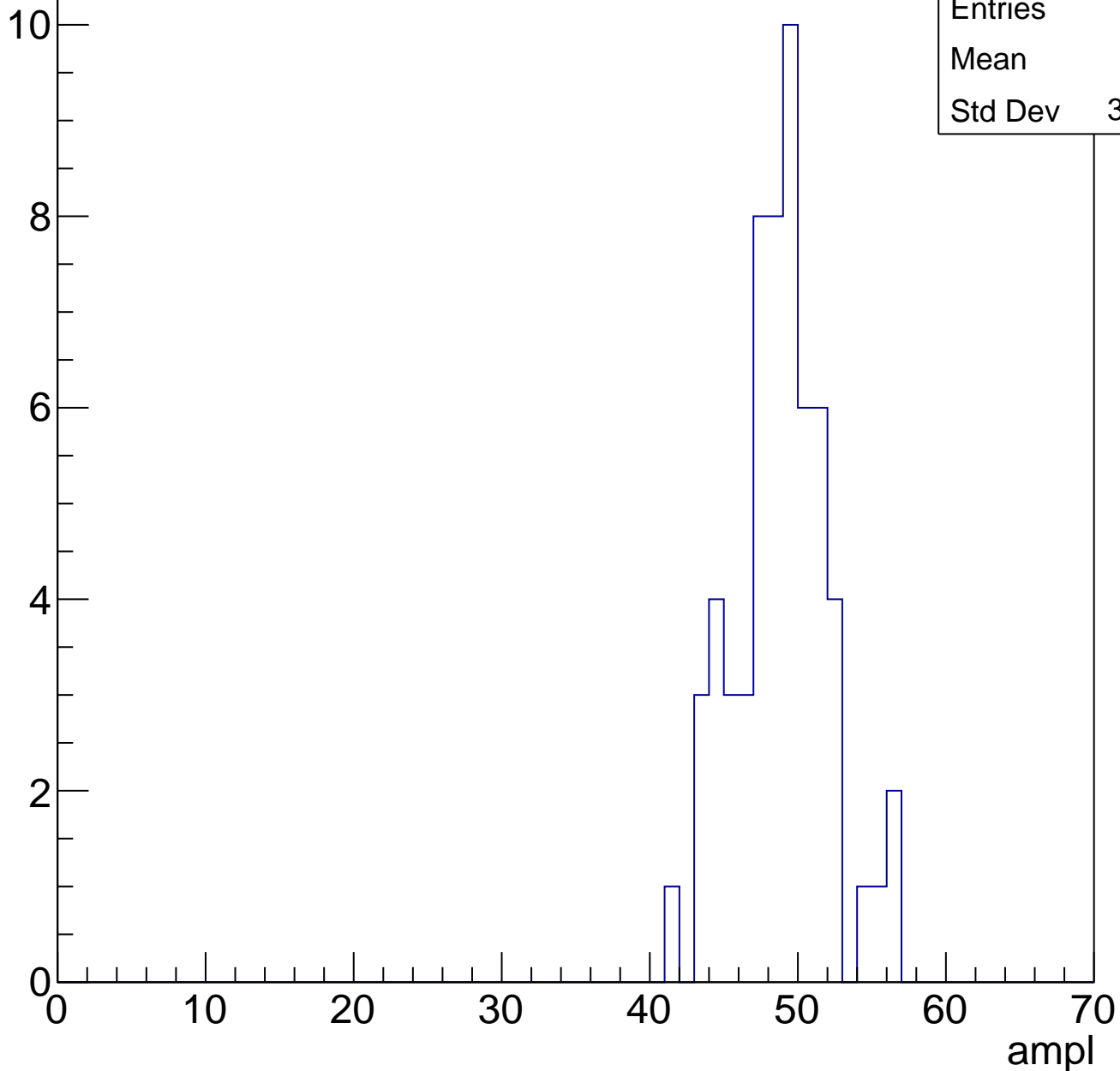


B1L101S, U15-ch25, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	48.4
Std Dev	3.137

Entry



B1L101S, U15-ch25, adc4

calib_packv5_042523_0143.root, FC#0, port D2

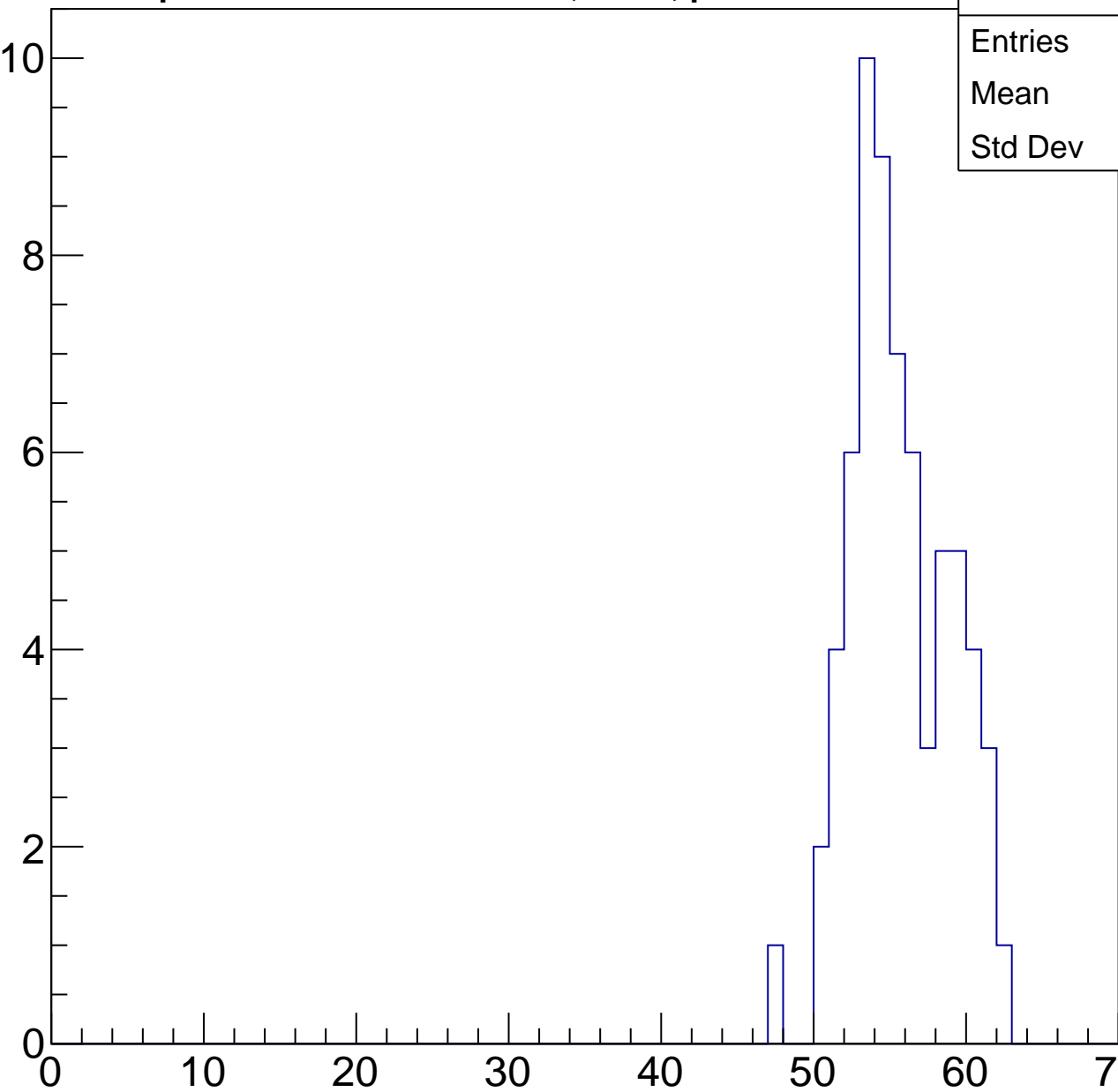
Entries	66
Mean	55.17
Std Dev	3.208

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

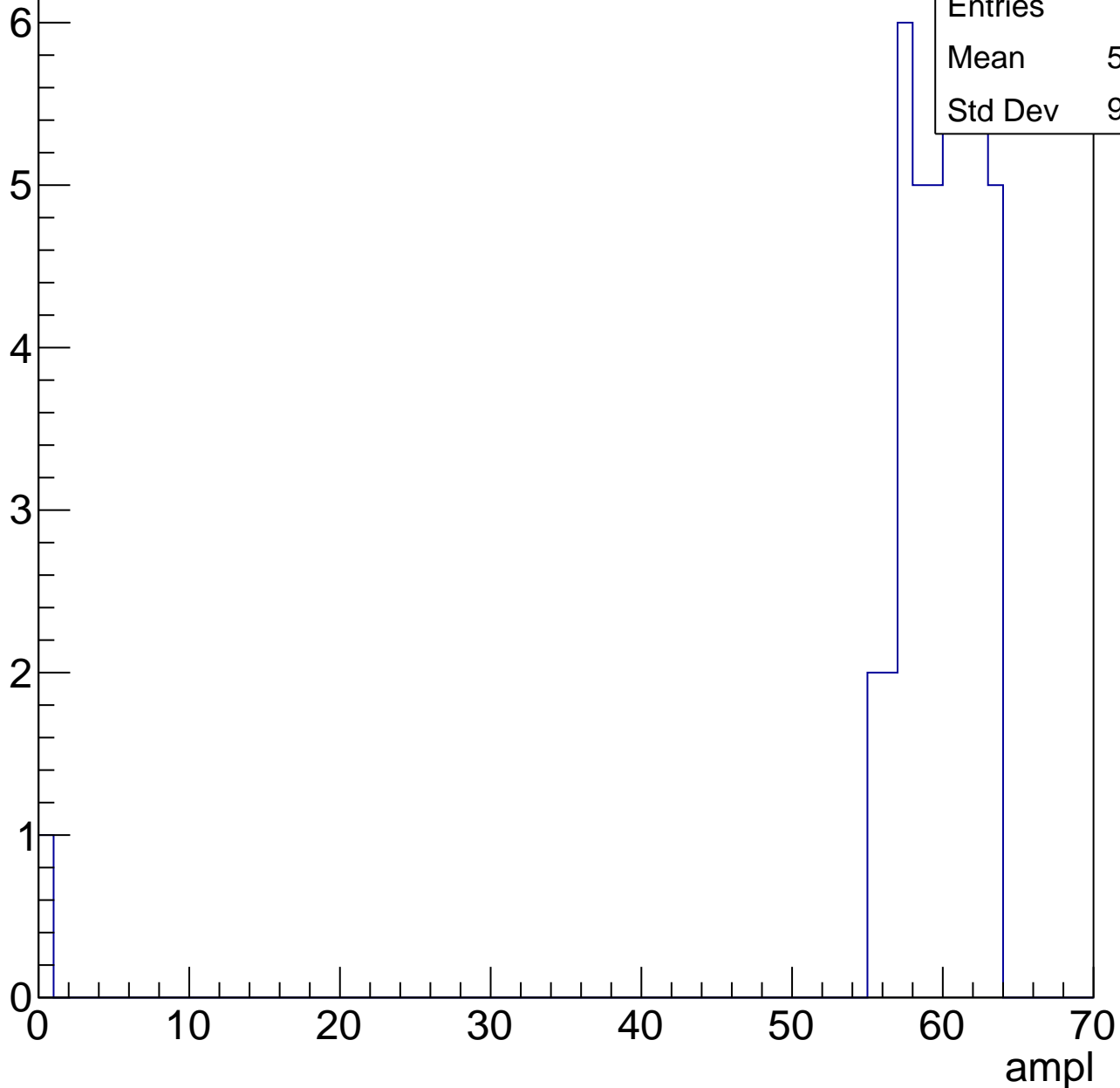
ampl



B1L101S, U15-ch25, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



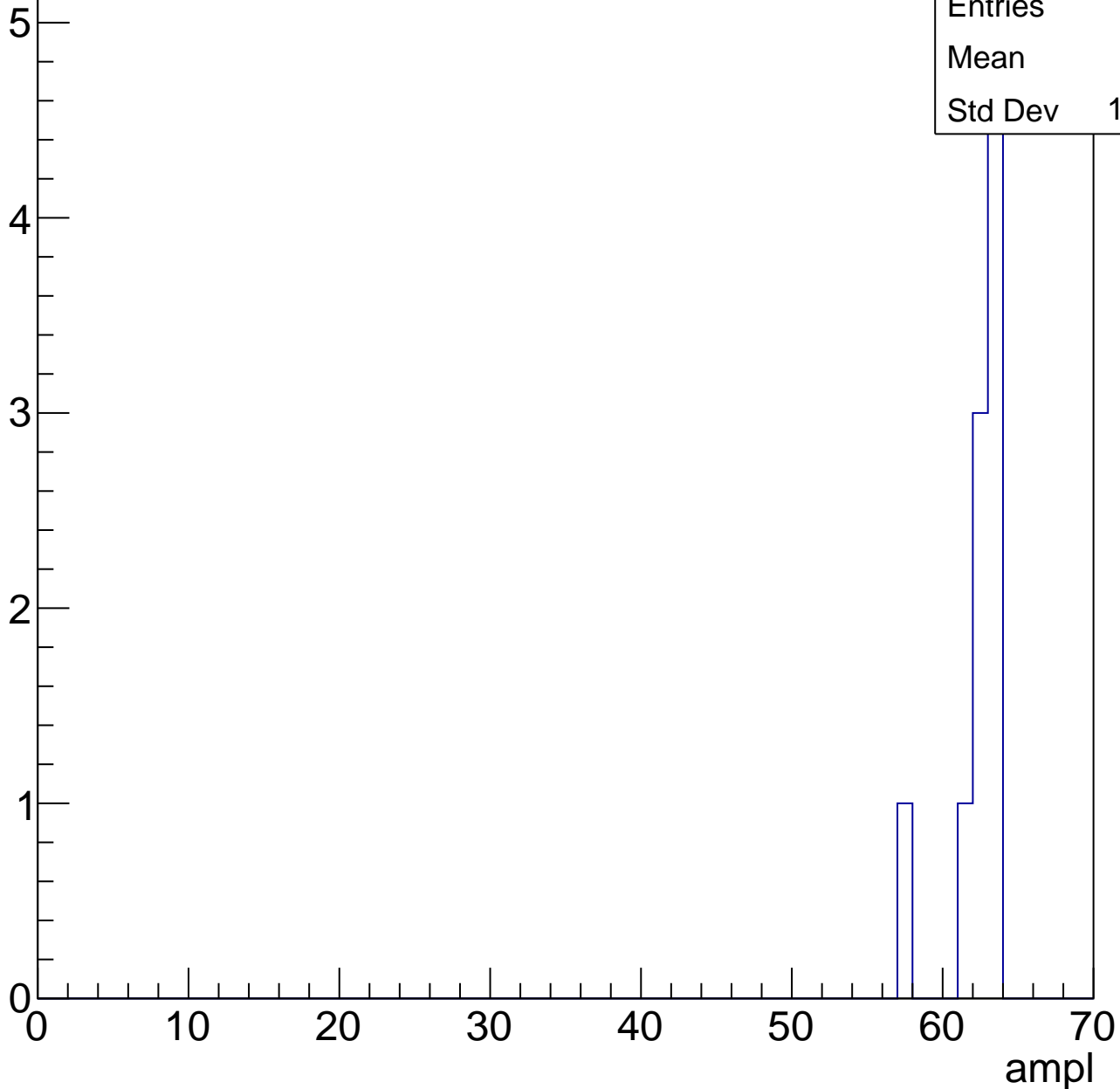
Entries	44
Mean	58.23
Std Dev	9.167

B1L101S, U15-ch25, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	61.9
Std Dev	1.758



B1L101S, U15-ch25, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U15-ch26, adc0

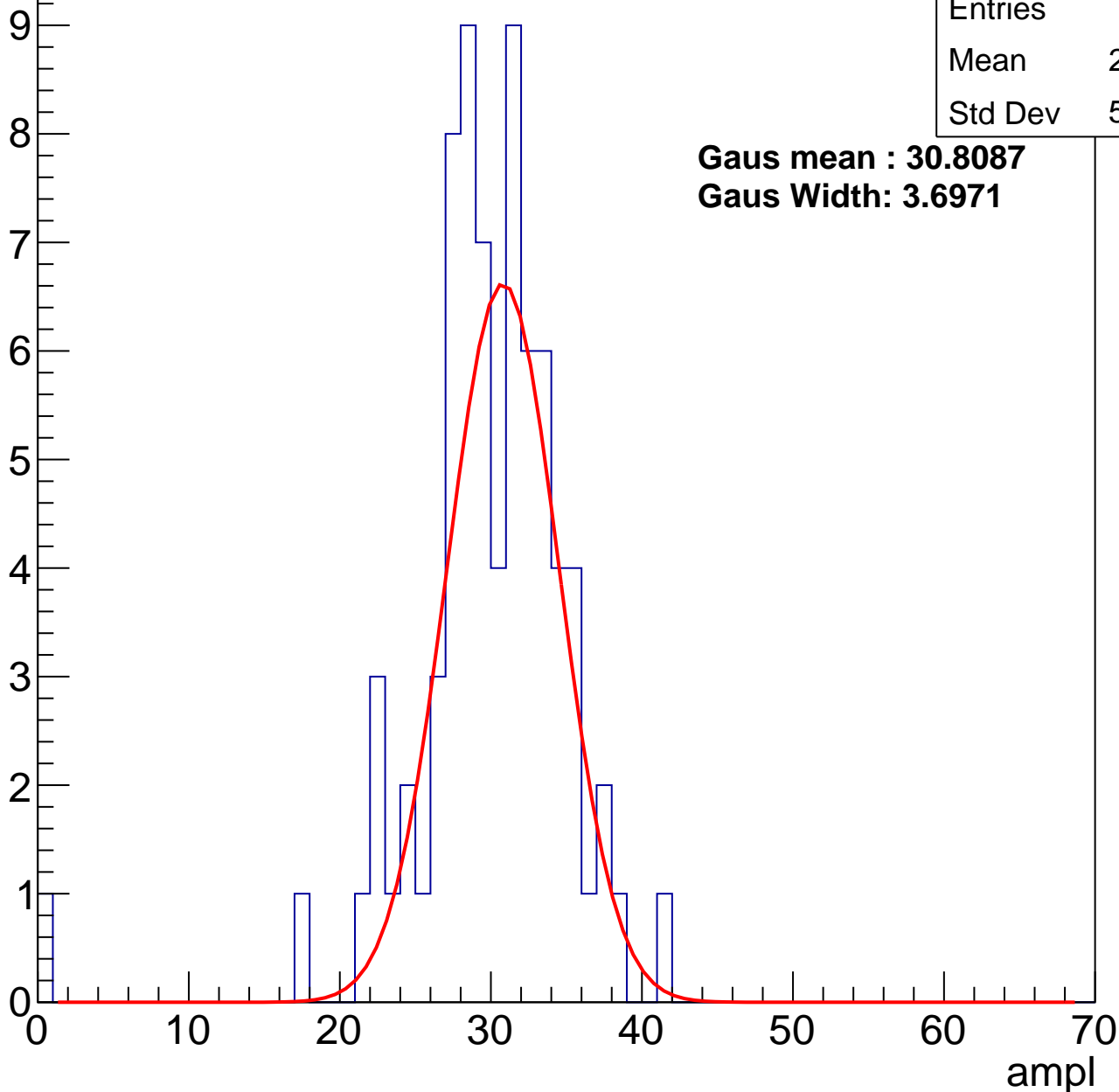
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	29.37
Std Dev	5.406

Gaus mean : 30.8087

Gaus Width: 3.6971



B1L101S, U15-ch26, adc1

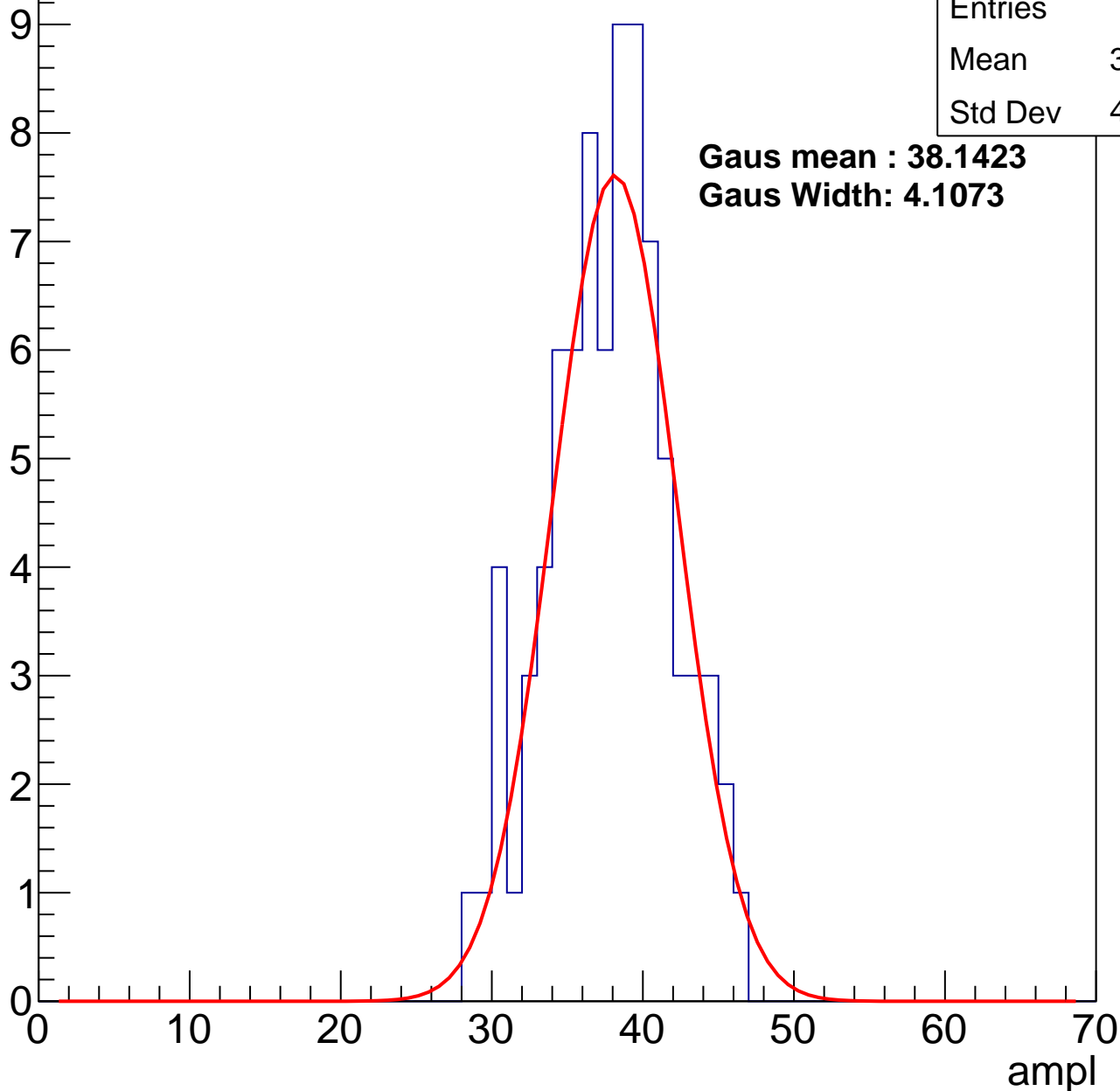
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	37.33
Std Dev	4.012

Gaus mean : 38.1423

Gaus Width: 4.1073



B1L101S, U15-ch26, adc2

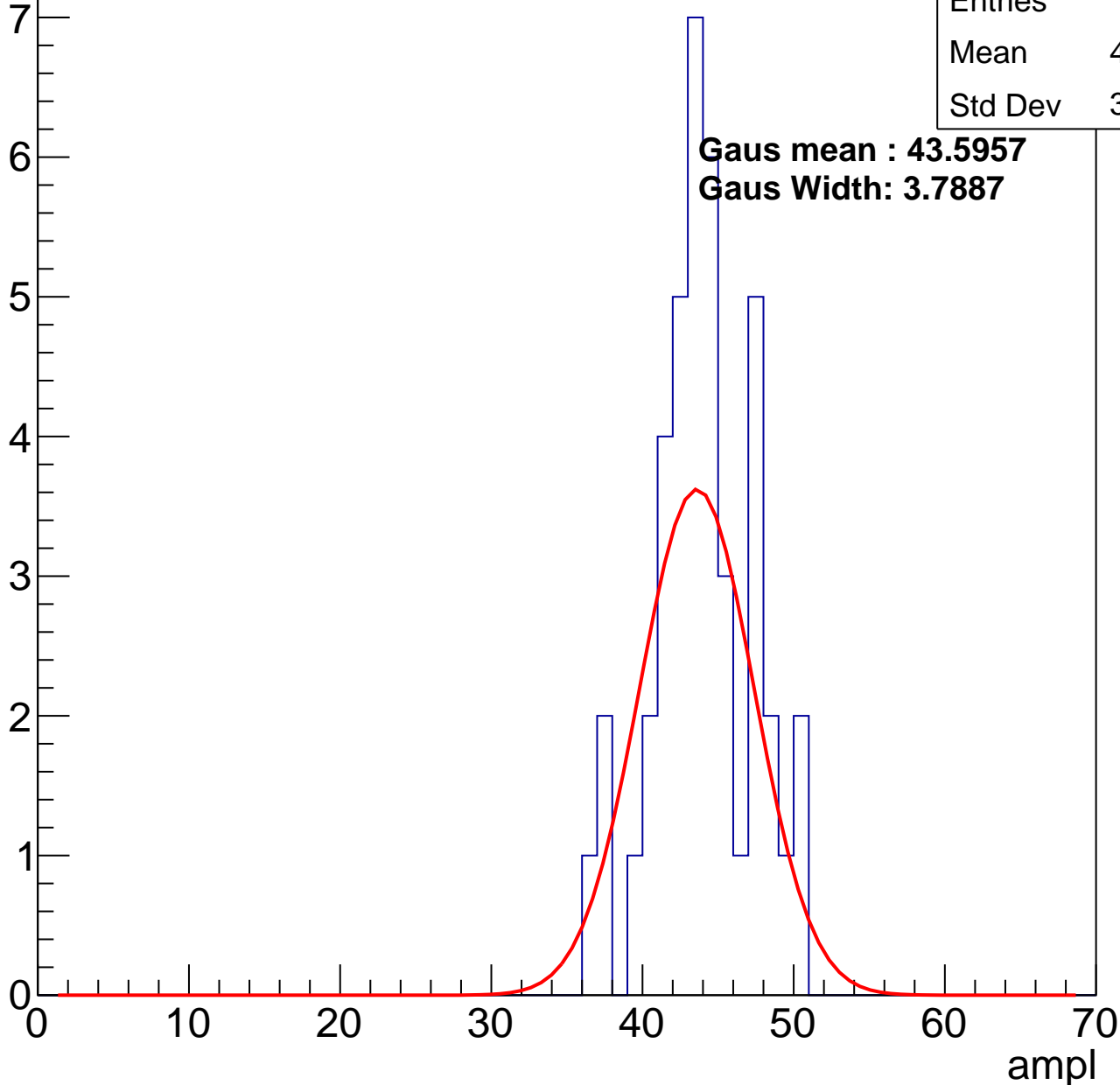
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	43.55
Std Dev	3.289

Gaus mean : 43.5957

Gaus Width: 3.7887

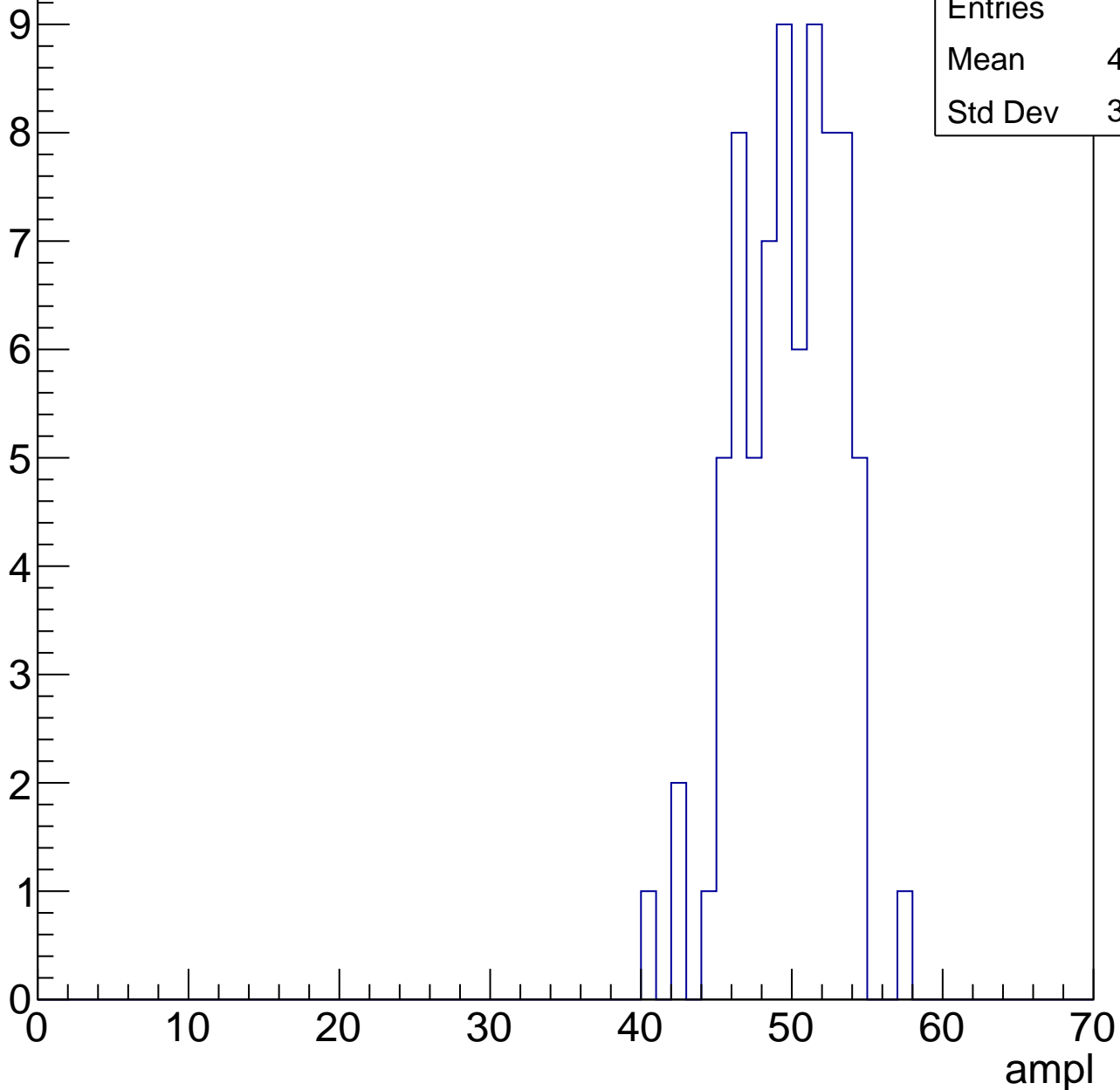


B1L101S, U15-ch26, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	49.32
Std Dev	3.279

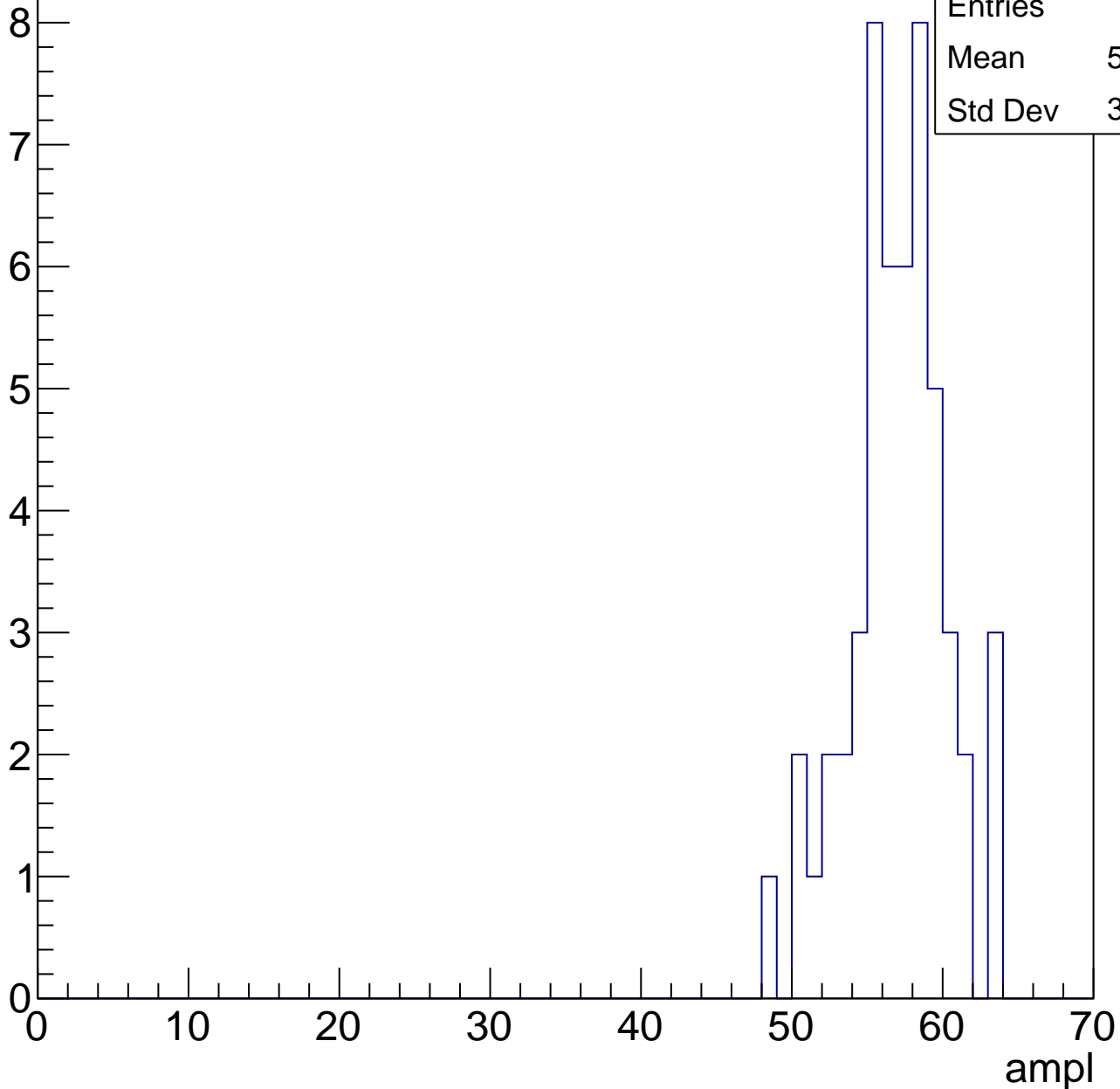


B1L101S, U15-ch26, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.52
Std Dev	3.237



B1L101S, U15-ch26, adc5

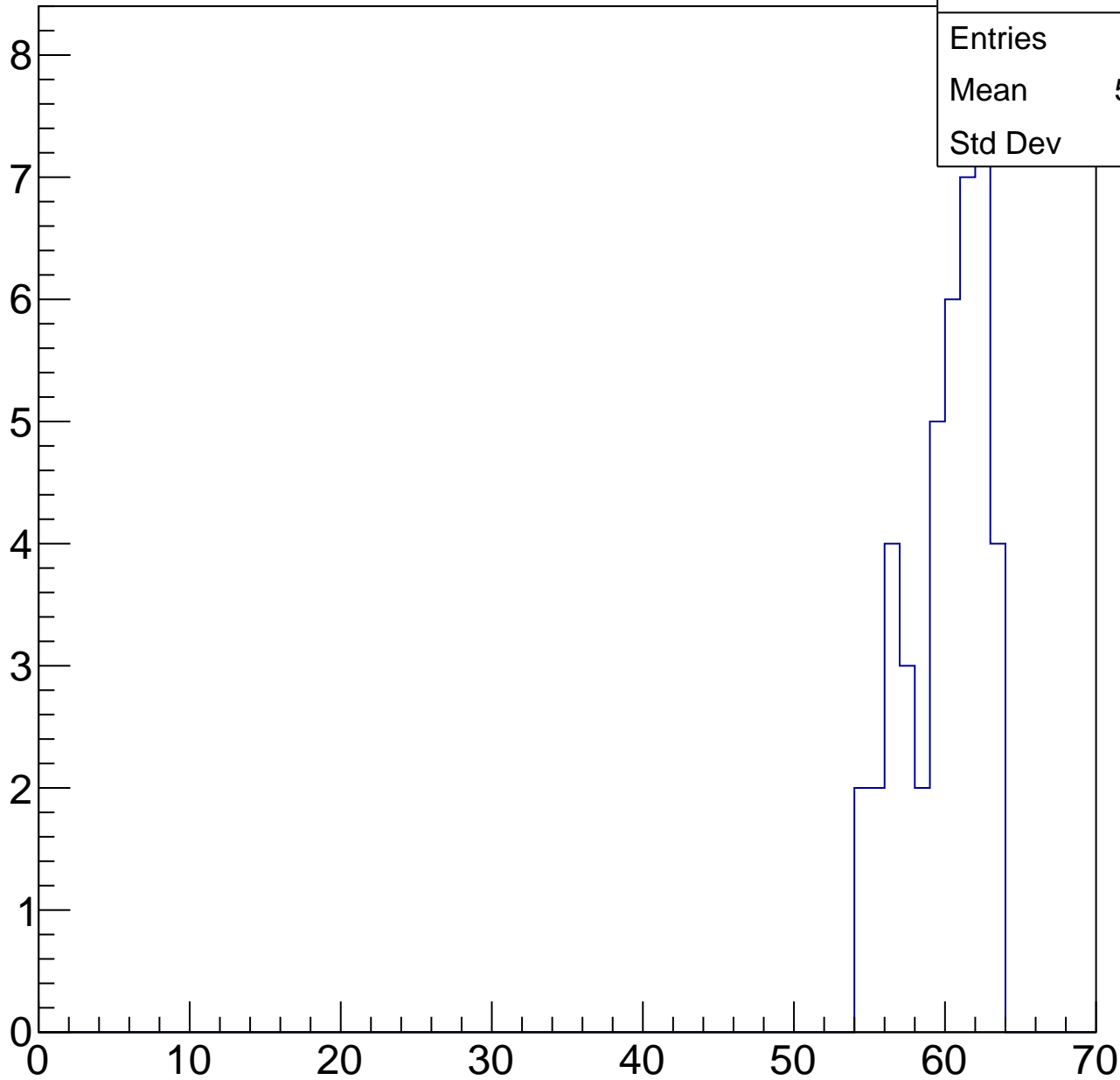
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	43
Mean	59.51
Std Dev	2.6

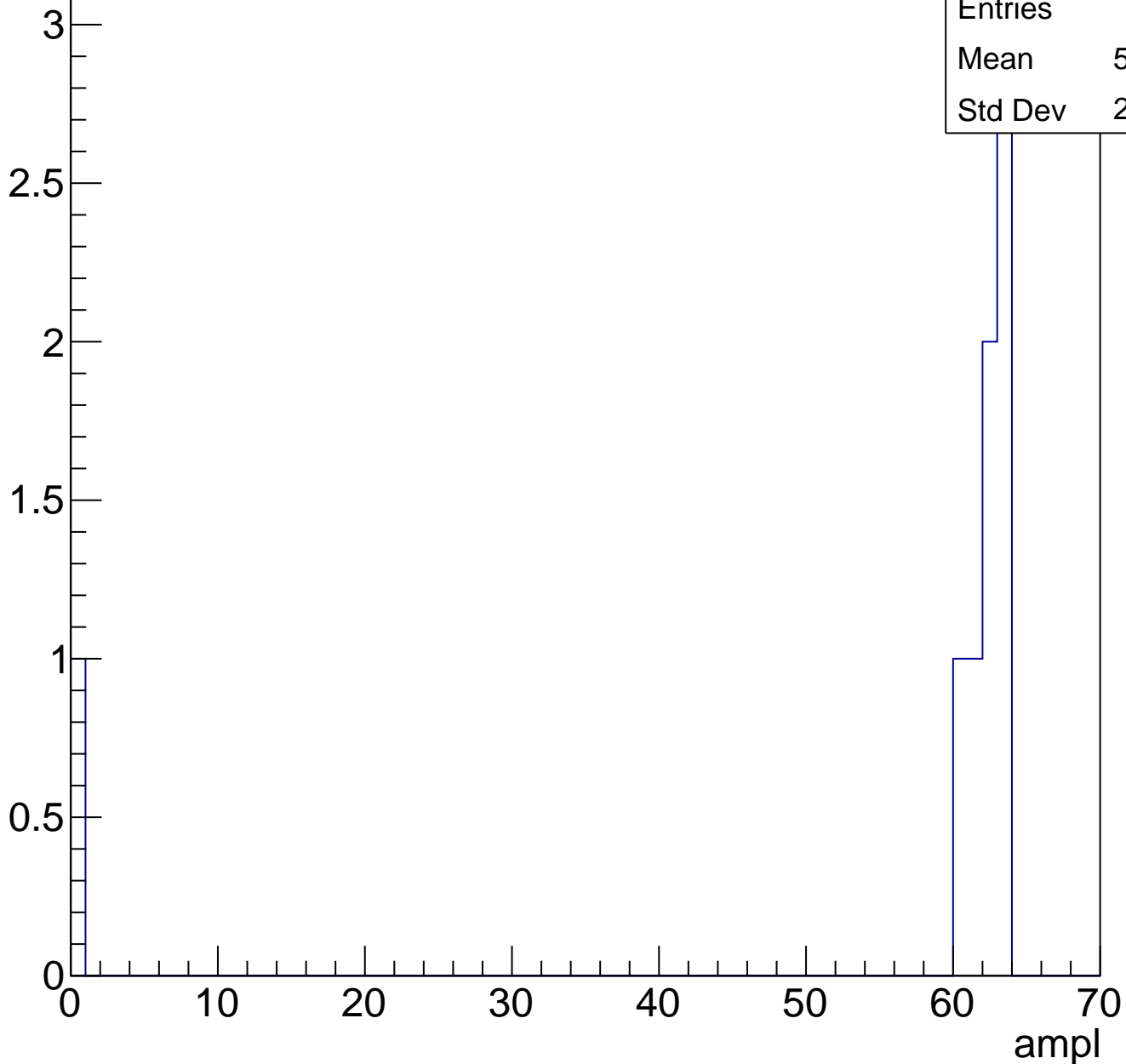
ampl



B1L101S, U15-ch26, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	54.25
Std Dev	20.53

B1L101S, U15-ch26, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch27, adc0

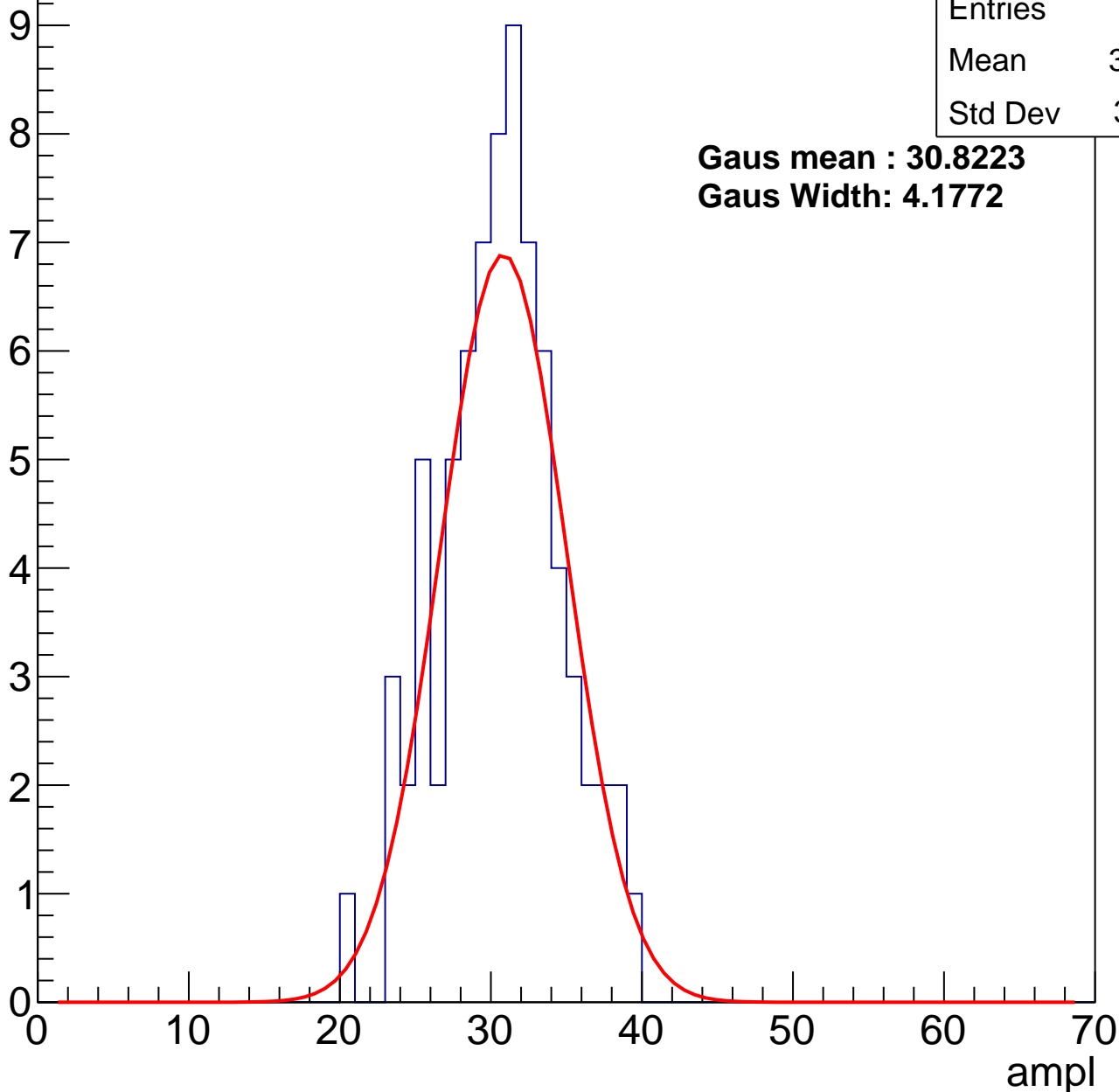
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	30.17
Std Dev	3.931

Gaus mean : 30.8223

Gaus Width: 4.1772



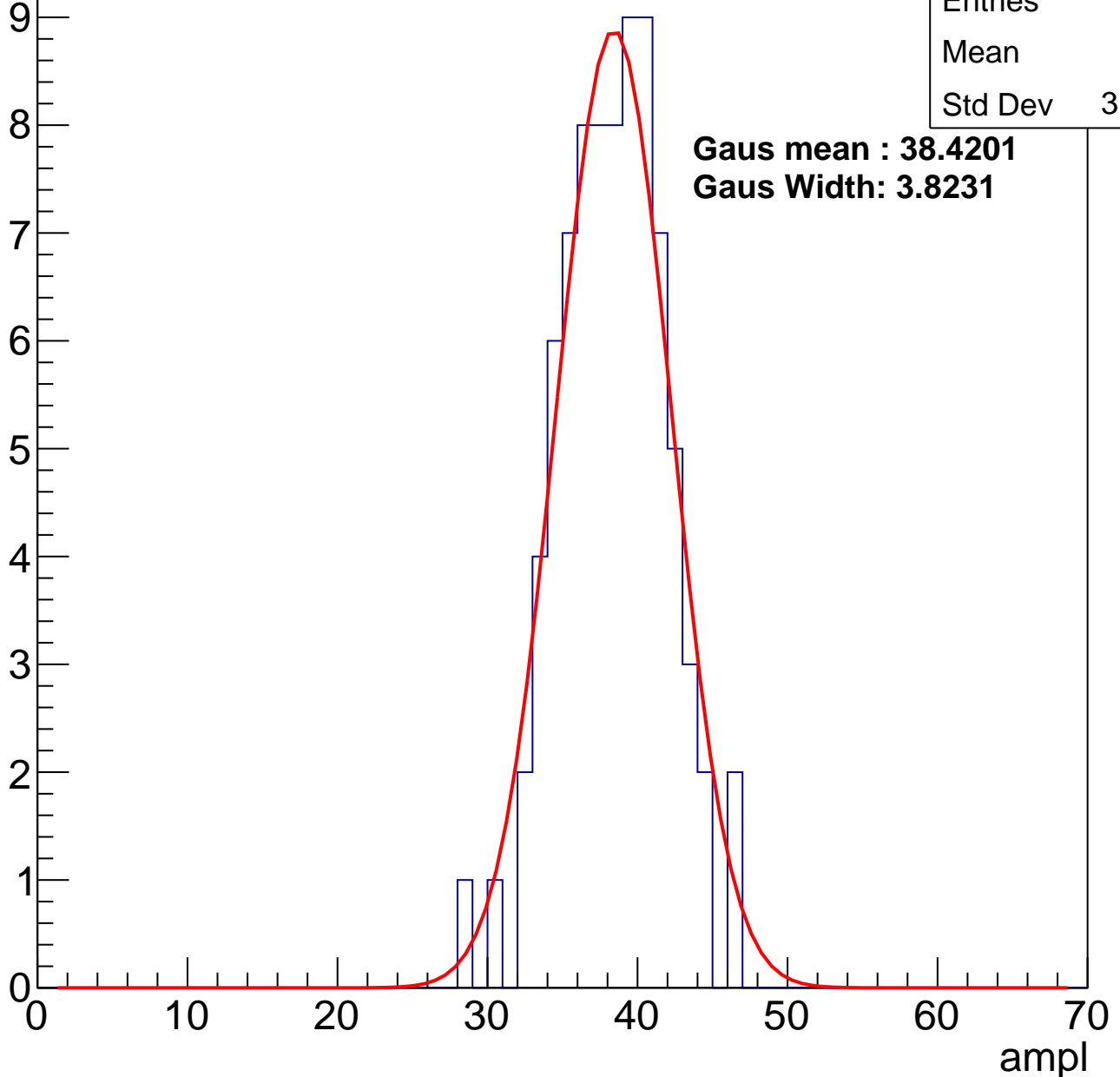
B1L101S, U15-ch27, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	37.9
Std Dev	3.487

Gaus mean : 38.4201
Gaus Width: 3.8231



B1L101S, U15-ch27, adc2

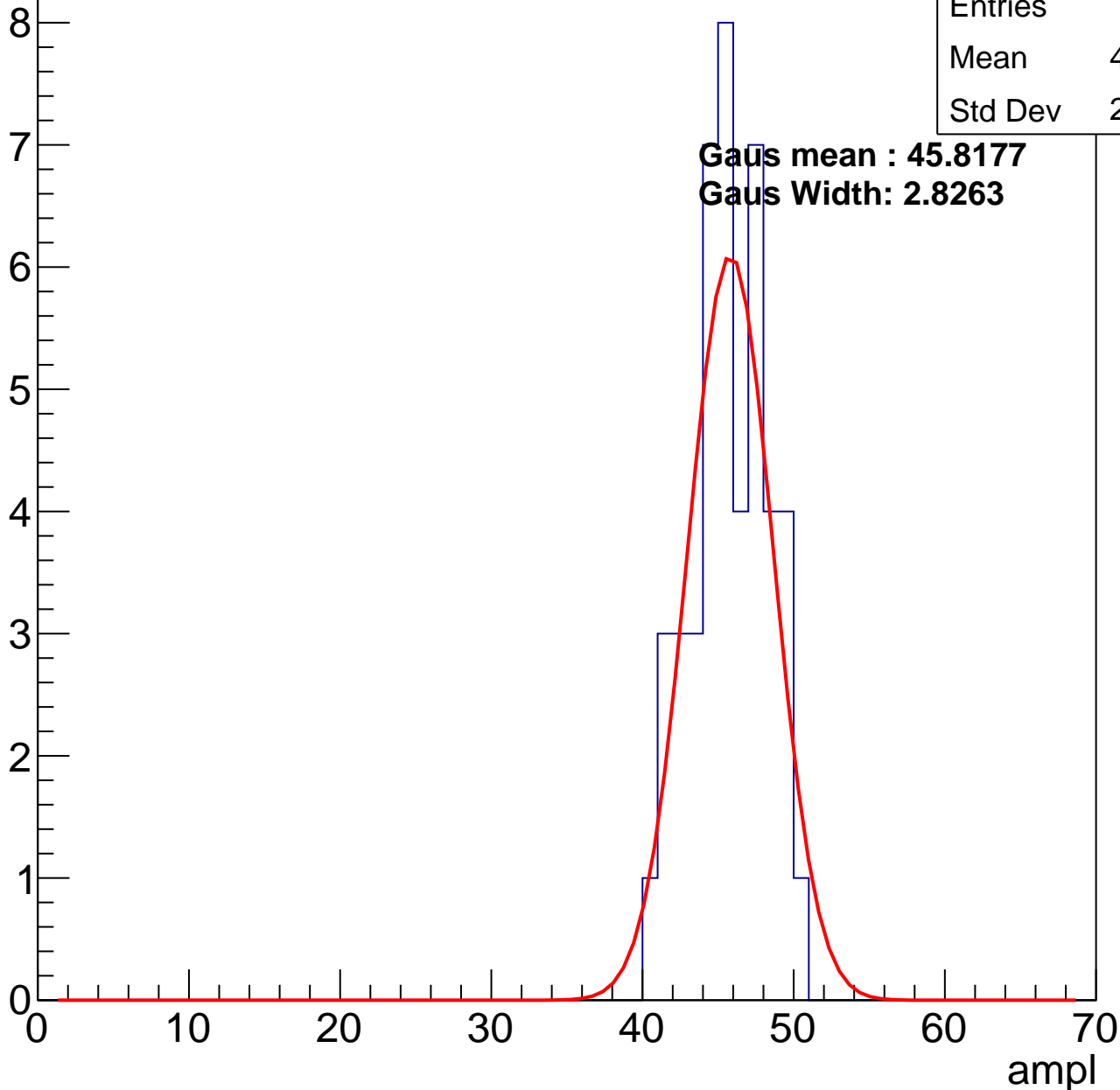
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	45.27
Std Dev	2.462

Gaus mean : 45.8177

Gaus Width: 2.8263

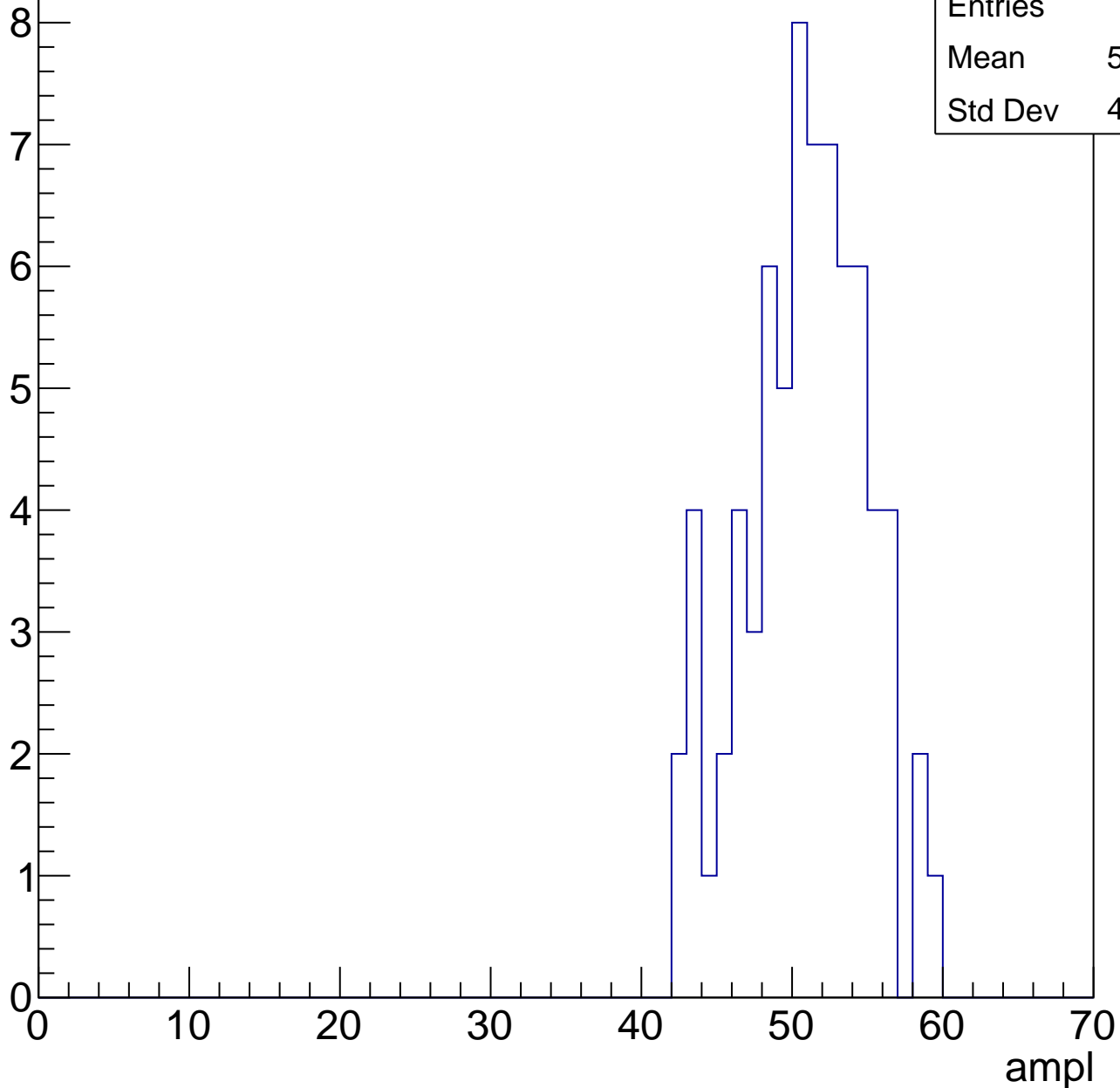


B1L101S, U15-ch27, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

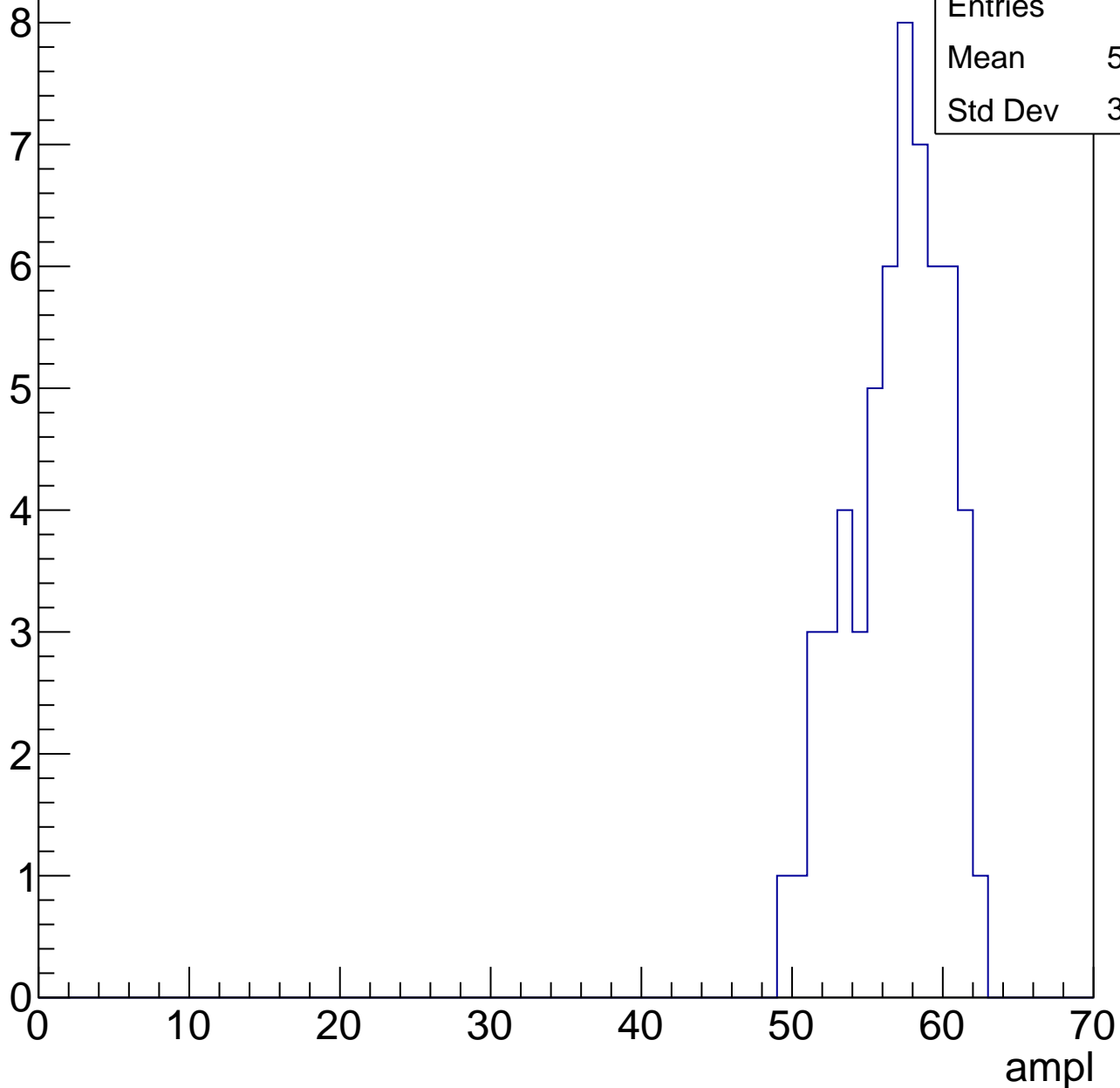
Entries	72
Mean	50.42
Std Dev	4.006



B1L101S, U15-ch27, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



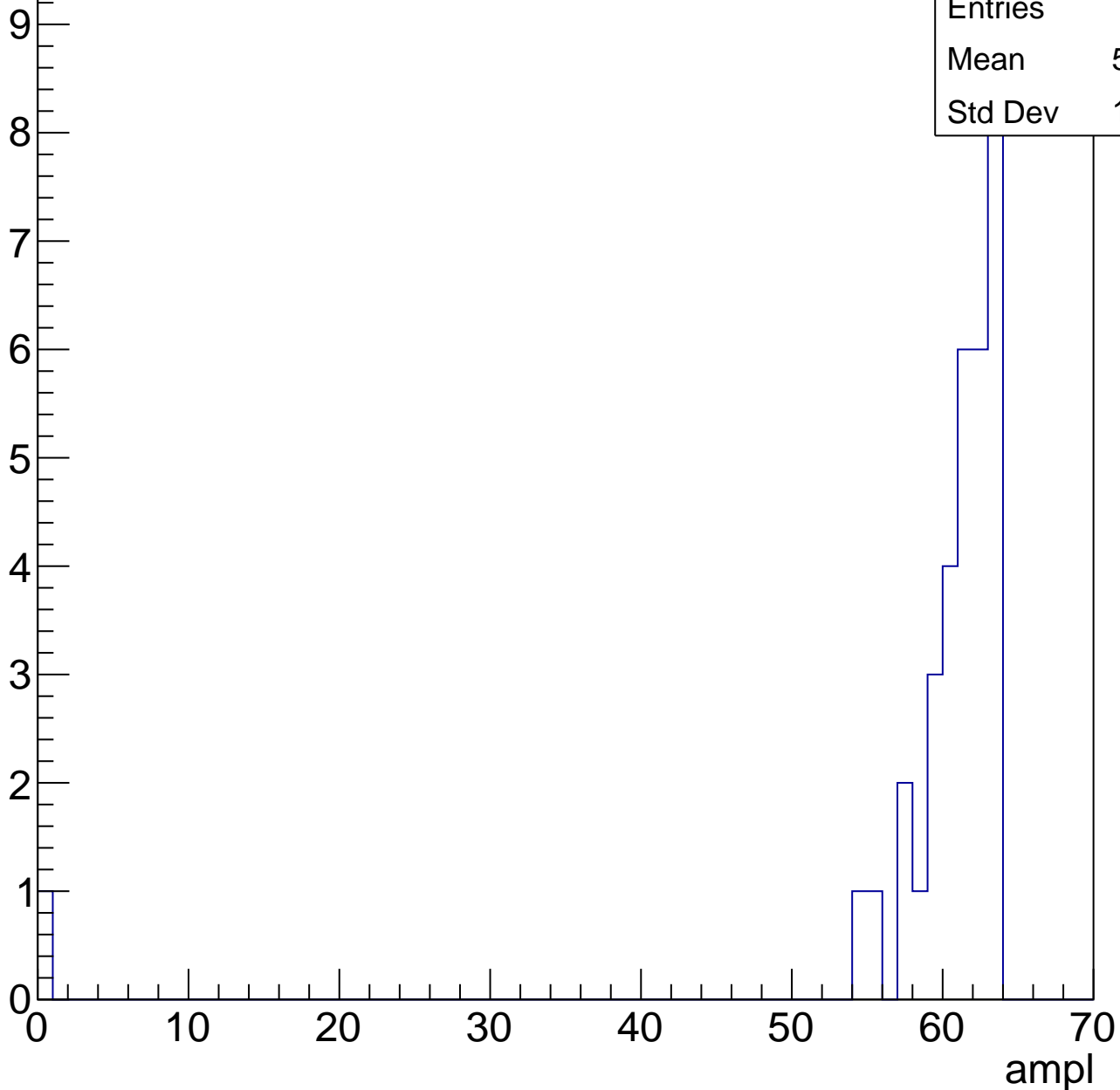
Entries	58
Mean	56.47
Std Dev	3.136

B1L101S, U15-ch27, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	58.91
Std Dev	10.51



B1L101S, U15-ch27, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch27, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch28, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	28.12
Std Dev	5.961

Gaus mean : 29.9050

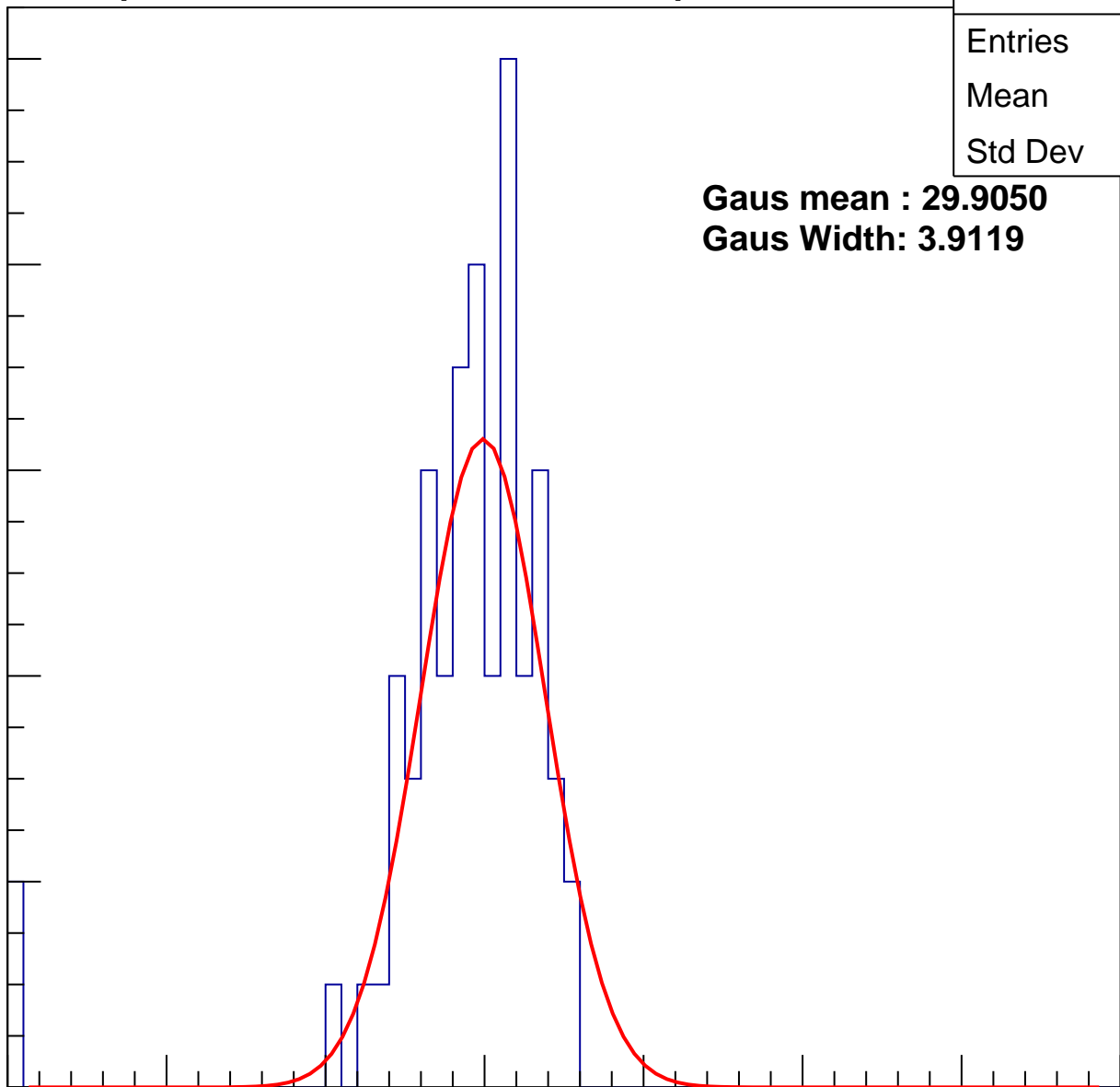
Gaus Width: 3.9119

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch28, adc1

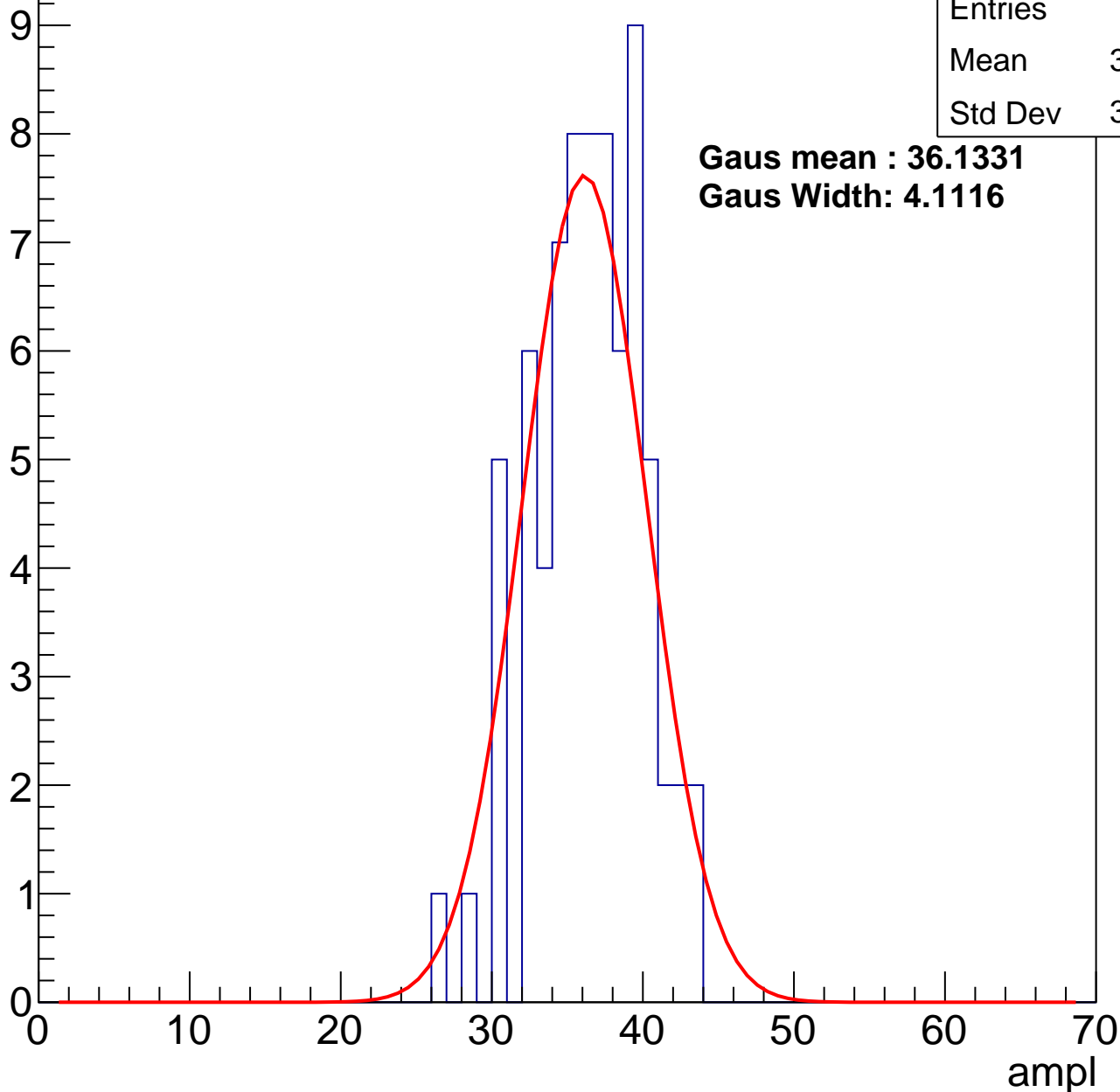
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.96
Std Dev	3.535

Gaus mean : 36.1331

Gaus Width: 4.1116



B1L101S, U15-ch28, adc2

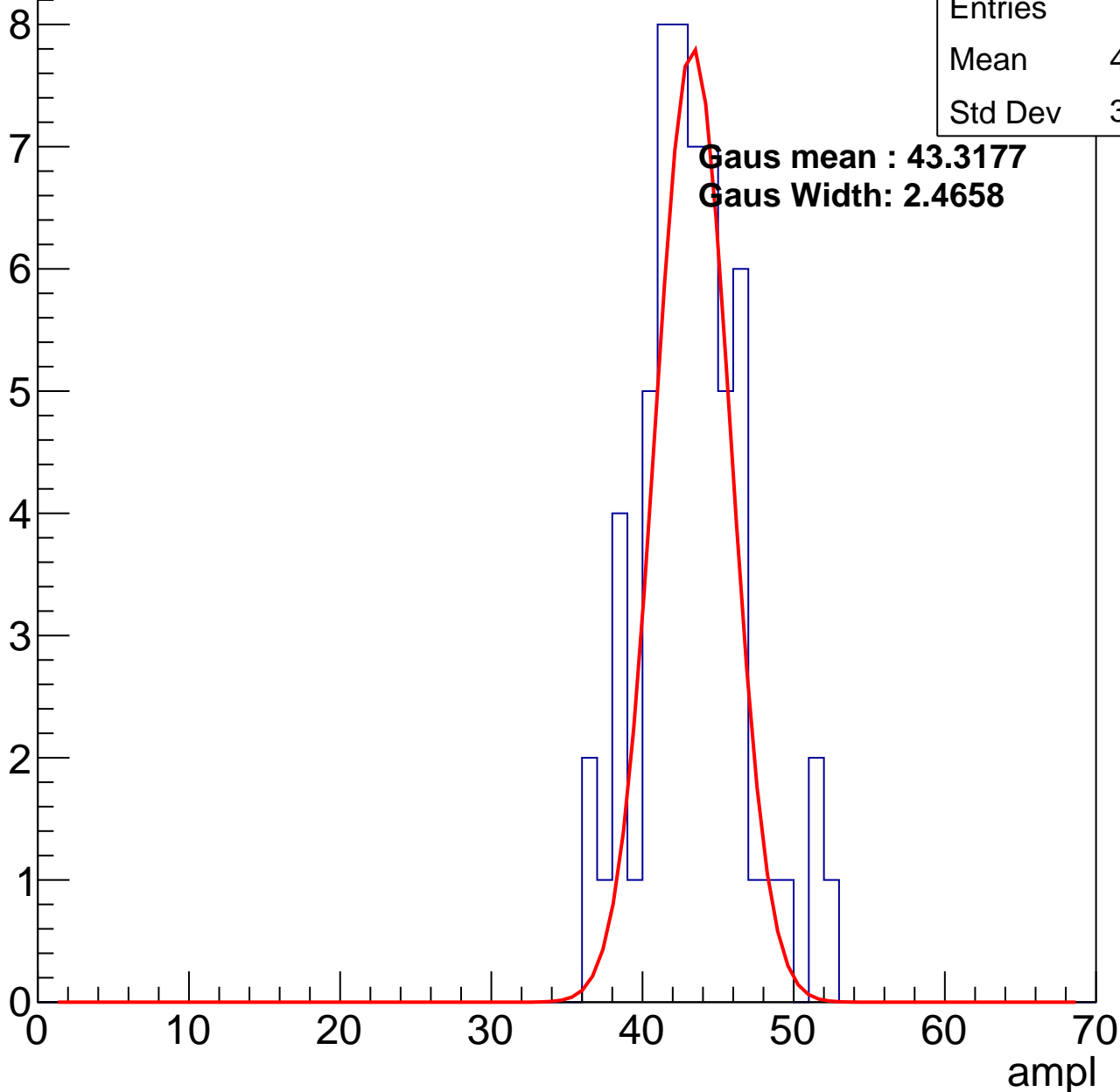
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.87
Std Dev	3.418

Gaus mean : 43.3177

Gaus Width: 2.4658

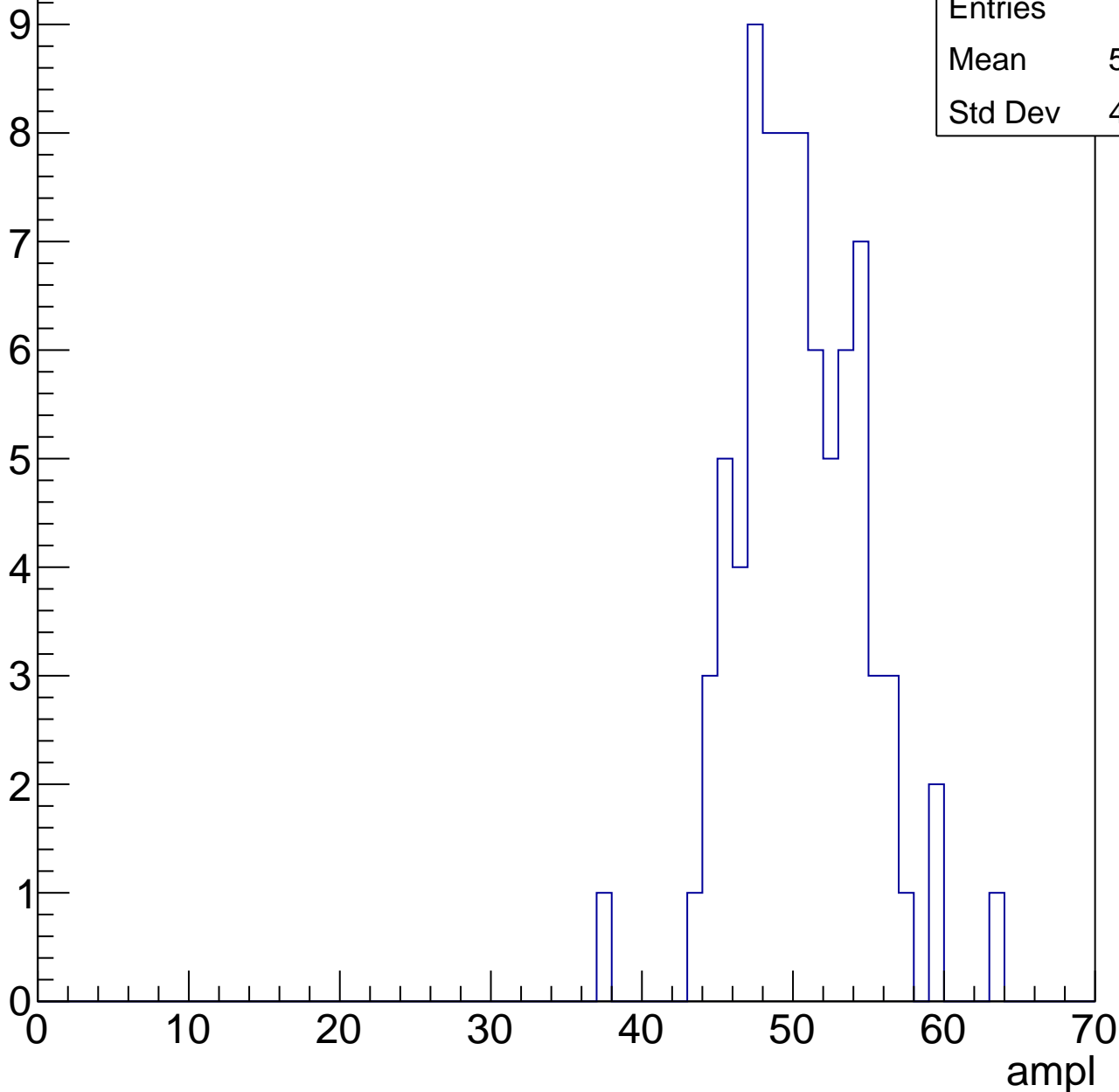


B1L101S, U15-ch28, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	50.04
Std Dev	4.144

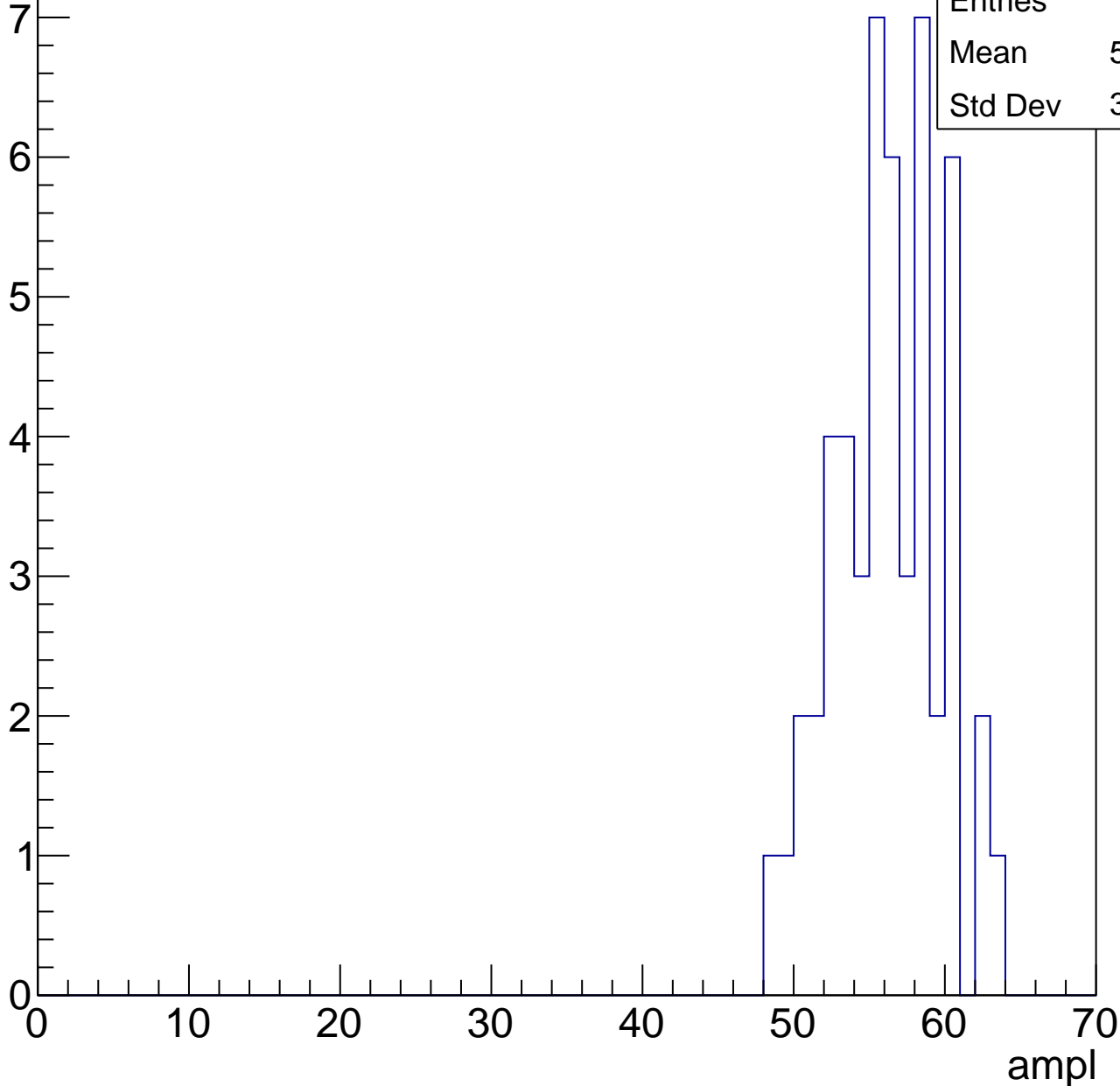


B1L101S, U15-ch28, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	55.76
Std Dev	3.479



B1L101S, U15-ch28, adc5

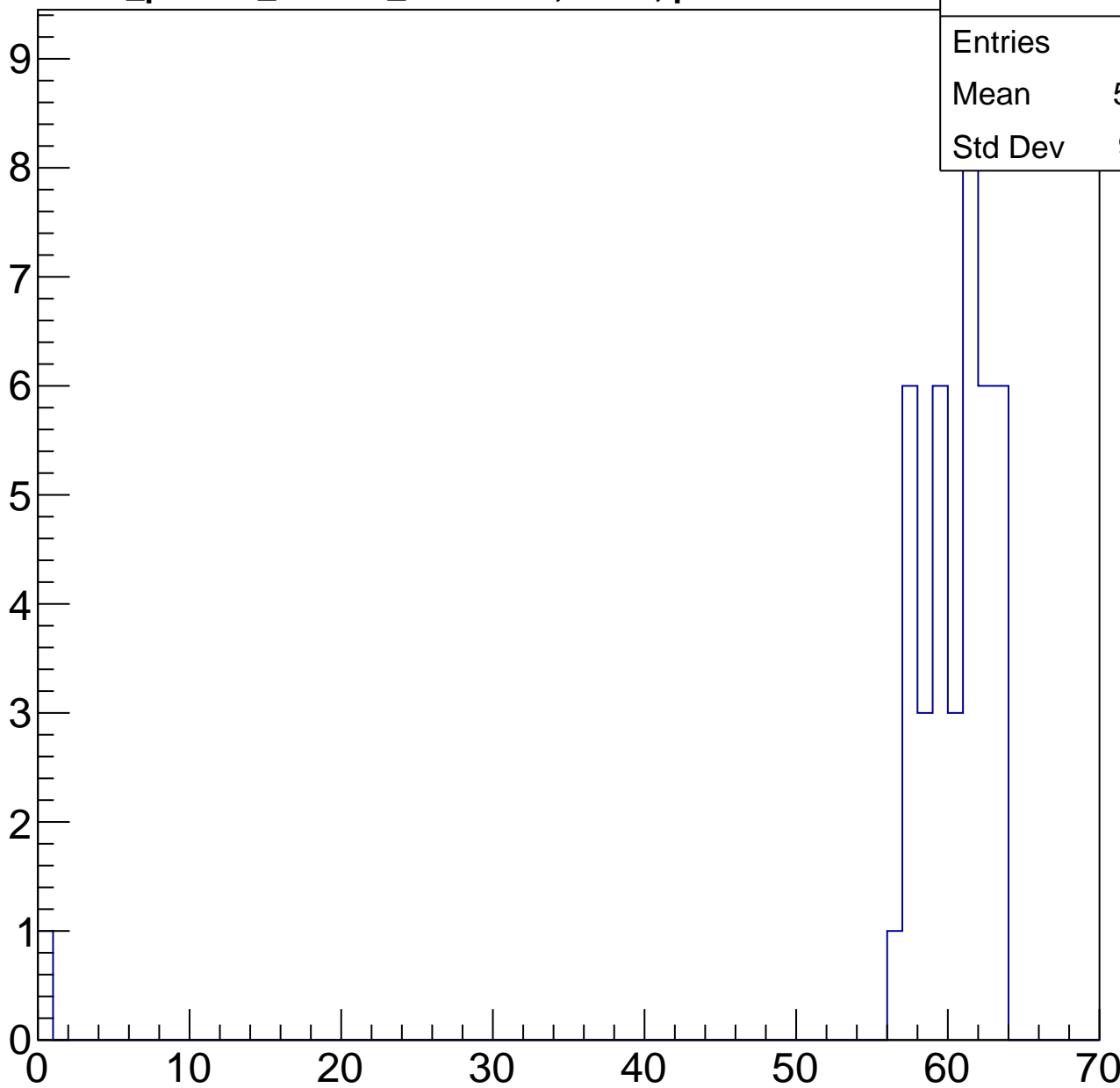
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	41
Mean	58.66
Std Dev	9.501

ampl



B1L101S, U15-ch28, adc6

calib_packv5_042523_0143.root, FC#0, port D2

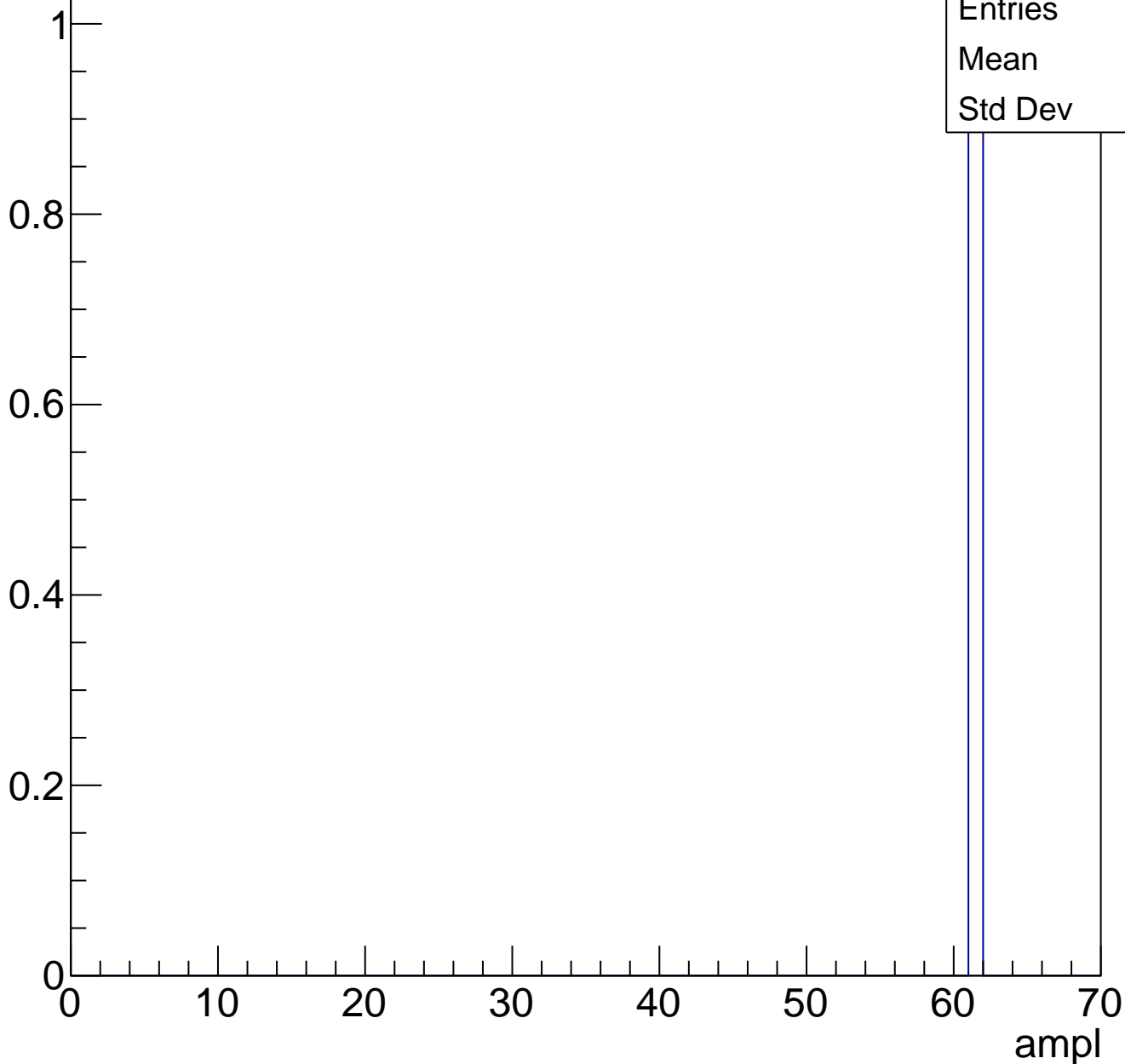
Entry



B1L101S, U15-ch28, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch29, adc0

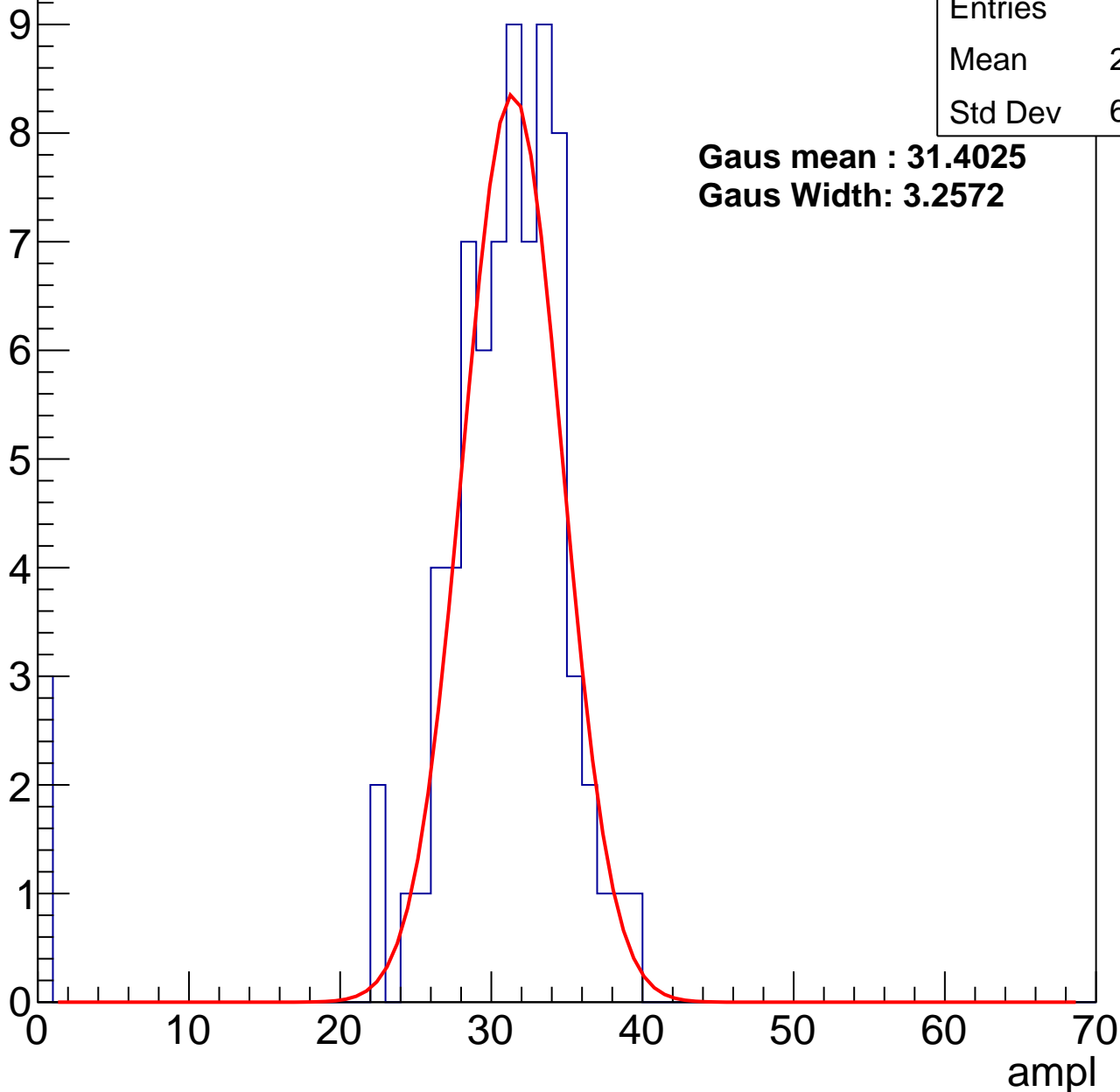
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.58
Std Dev	6.879

Gaus mean : 31.4025

Gaus Width: 3.2572



B1L101S, U15-ch29, adc1

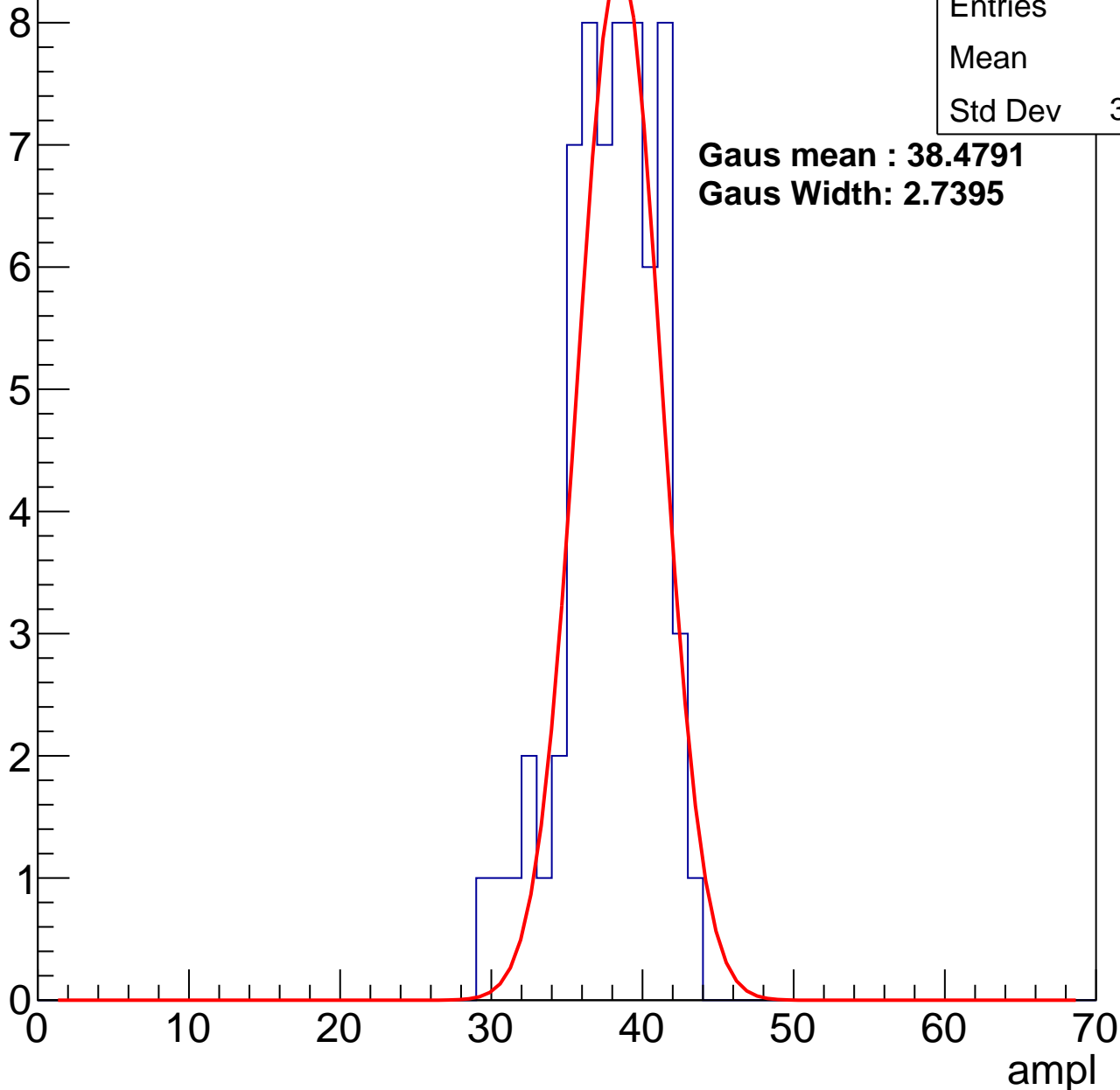
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	37.5
Std Dev	3.026

Gaus mean : 38.4791

Gaus Width: 2.7395



B1L101S, U15-ch29, adc2

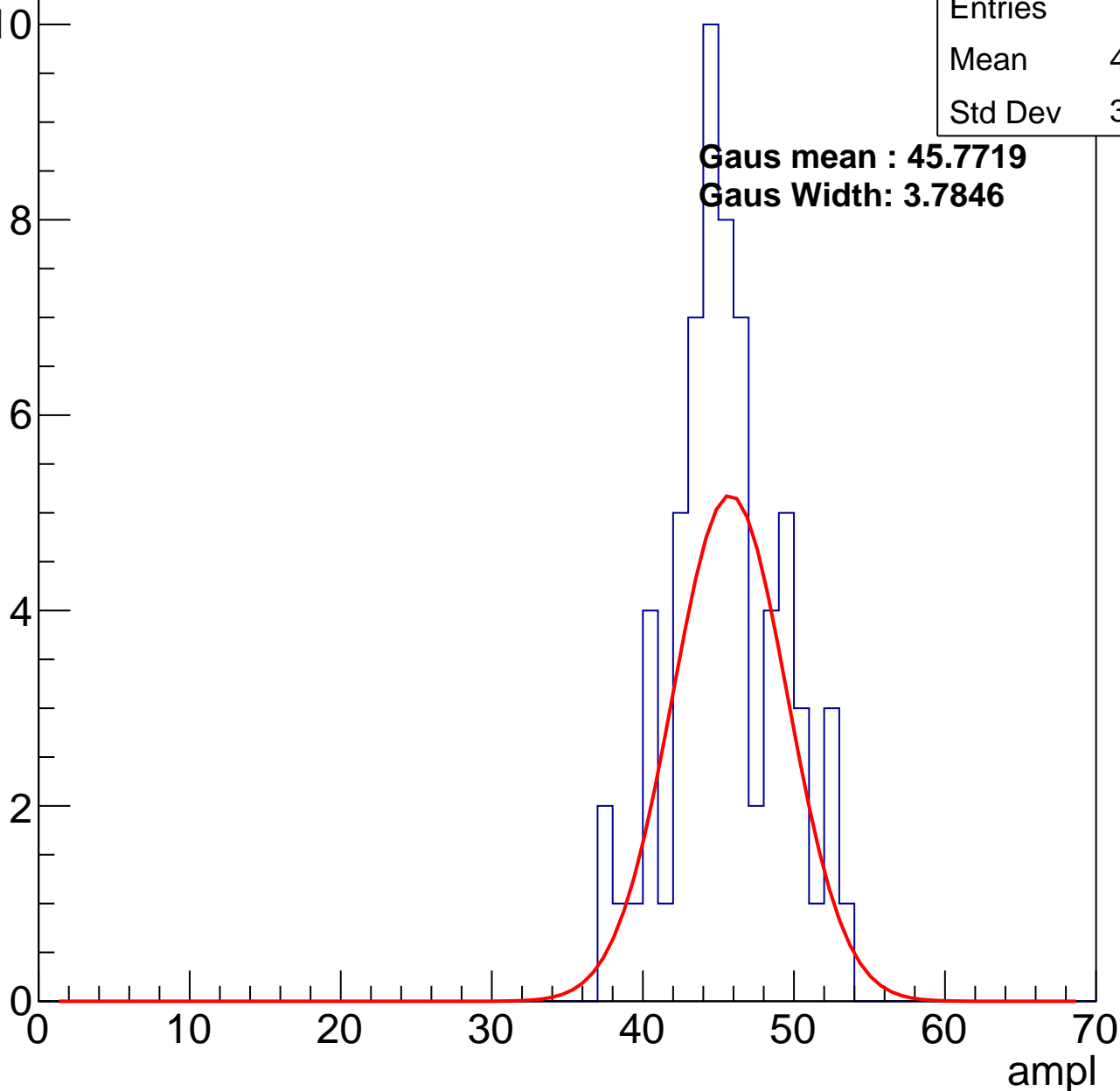
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	45.02
Std Dev	3.648

Gaus mean : 45.7719

Gaus Width: 3.7846

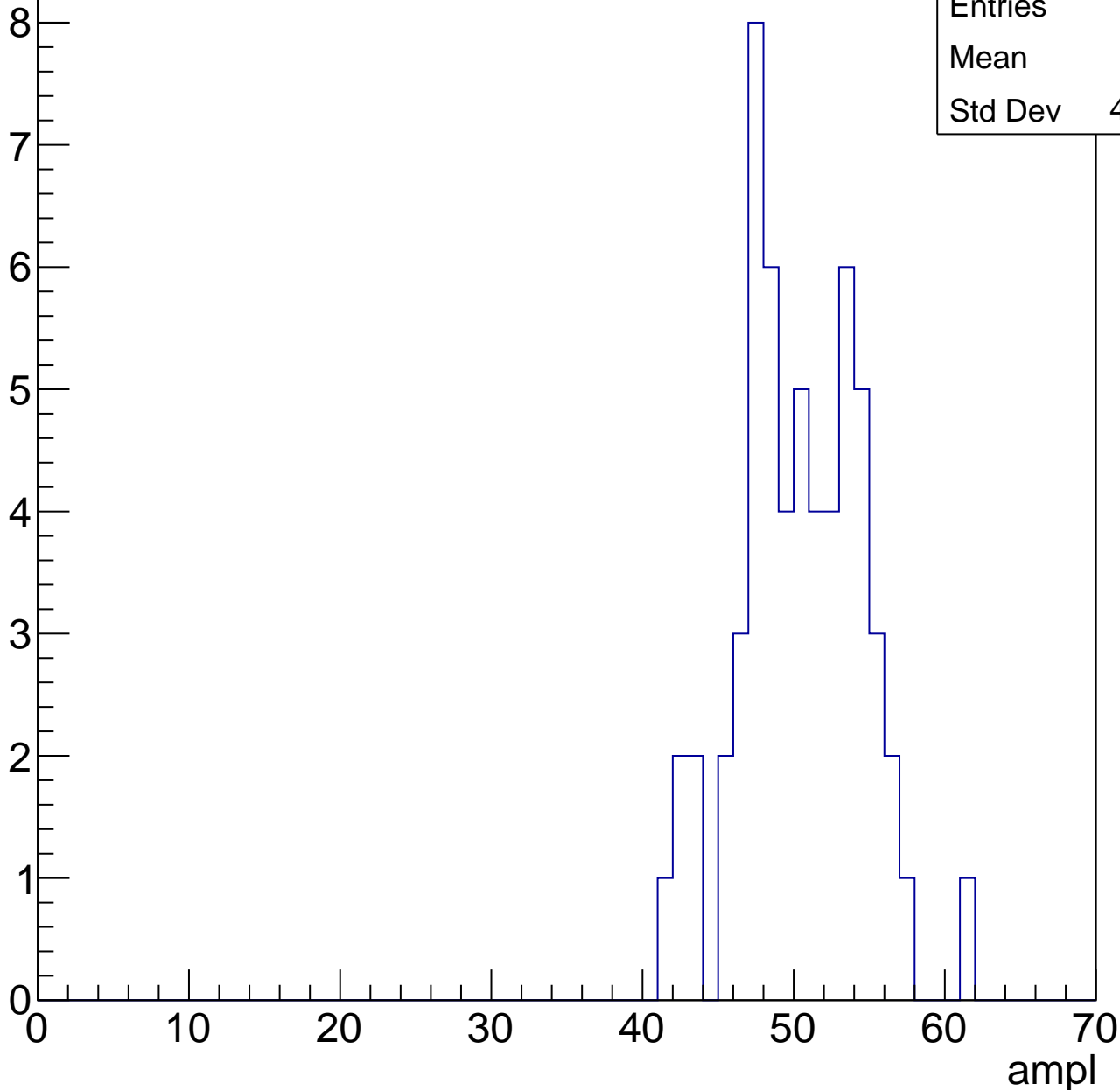


B1L101S, U15-ch29, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

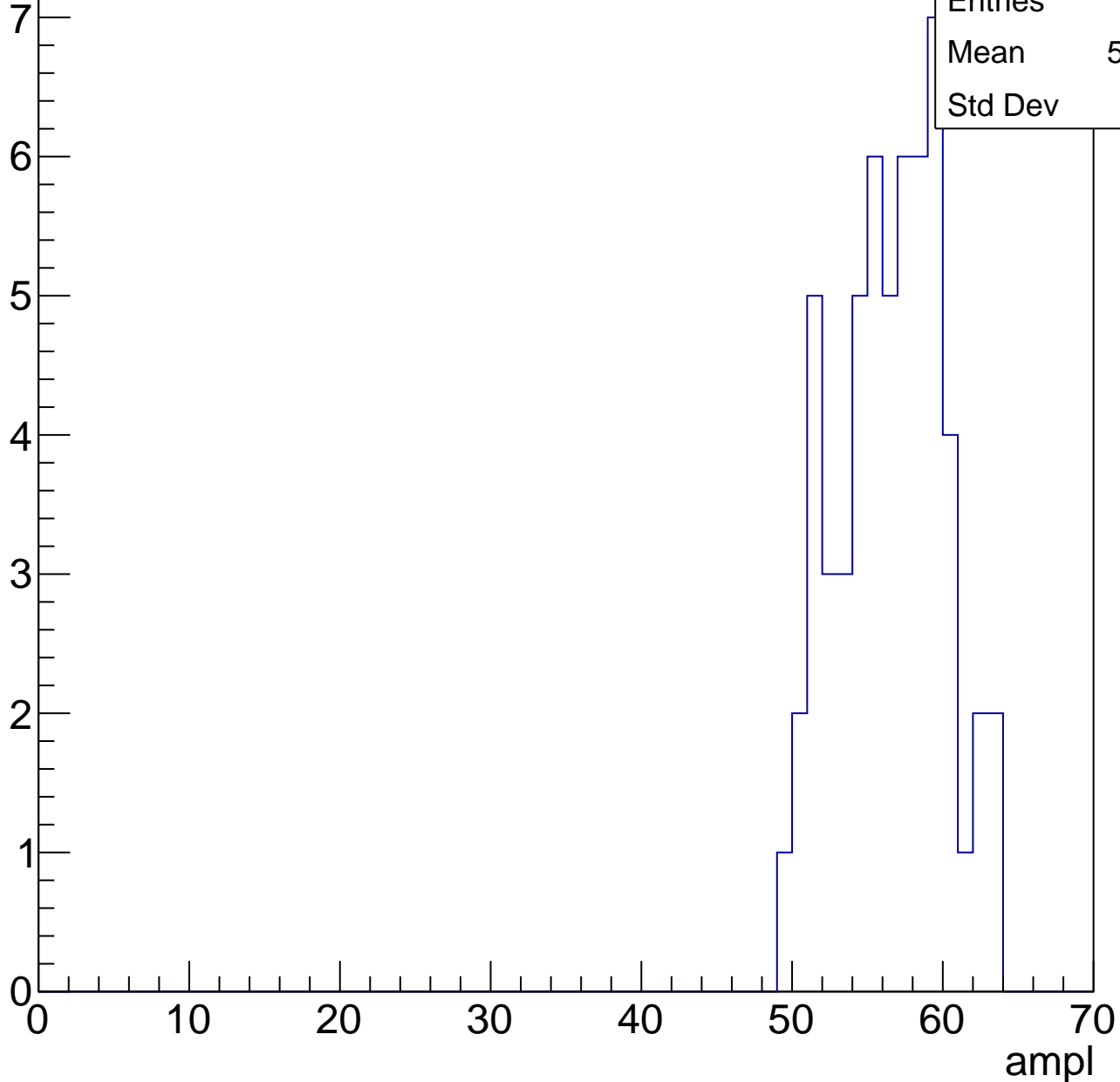
Entries	59
Mean	49.9
Std Dev	4.066



B1L101S, U15-ch29, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



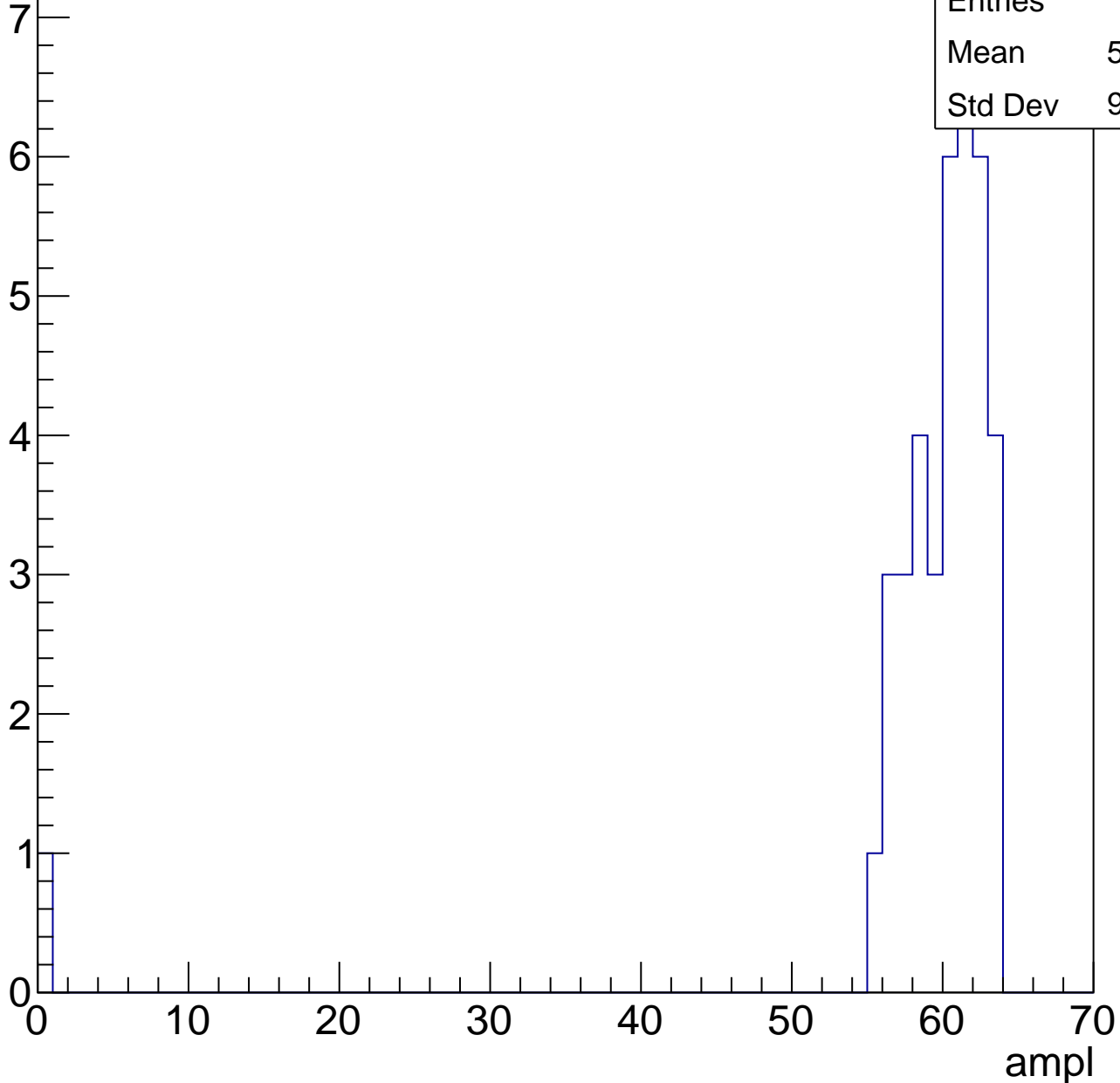
Entries	58
Mean	56.09
Std Dev	3.47

B1L101S, U15-ch29, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

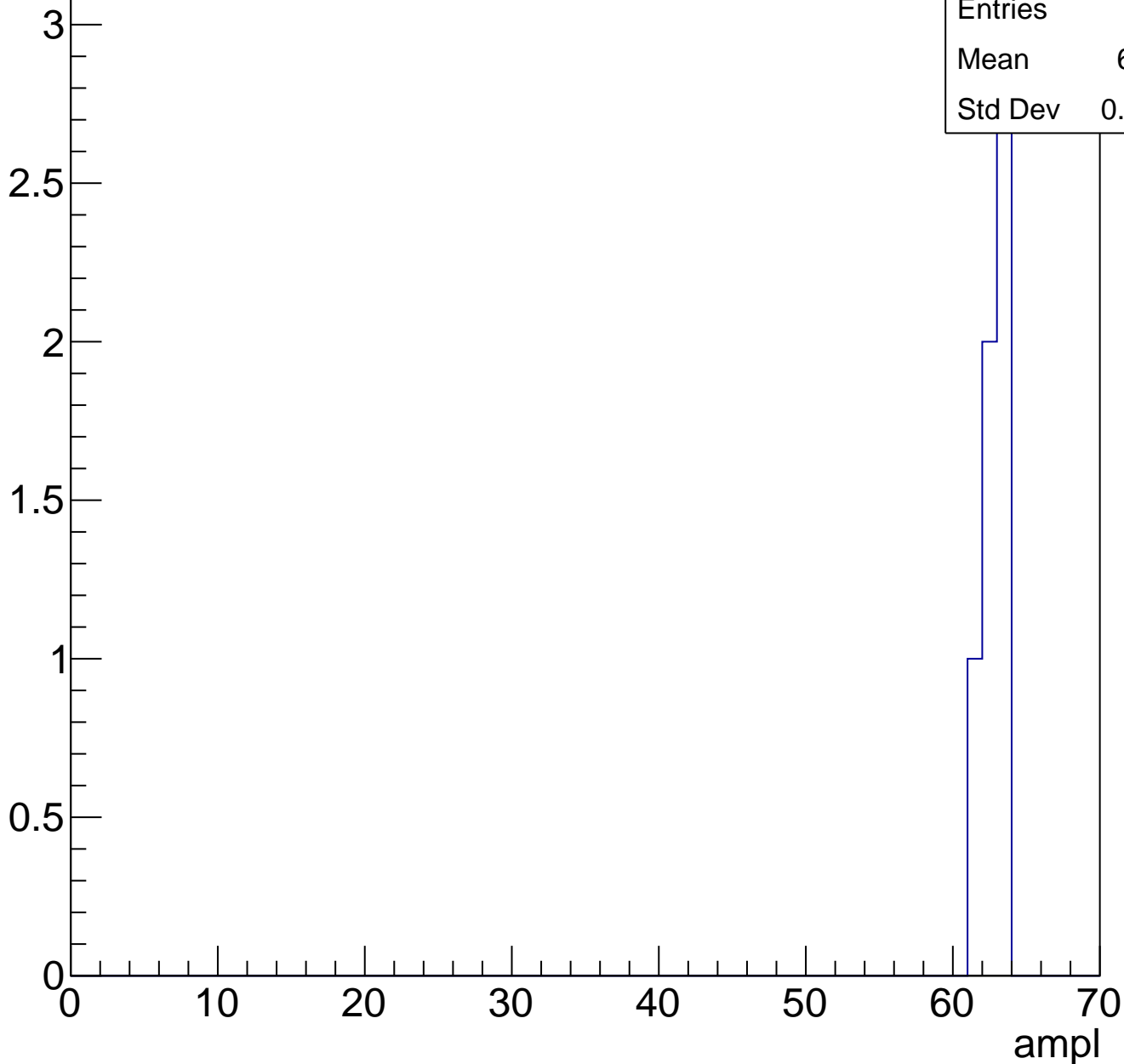
Entries	38
Mean	58.26
Std Dev	9.829



B1L101S, U15-ch29, adc6

calib_packv5_042523_0143.root, FC#0, port D2

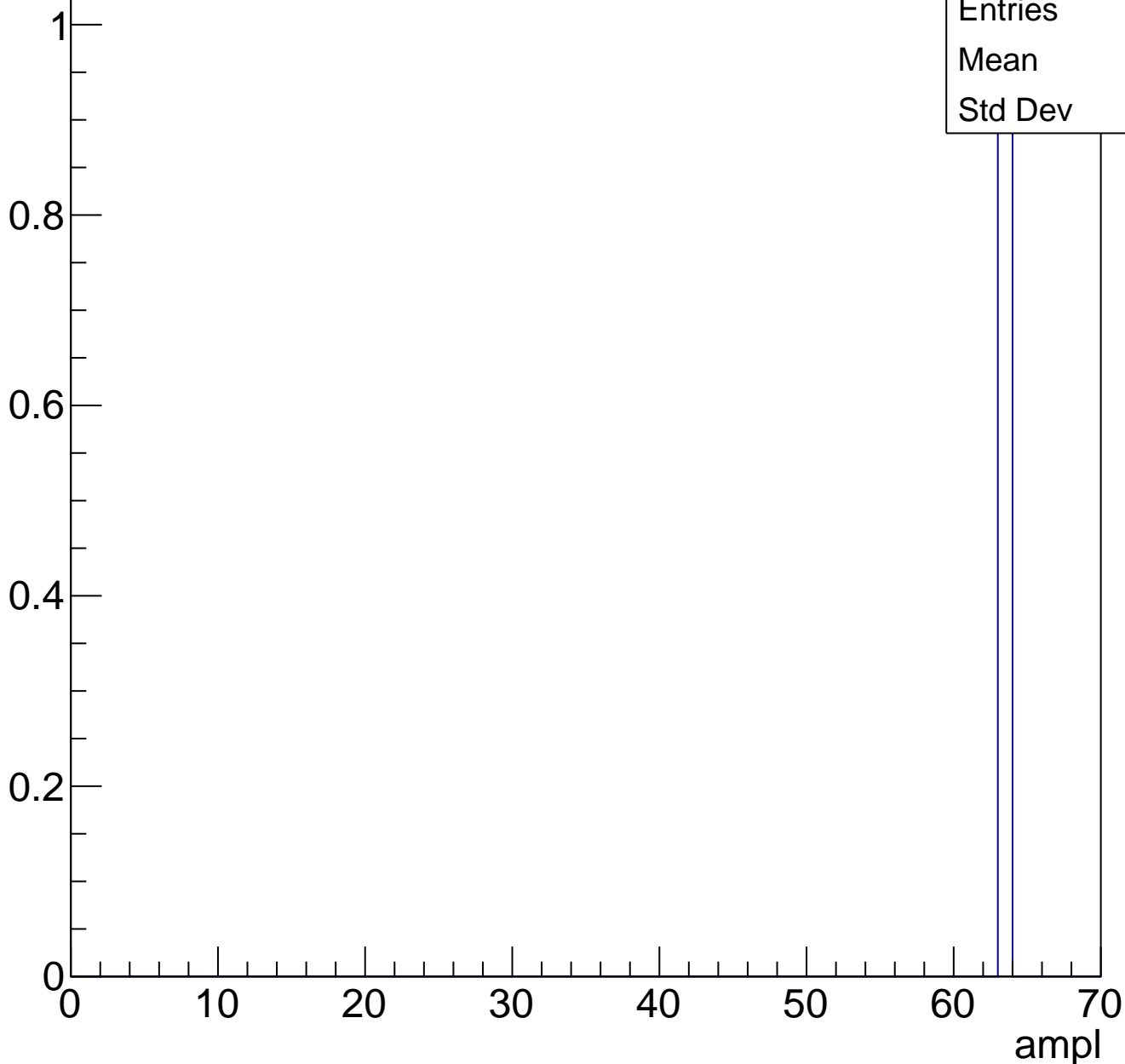
Entry



B1L101S, U15-ch29, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch30, adc0

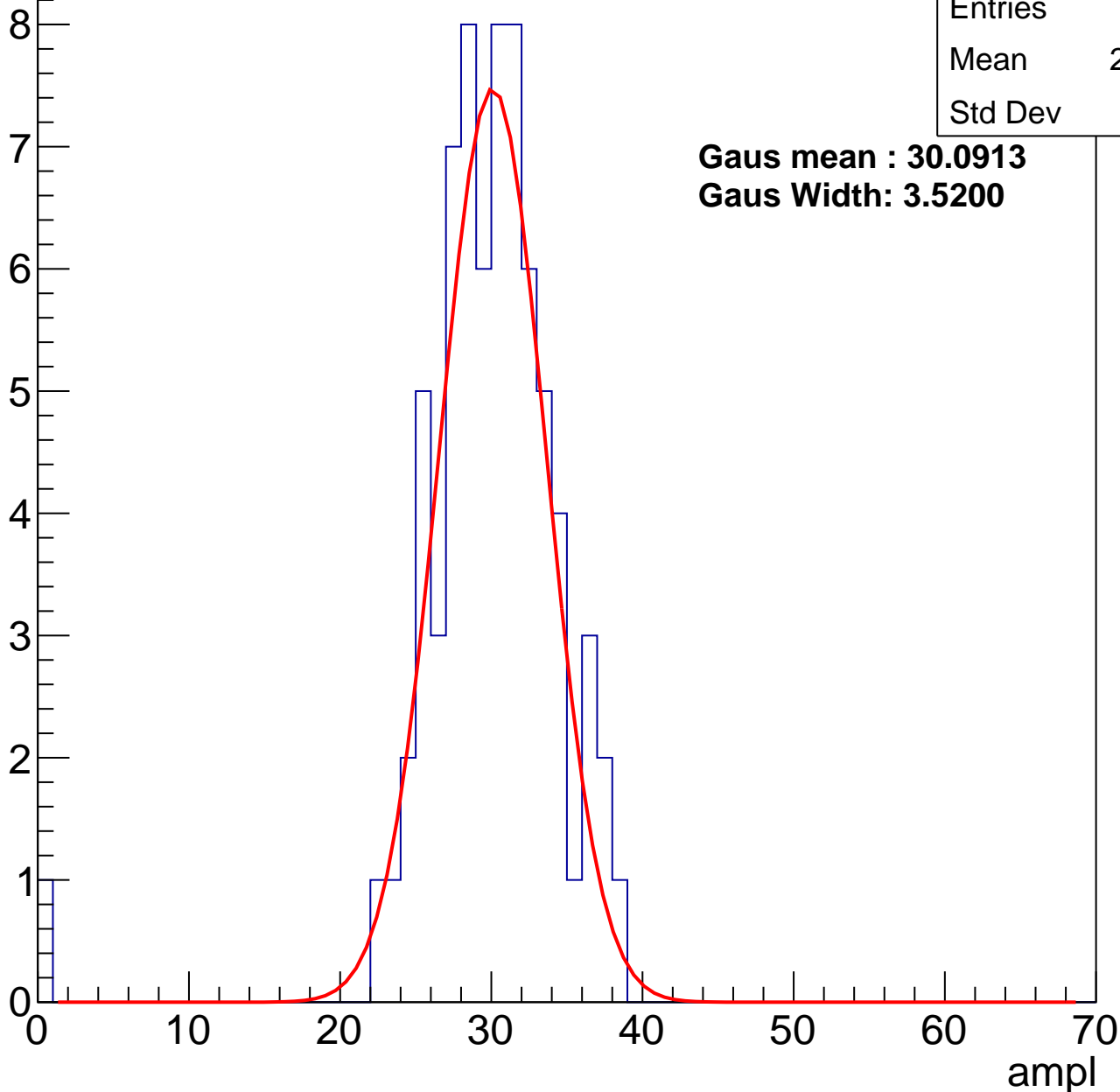
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	29.43
Std Dev	4.96

Gaus mean : 30.0913

Gaus Width: 3.5200



B1L101S, U15-ch30, adc1

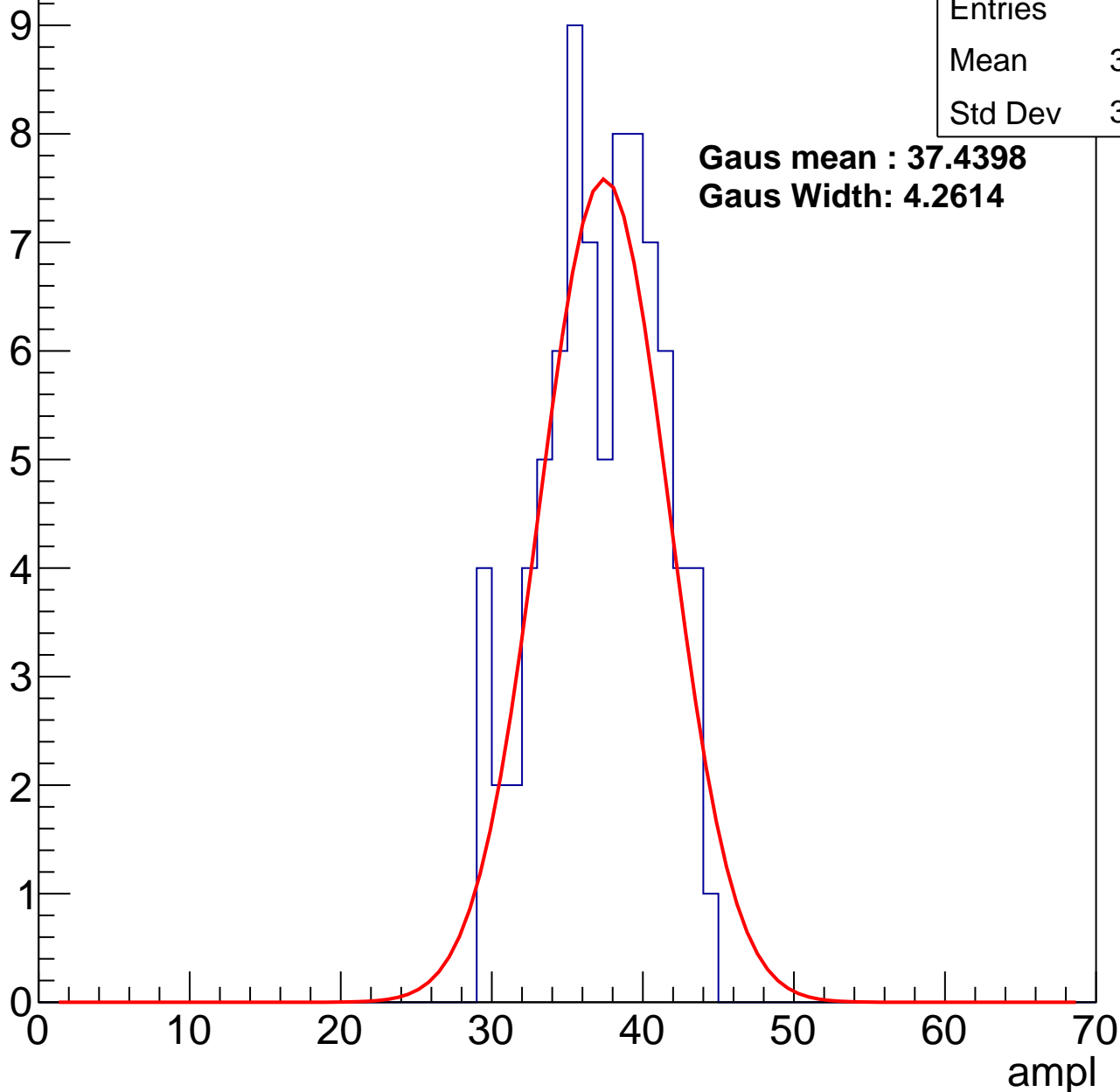
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.74
Std Dev	3.809

Gaus mean : 37.4398

Gaus Width: 4.2614



B1L101S, U15-ch30, adc2

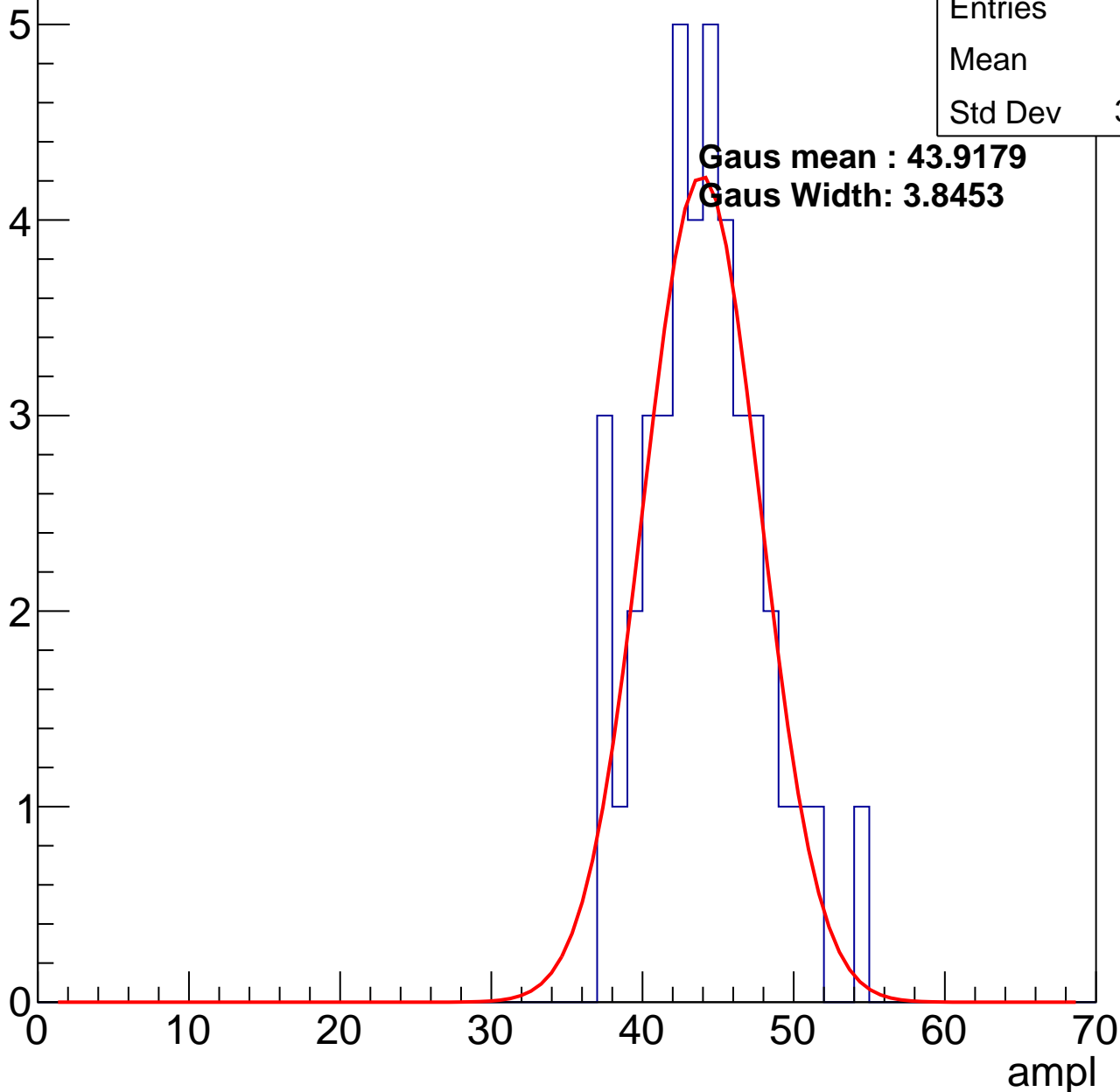
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	43.6
Std Dev	3.811

Gaus mean : 43.9179

Gaus Width: 3.8453

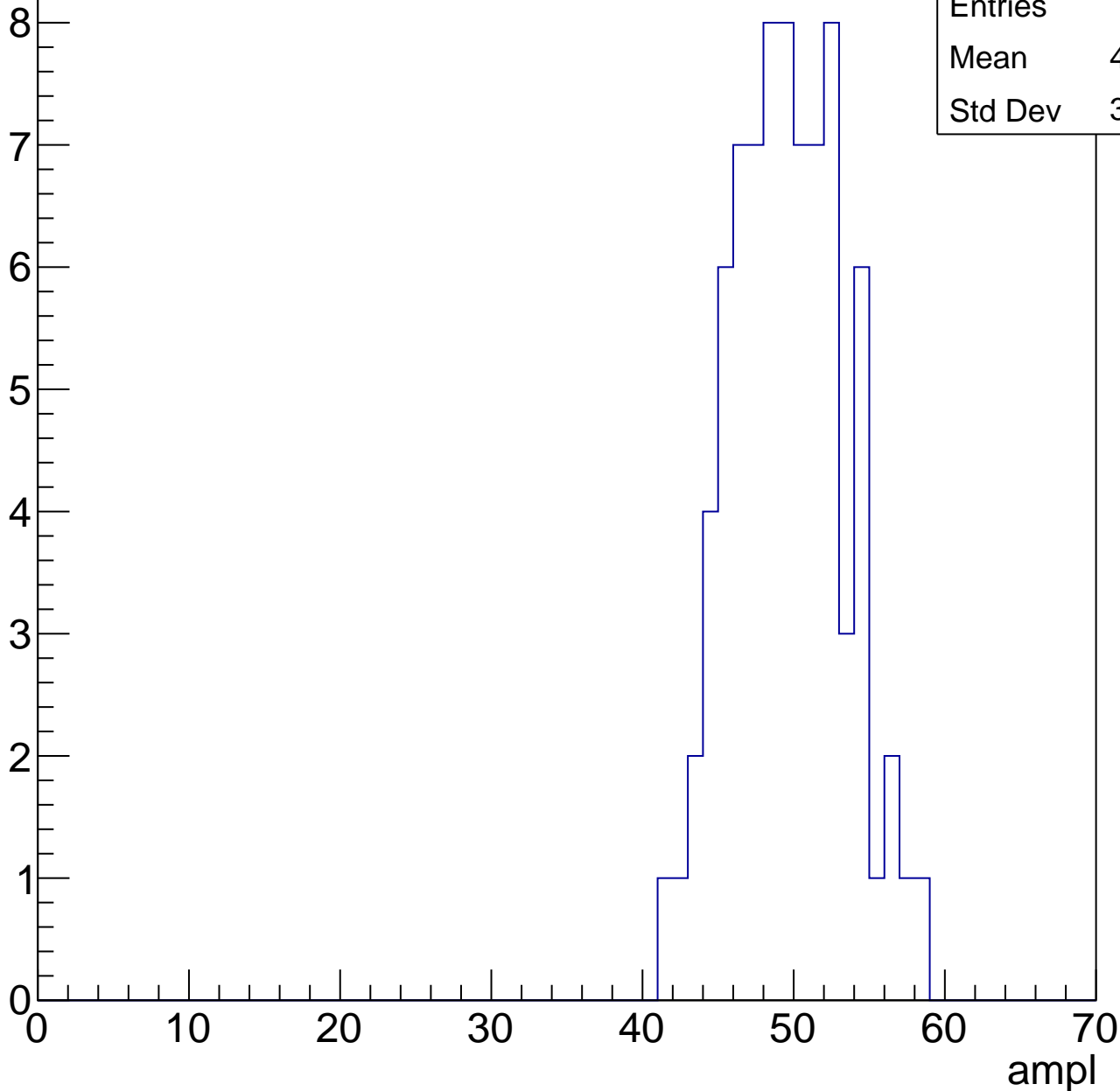


B1L101S, U15-ch30, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	49.12
Std Dev	3.645

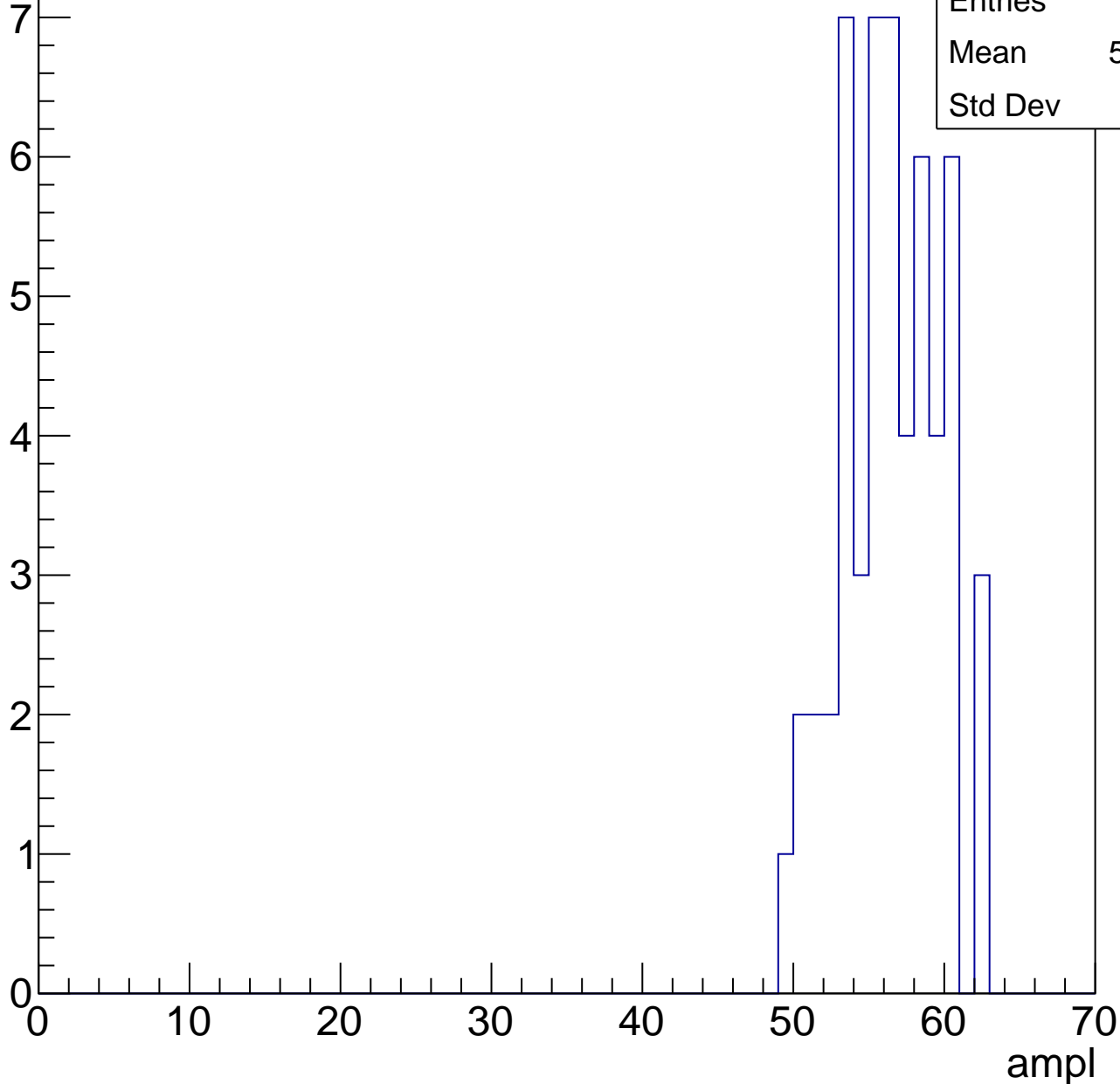


B1L101S, U15-ch30, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.98
Std Dev	3.2

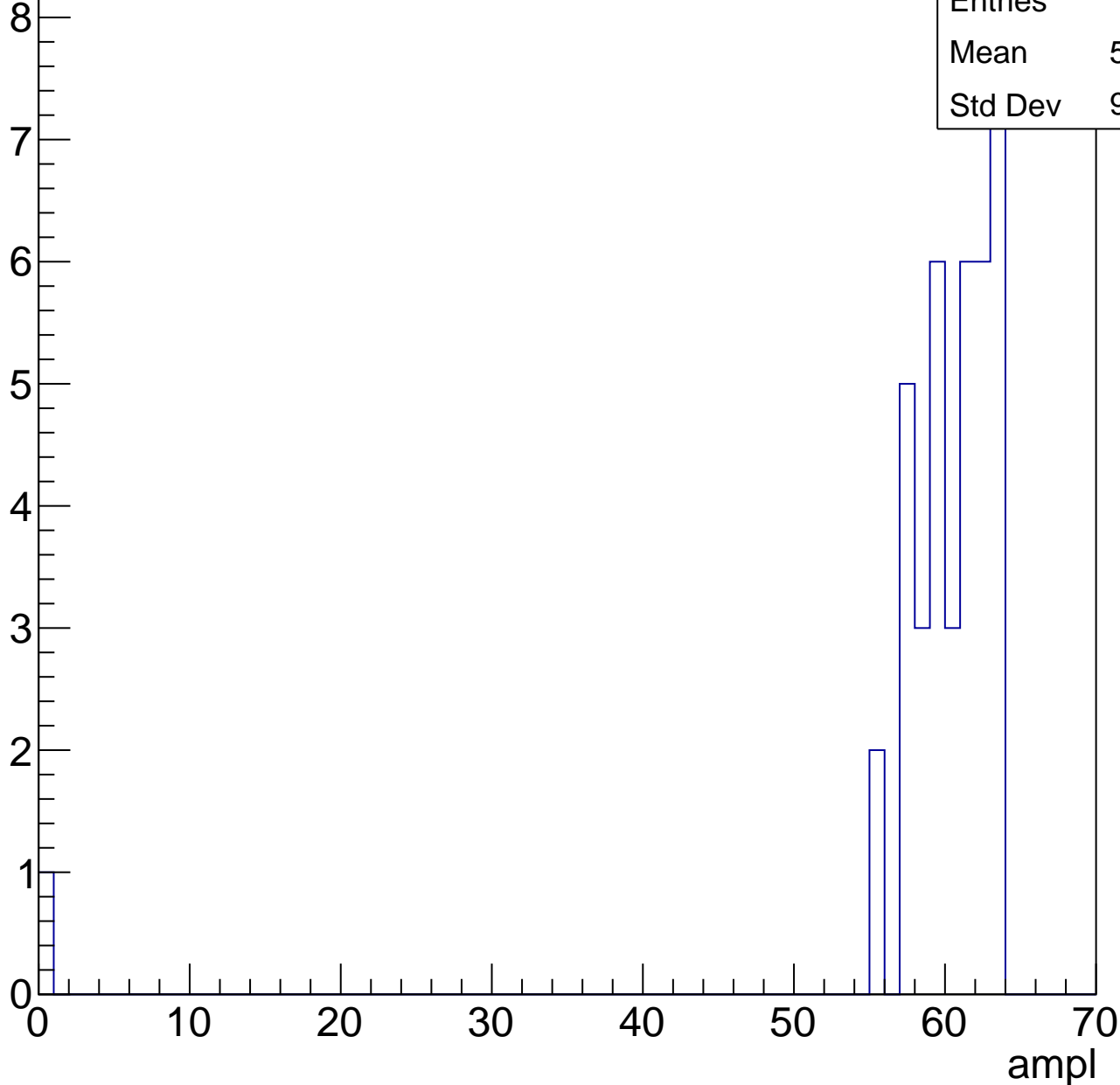


B1L101S, U15-ch30, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

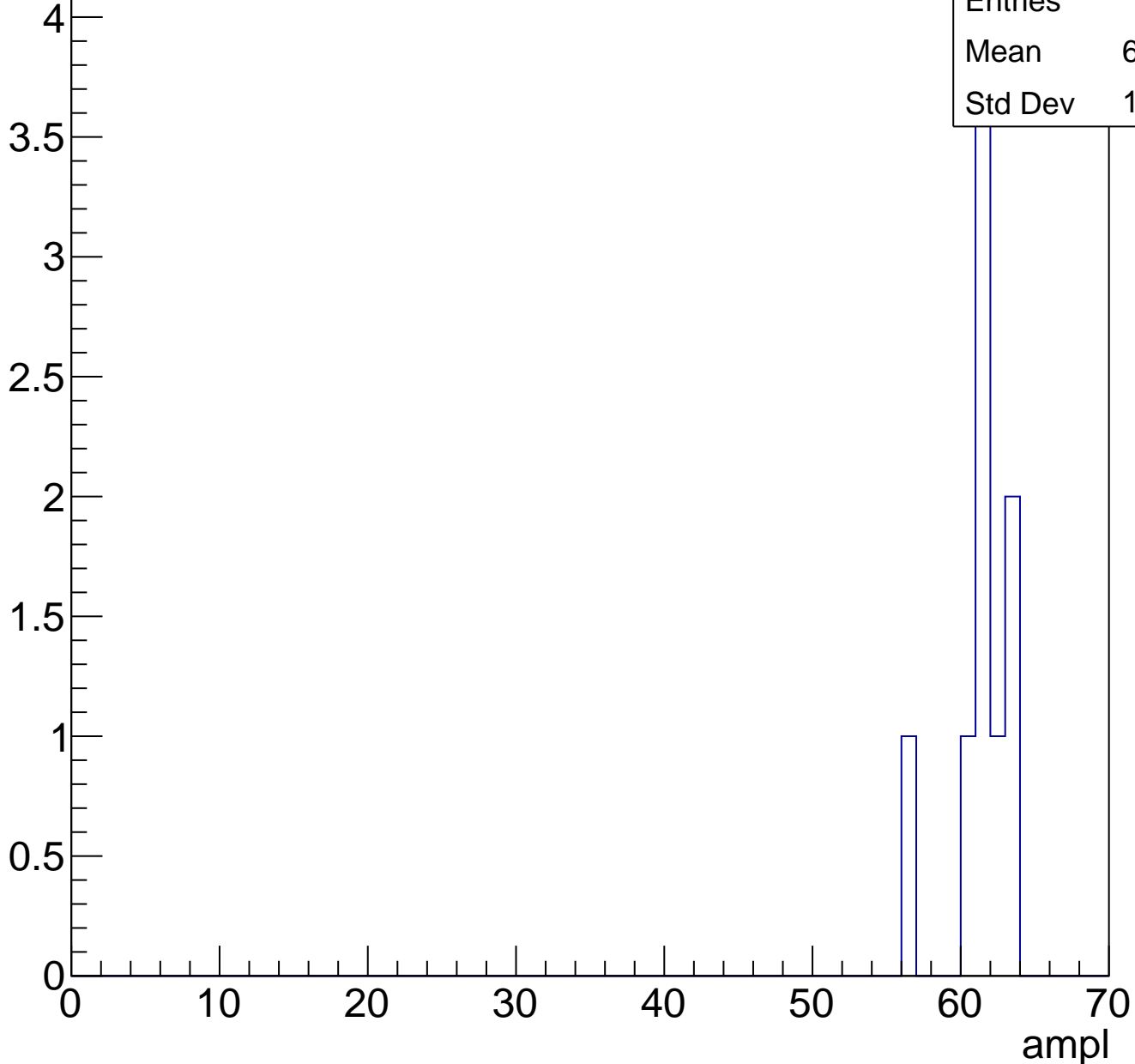
Entries	40
Mean	58.62
Std Dev	9.669



B1L101S, U15-ch30, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	9
Mean	60.89
Std Dev	1.969

B1L101S, U15-ch30, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch31, adc0

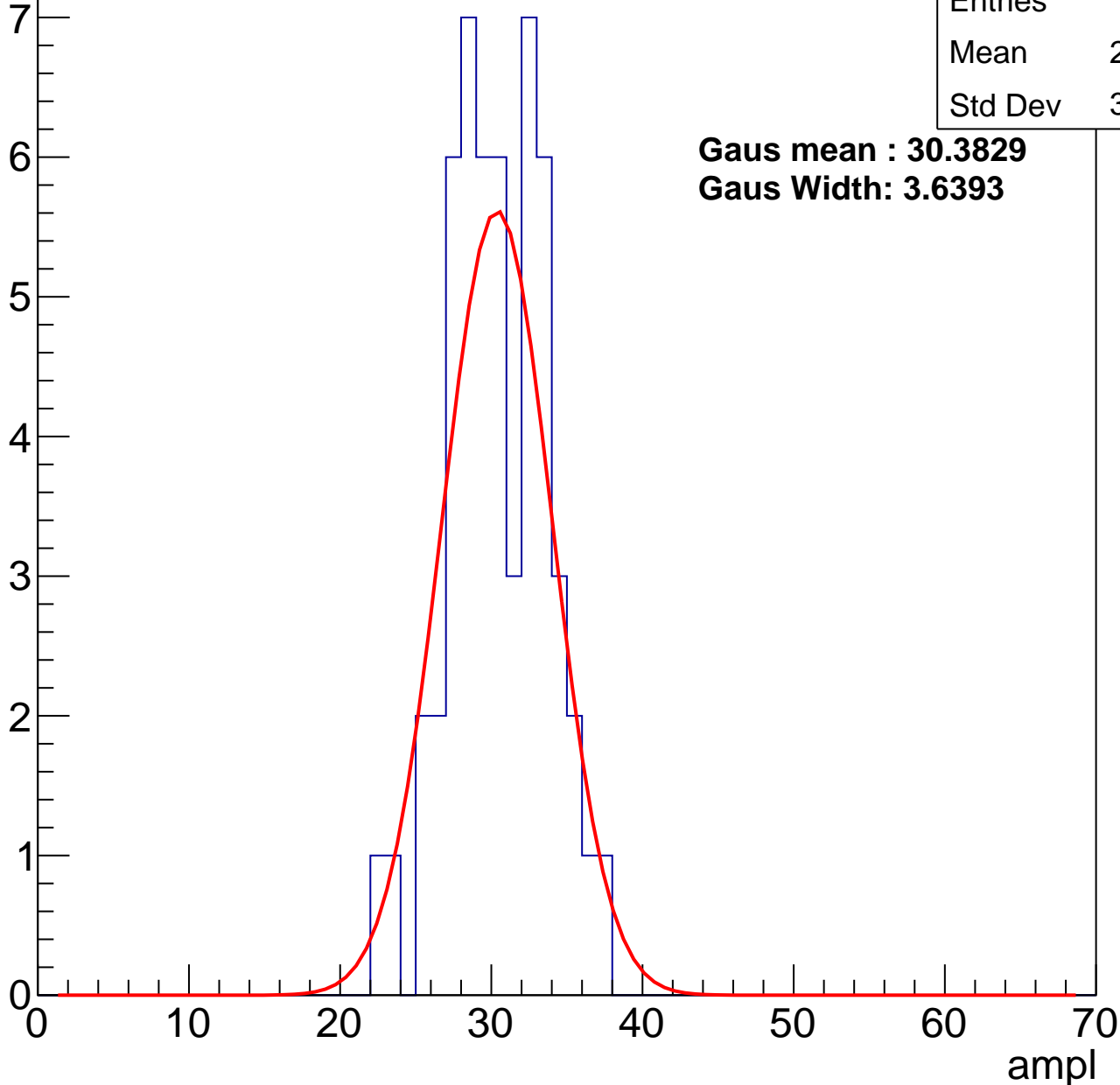
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	29.98
Std Dev	3.194

Gaus mean : 30.3829

Gaus Width: 3.6393



B1L101S, U15-ch31, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	68
Mean	36
Std Dev	3.698

Gaus mean : 36.3747

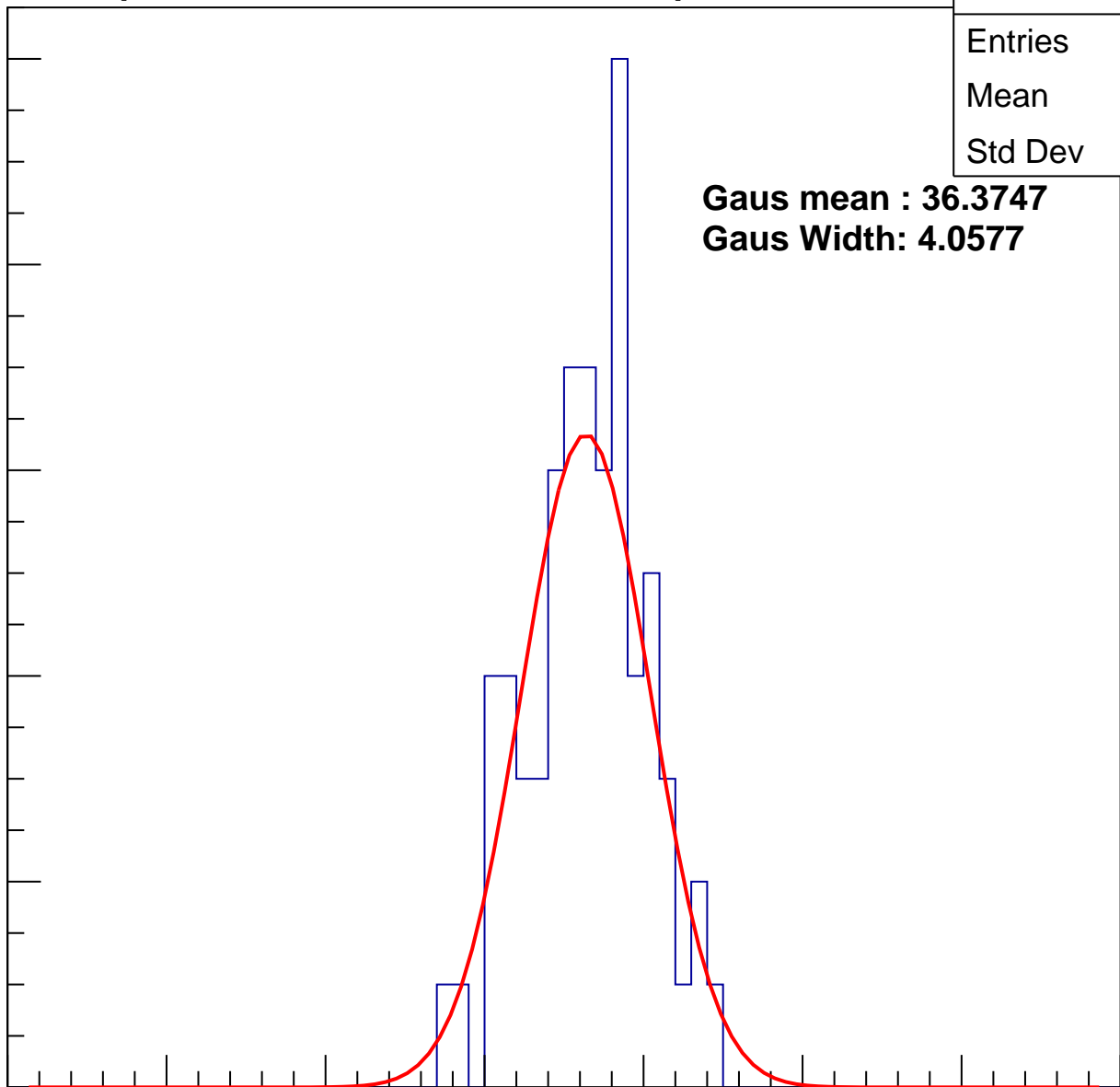
Gaus Width: 4.0577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch31, adc2

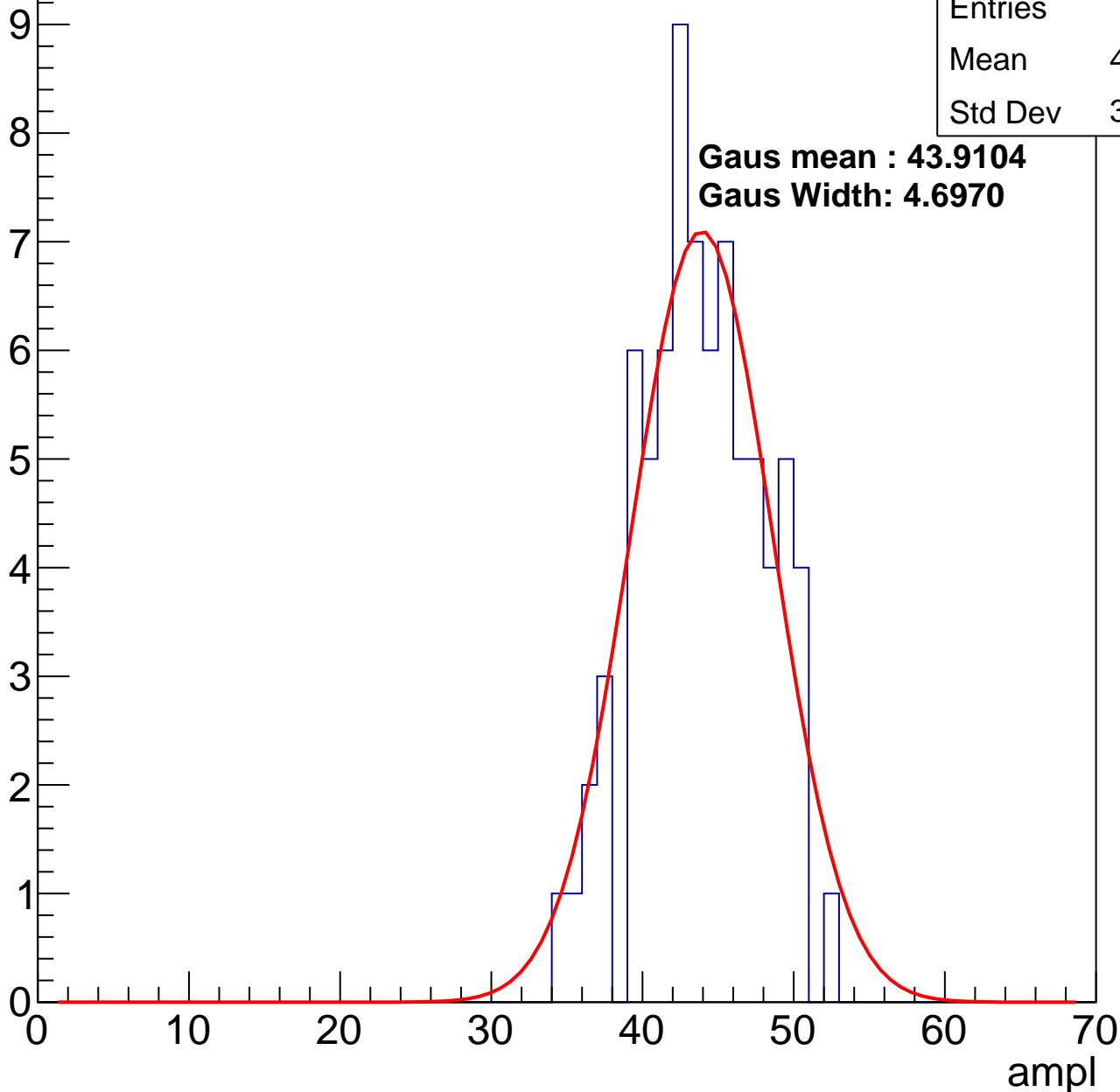
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	43.43
Std Dev	3.992

Gaus mean : 43.9104

Gaus Width: 4.6970

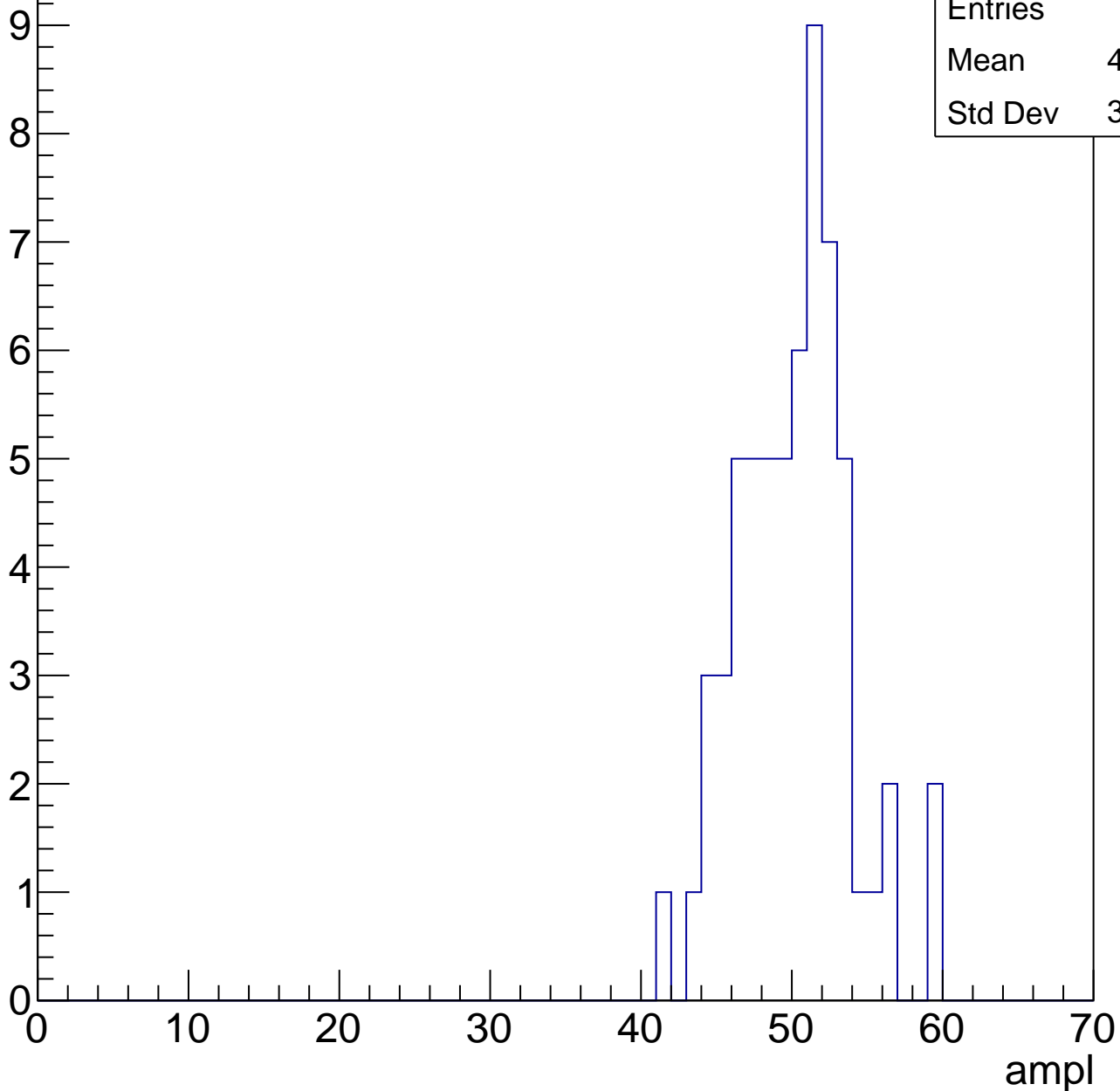


B1L101S, U15-ch31, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.64
Std Dev	3.649

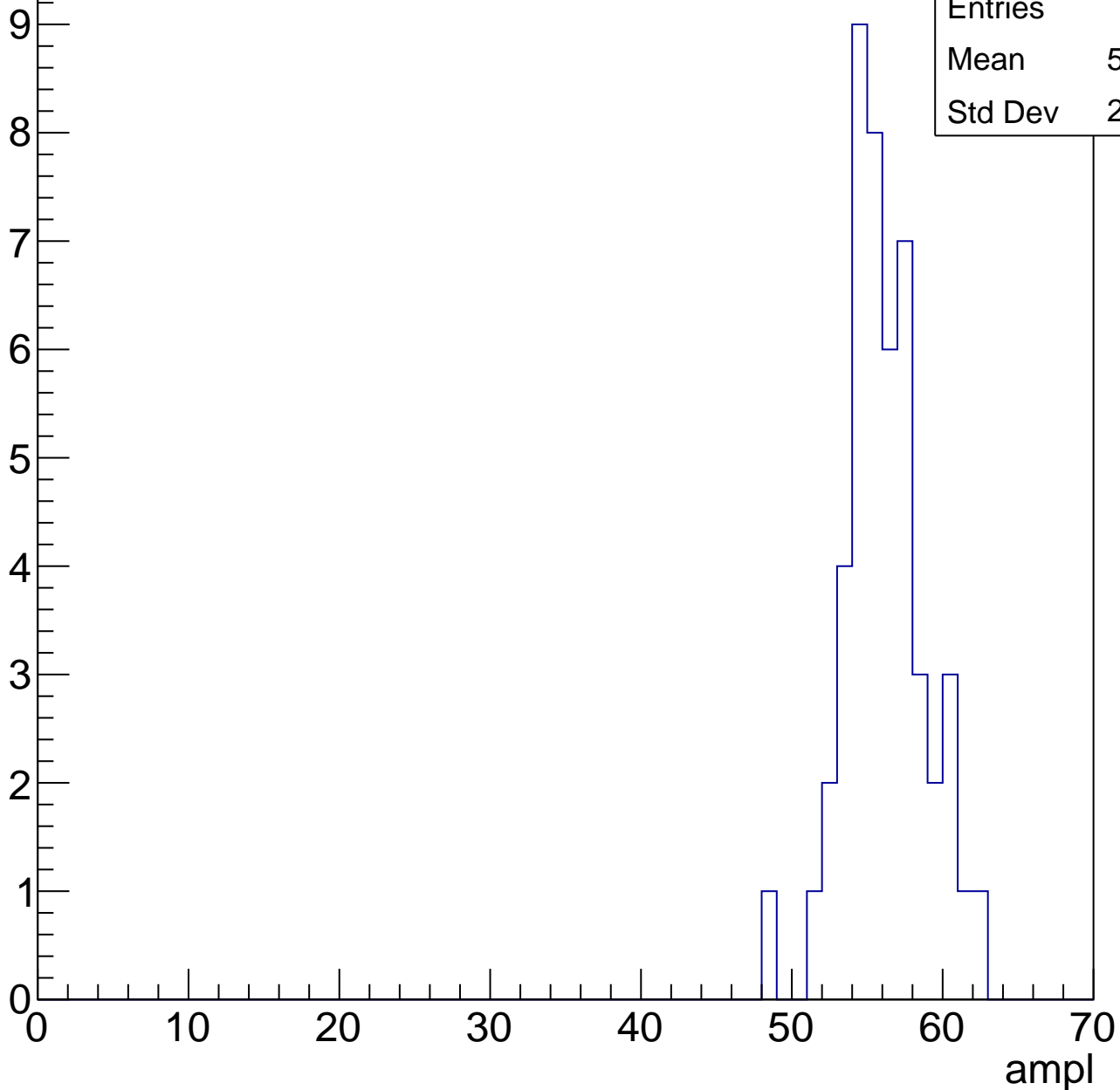


B1L101S, U15-ch31, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	55.65
Std Dev	2.665

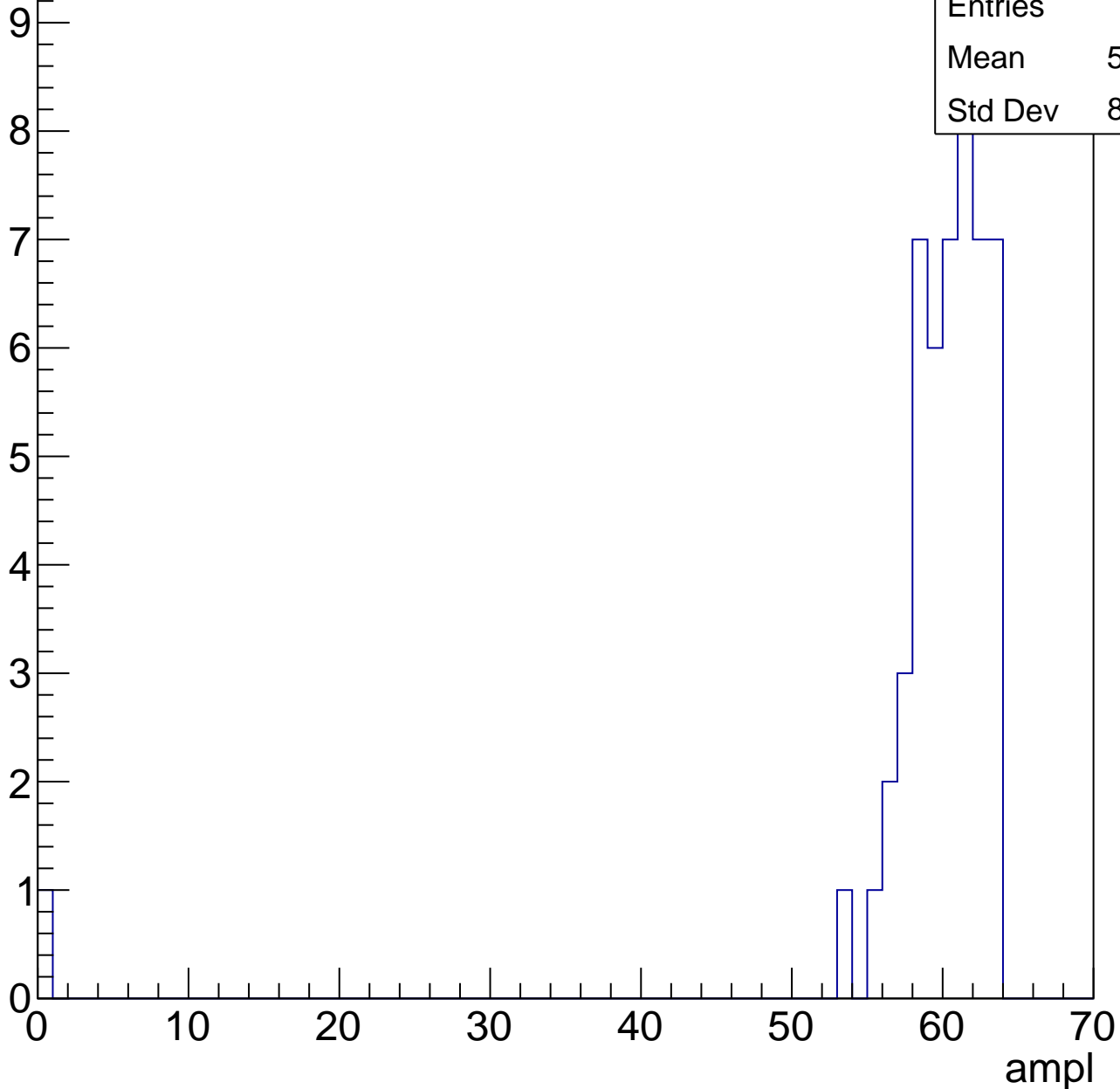


B1L101S, U15-ch31, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	58.73
Std Dev	8.614



B1L101S, U15-ch31, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch31, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch32, adc0

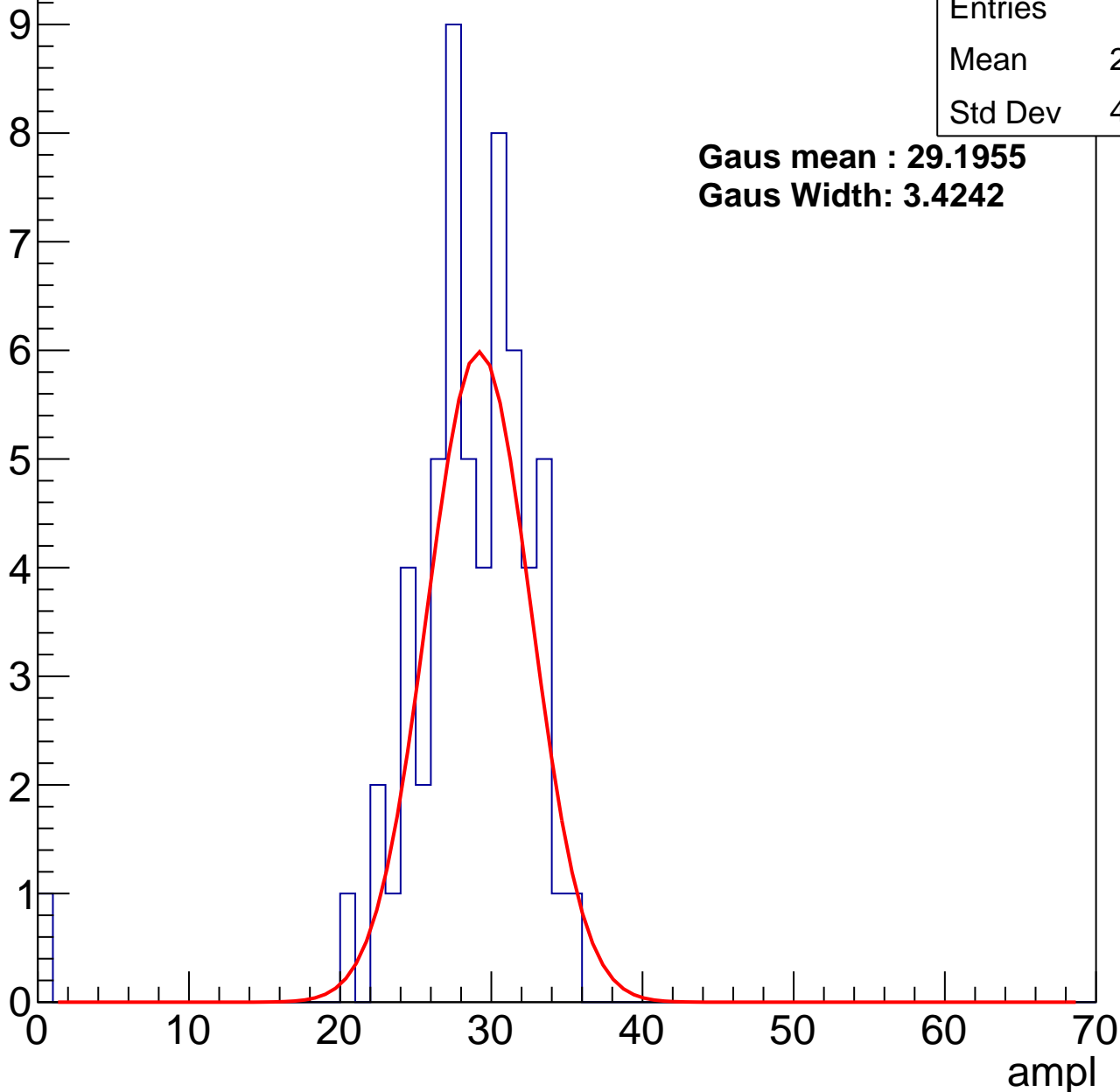
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.97
Std Dev	4.906

Gaus mean : 29.1955

Gaus Width: 3.4242



B1L101S, U15-ch32, adc1

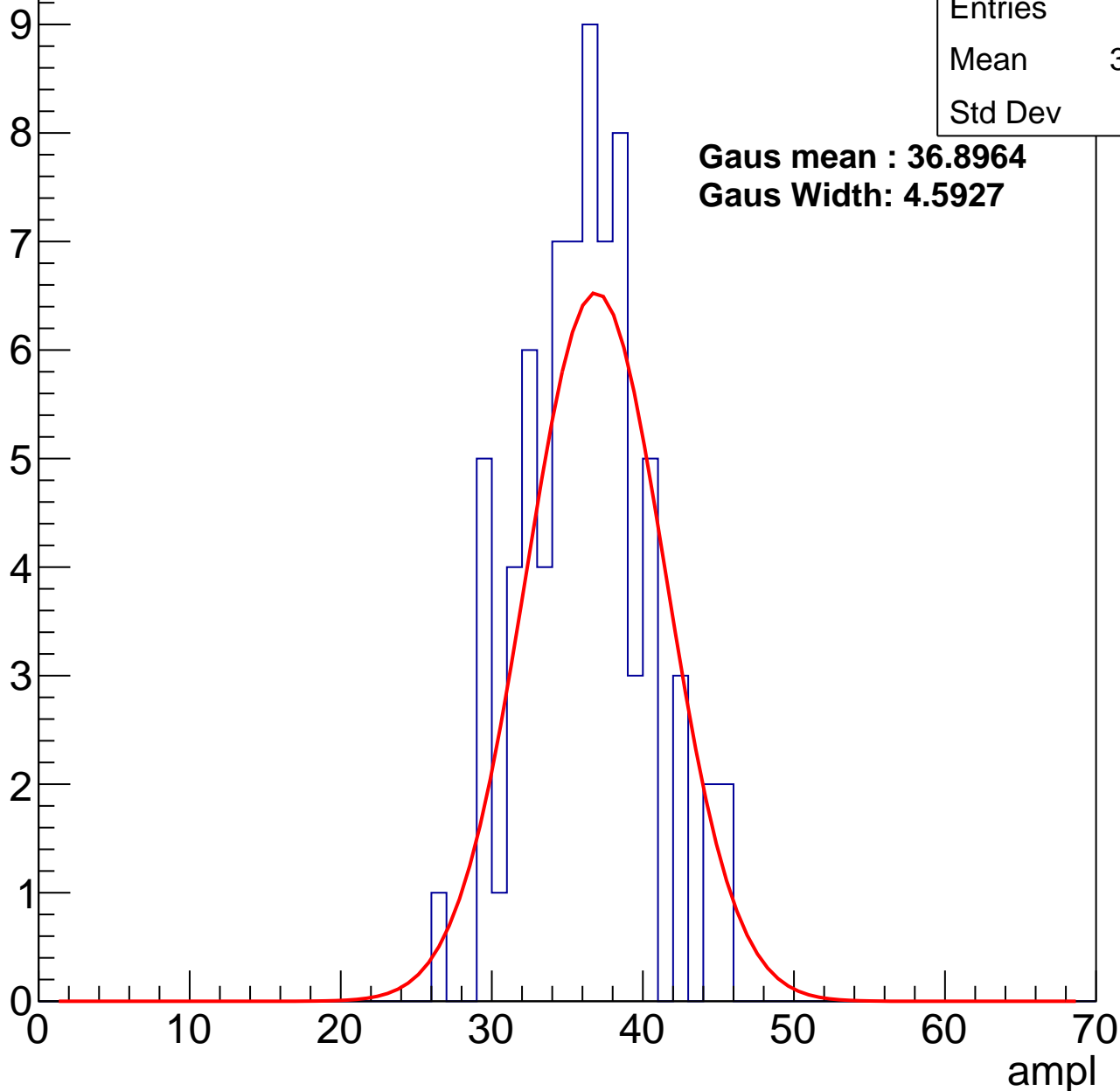
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.68
Std Dev	4

Gaus mean : 36.8964

Gaus Width: 4.5927



B1L101S, U15-ch32, adc2

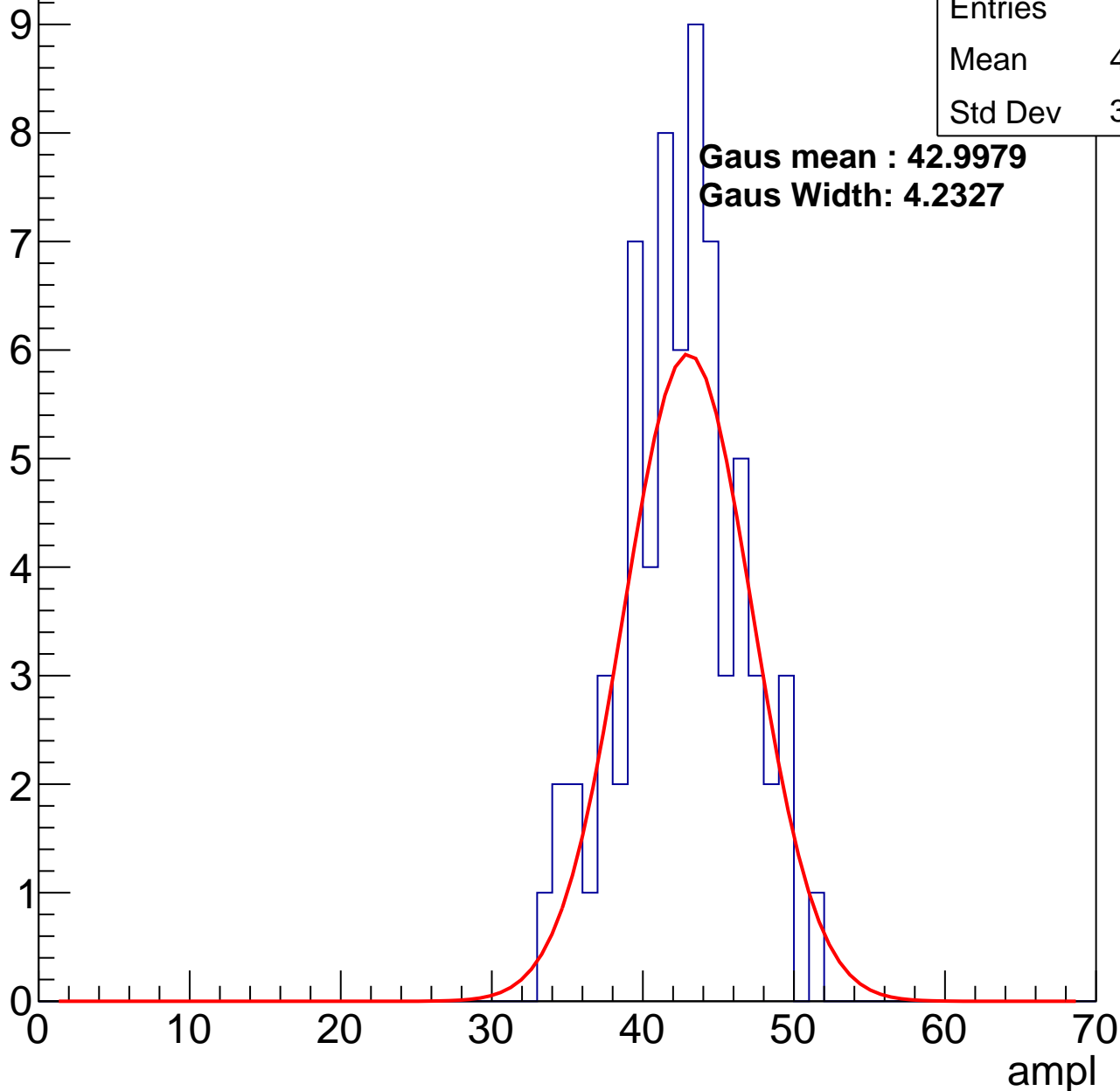
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.06
Std Dev	3.912

Gaus mean : 42.9979

Gaus Width: 4.2327

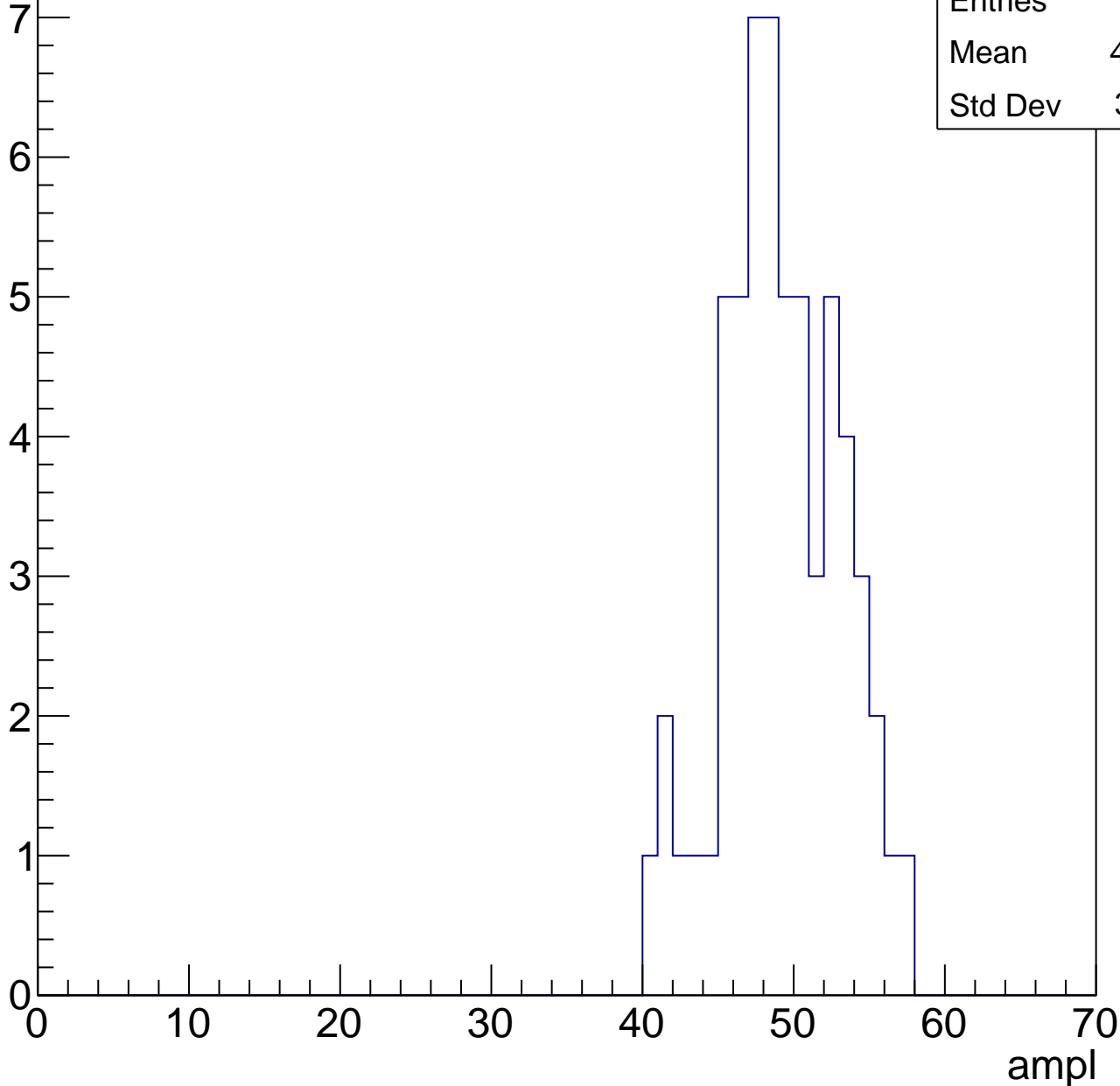


B1L101S, U15-ch32, adc3

calib_packv5_042523_0143.root, FC#0, port D2

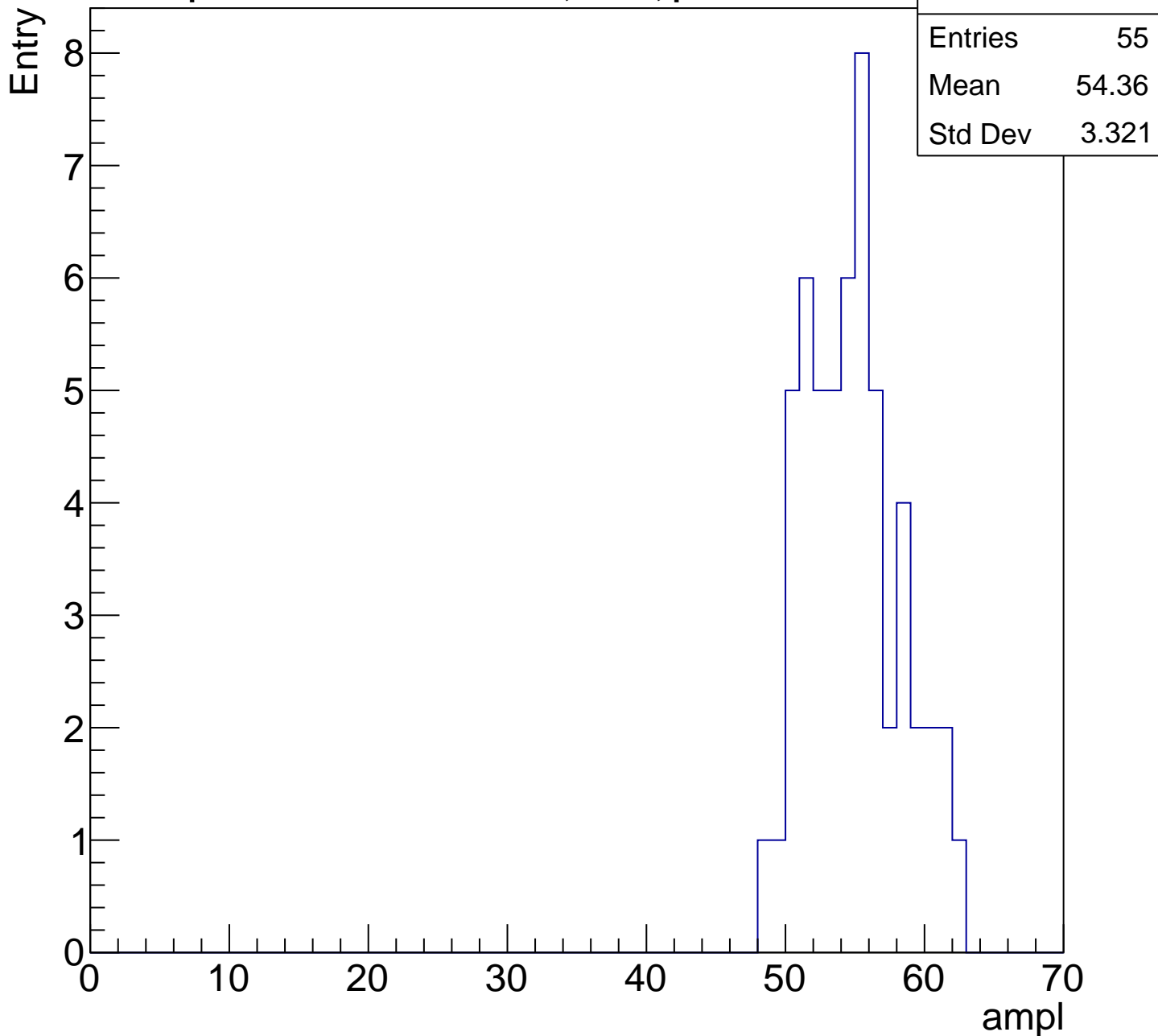
Entry

Entries	59
Mean	48.75
Std Dev	3.821



B1L101S, U15-ch32, adc4

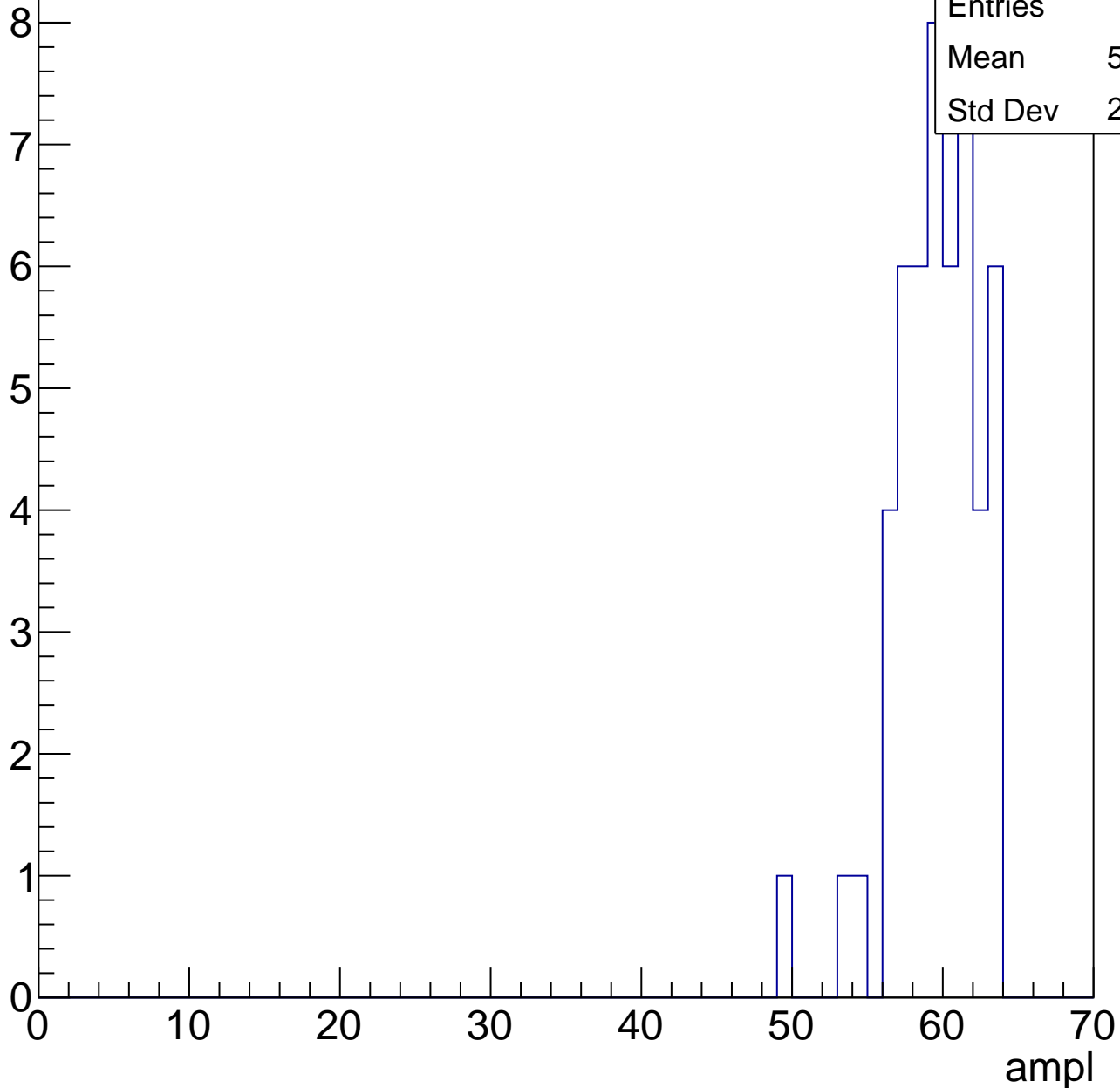
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch32, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

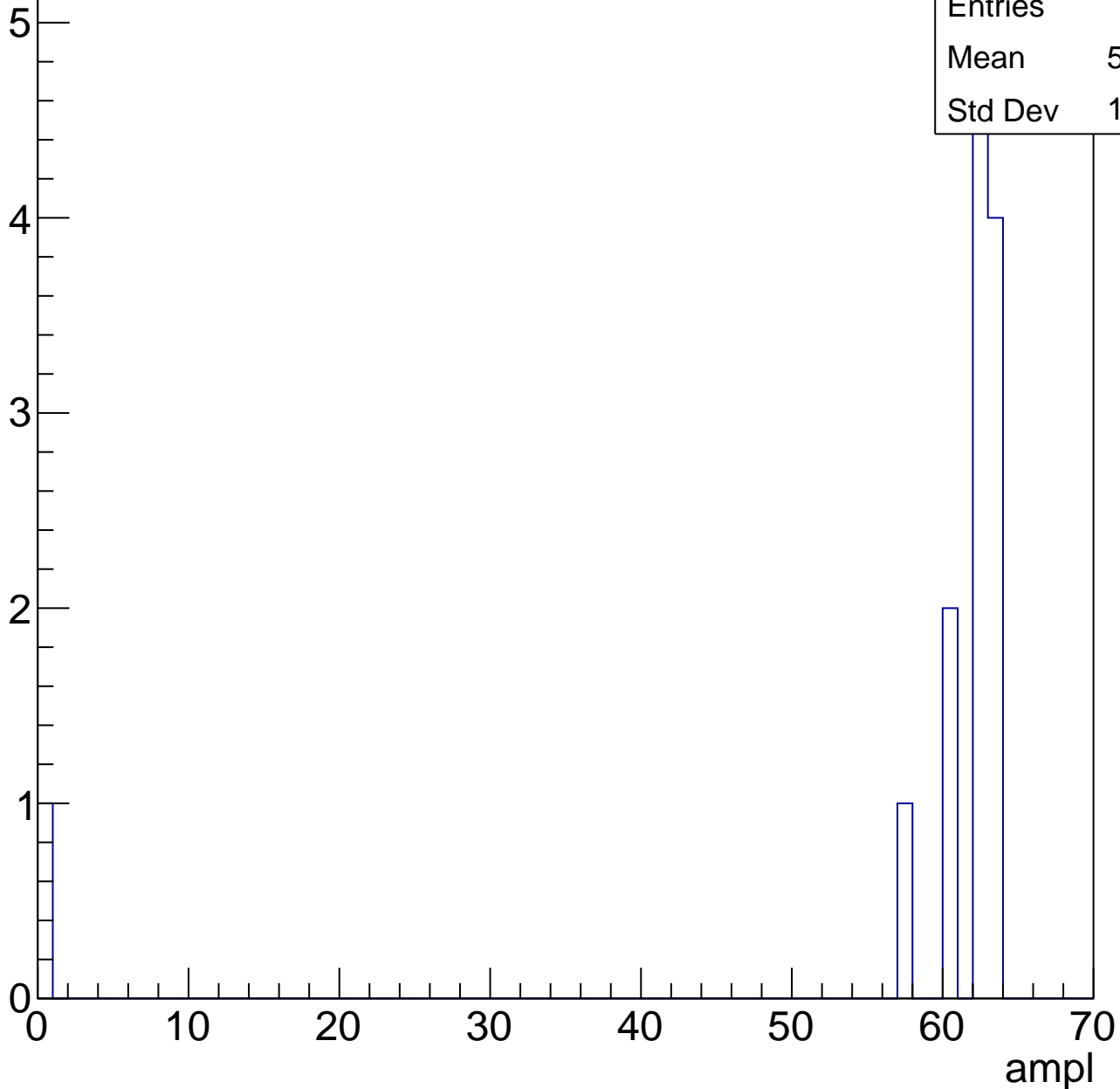


B1L101S, U15-ch32, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	13
Mean	56.85
Std Dev	16.49



B1L101S, U15-ch32, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch33, adc0

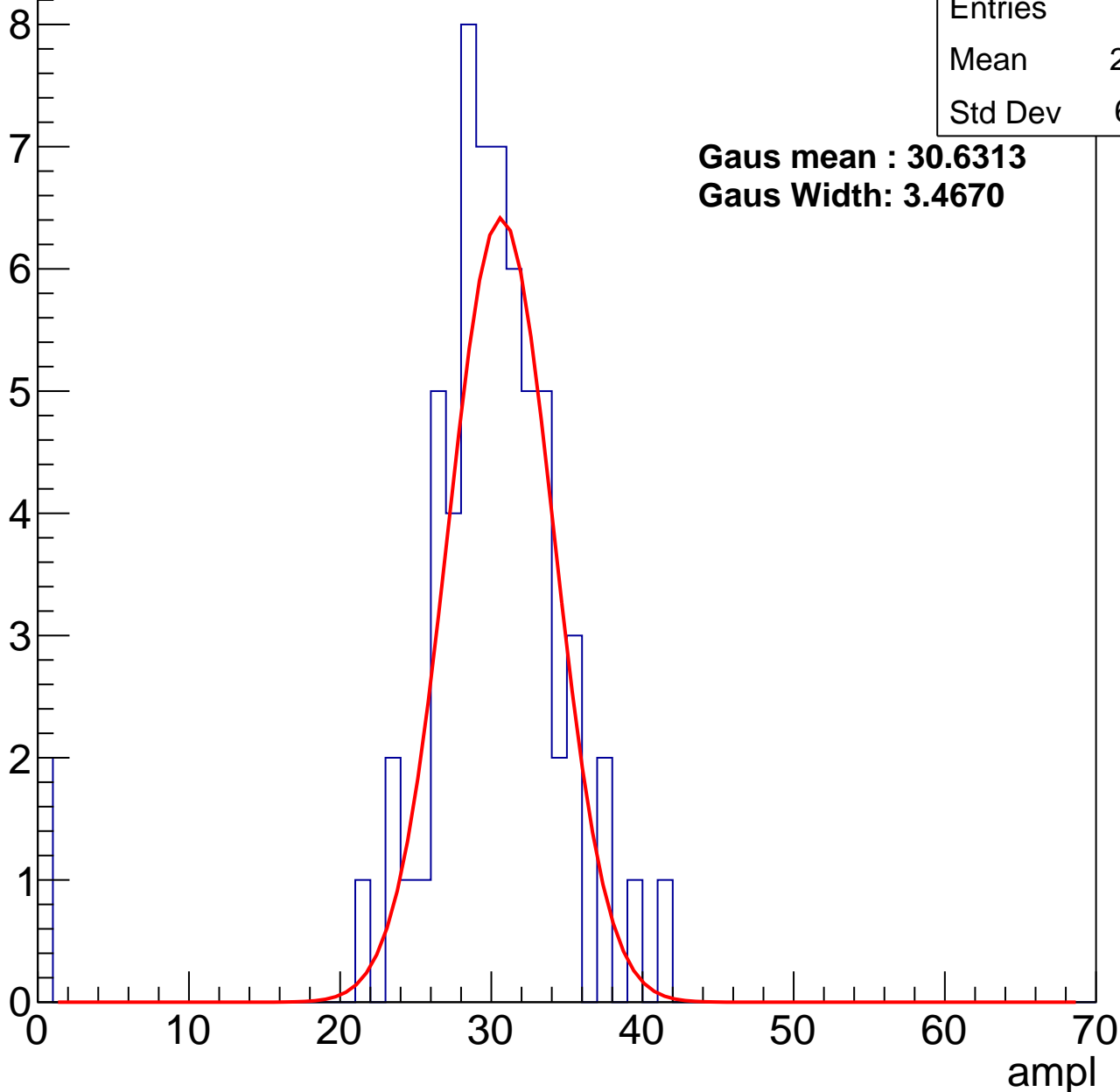
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.03
Std Dev	6.441

Gaus mean : 30.6313

Gaus Width: 3.4670



B1L101S, U15-ch33, adc1

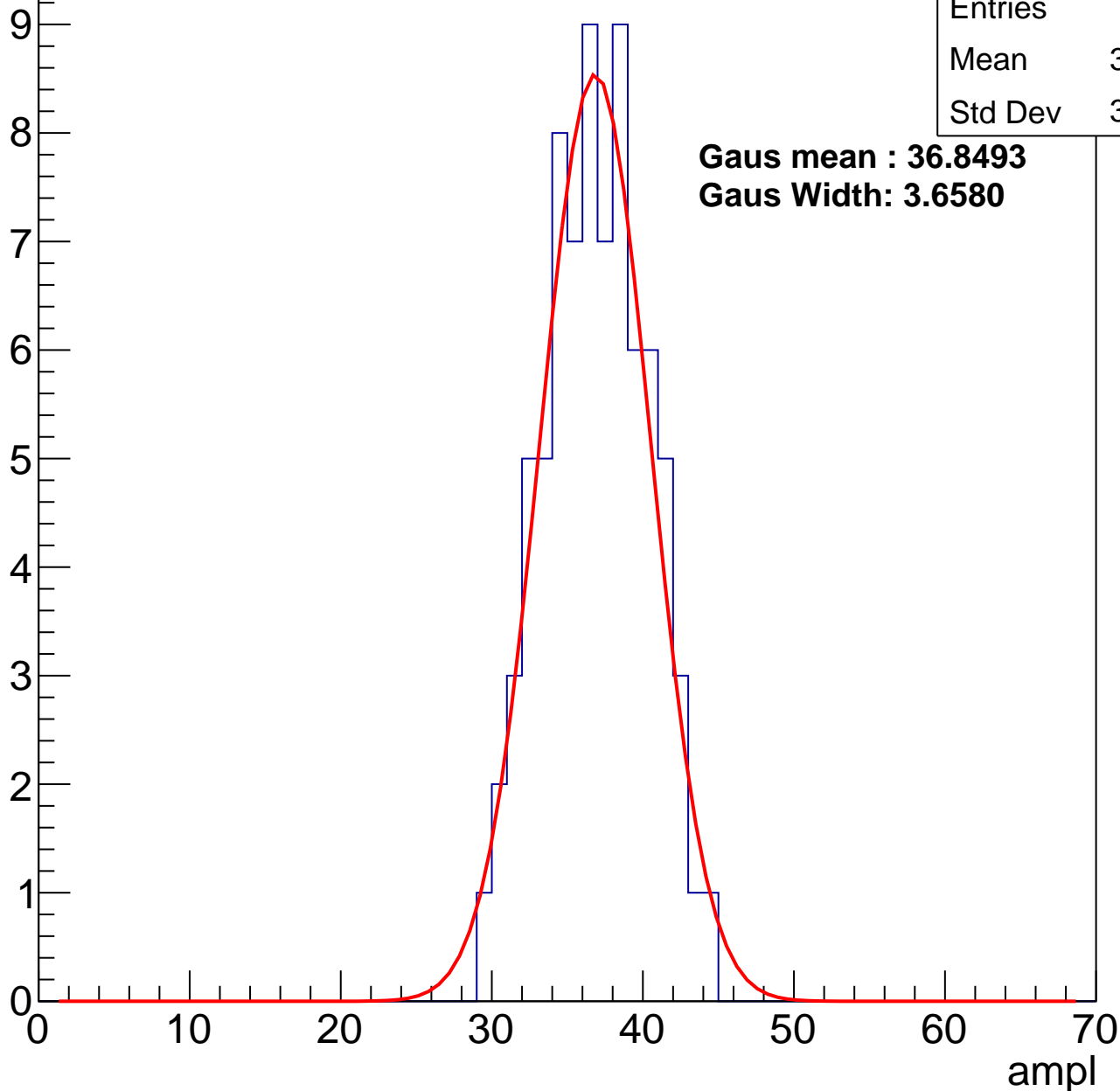
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	36.42
Std Dev	3.368

Gaus mean : 36.8493

Gaus Width: 3.6580

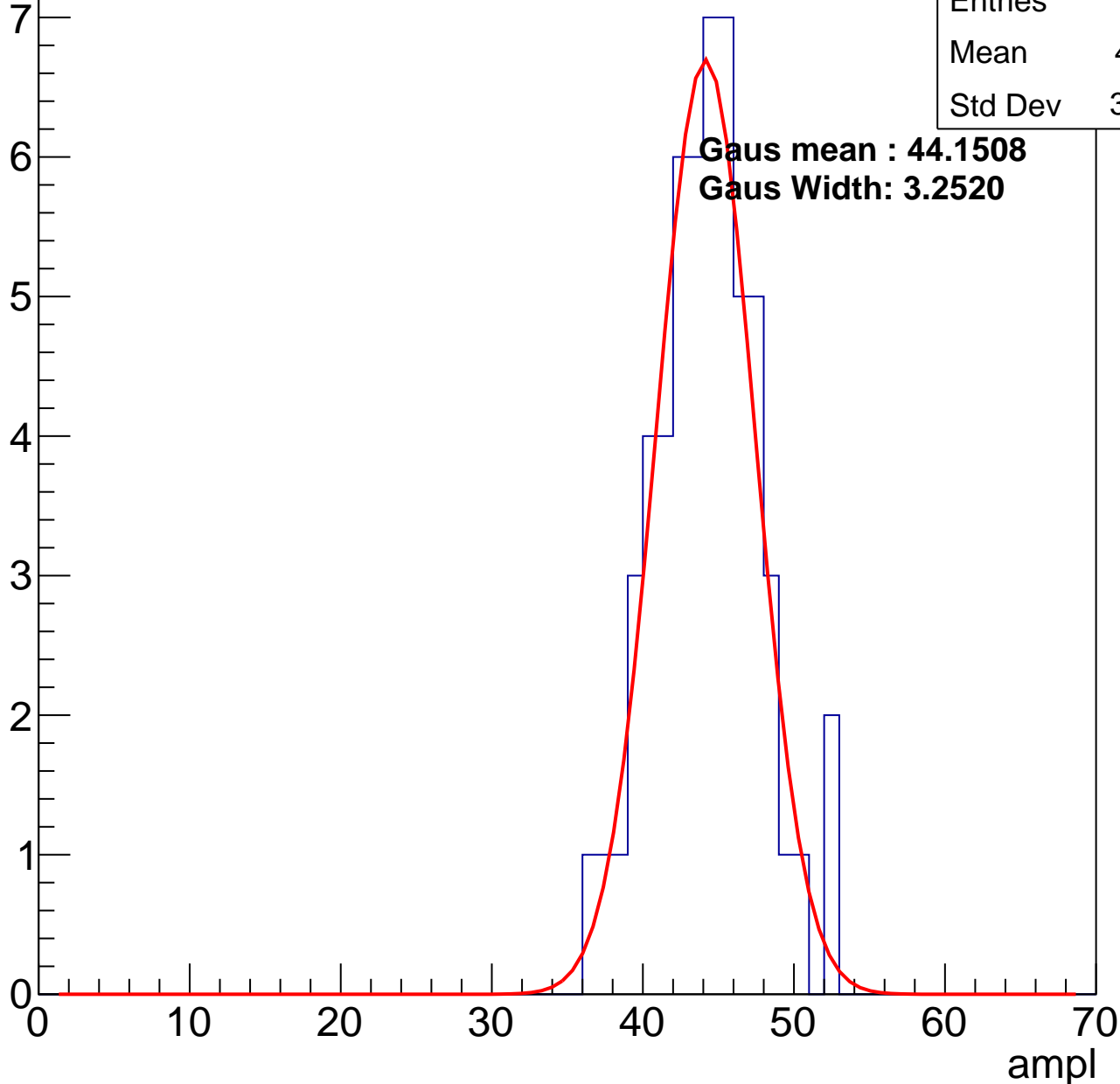


B1L101S, U15-ch33, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	43.81
Std Dev	3.405

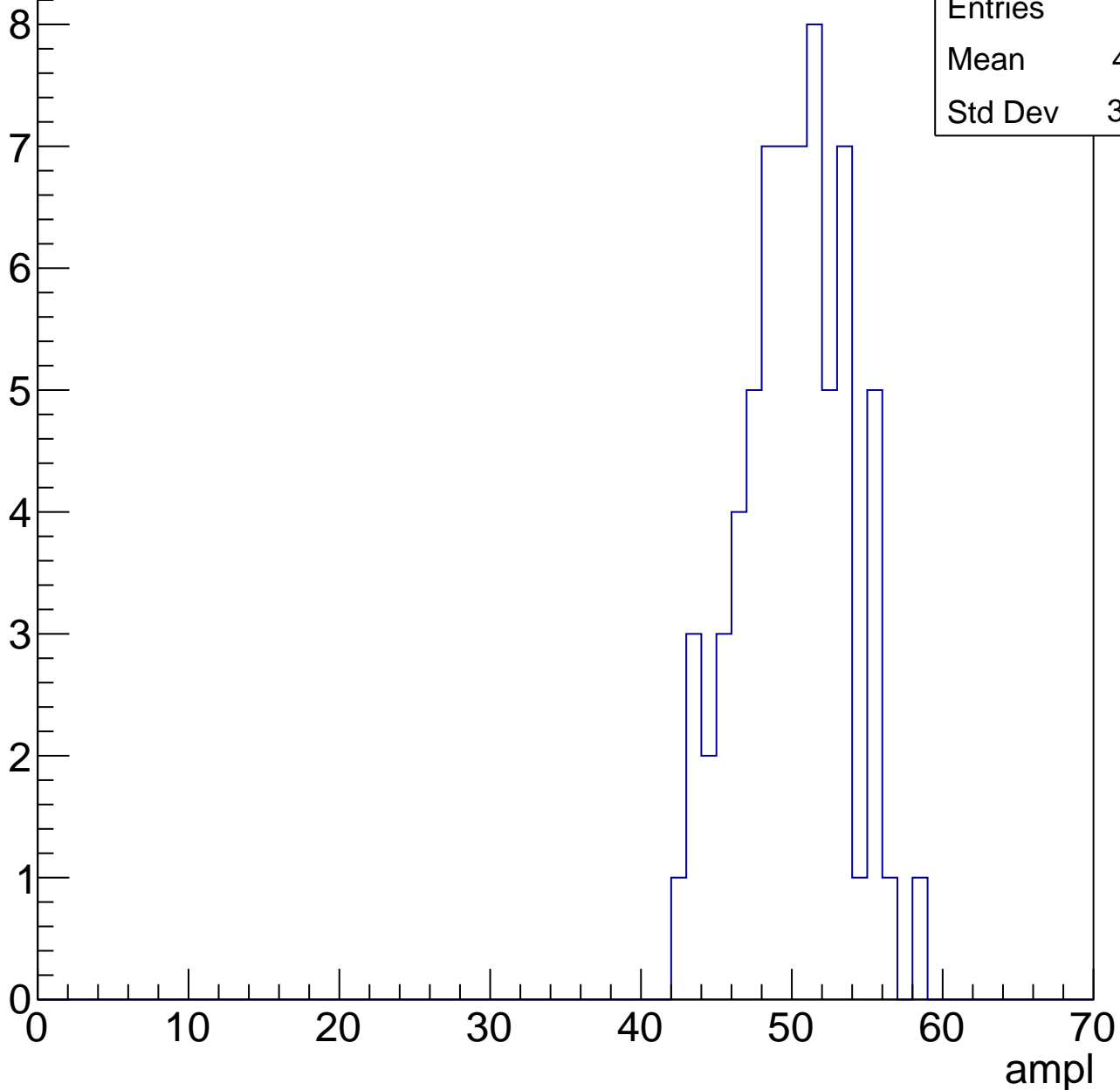


B1L101S, U15-ch33, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.61
Std Dev	3.507

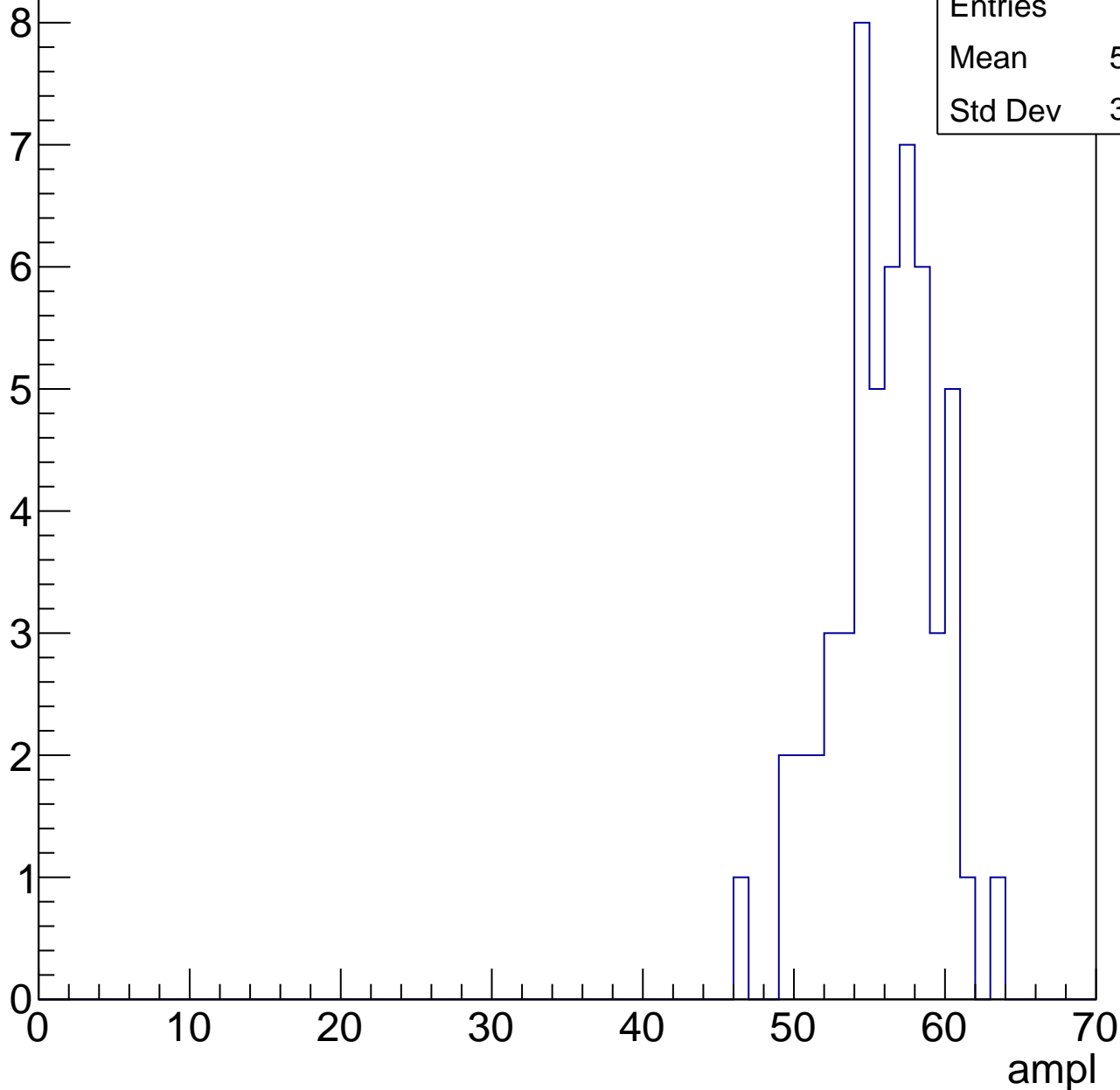


B1L101S, U15-ch33, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	55.49
Std Dev	3.384

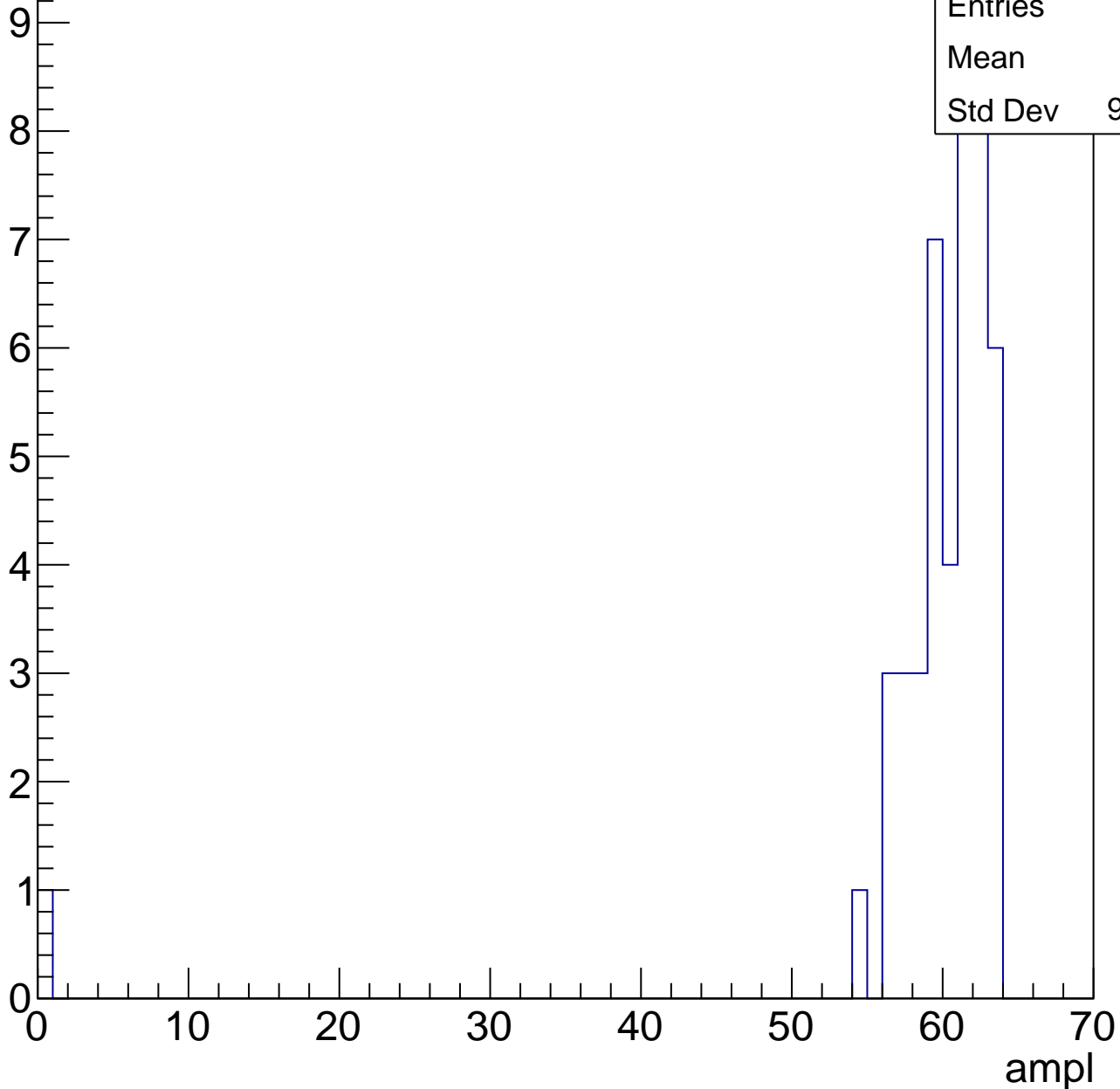


B1L101S, U15-ch33, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.8
Std Dev	9.045



B1L101S, U15-ch33, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	5
Mean	62.2
Std Dev	1.166

B1L101S, U15-ch33, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch34, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	28.85
Std Dev	3.792

Gaus mean : 29.7343

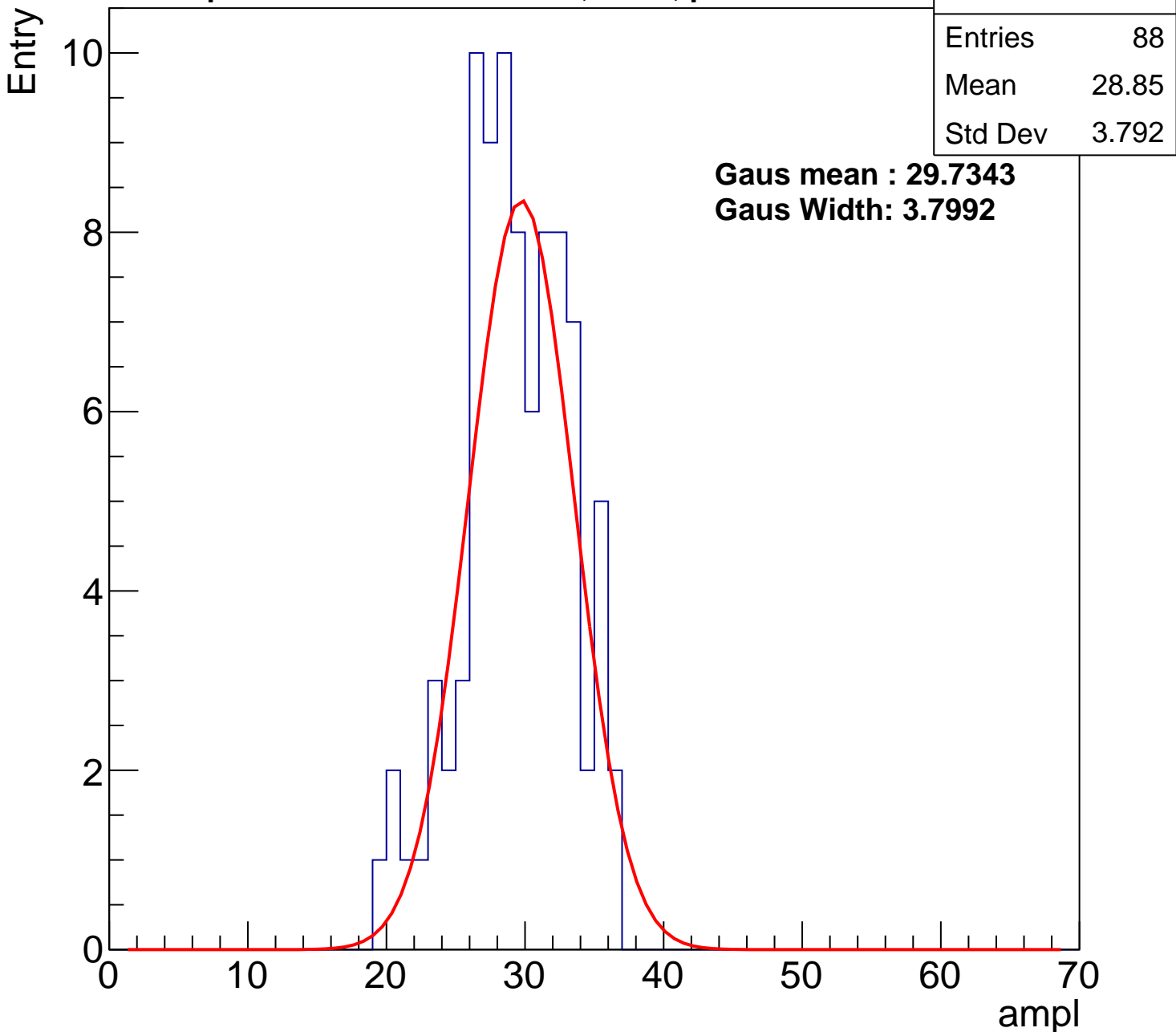
Gaus Width: 3.7992

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch34, adc1

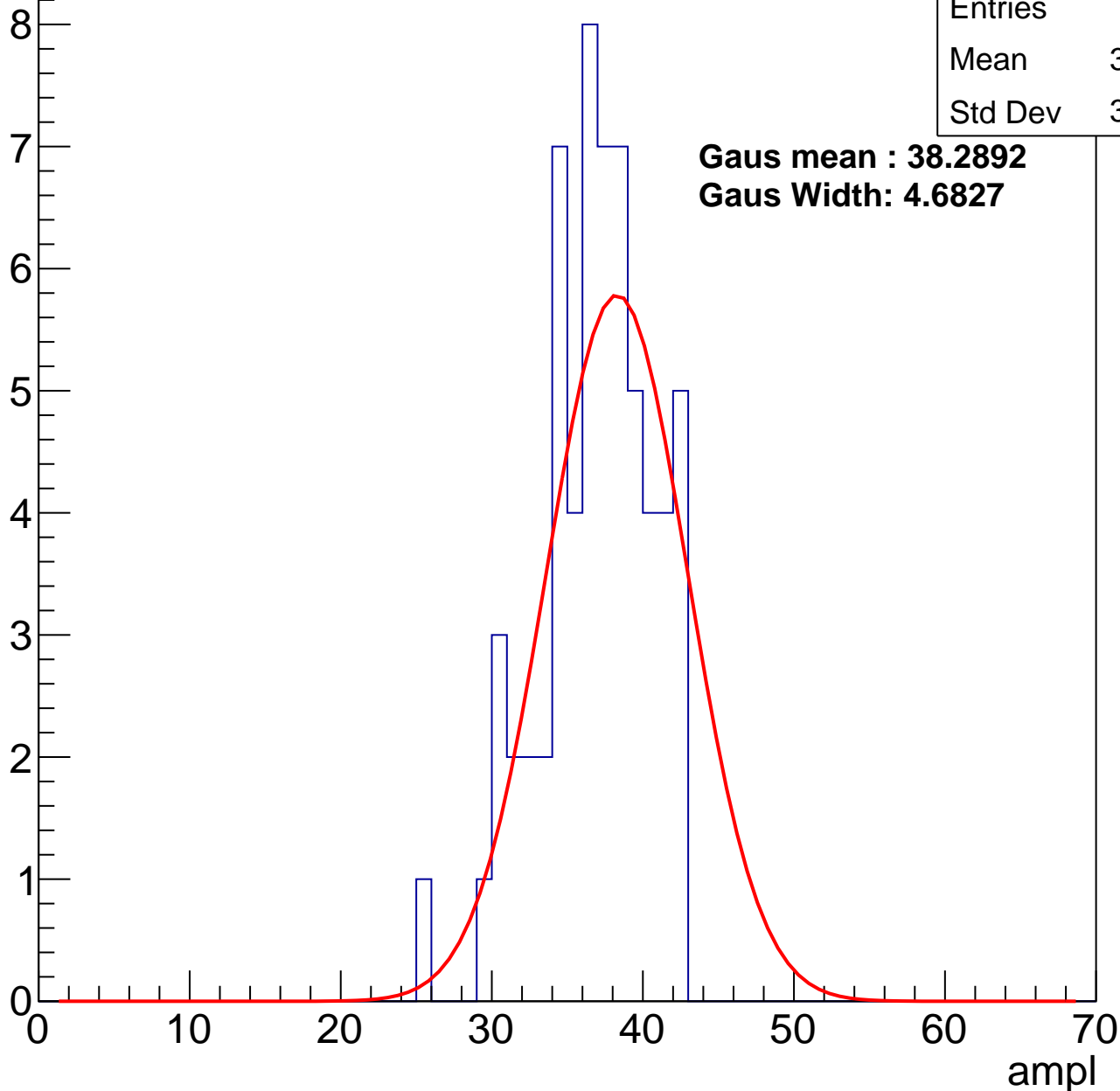
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.39
Std Dev	3.647

Gaus mean : 38.2892

Gaus Width: 4.6827



B1L101S, U15-ch34, adc2

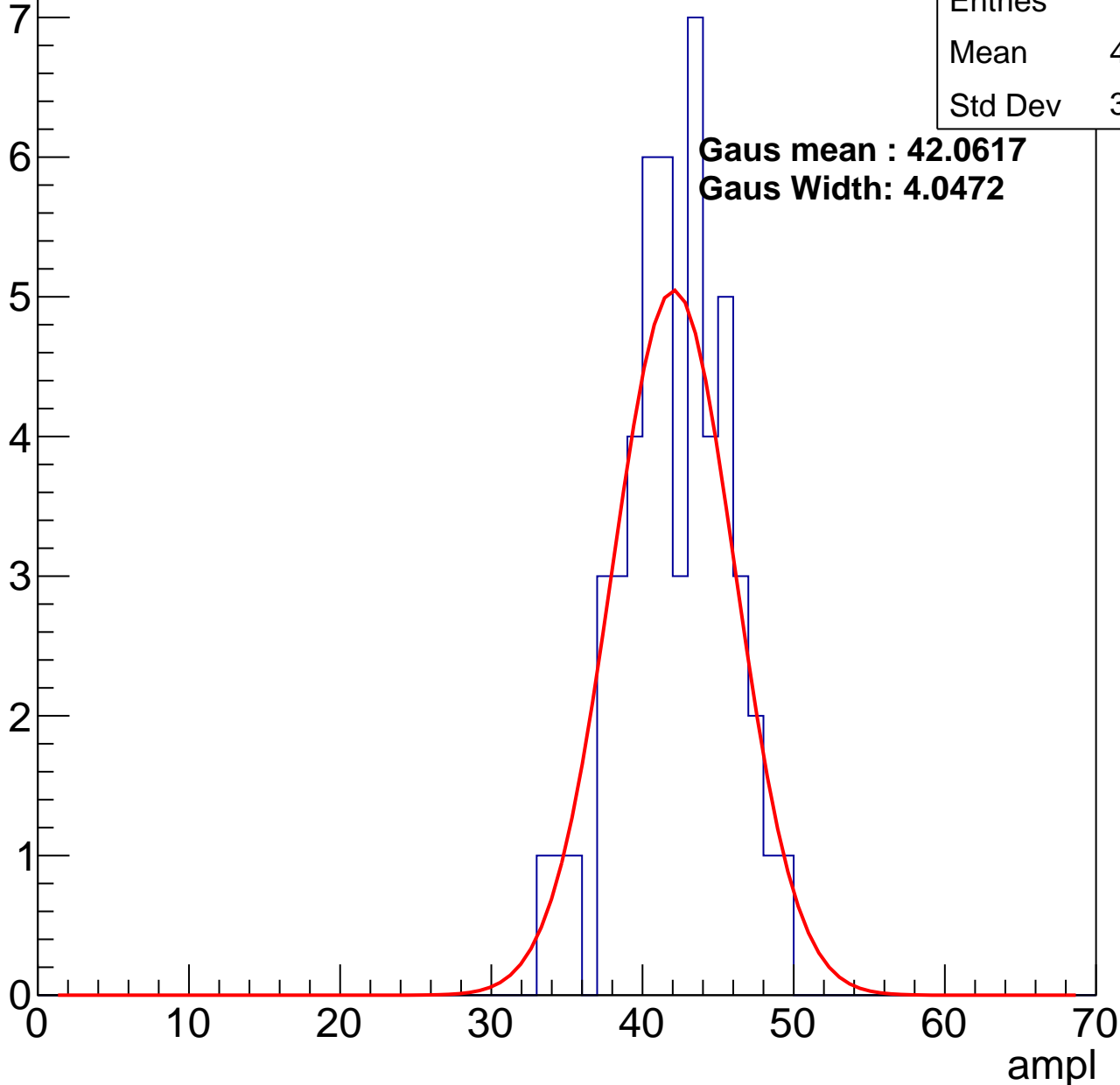
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	41.69
Std Dev	3.512

Gaus mean : 42.0617

Gaus Width: 4.0472

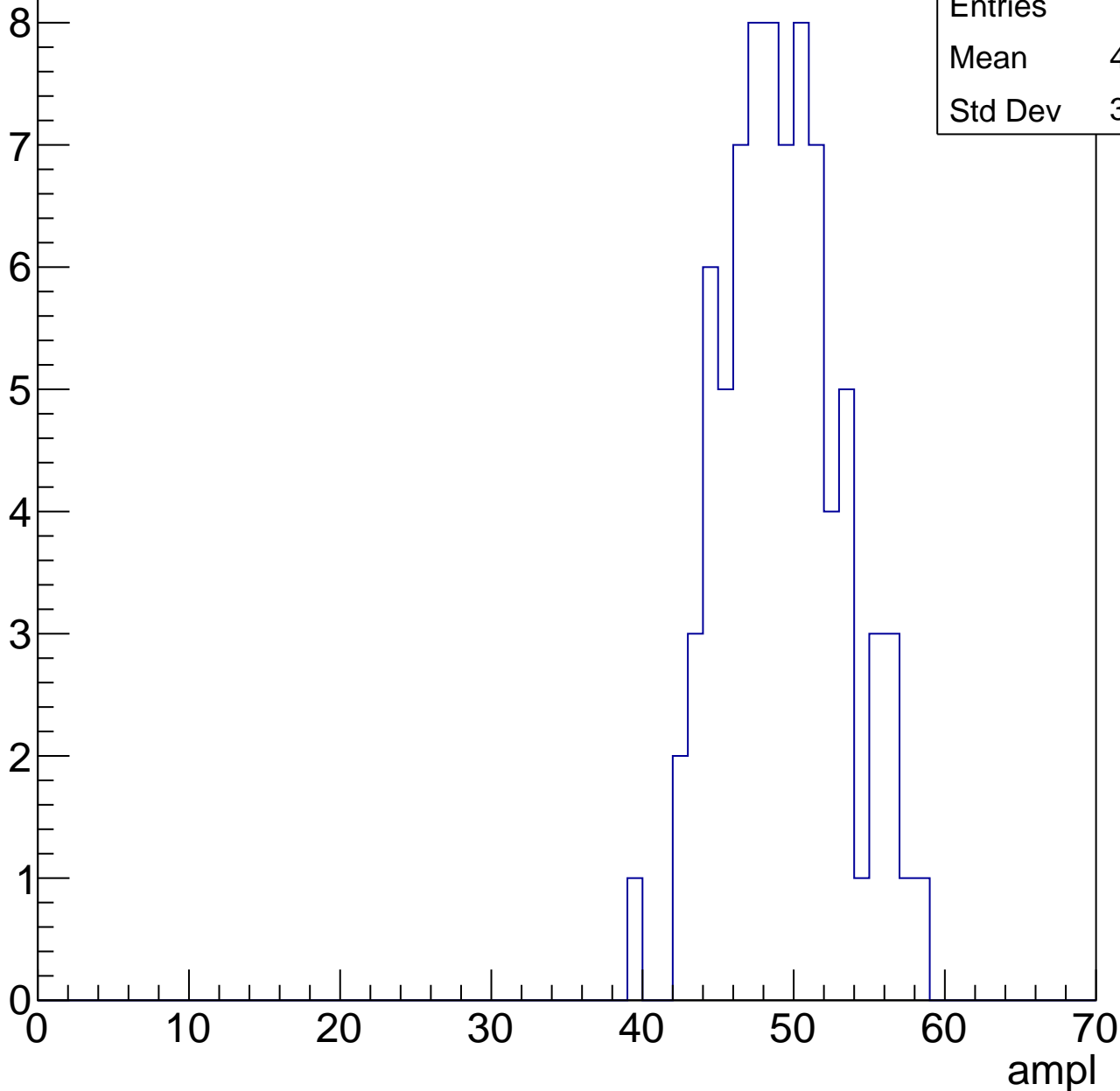


B1L101S, U15-ch34, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	48.73
Std Dev	3.889

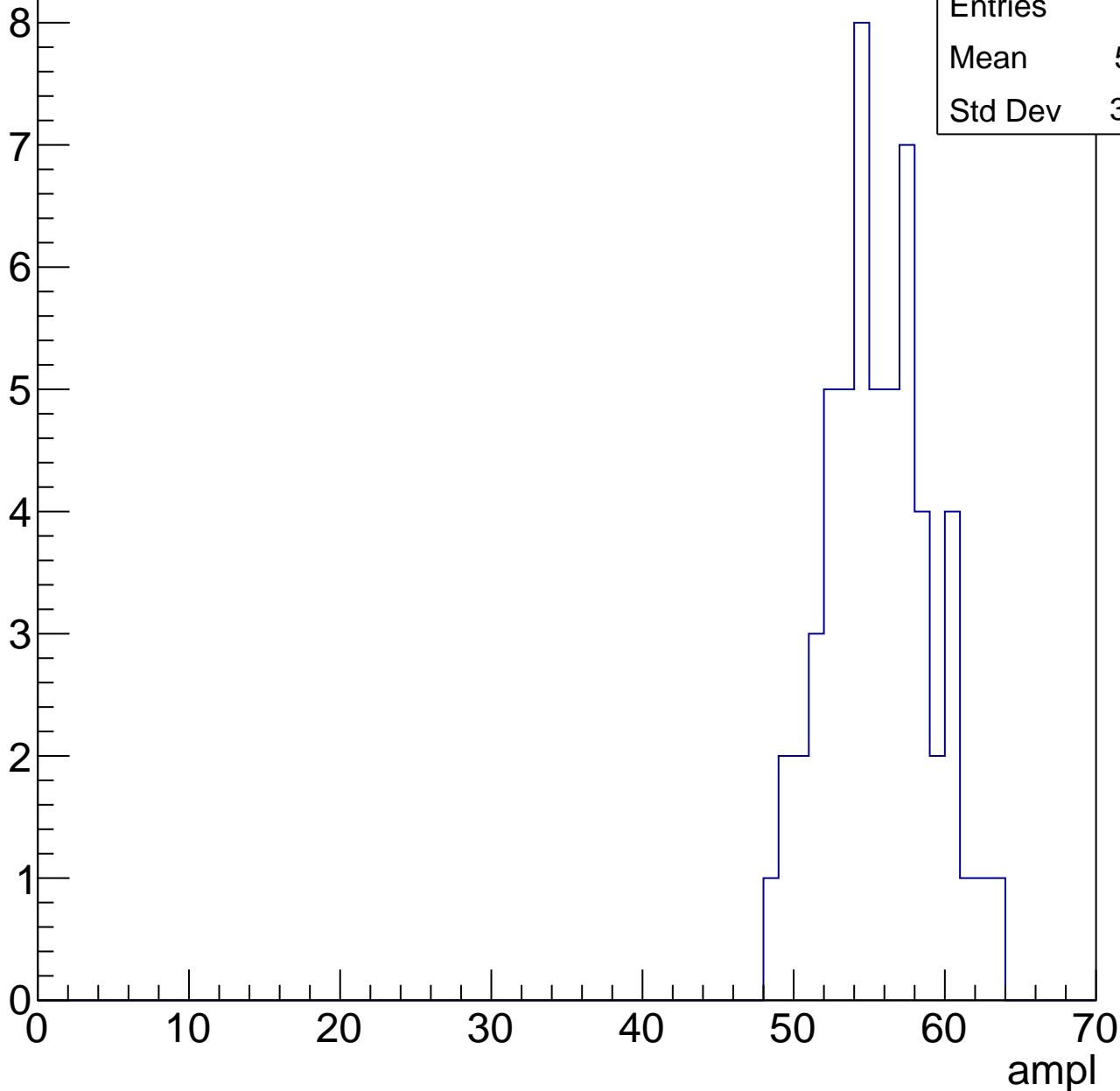


B1L101S, U15-ch34, adc4

calib_packv5_042523_0143.root, FC#0, port D2

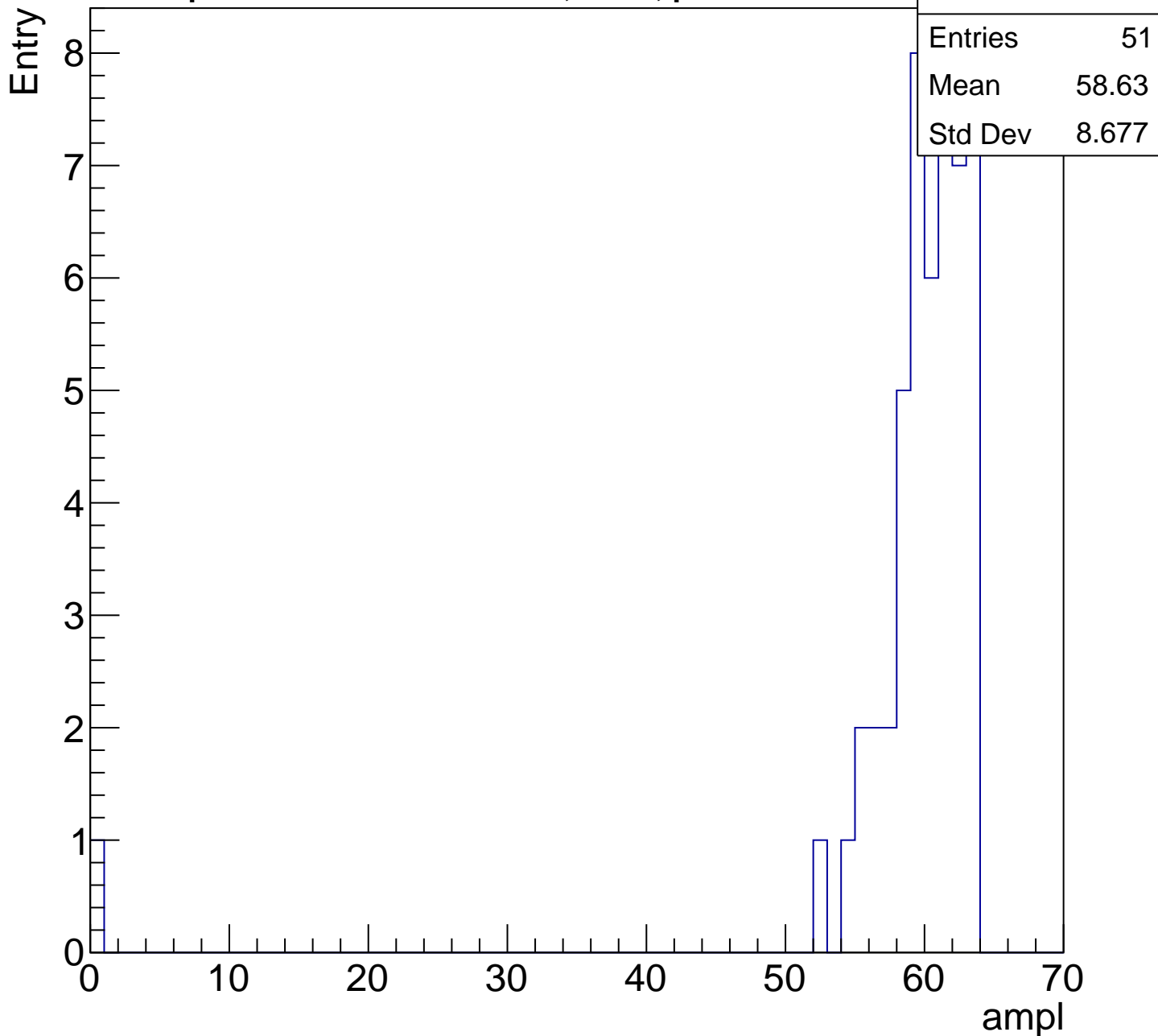
Entry

Entries	56
Mean	55.11
Std Dev	3.384



B1L101S, U15-ch34, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch34, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch34, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch35, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	30.05
Std Dev	3.641

Gaus mean : 30.2773

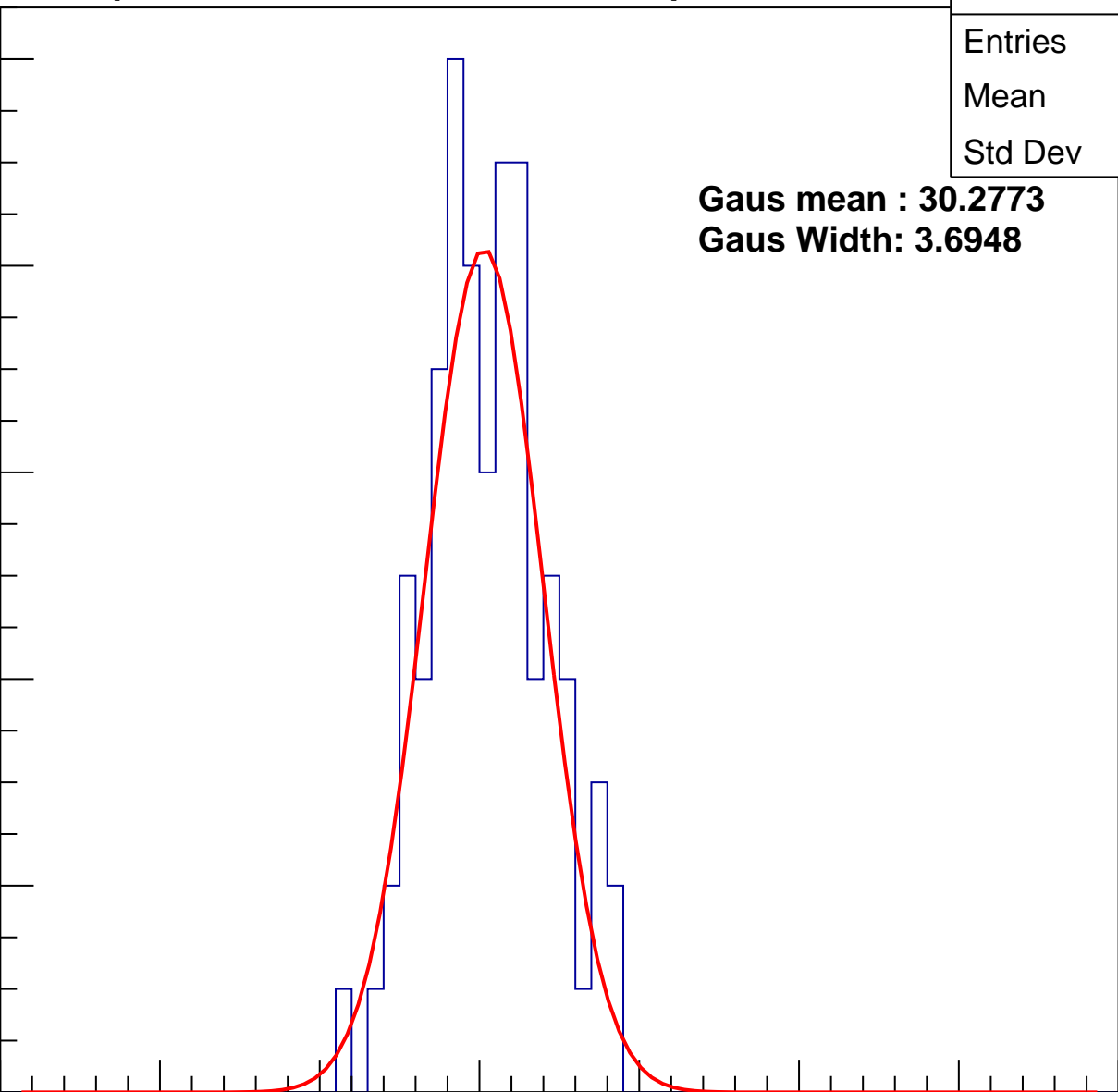
Gaus Width: 3.6948

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



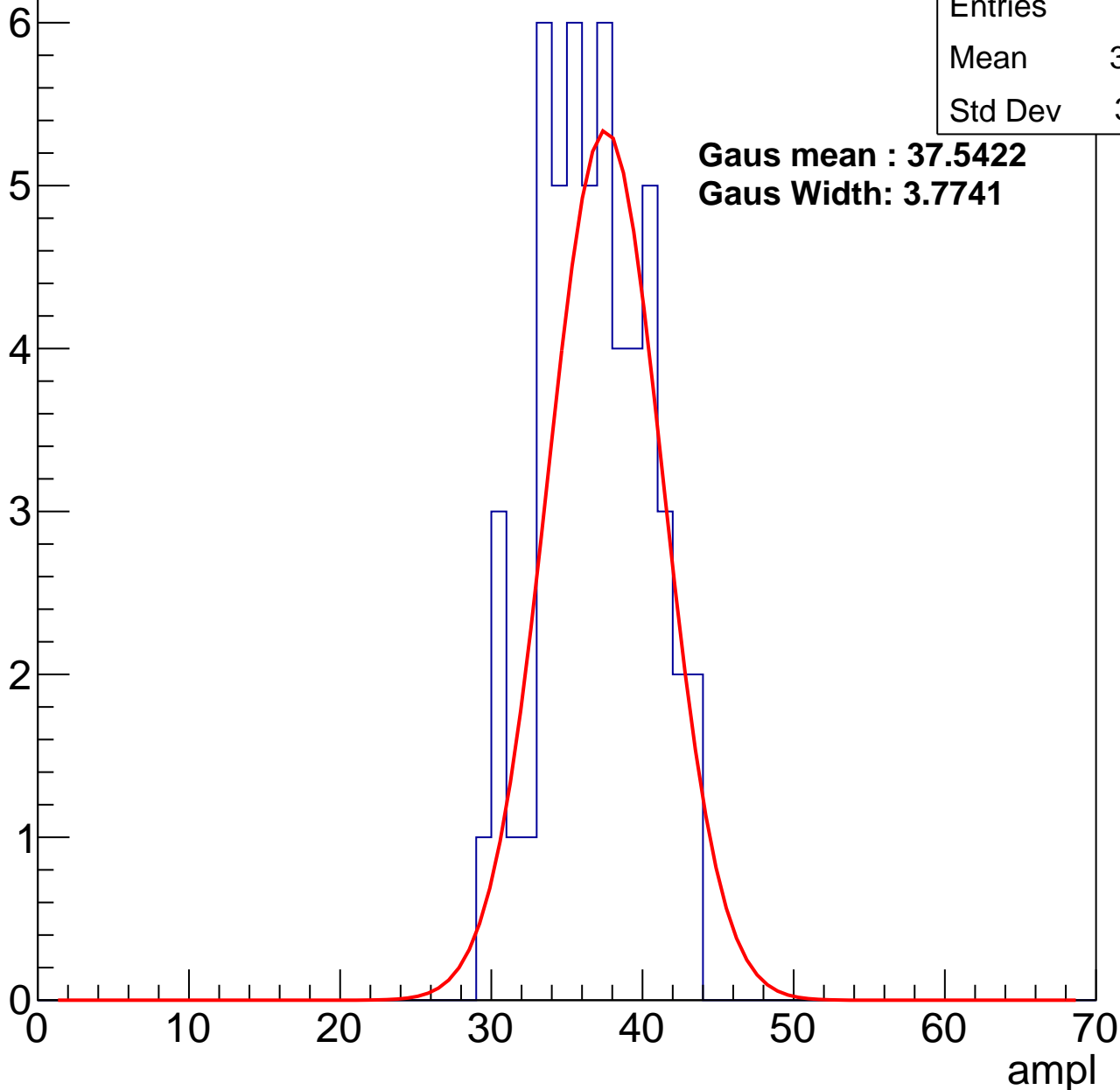
B1L101S, U15-ch35, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.35
Std Dev	3.481

Gaus mean : 37.5422
Gaus Width: 3.7741



B1L101S, U15-ch35, adc2

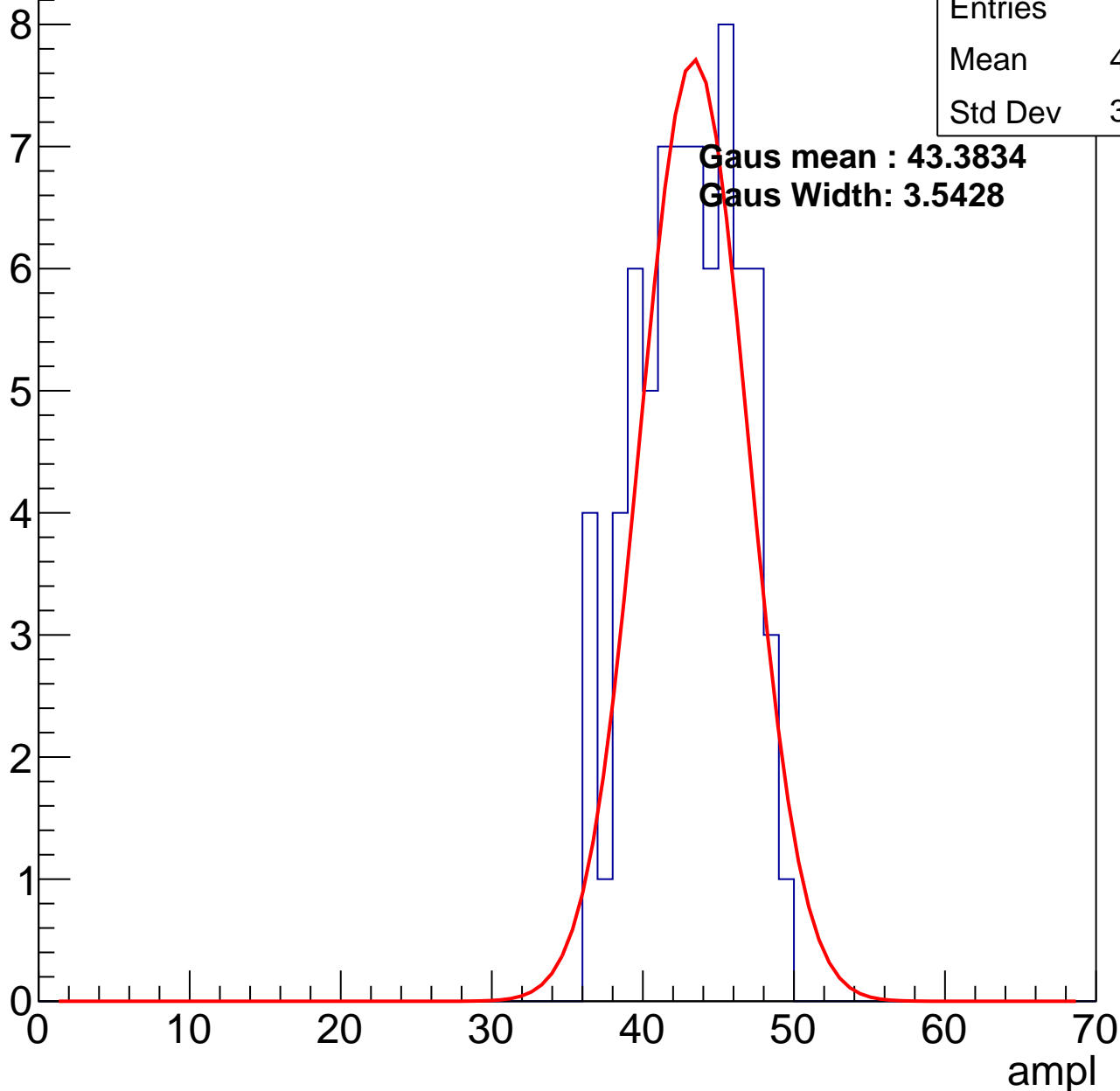
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.59
Std Dev	3.347

Gaus mean : 43.3834

Gaus Width: 3.5428

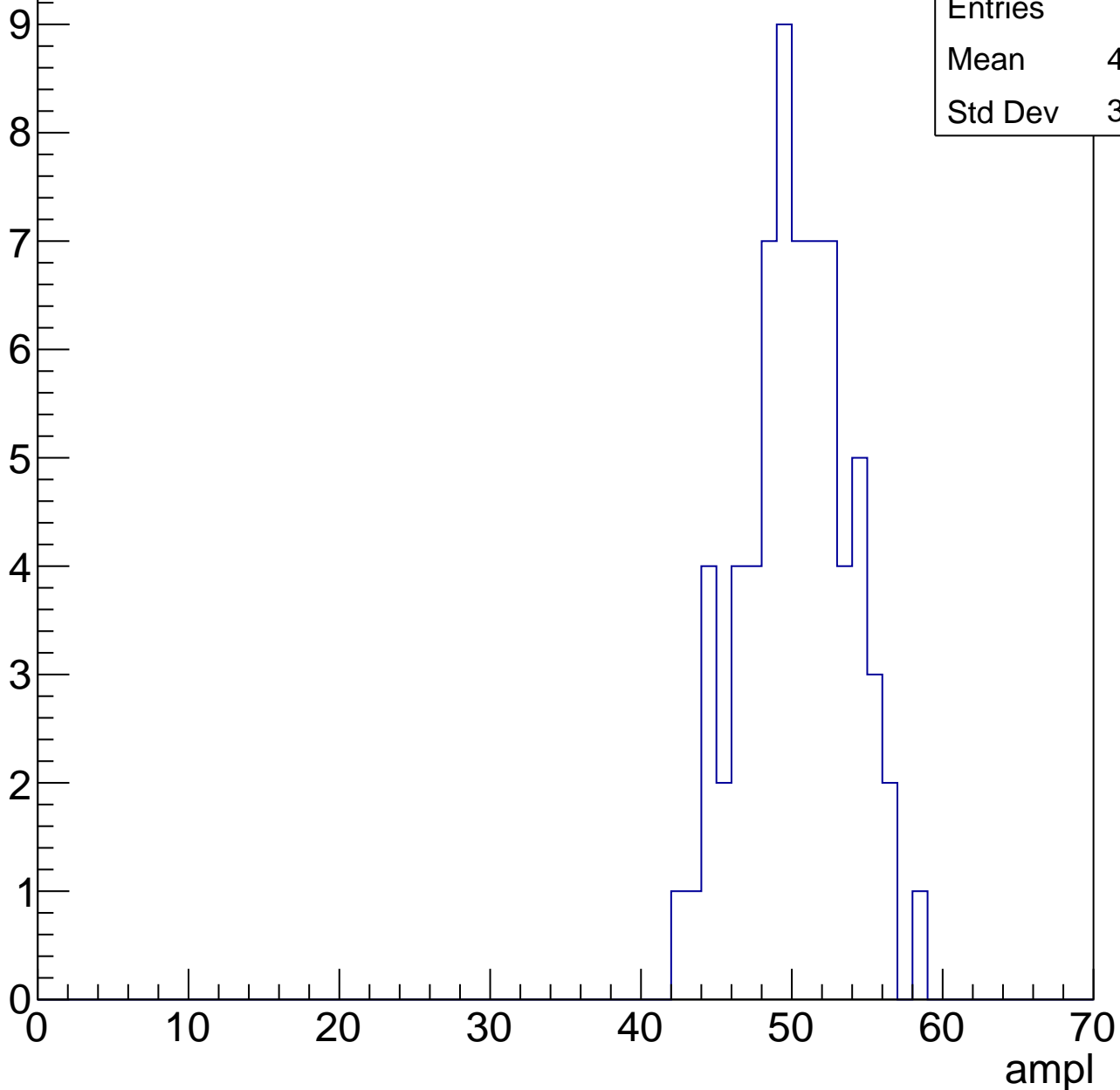


B1L101S, U15-ch35, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	49.82
Std Dev	3.438

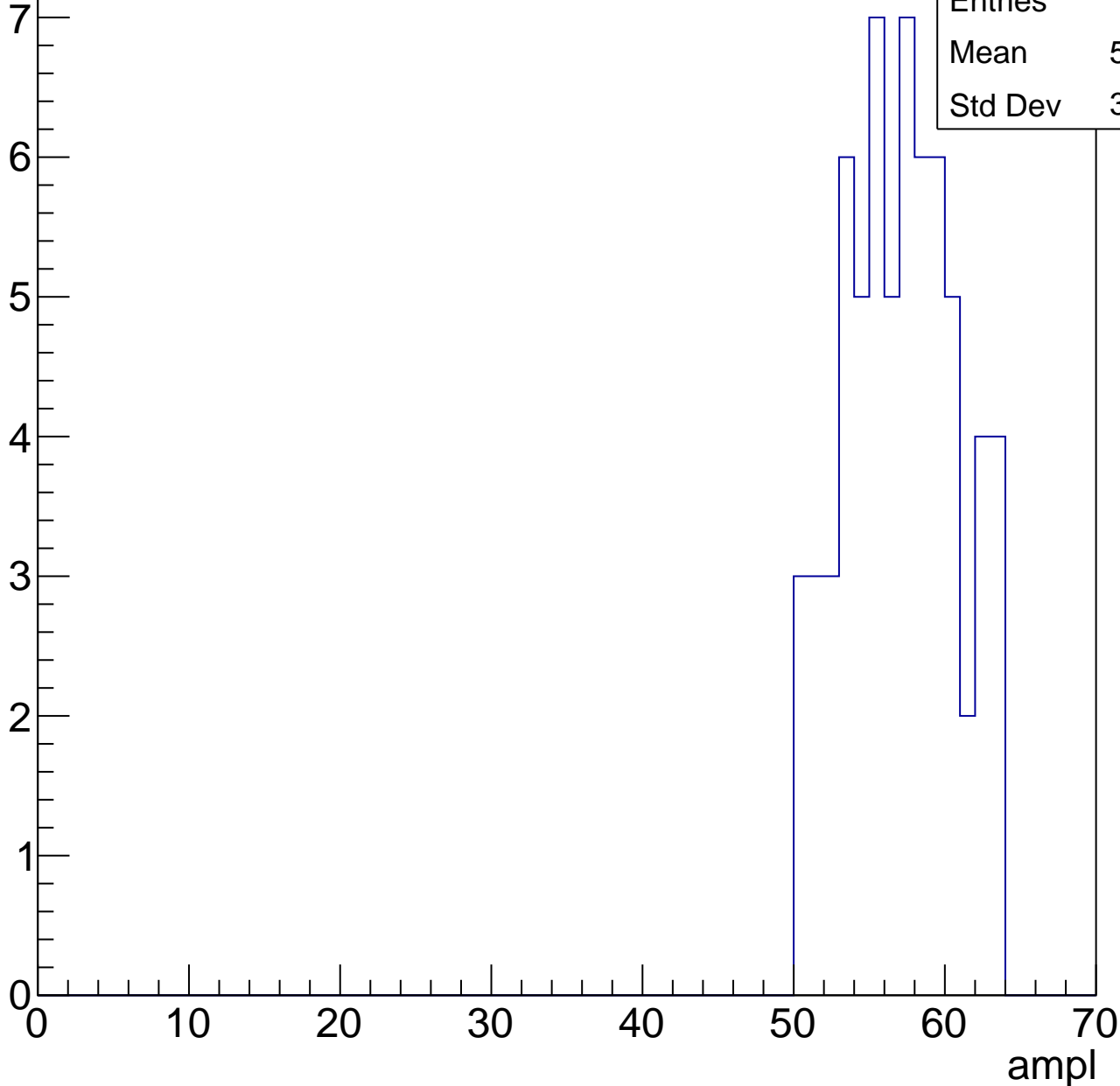


B1L101S, U15-ch35, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

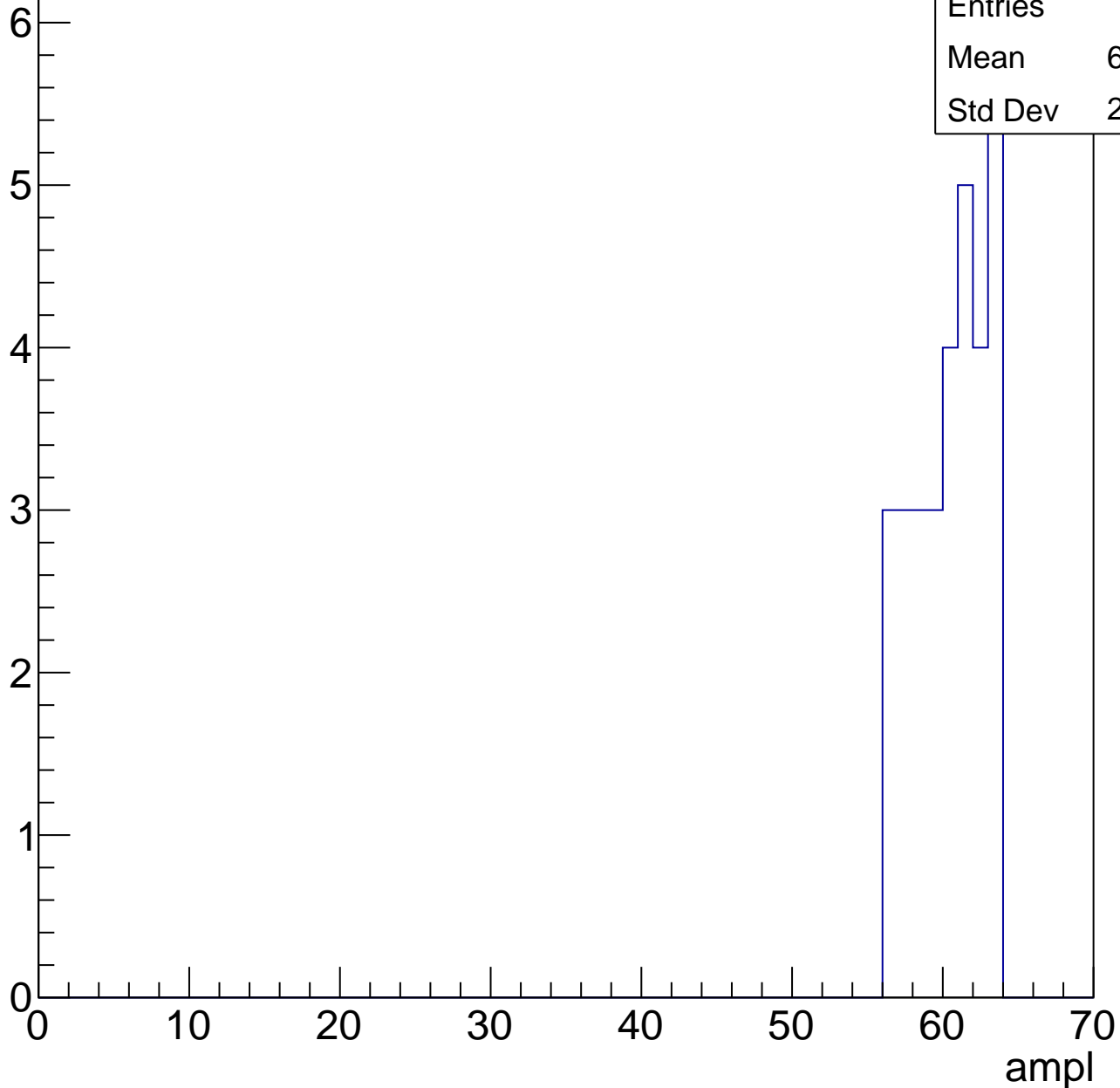
Entries	66
Mean	56.59
Std Dev	3.576



B1L101S, U15-ch35, adc5

calib_packv5_042523_0143.root, FC#0, port D2

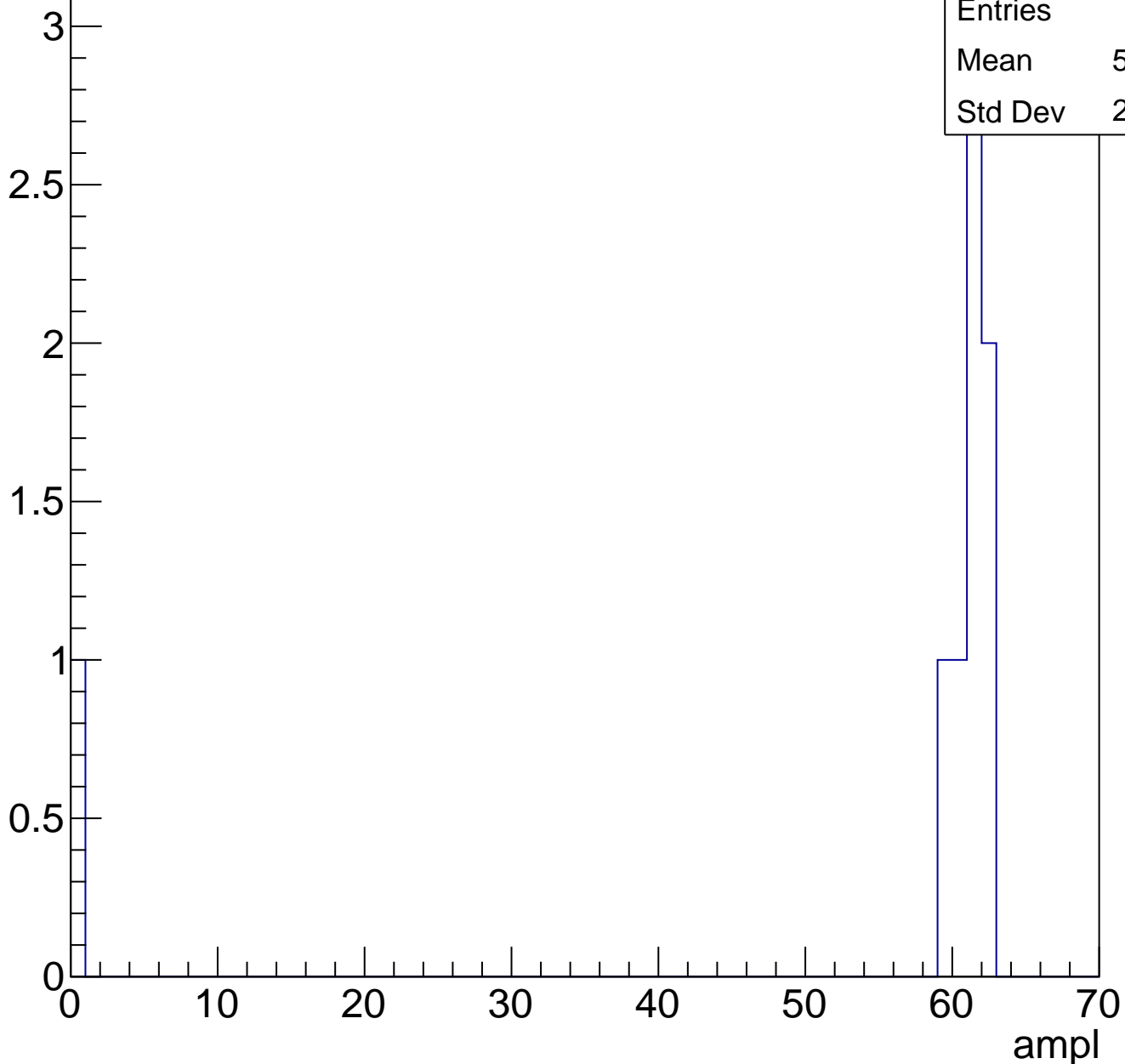
Entry



B1L101S, U15-ch35, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	53.25
Std Dev	20.15

B1L101S, U15-ch35, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch36, adc0

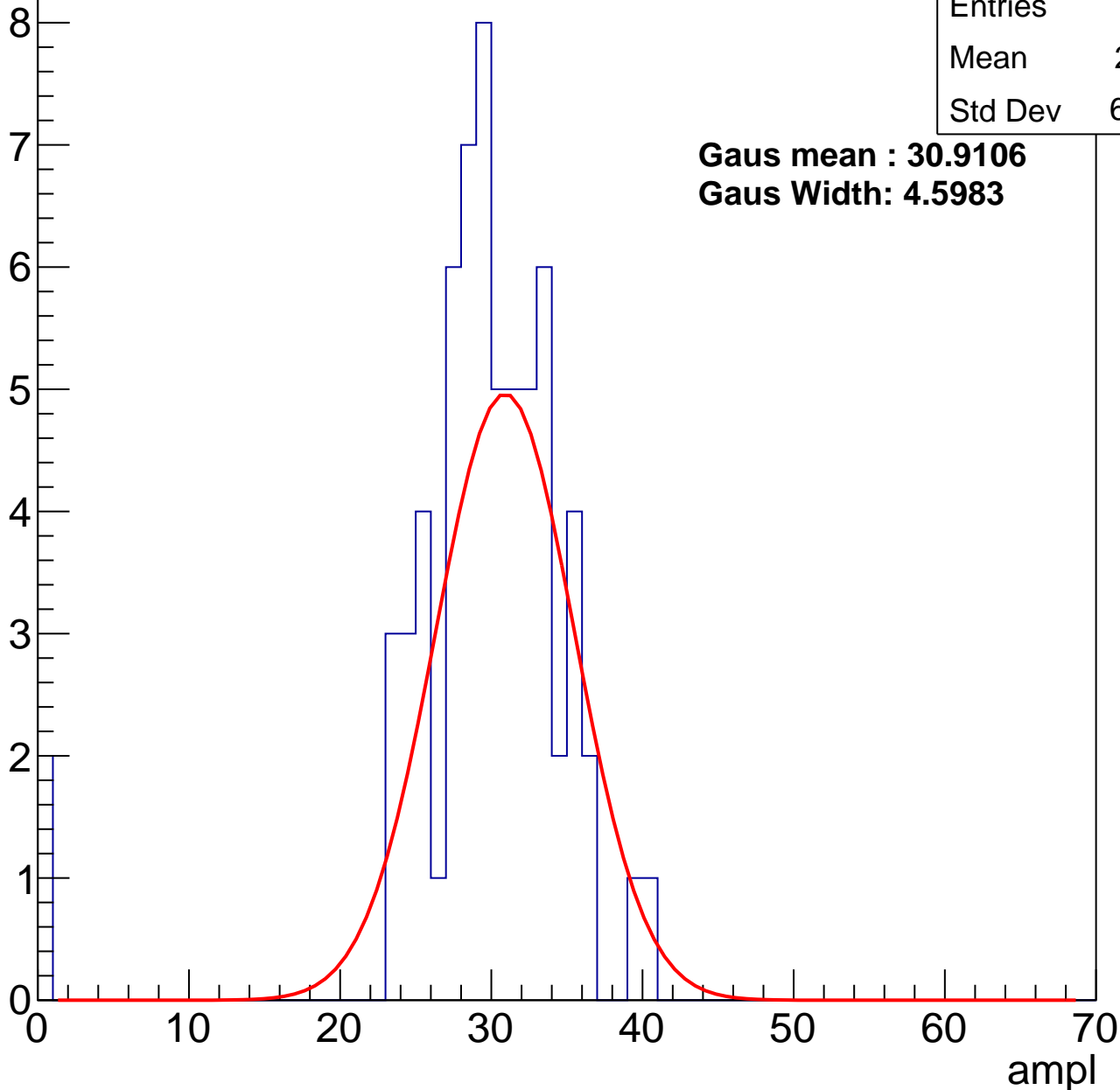
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	28.91
Std Dev	6.382

Gaus mean : 30.9106

Gaus Width: 4.5983



B1L101S, U15-ch36, adc1

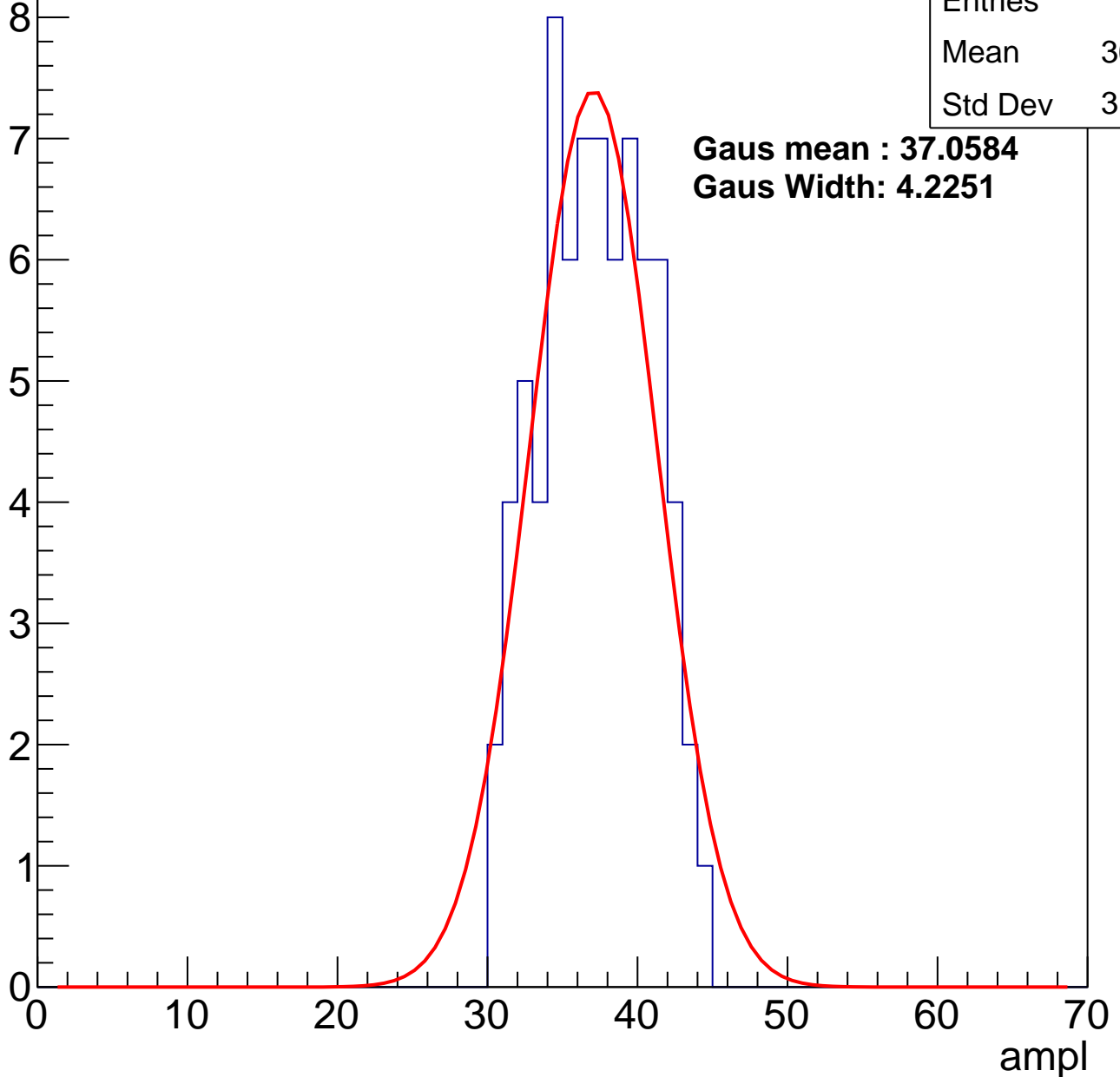
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.72
Std Dev	3.527

Gaus mean : 37.0584

Gaus Width: 4.2251



B1L101S, U15-ch36, adc2

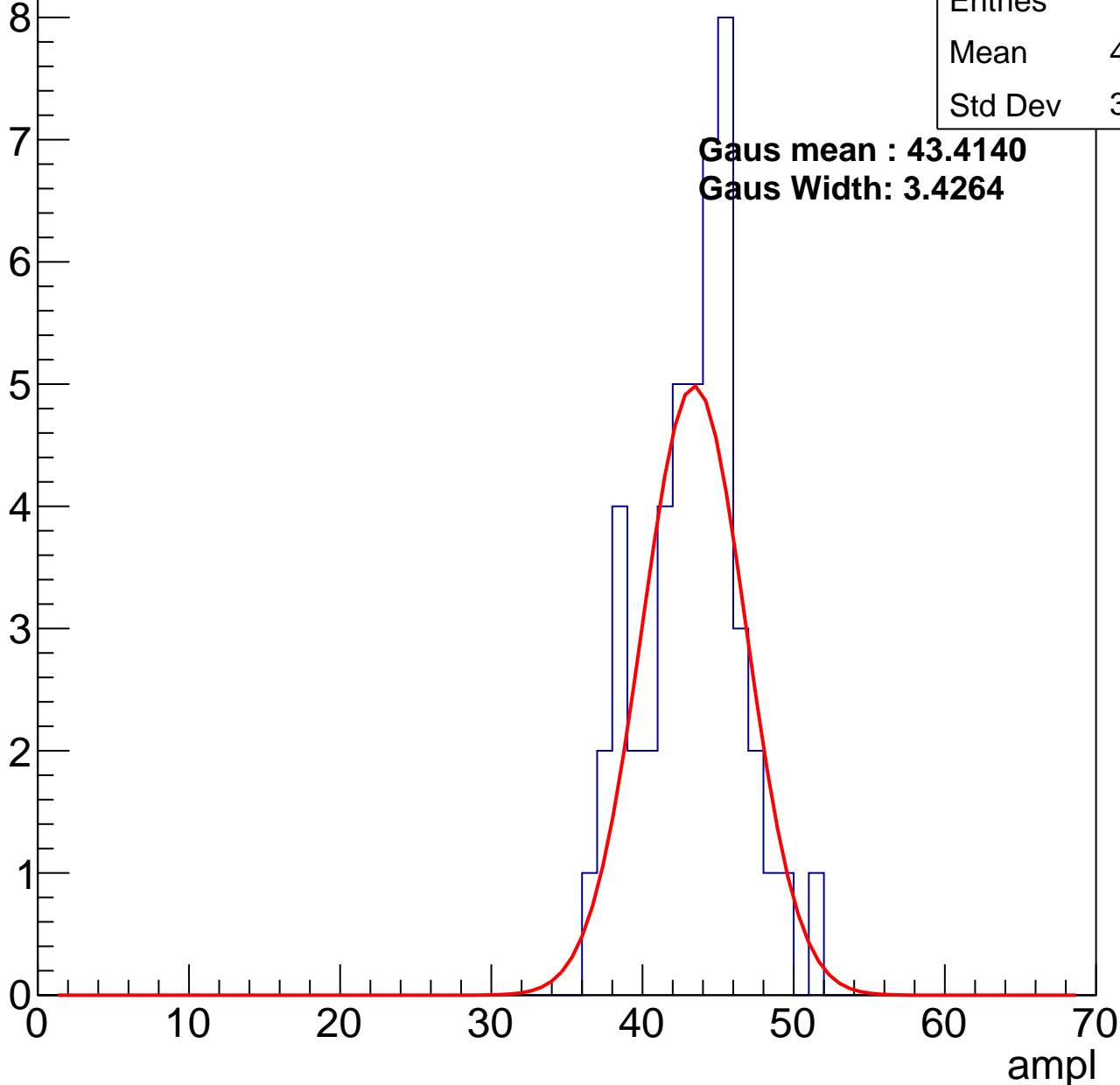
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	42.85
Std Dev	3.272

Gaus mean : 43.4140

Gaus Width: 3.4264

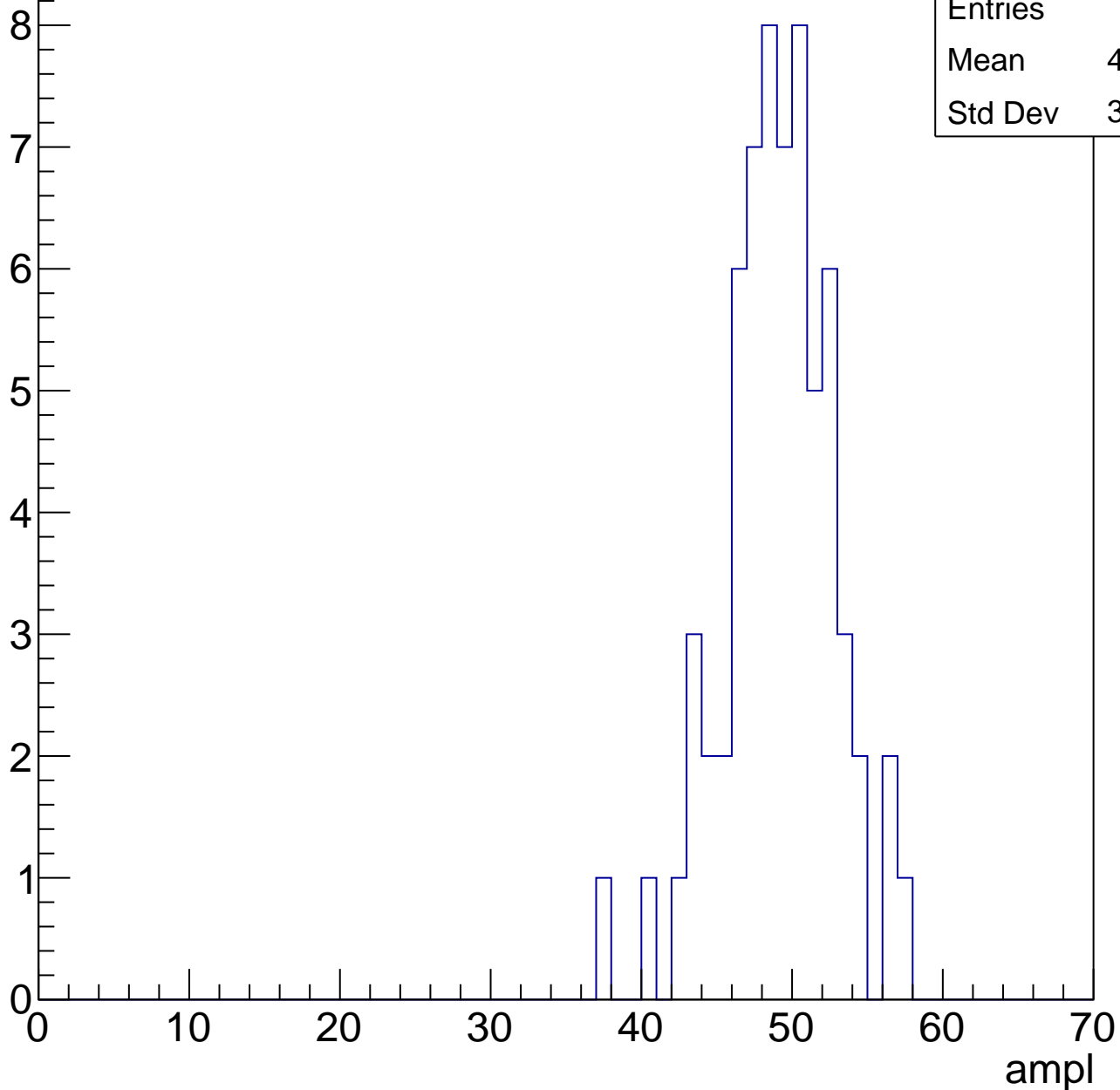


B1L101S, U15-ch36, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

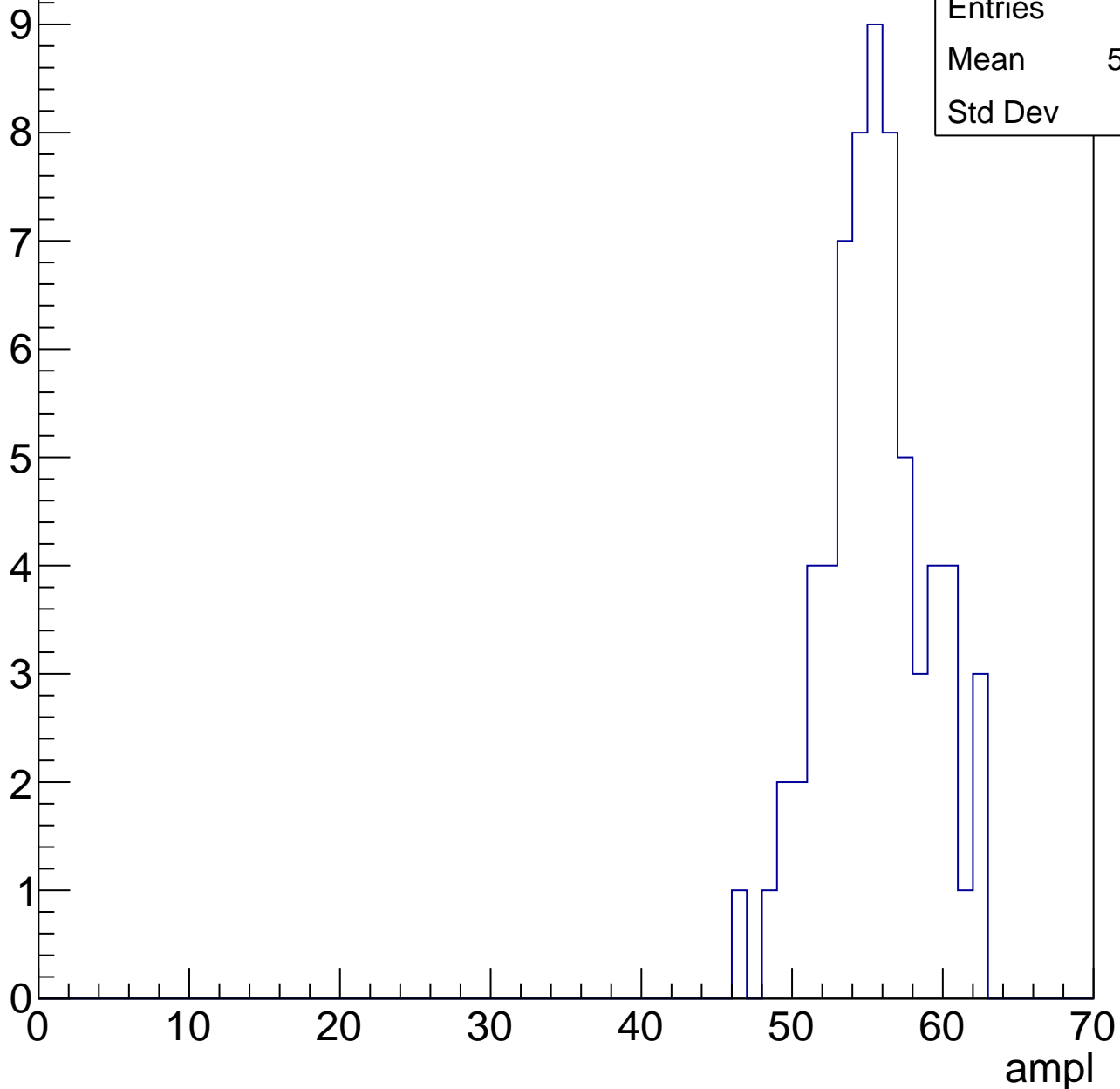
Entries	65
Mean	48.63
Std Dev	3.694



B1L101S, U15-ch36, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

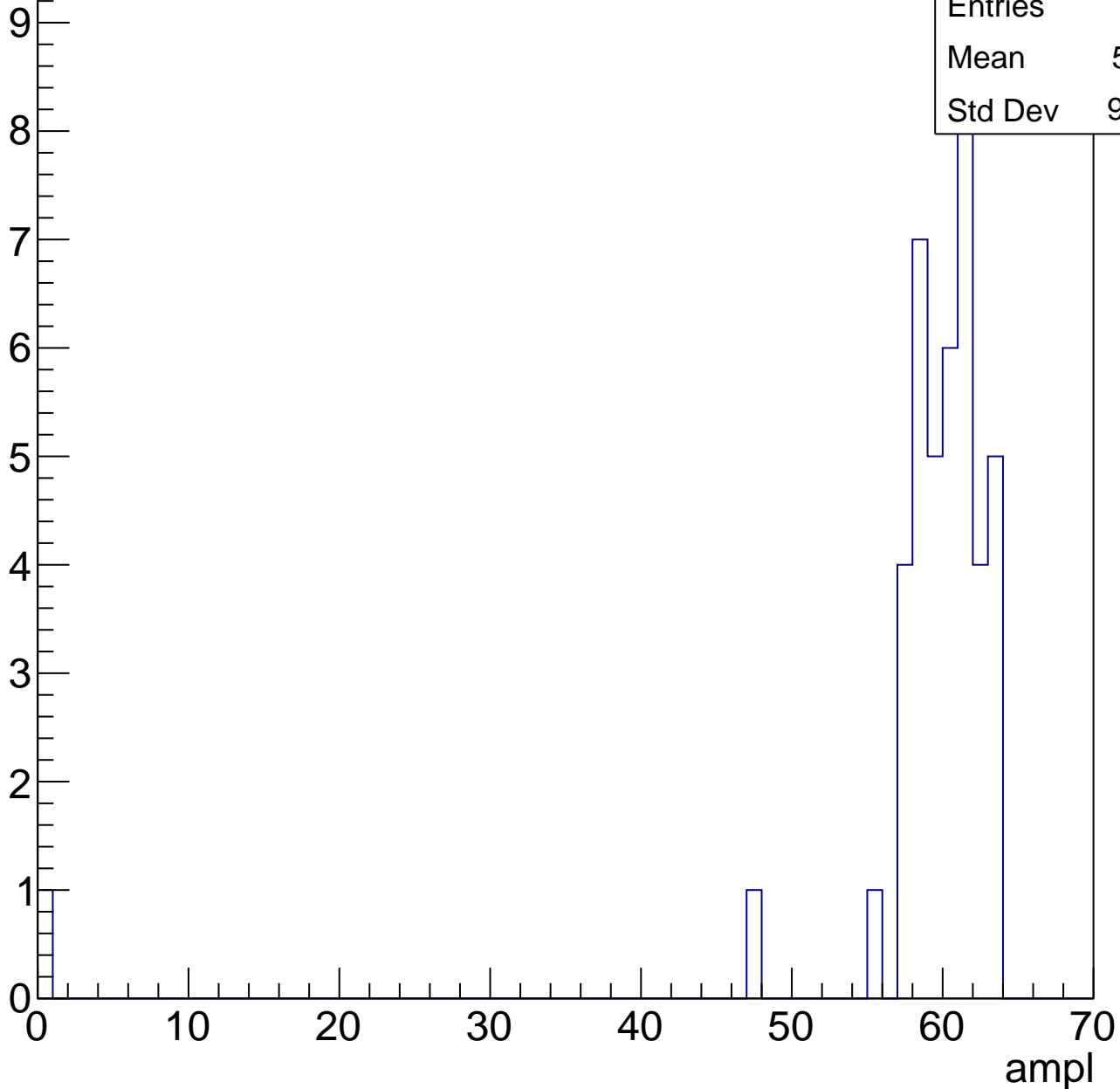


B1L101S, U15-ch36, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.21
Std Dev	9.395

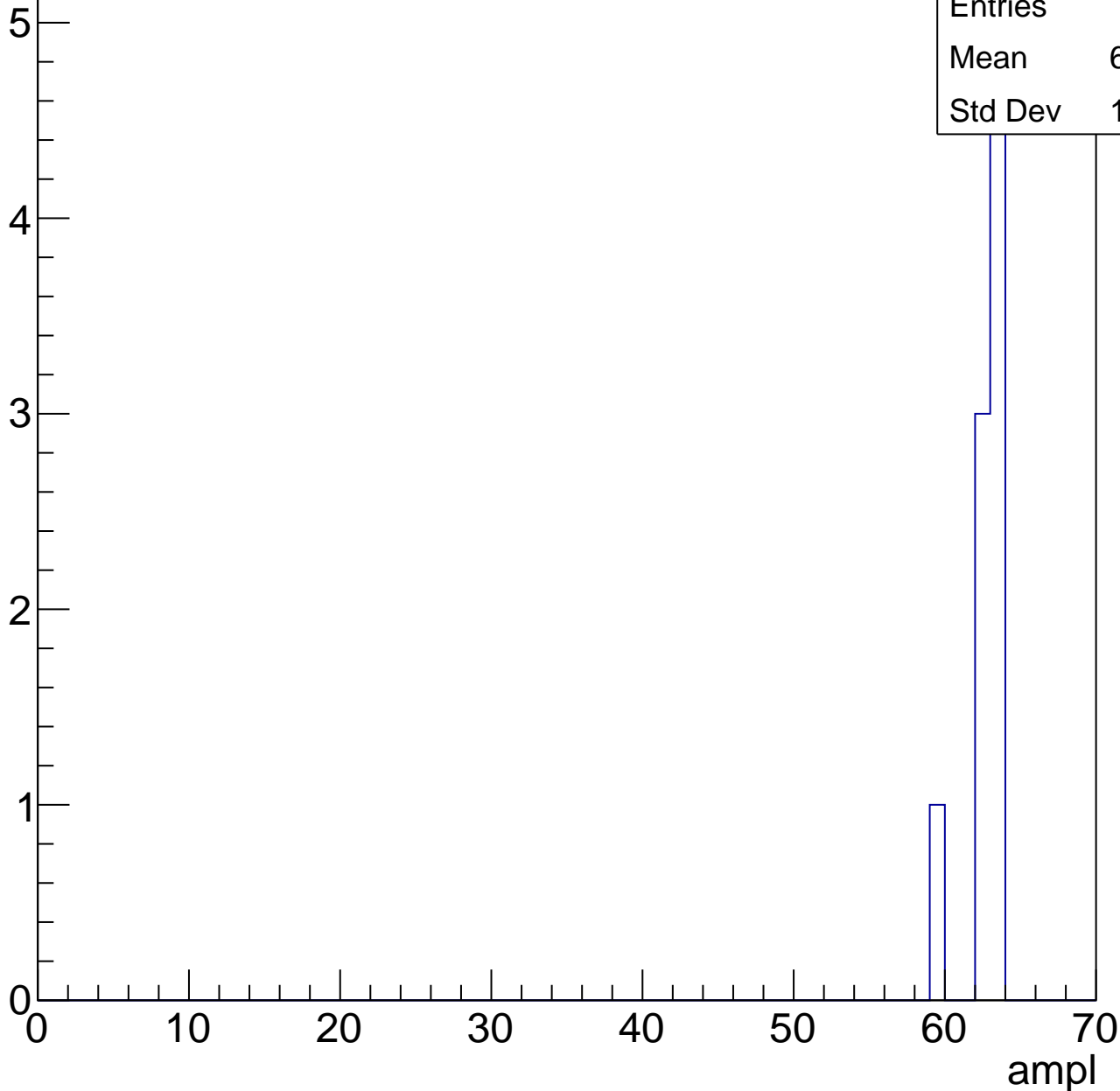


B1L101S, U15-ch36, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	62.22
Std Dev	1.227



B1L101S, U15-ch36, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch37, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	92
Mean	29.32
Std Dev	4.07

Gaus mean : 29.8665

Gaus Width: 4.2629

Entry

10
8
6
4
2
0

0

10

20

30

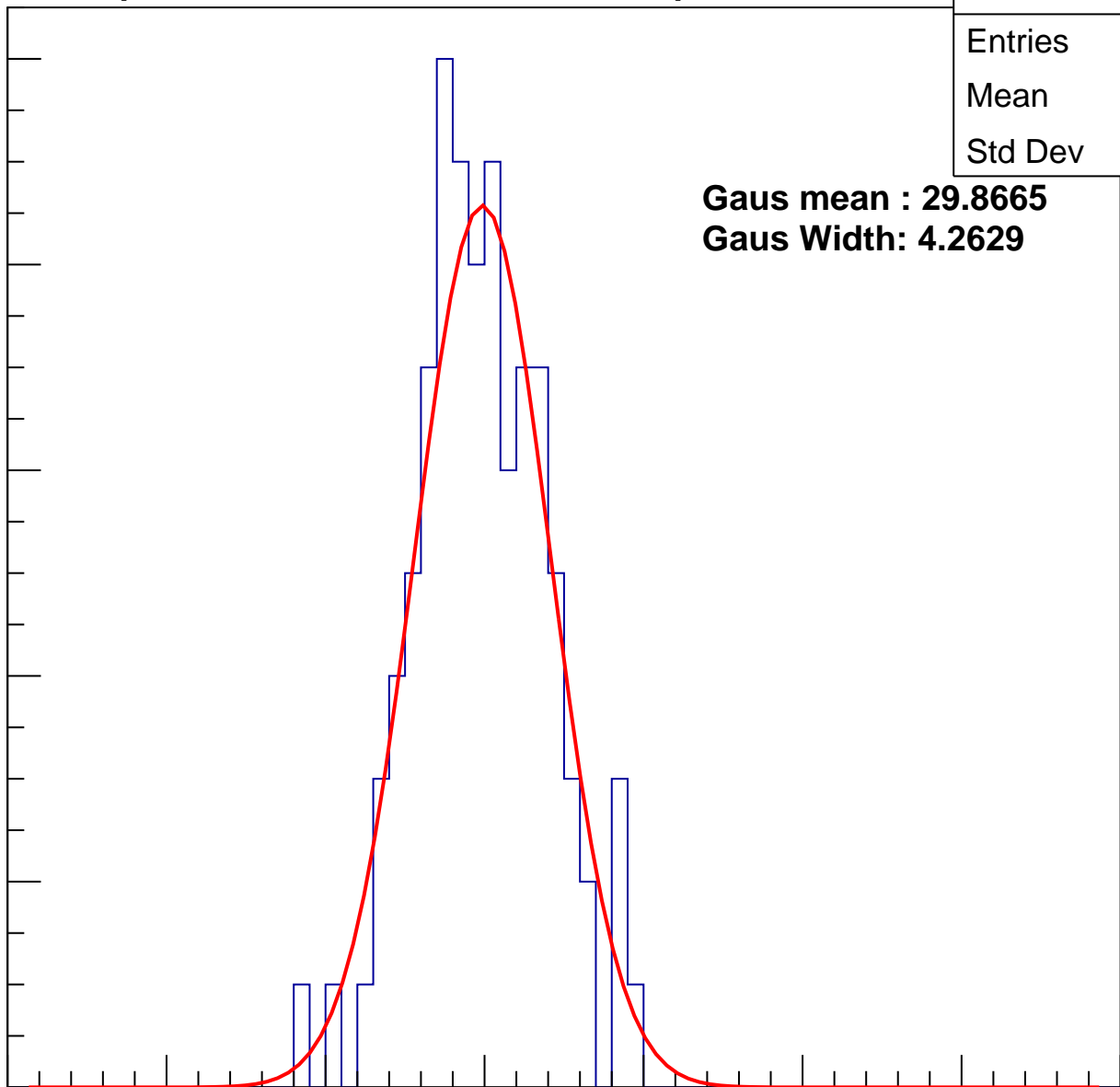
40

50

60

70

ampl



B1L101S, U15-ch37, adc1

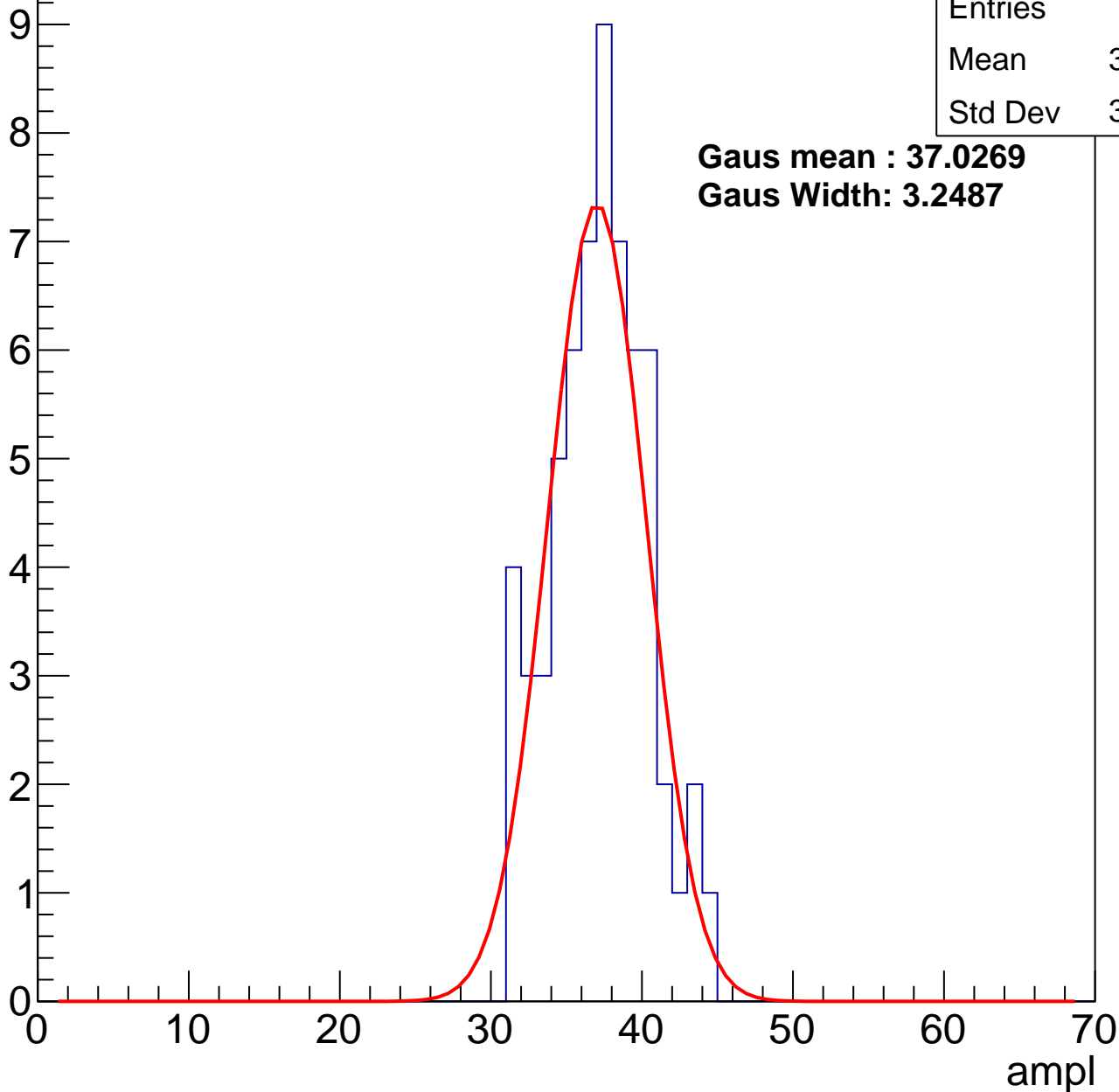
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.74
Std Dev	3.116

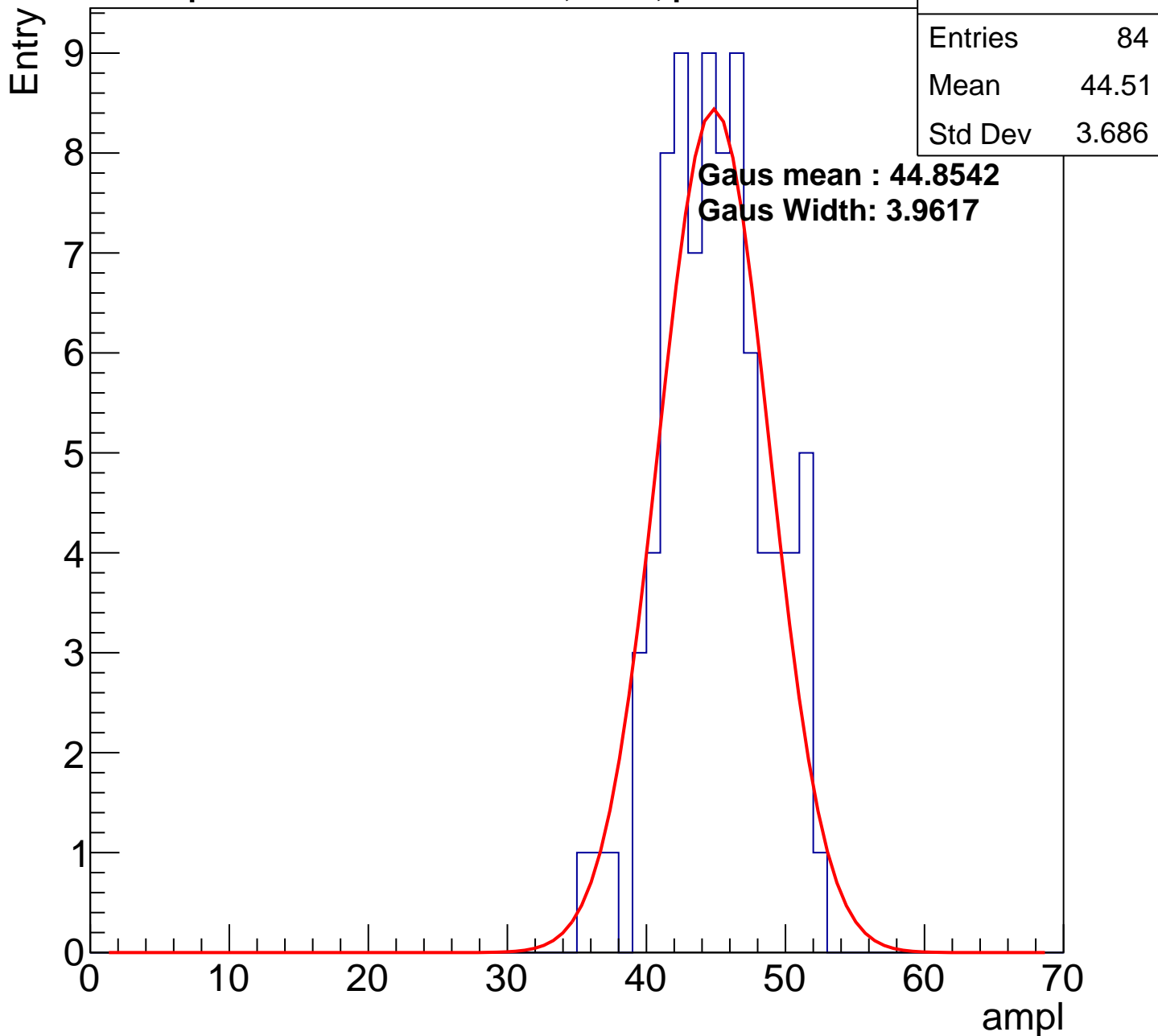
Gaus mean : 37.0269

Gaus Width: 3.2487



B1L101S, U15-ch37, adc2

calib_packv5_042523_0143.root, FC#0, port D2

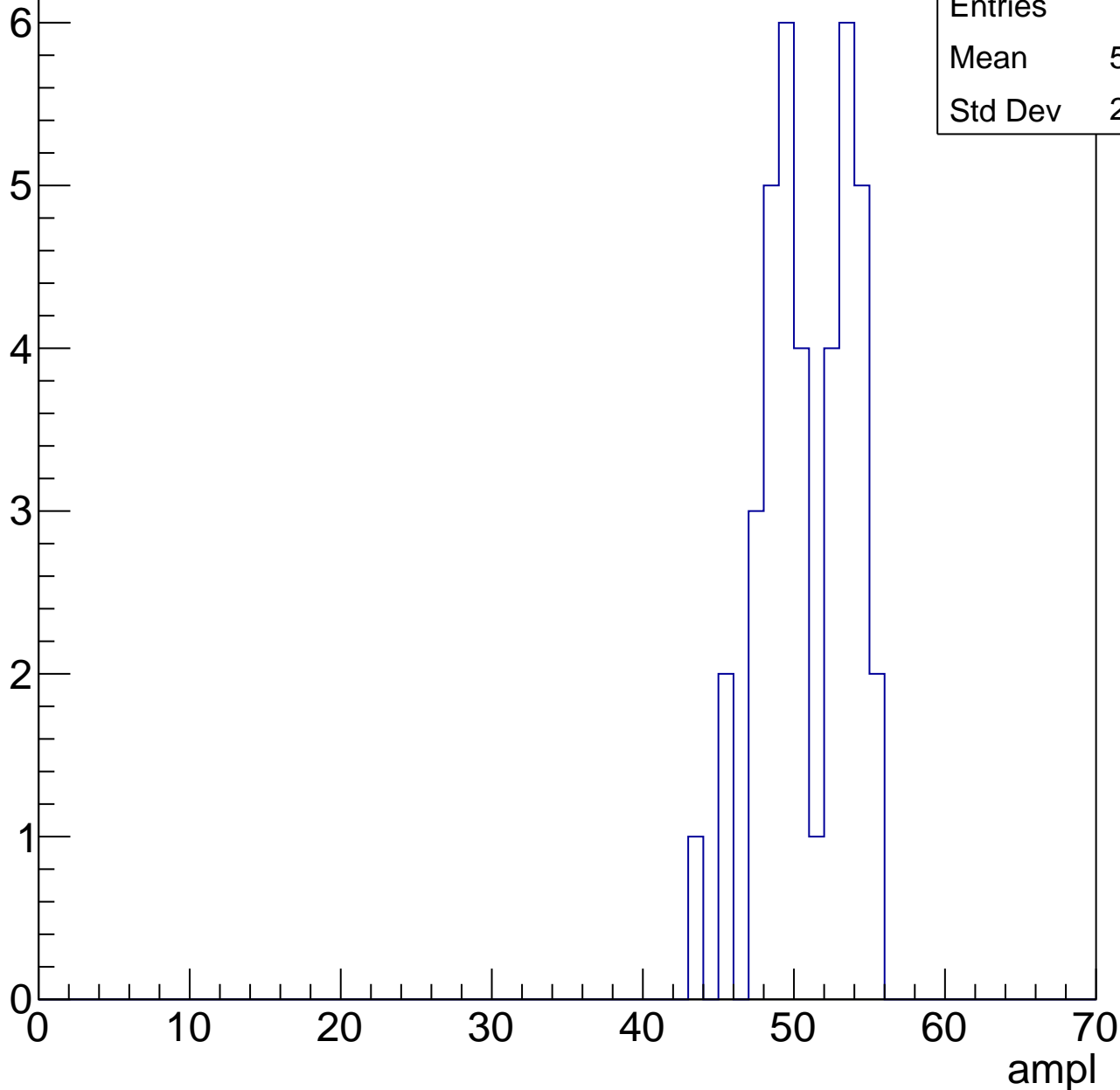


B1L101S, U15-ch37, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	50.38
Std Dev	2.984

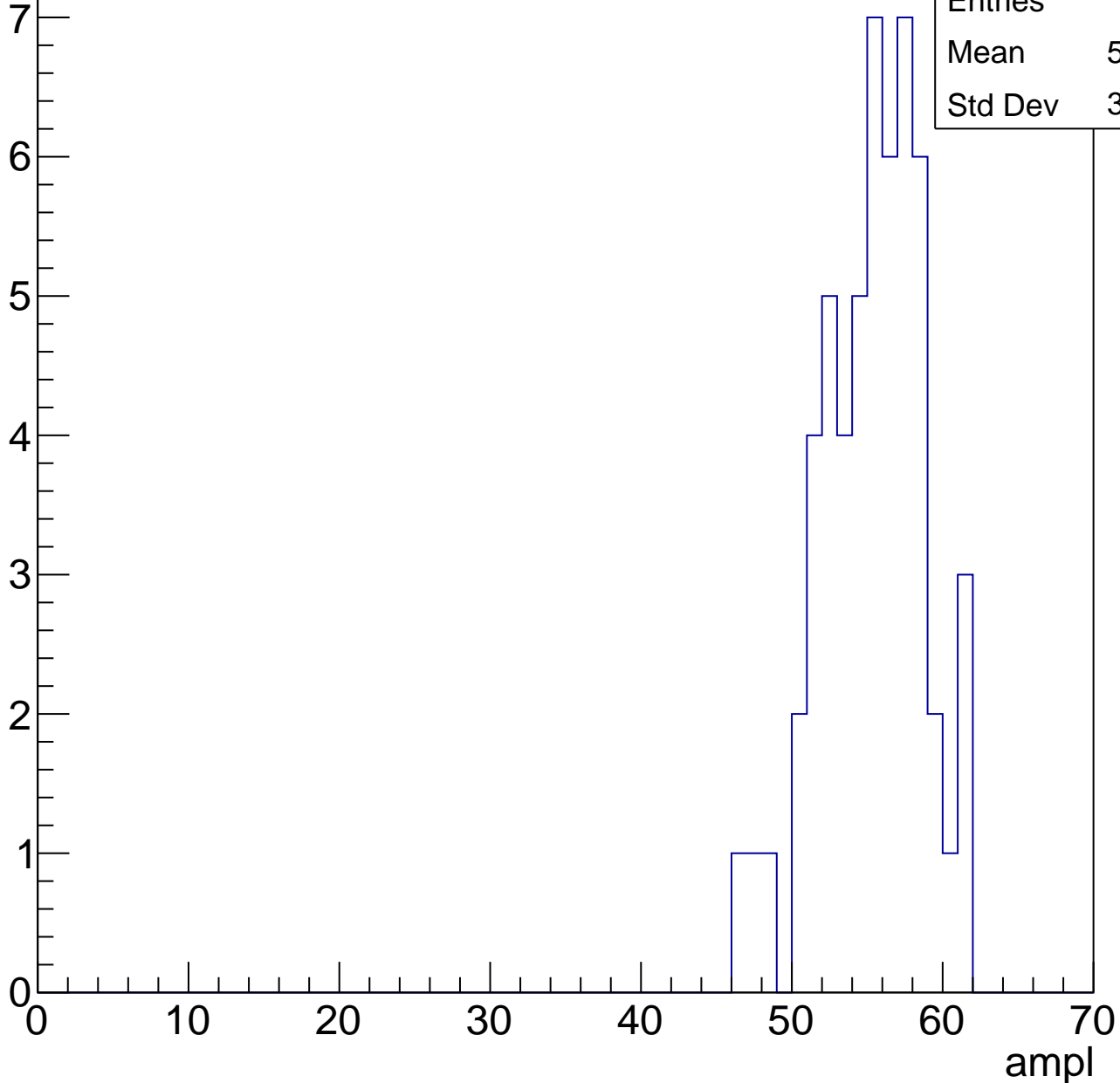


B1L101S, U15-ch37, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	54.84
Std Dev	3.372



B1L101S, U15-ch37, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

10

8

6

4

2

0

0

10

20

30

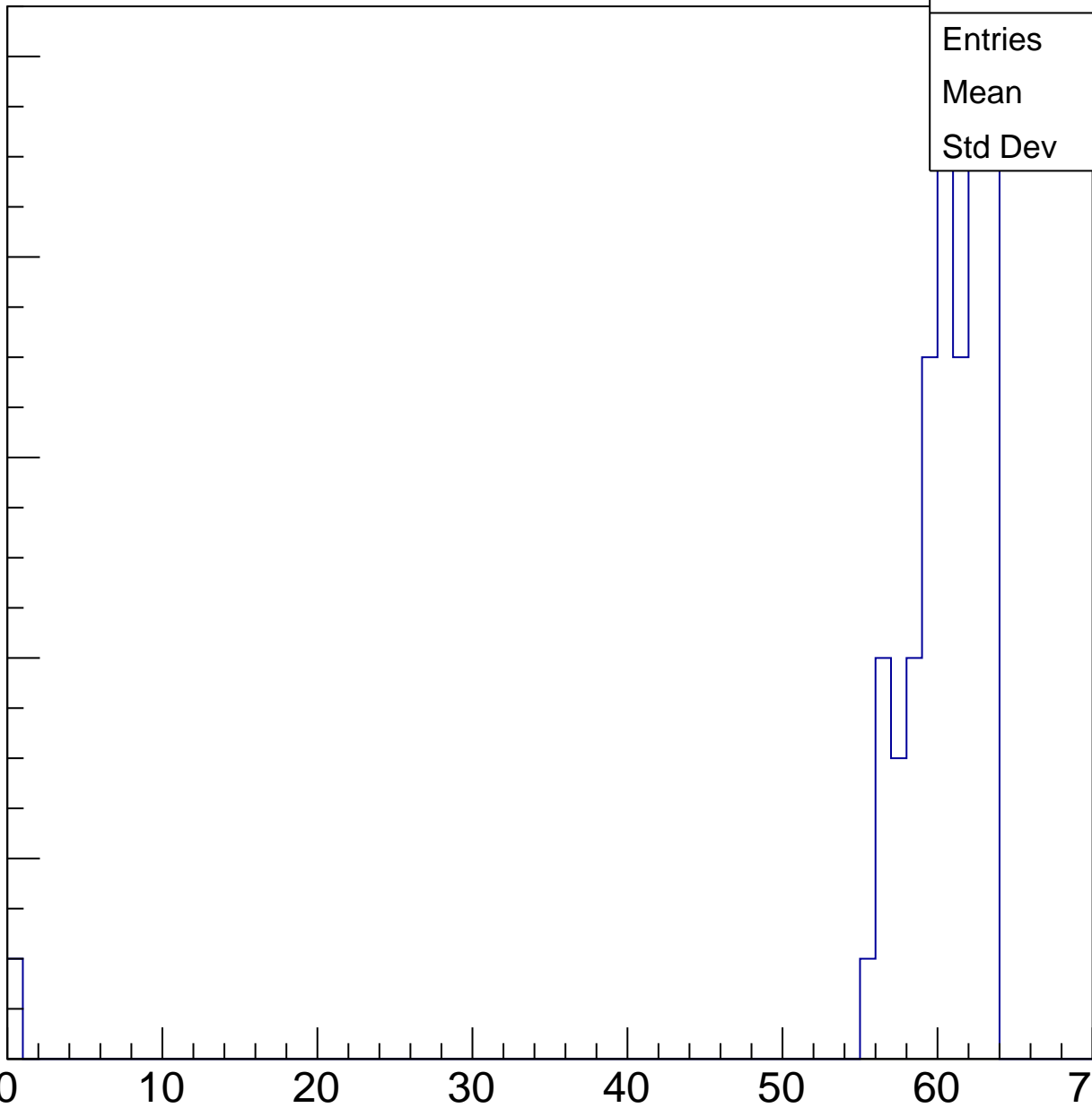
40

50

60

ampl

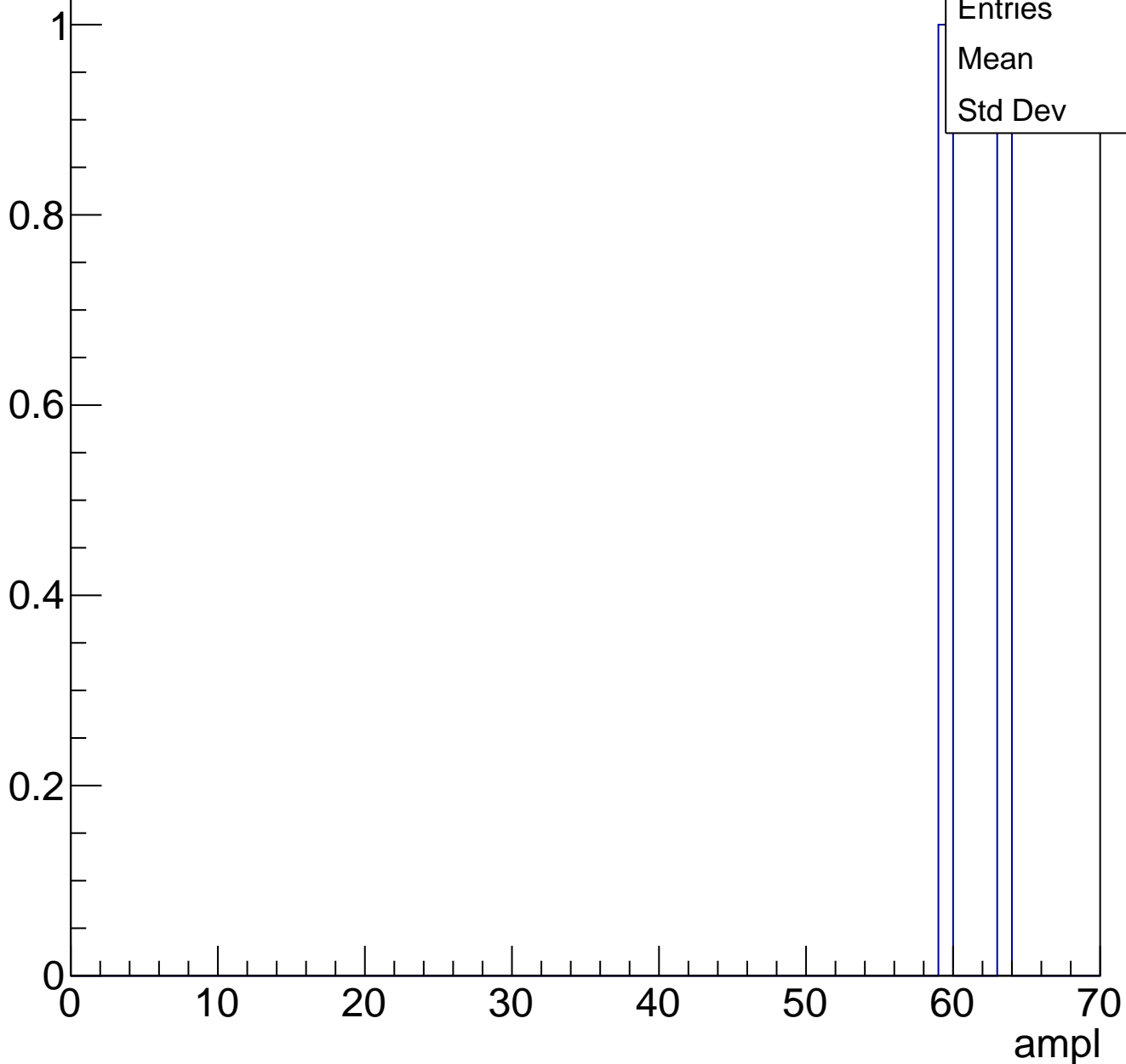
Entries	55
Mean	59.07
Std Dev	8.334



B1L101S, U15-ch37, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch37, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch38, adc0

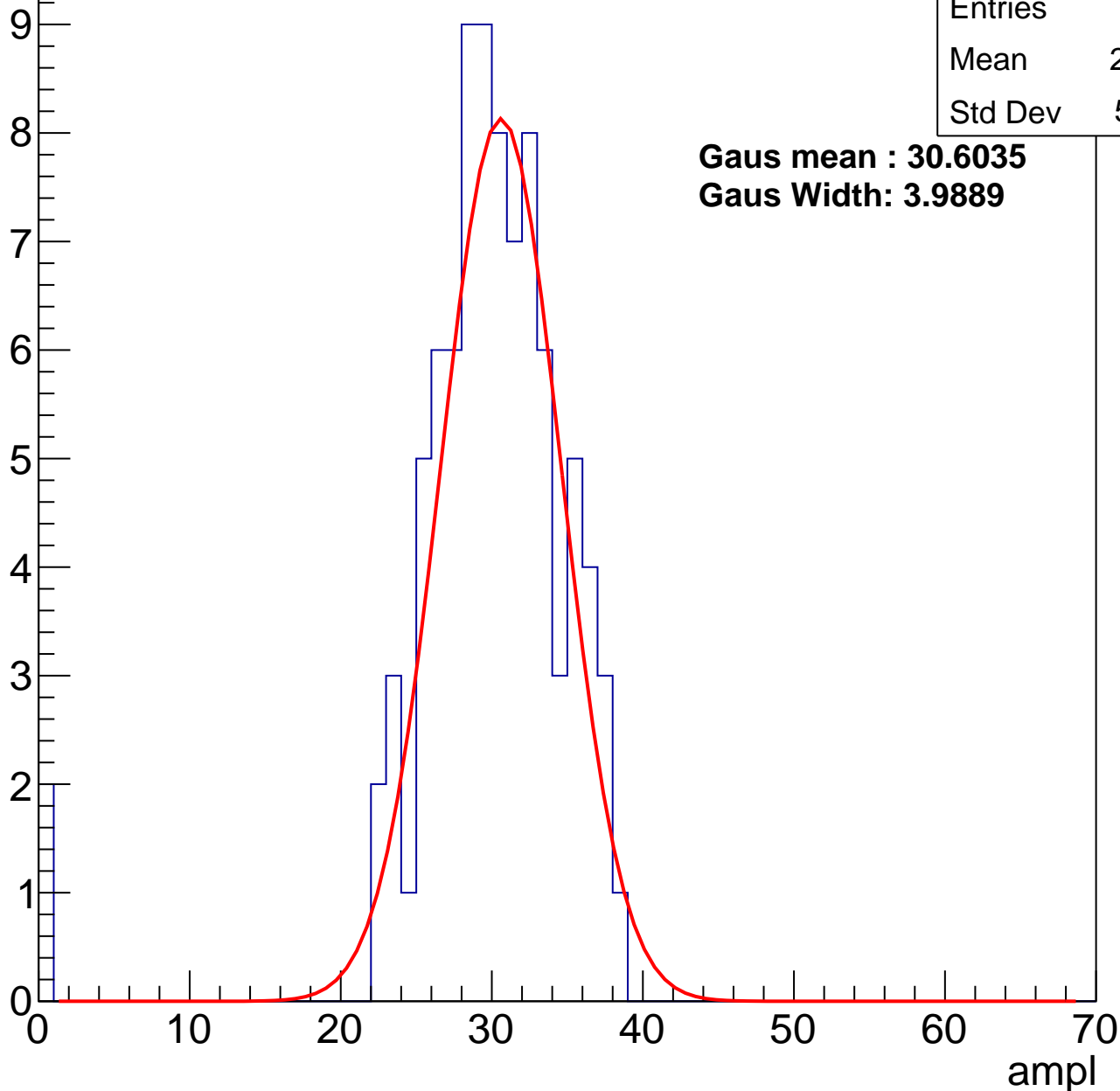
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	29.25
Std Dev	5.841

Gaus mean : 30.6035

Gaus Width: 3.9889



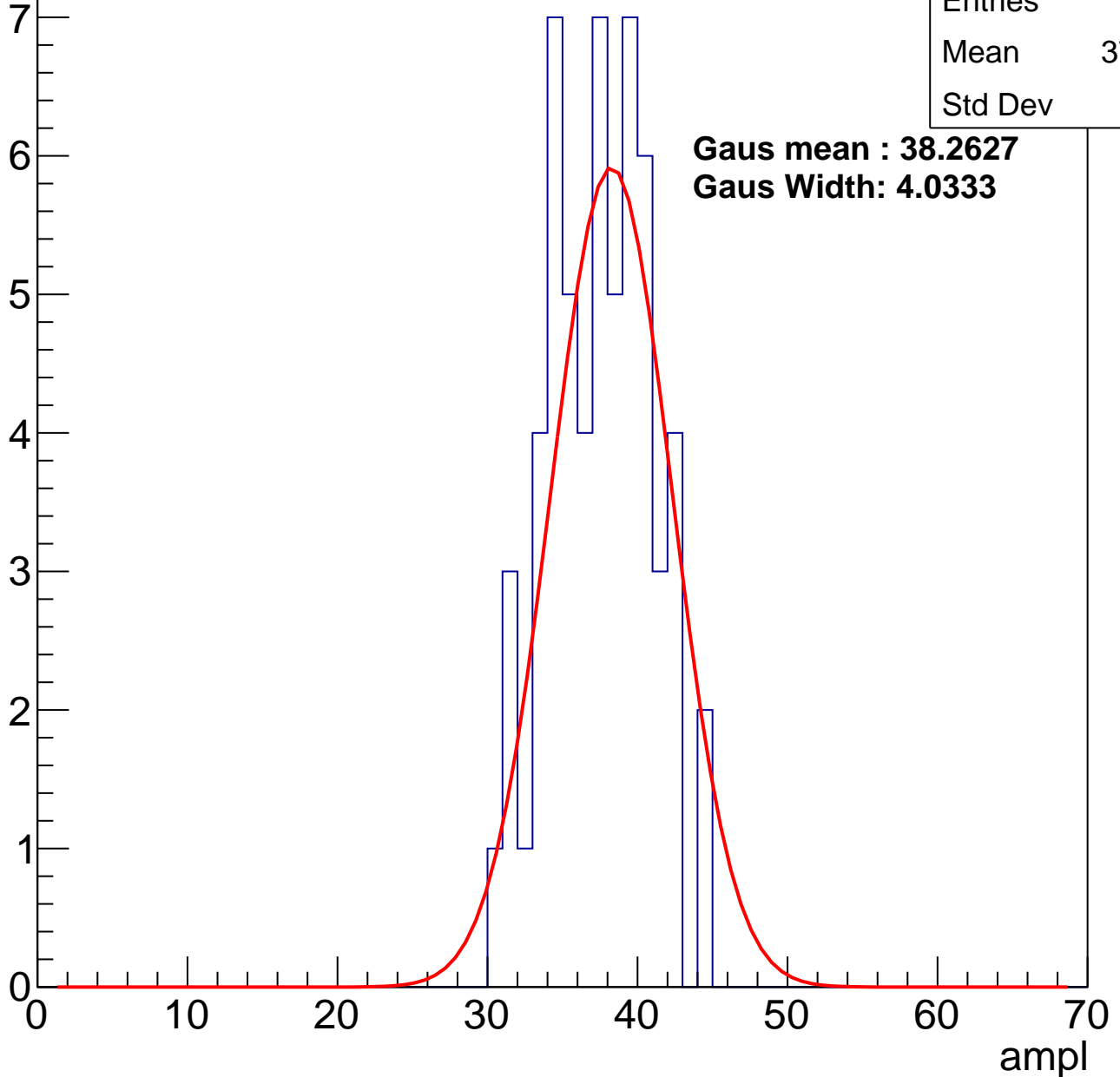
B1L101S, U15-ch38, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.03
Std Dev	3.36

Gaus mean : 38.2627
Gaus Width: 4.0333



B1L101S, U15-ch38, adc2

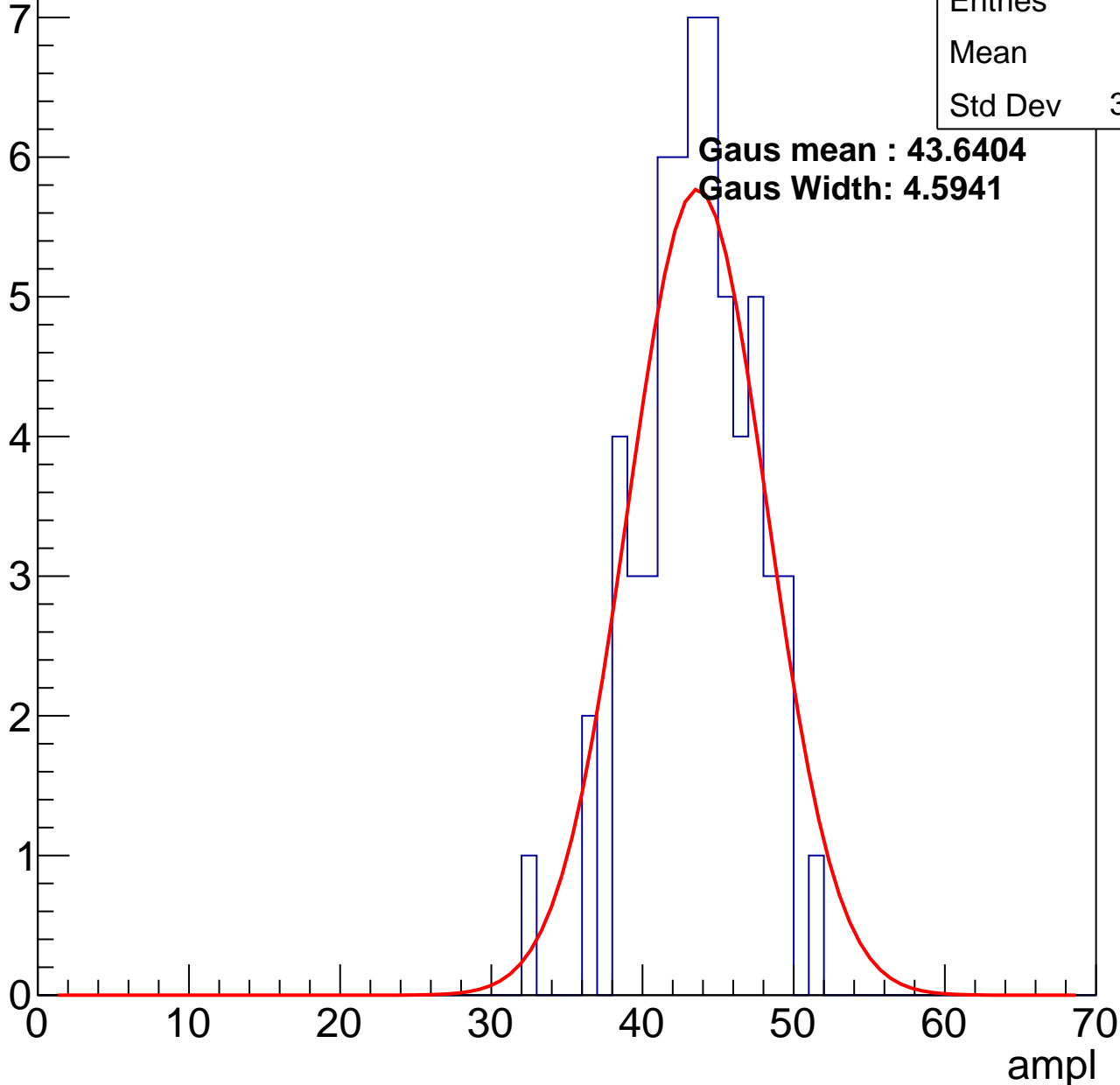
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	43.1
Std Dev	3.682

Gaus mean : 43.6404

Gaus Width: 4.5941

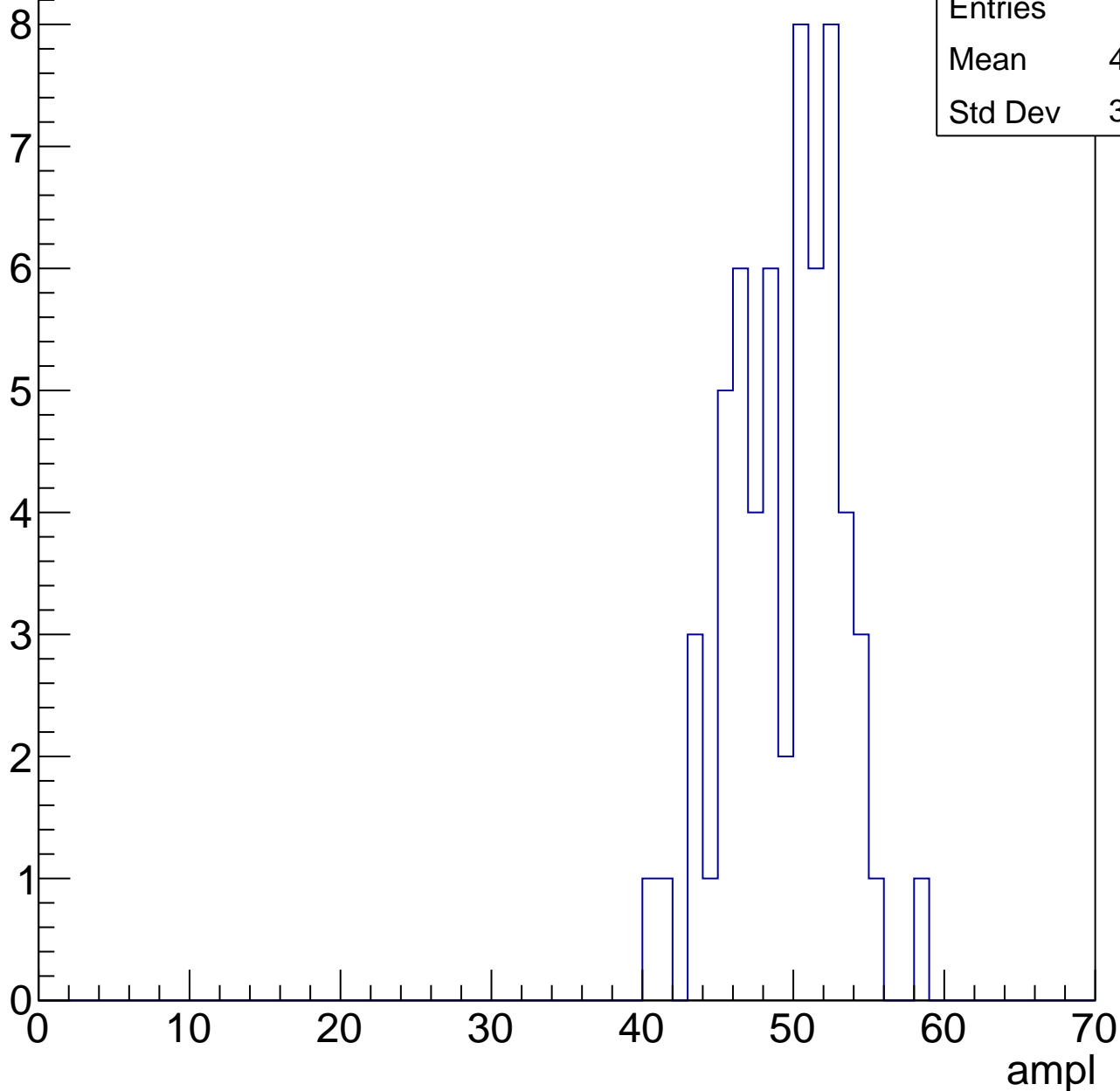


B1L101S, U15-ch38, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.97
Std Dev	3.638

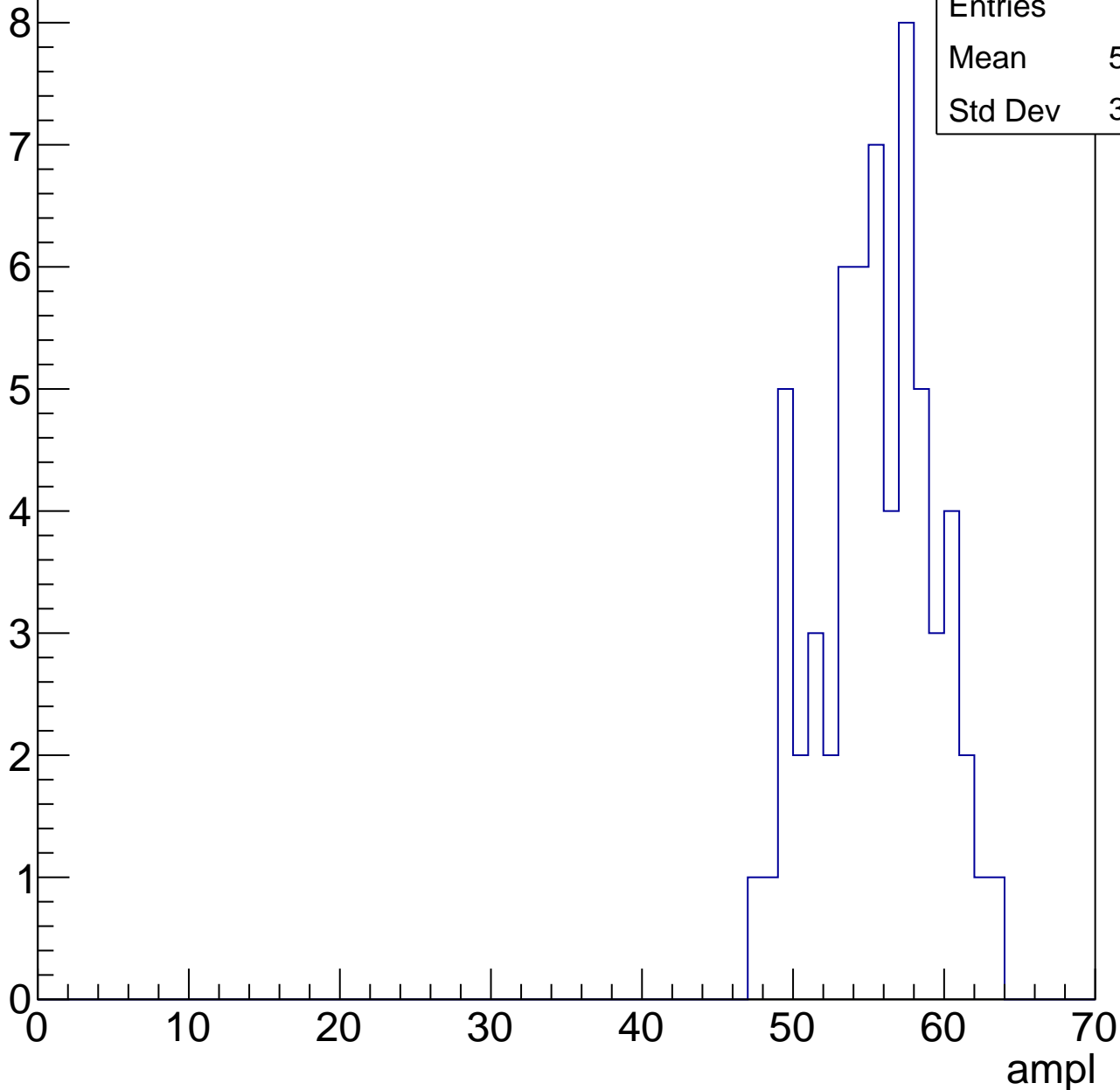


B1L101S, U15-ch38, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.05
Std Dev	3.739

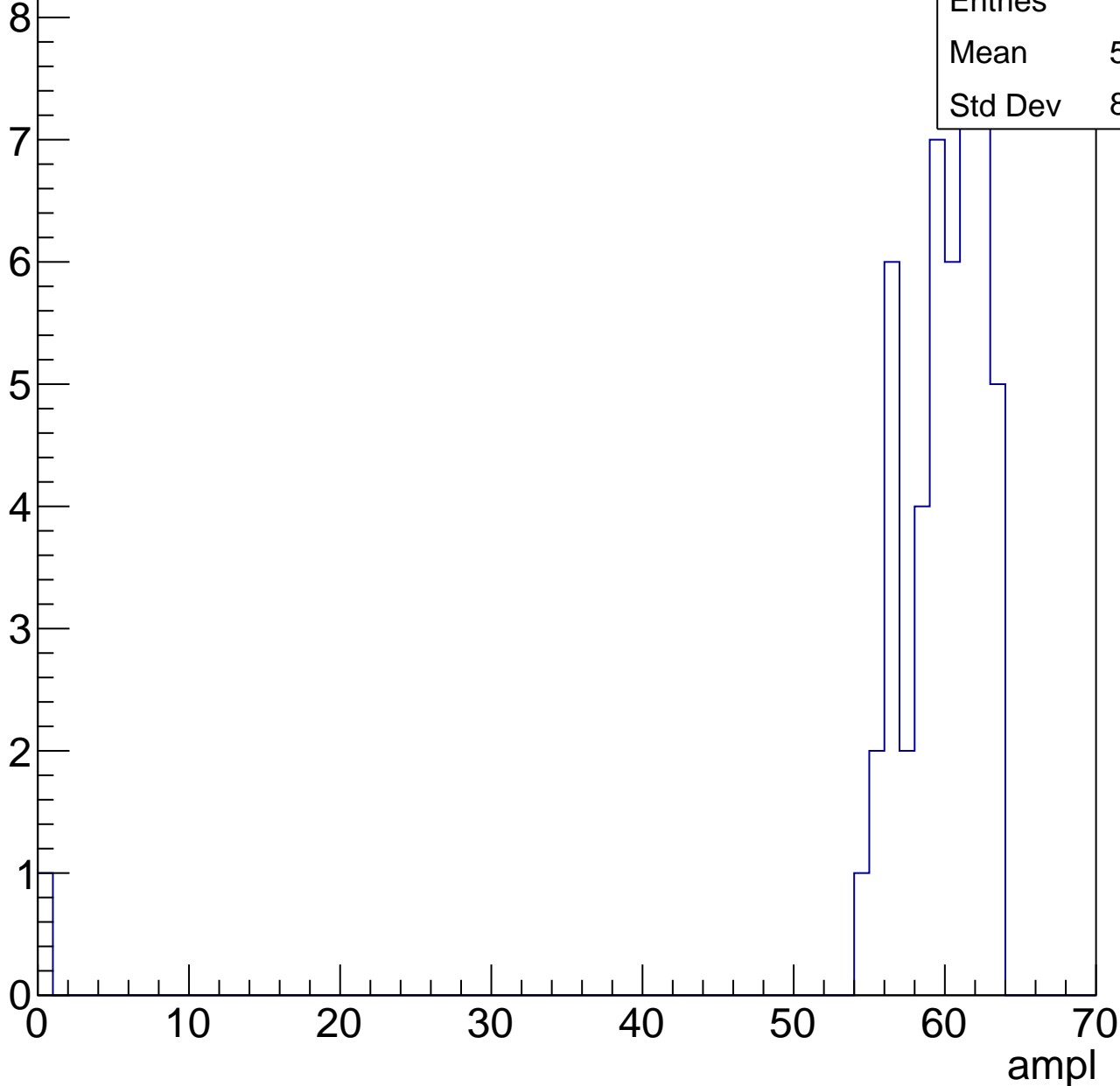


B1L101S, U15-ch38, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.36
Std Dev	8.687



B1L101S, U15-ch38, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch38, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch39, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	77
Mean	27.6
Std Dev	5.609

Gaus mean : 28.4572

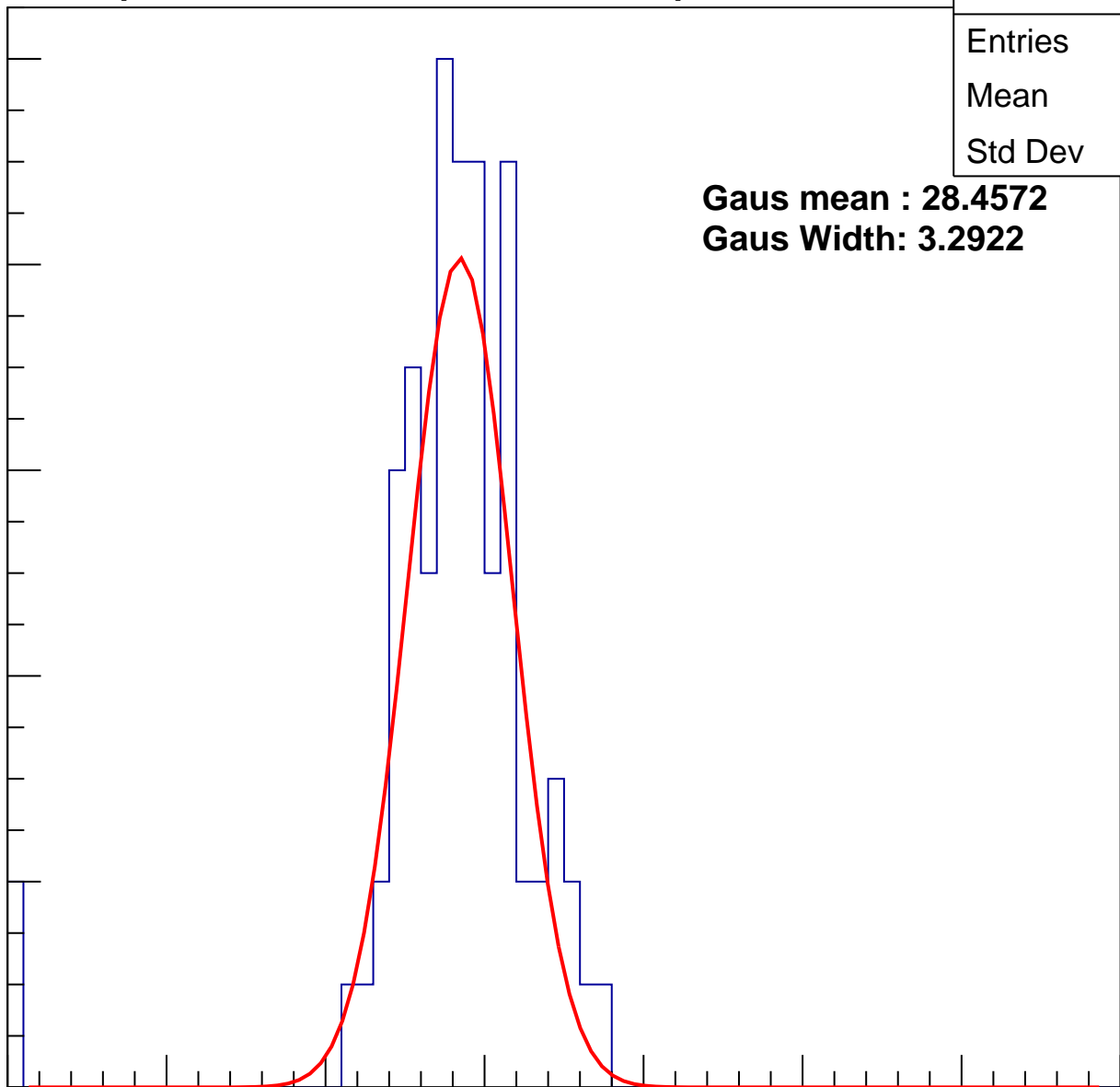
Gaus Width: 3.2922

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch39, adc1

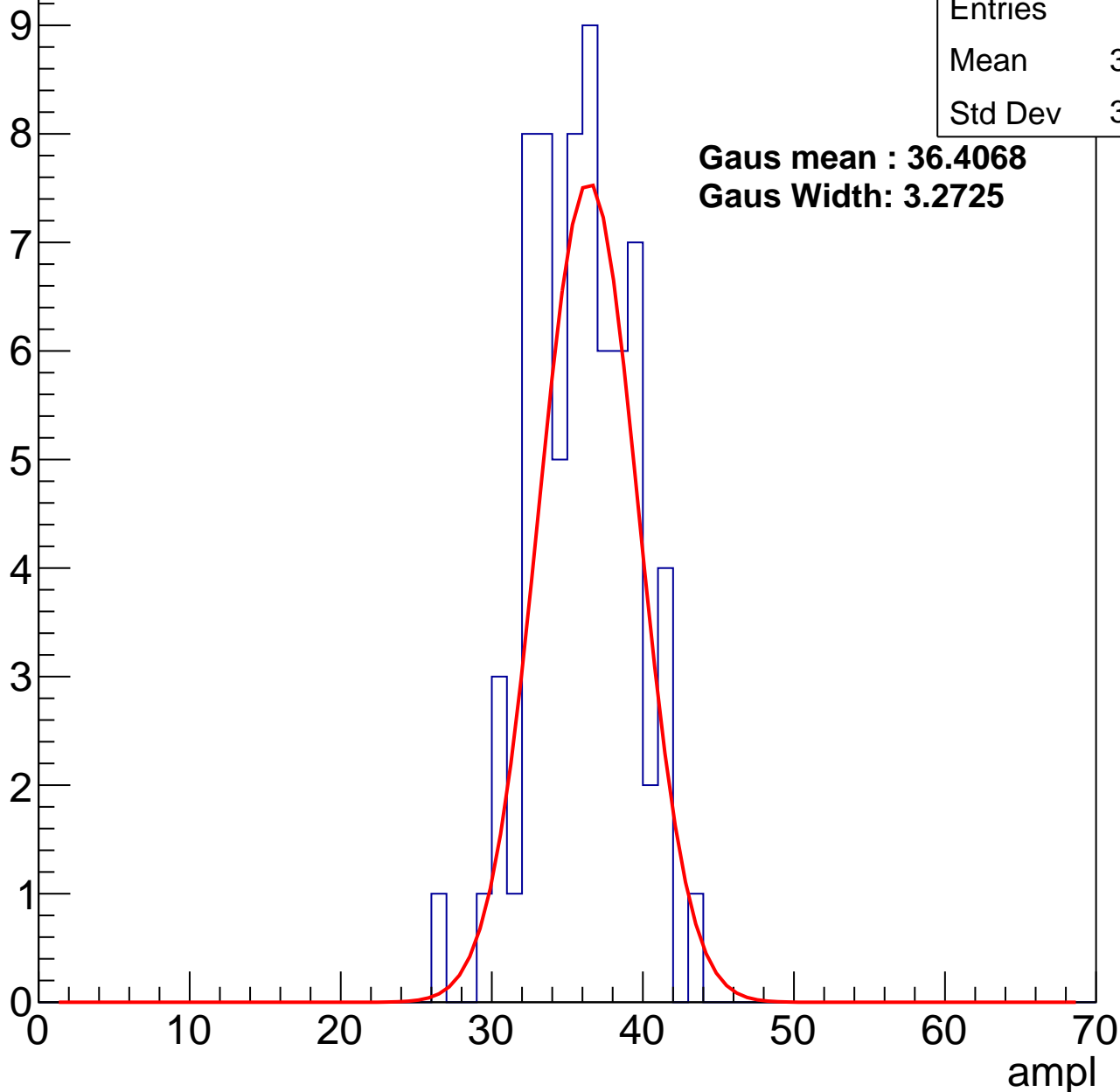
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.43
Std Dev	3.297

Gaus mean : 36.4068

Gaus Width: 3.2725



B1L101S, U15-ch39, adc2

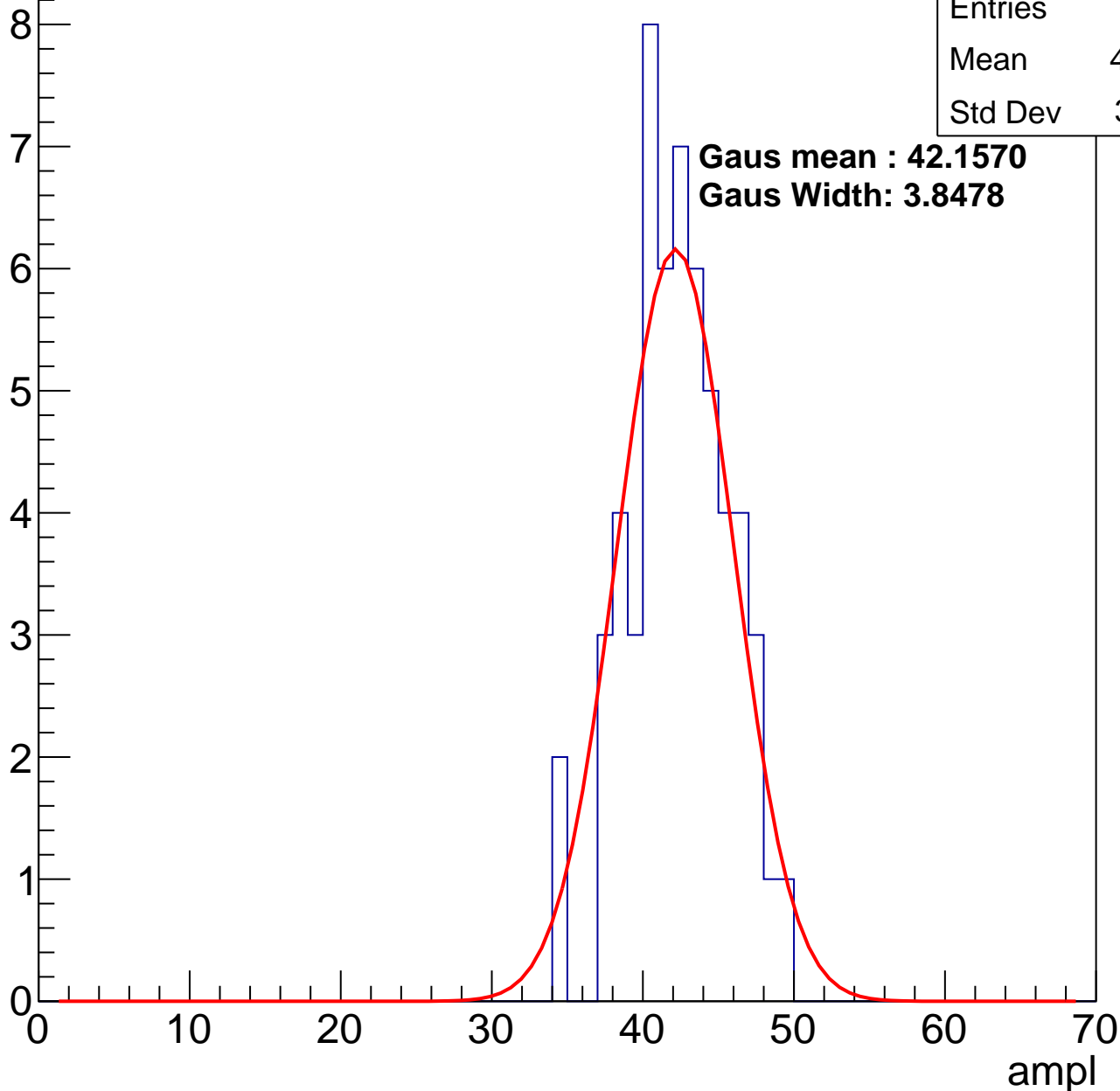
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.89
Std Dev	3.291

Gaus mean : 42.1570

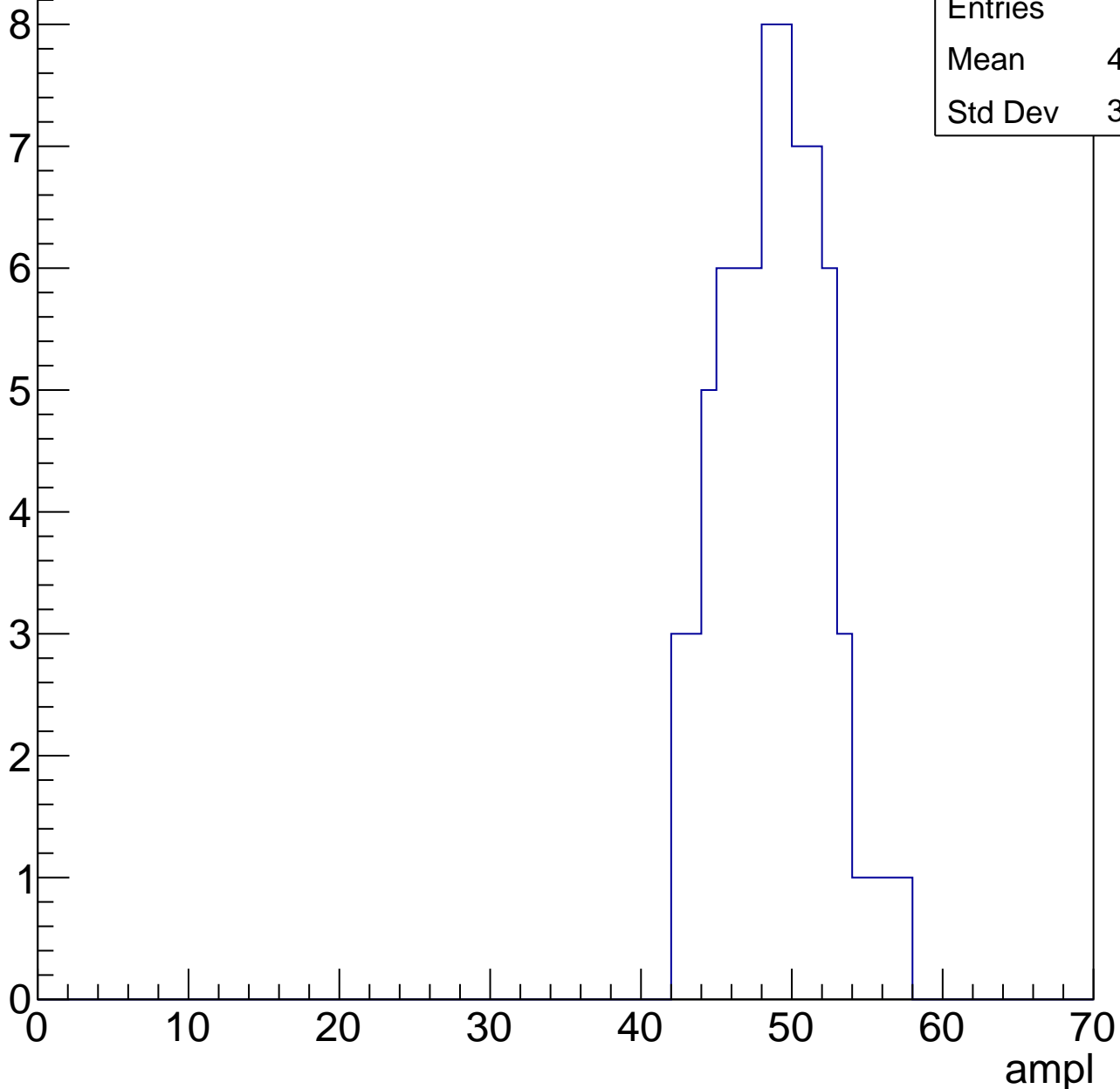
Gaus Width: 3.8478



B1L101S, U15-ch39, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



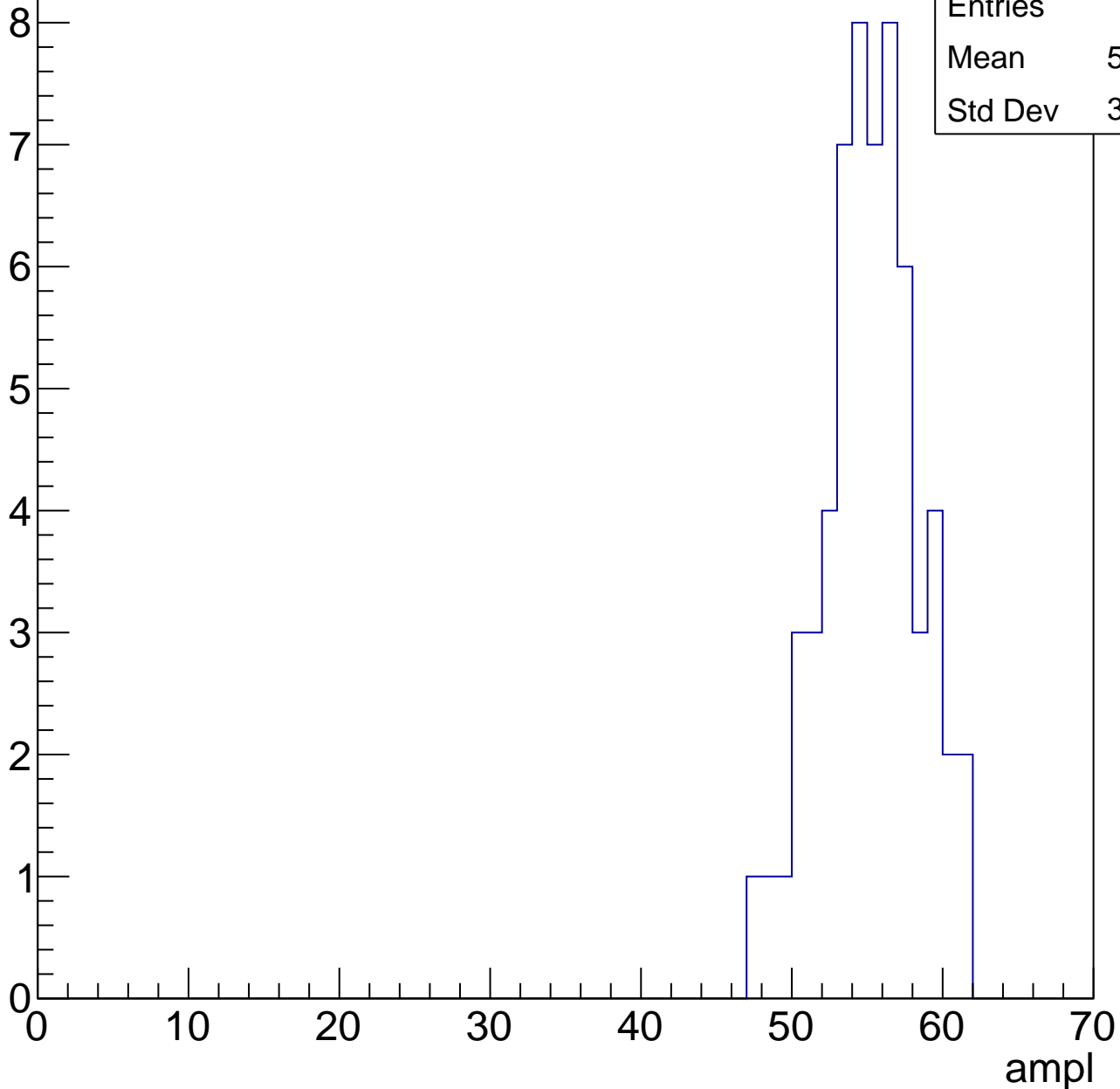
Entries	72
Mean	48.32
Std Dev	3.415

B1L101S, U15-ch39, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

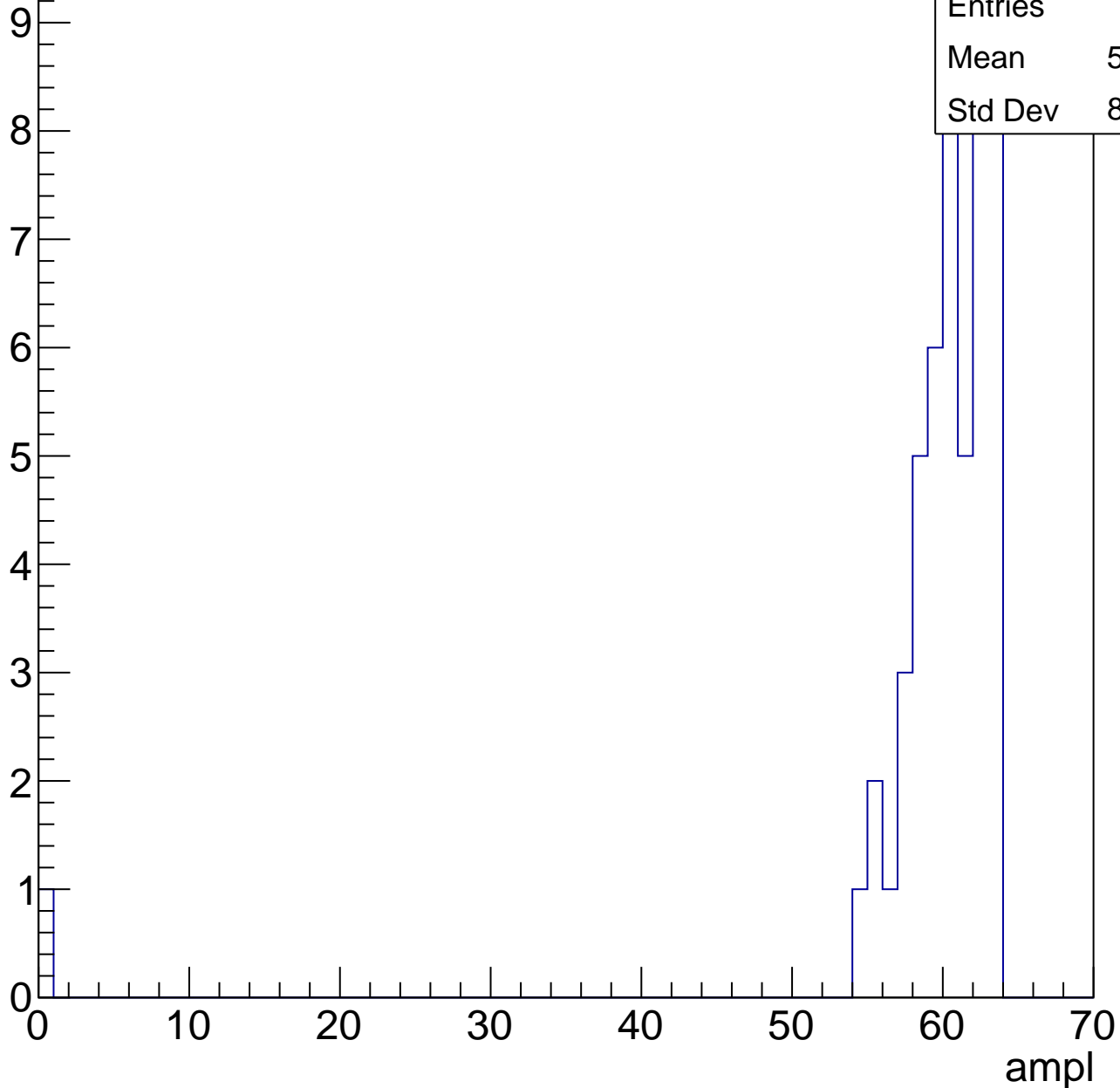
Entries	60
Mean	54.75
Std Dev	3.123



B1L101S, U15-ch39, adc5

calib_packv5_042523_0143.root, FC#0, port D2

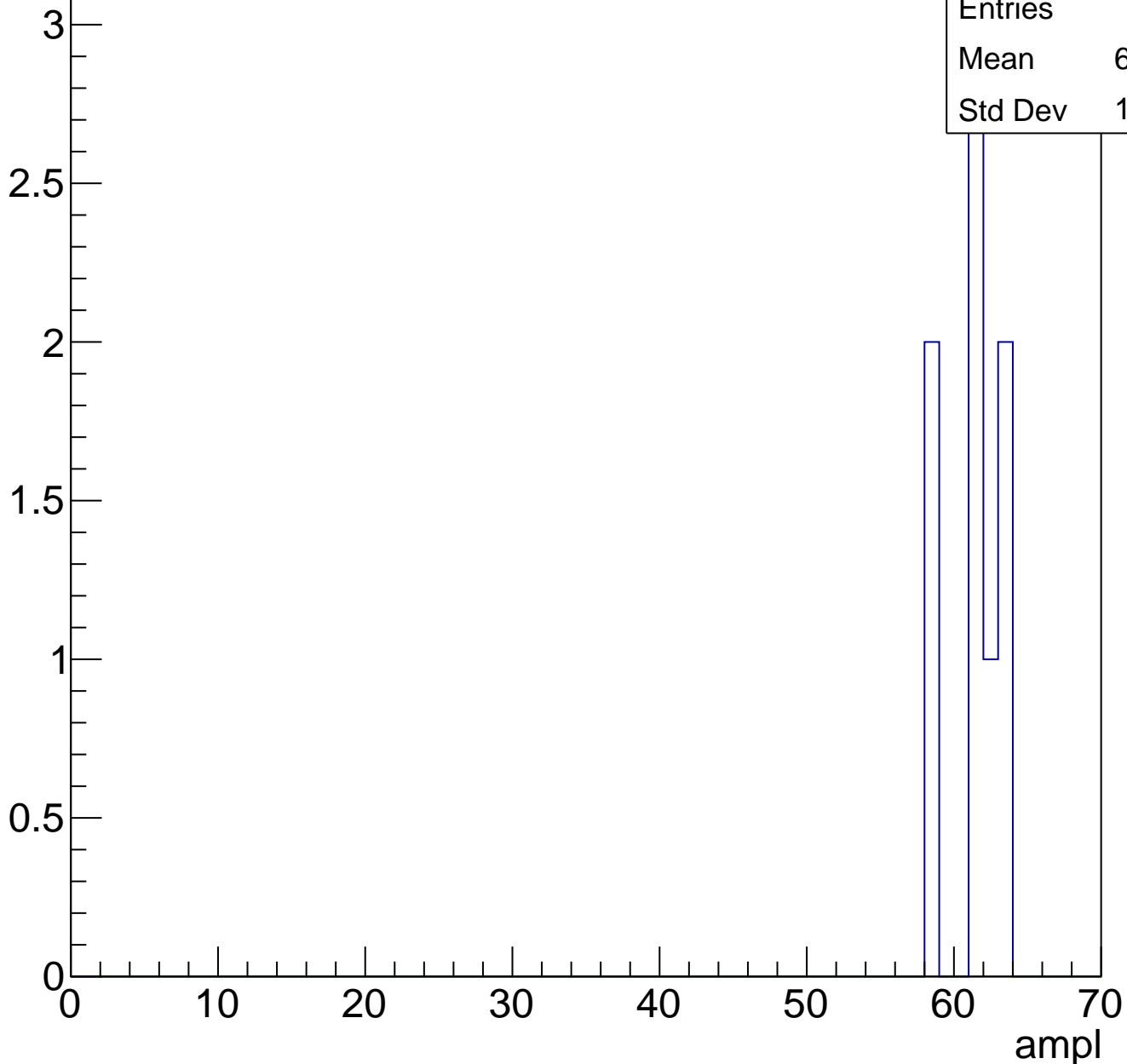
Entry



B1L101S, U15-ch39, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	8
Mean	60.88
Std Dev	1.833

B1L101S, U15-ch39, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch40, adc0

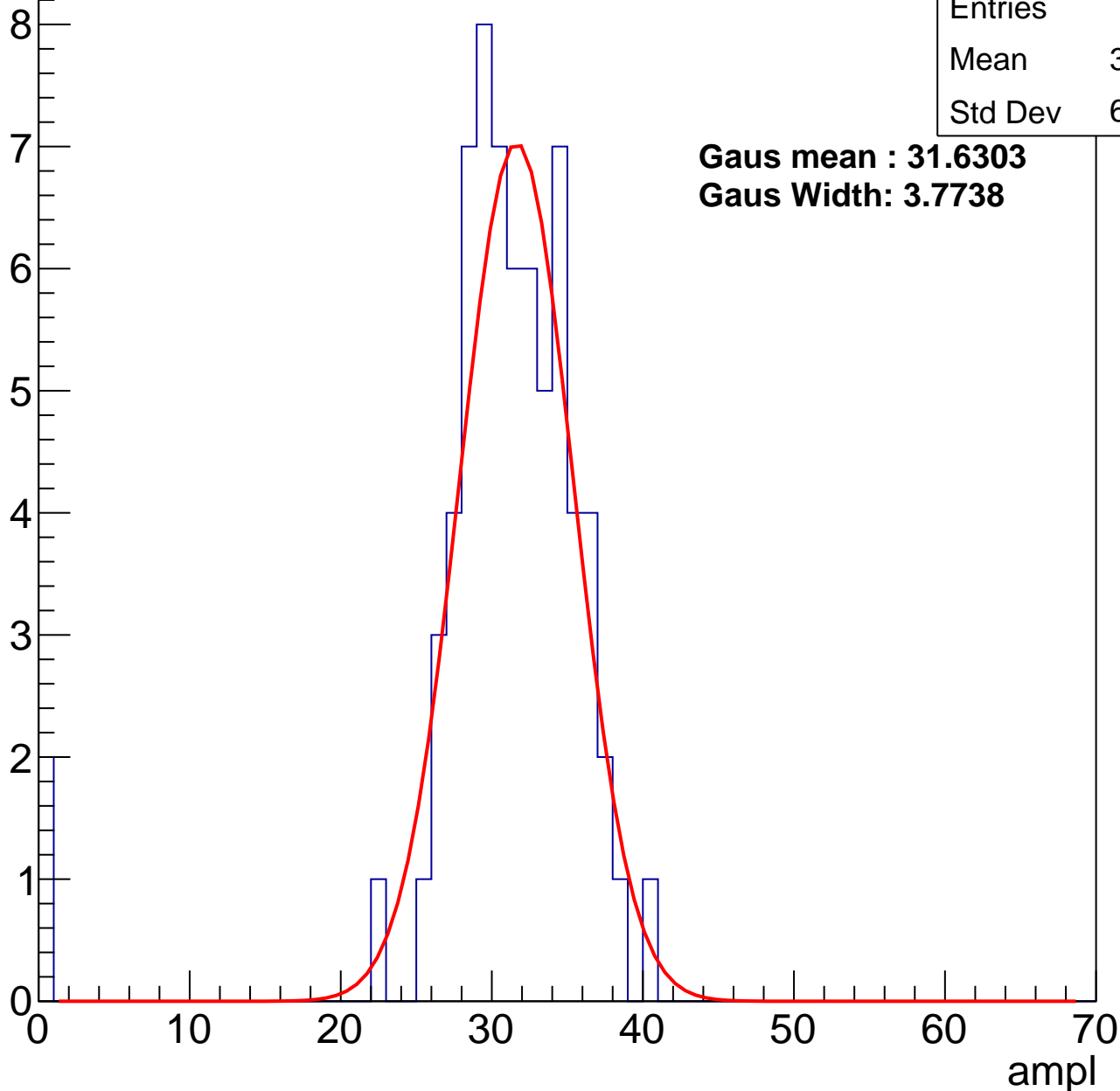
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.26
Std Dev	6.252

Gaus mean : 31.6303

Gaus Width: 3.7738



B1L101S, U15-ch40, adc1

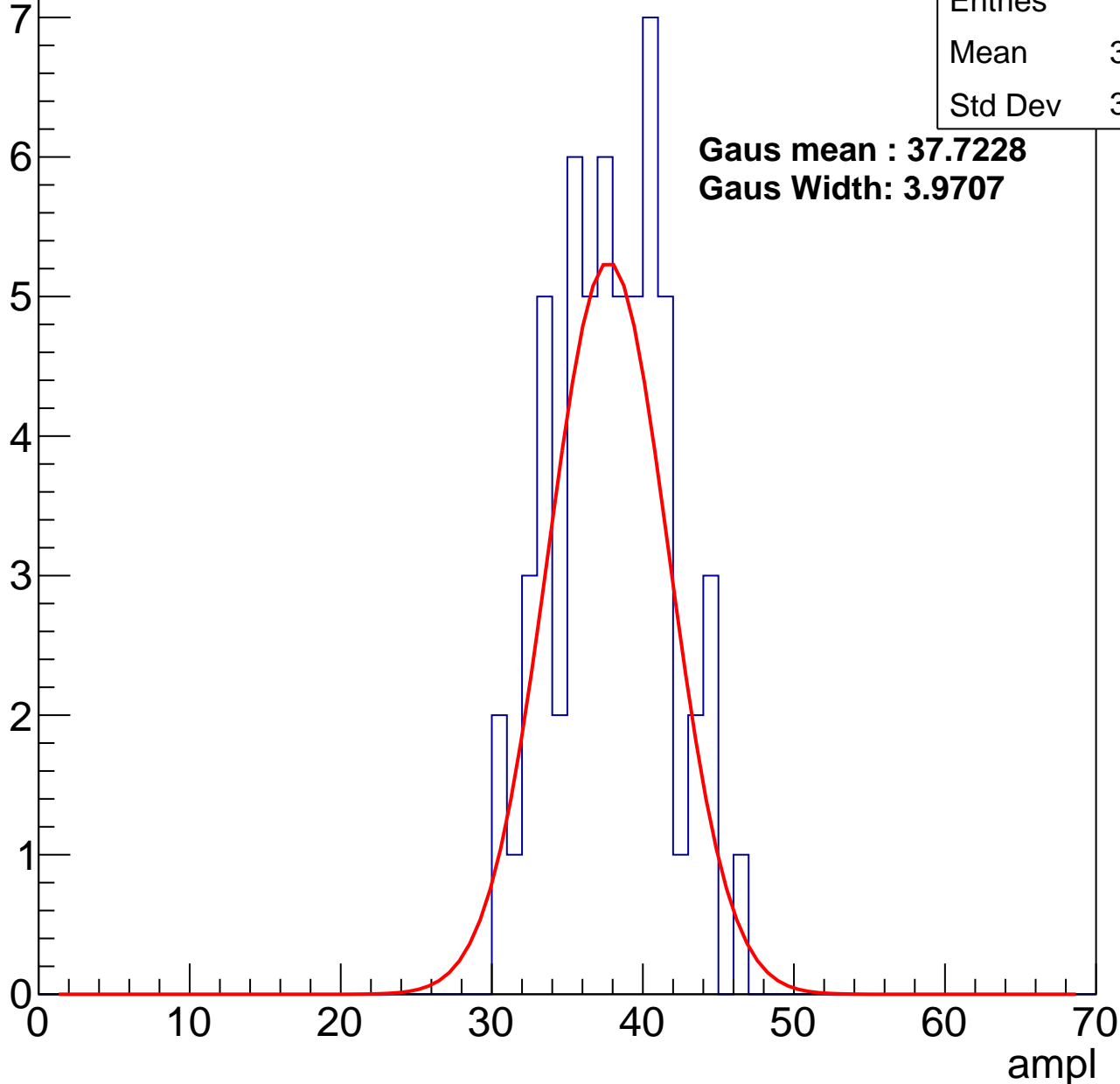
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	37.42
Std Dev	3.724

Gaus mean : 37.7228

Gaus Width: 3.9707



B1L101S, U15-ch40, adc2

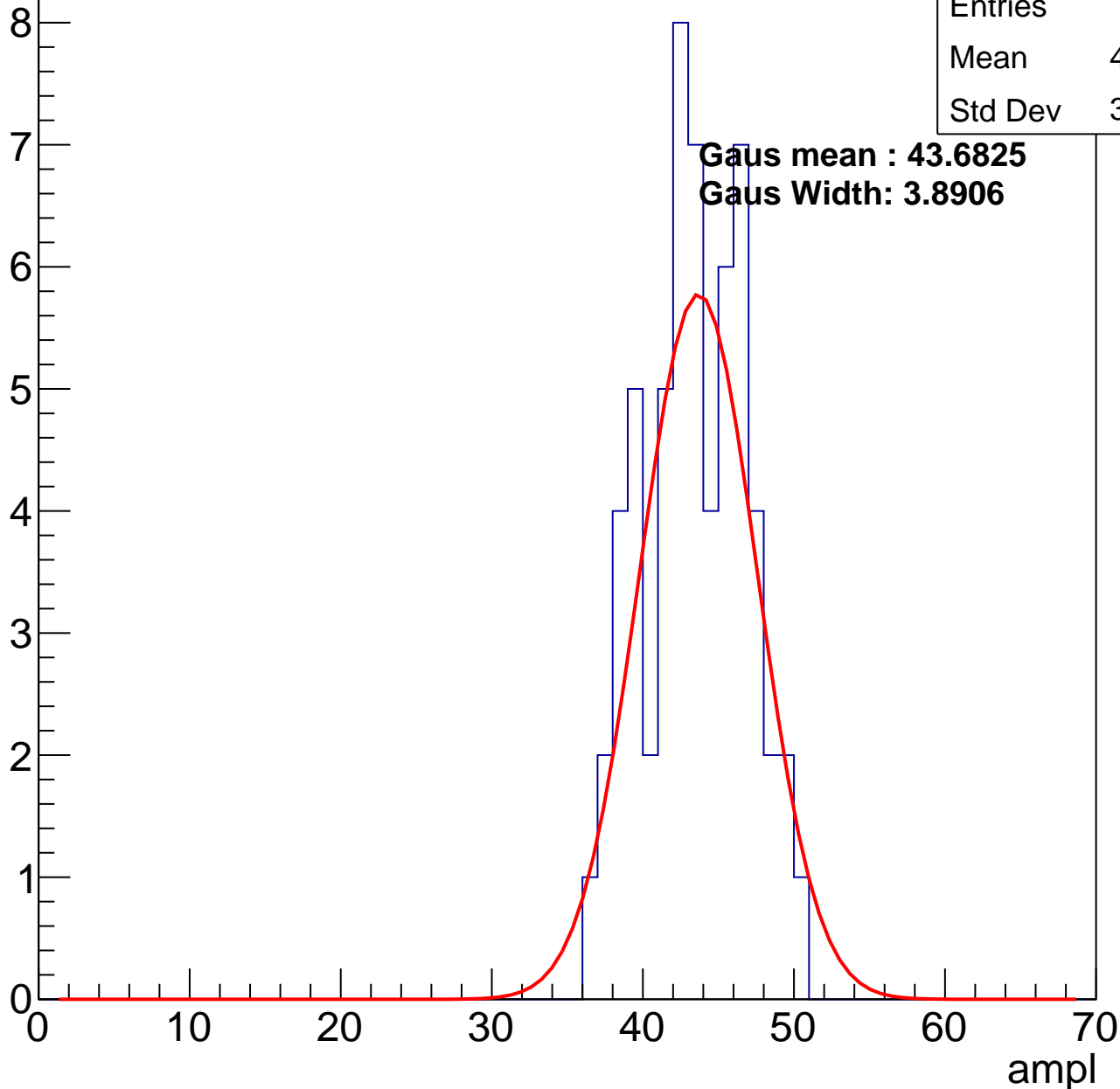
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	42.98
Std Dev	3.349

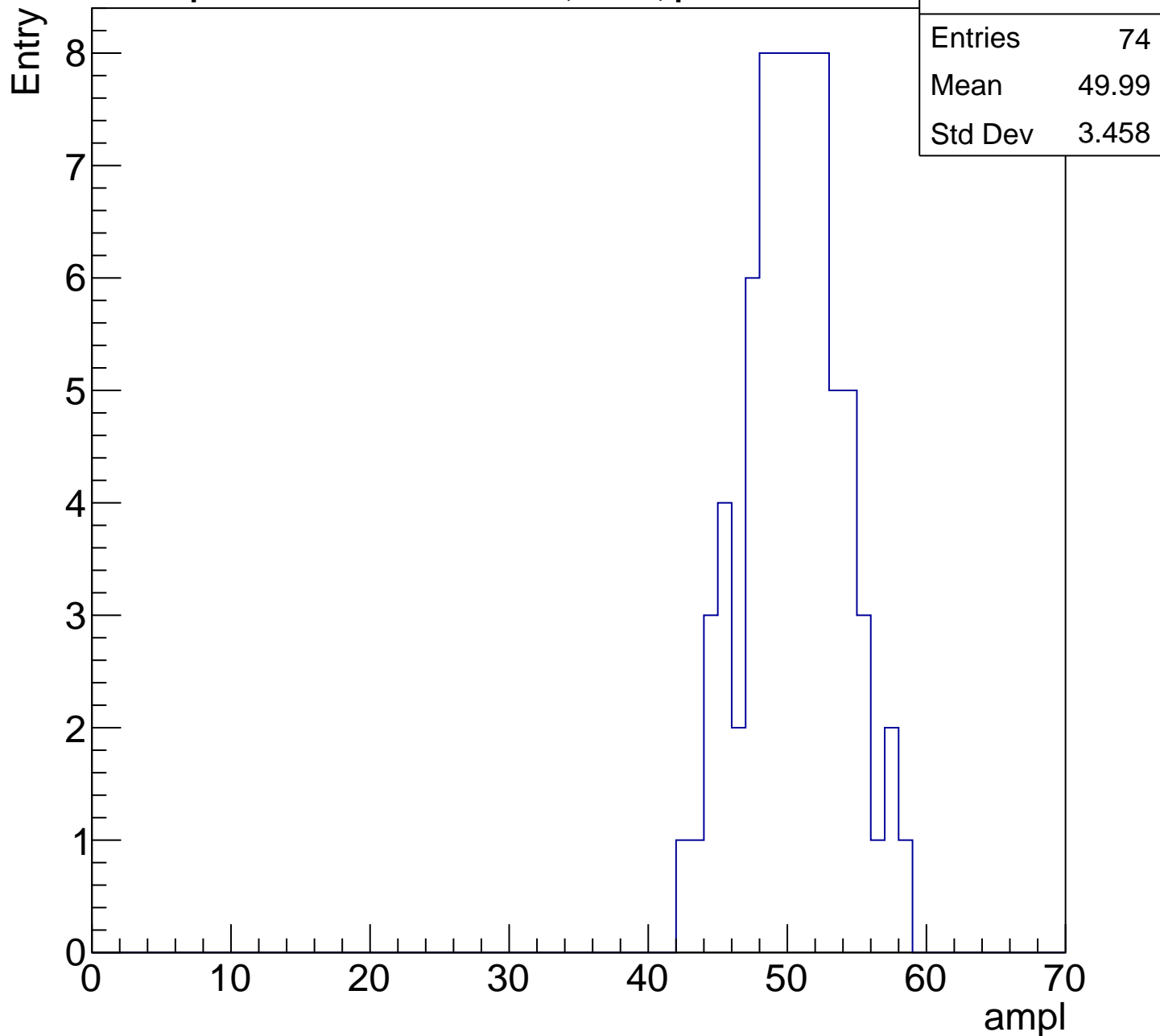
Gaus mean : 43.6825

Gaus Width: 3.8906



B1L101S, U15-ch40, adc3

calib_packv5_042523_0143.root, FC#0, port D2

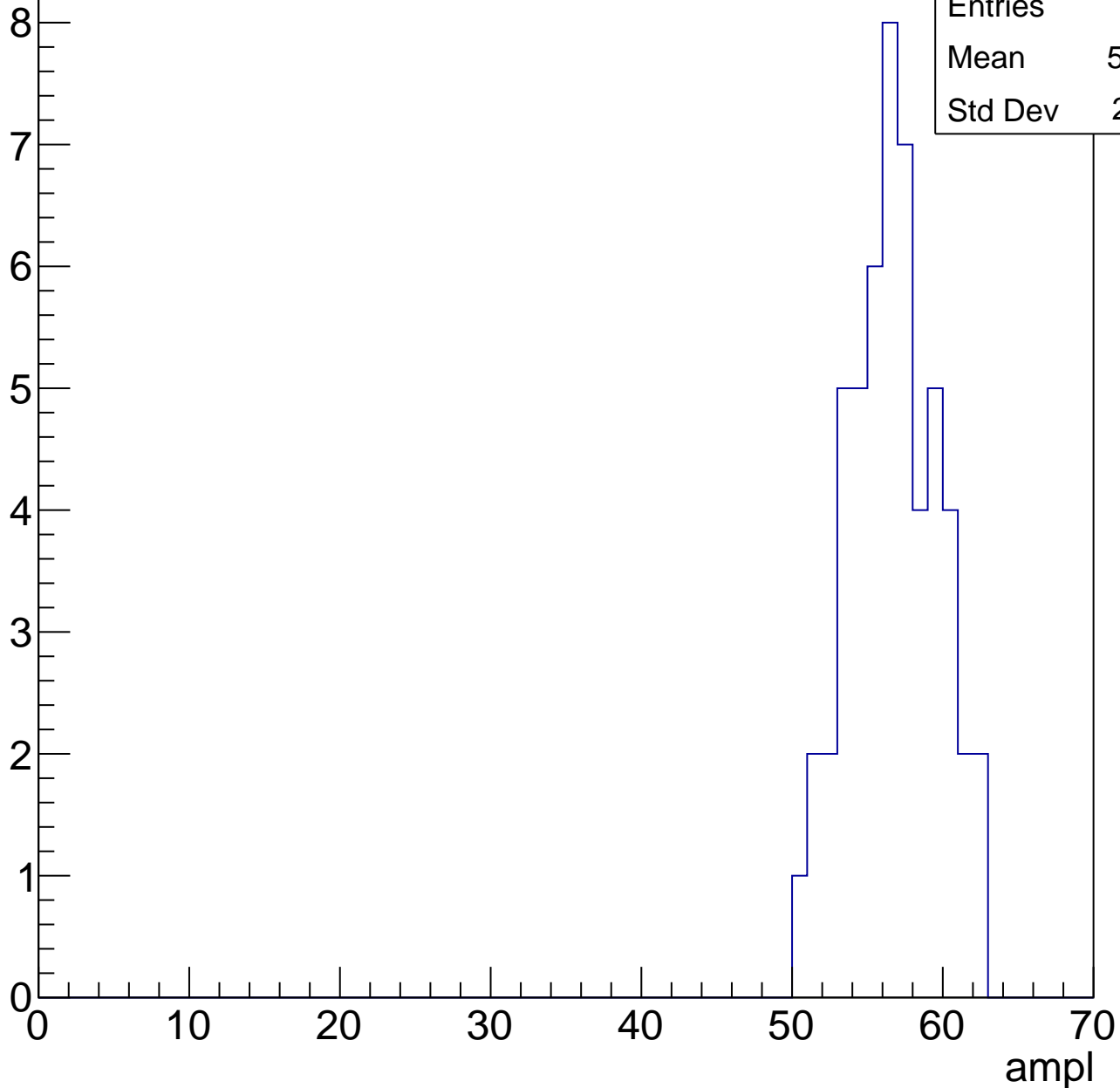


B1L101S, U15-ch40, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

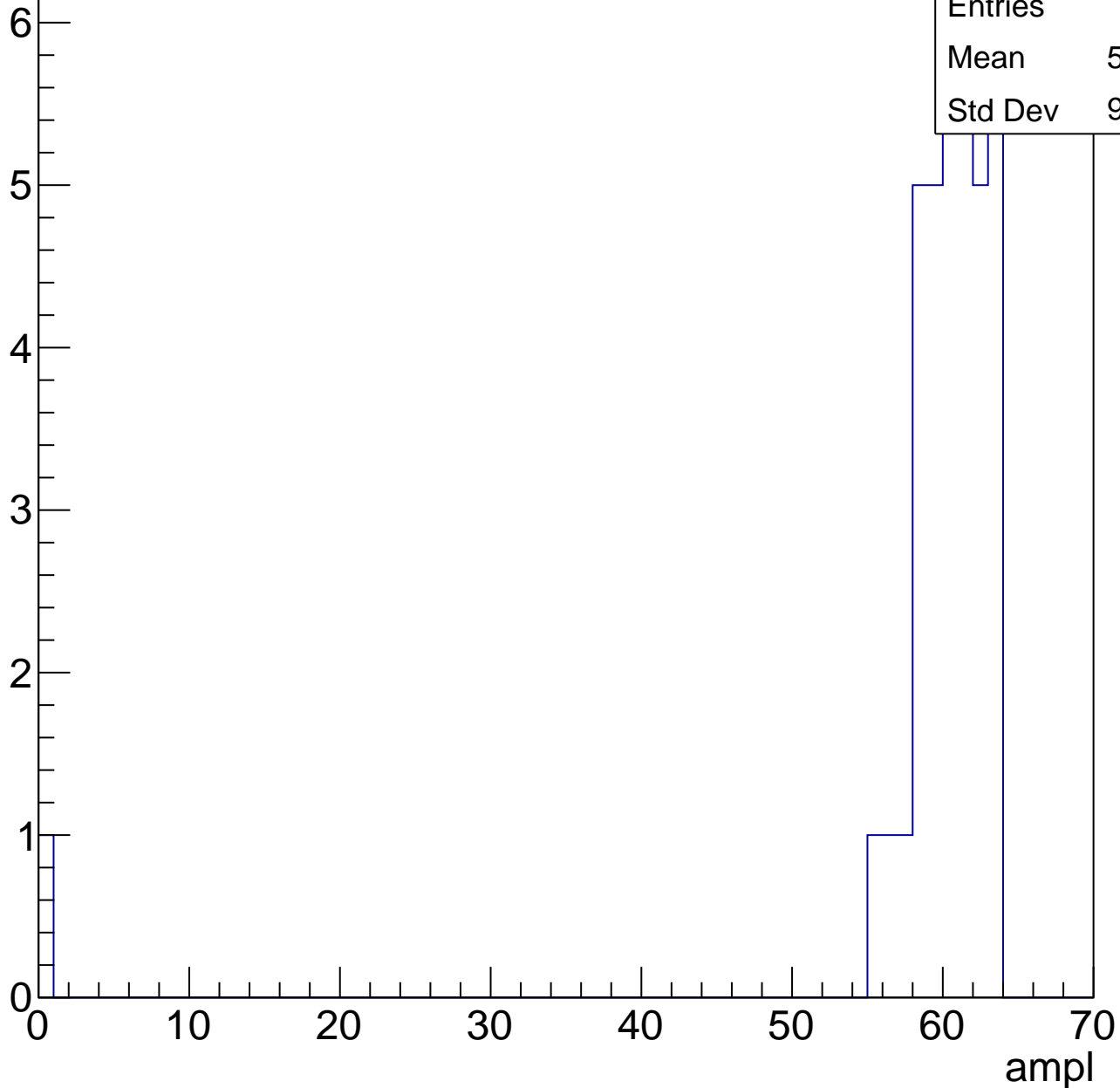
Entries	53
Mean	56.25
Std Dev	2.881



B1L101S, U15-ch40, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch40, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch40, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L101S, U15-ch41, adc0

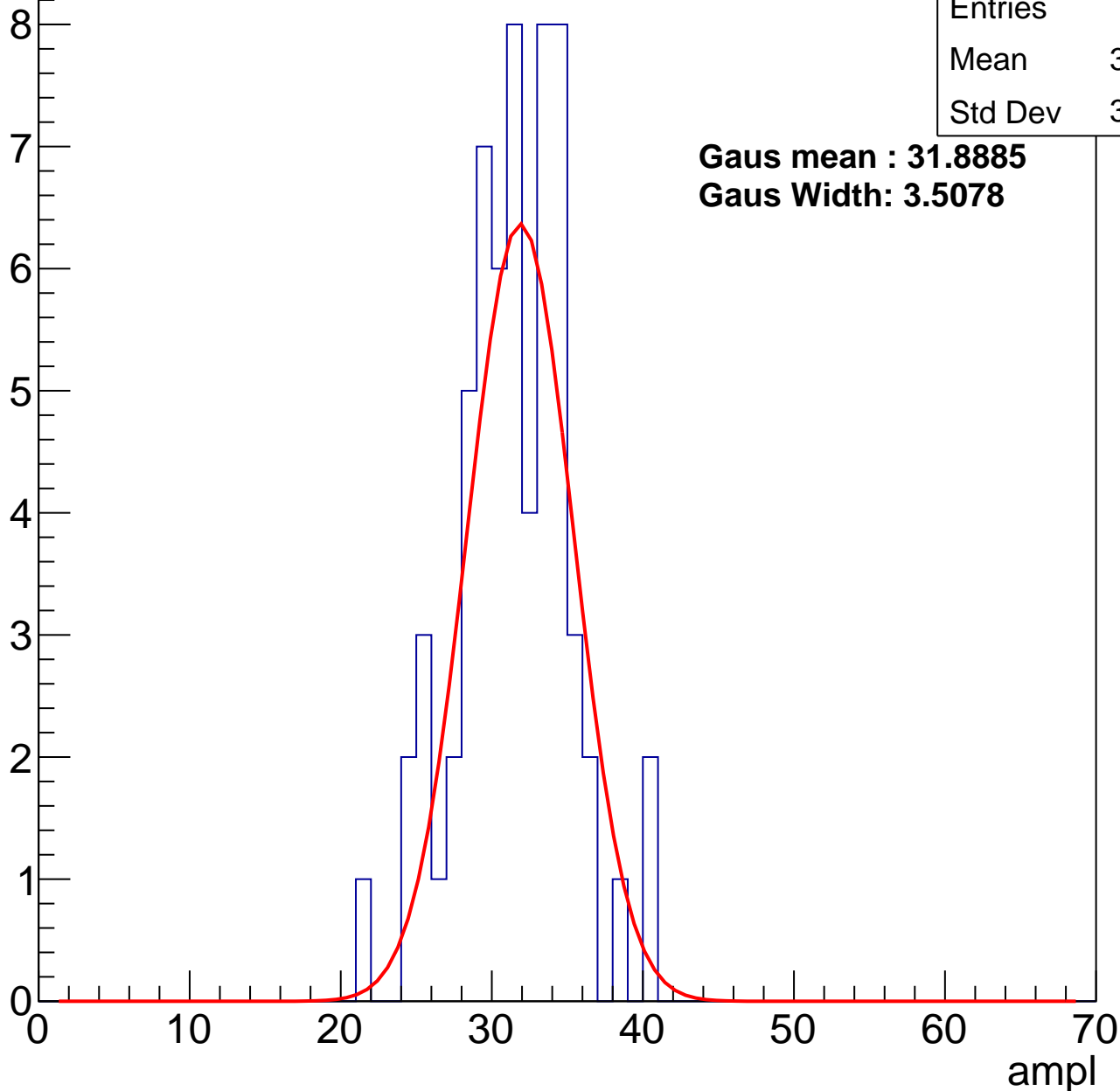
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	31.02
Std Dev	3.693

Gaus mean : 31.8885

Gaus Width: 3.5078



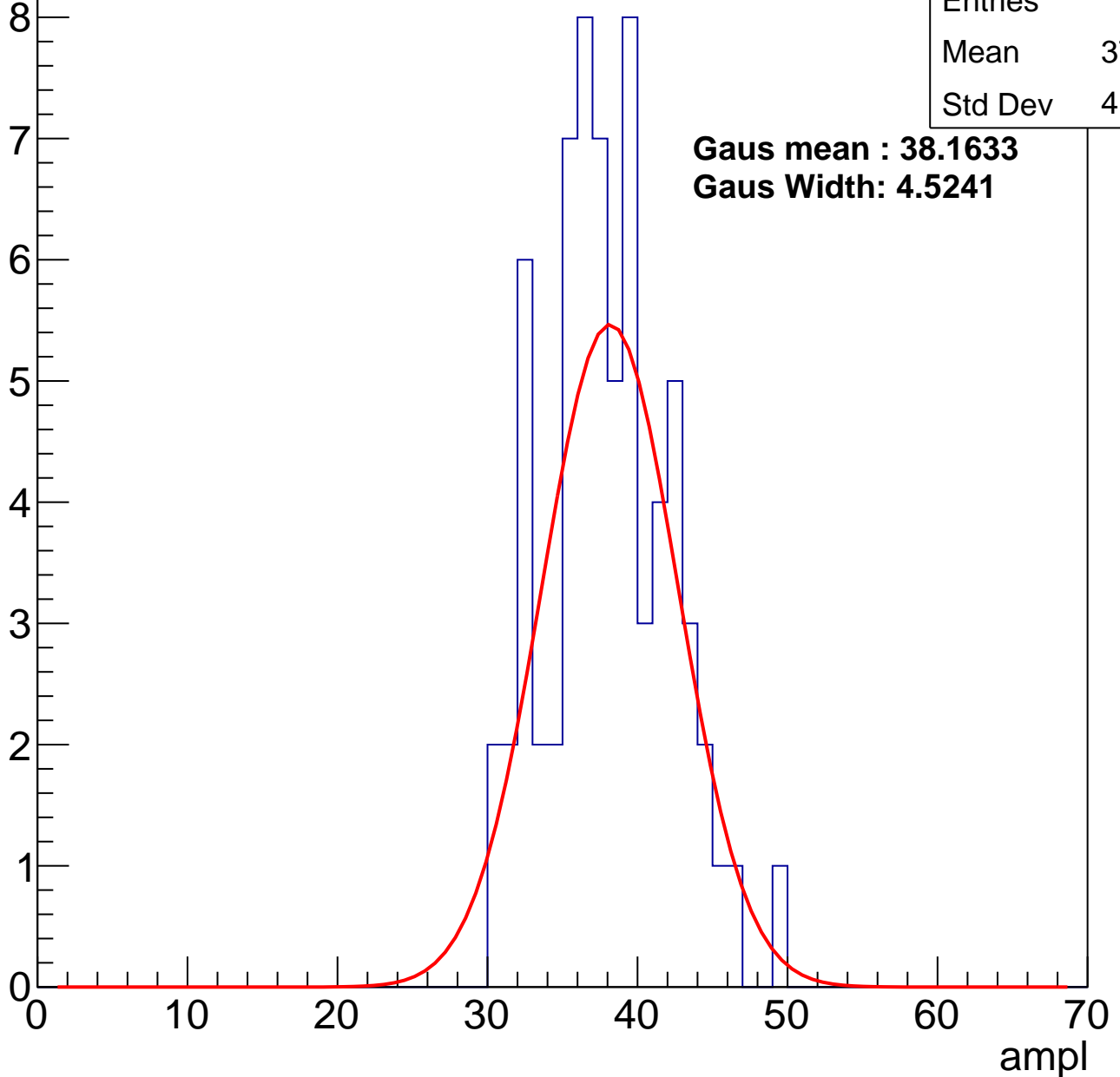
B1L101S, U15-ch41, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.58
Std Dev	4.048

Gaus mean : 38.1633
Gaus Width: 4.5241



B1L101S, U15-ch41, adc2

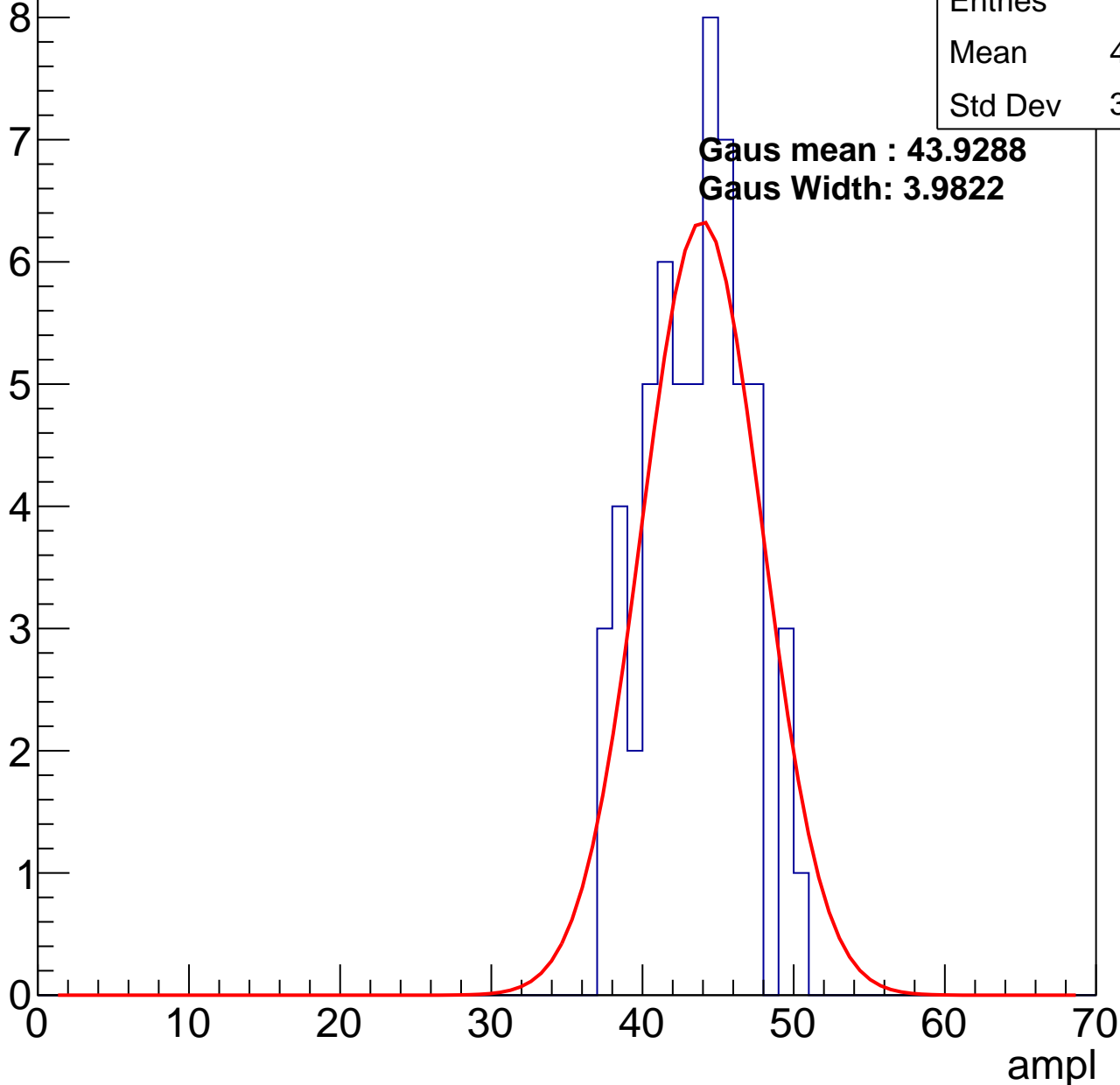
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.07
Std Dev	3.272

Gaus mean : 43.9288

Gaus Width: 3.9822

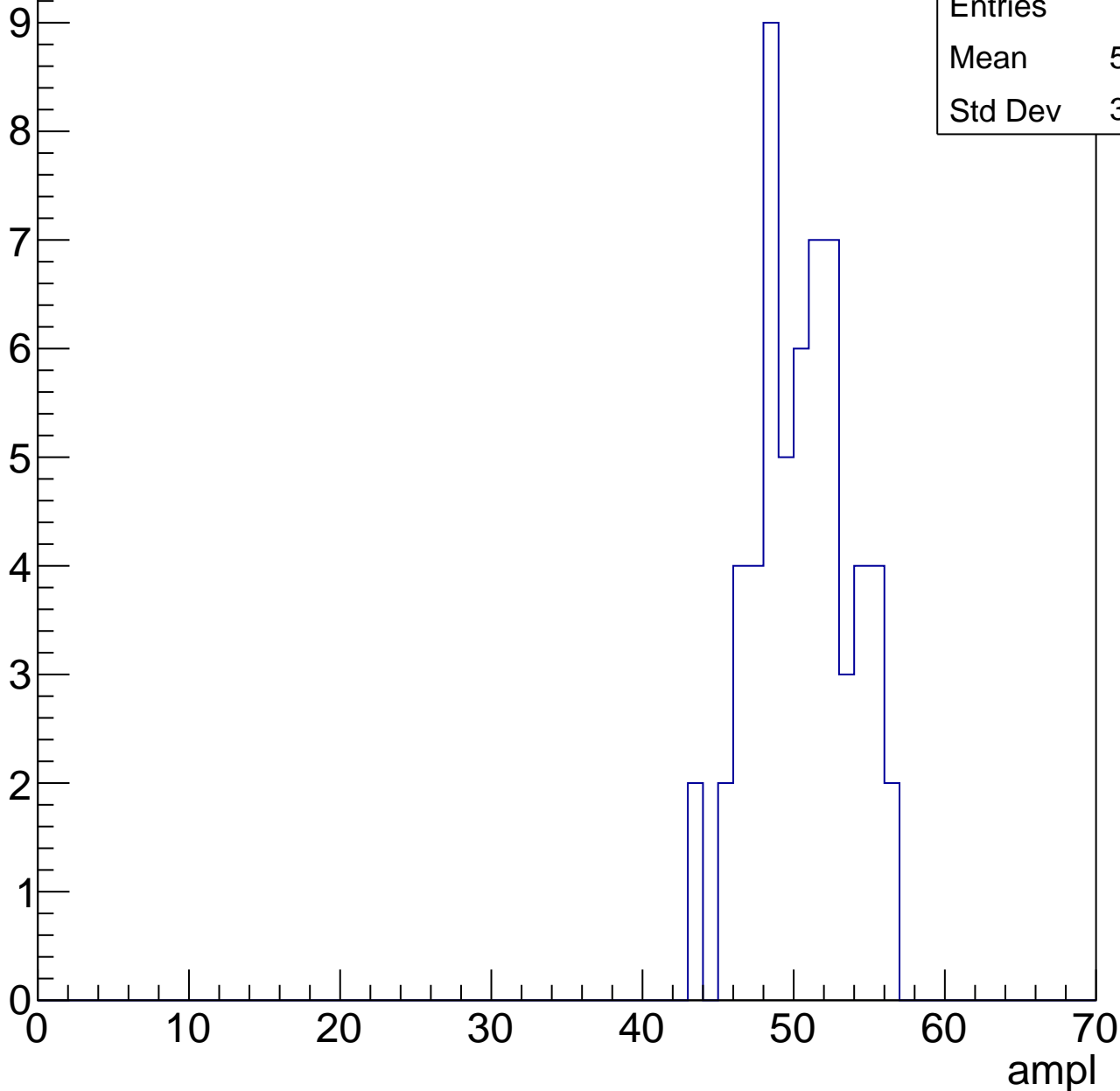


B1L101S, U15-ch41, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	50.05
Std Dev	3.154

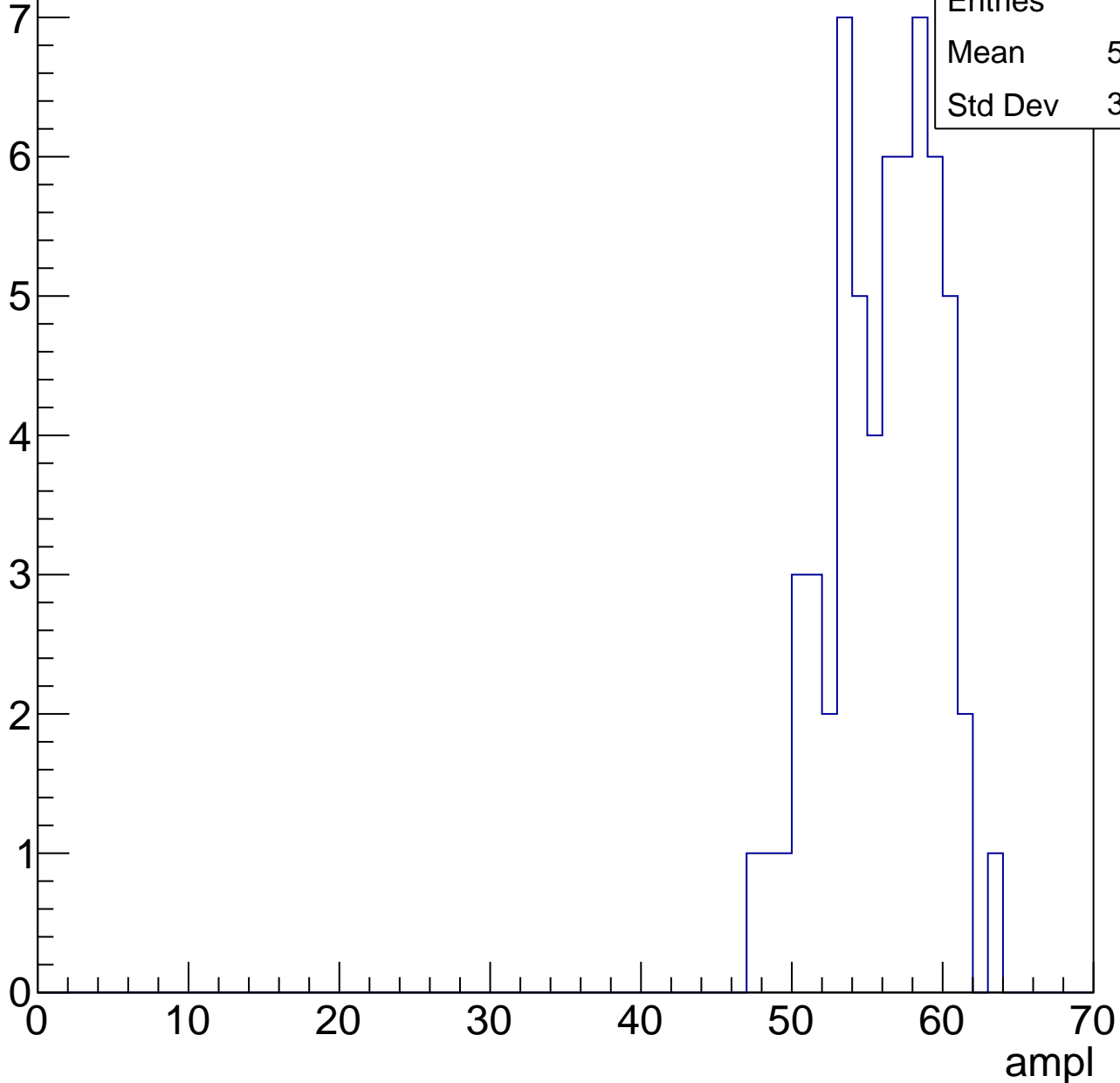


B1L101S, U15-ch41, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.58
Std Dev	3.542



B1L101S, U15-ch41, adc5

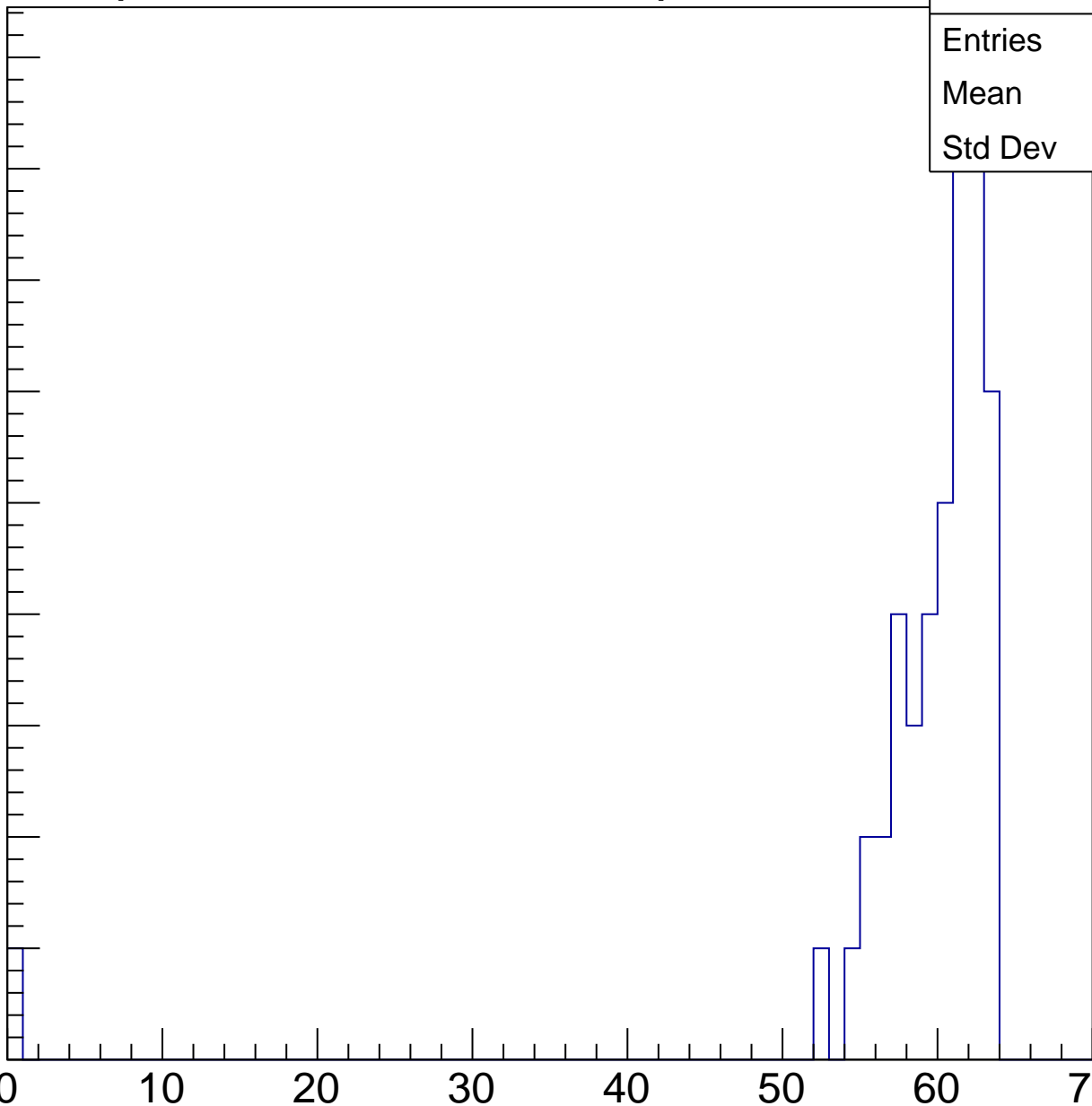
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	46
Mean	58.48
Std Dev	9.119

ampl



B1L101S, U15-ch41, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch41, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch42, adc0

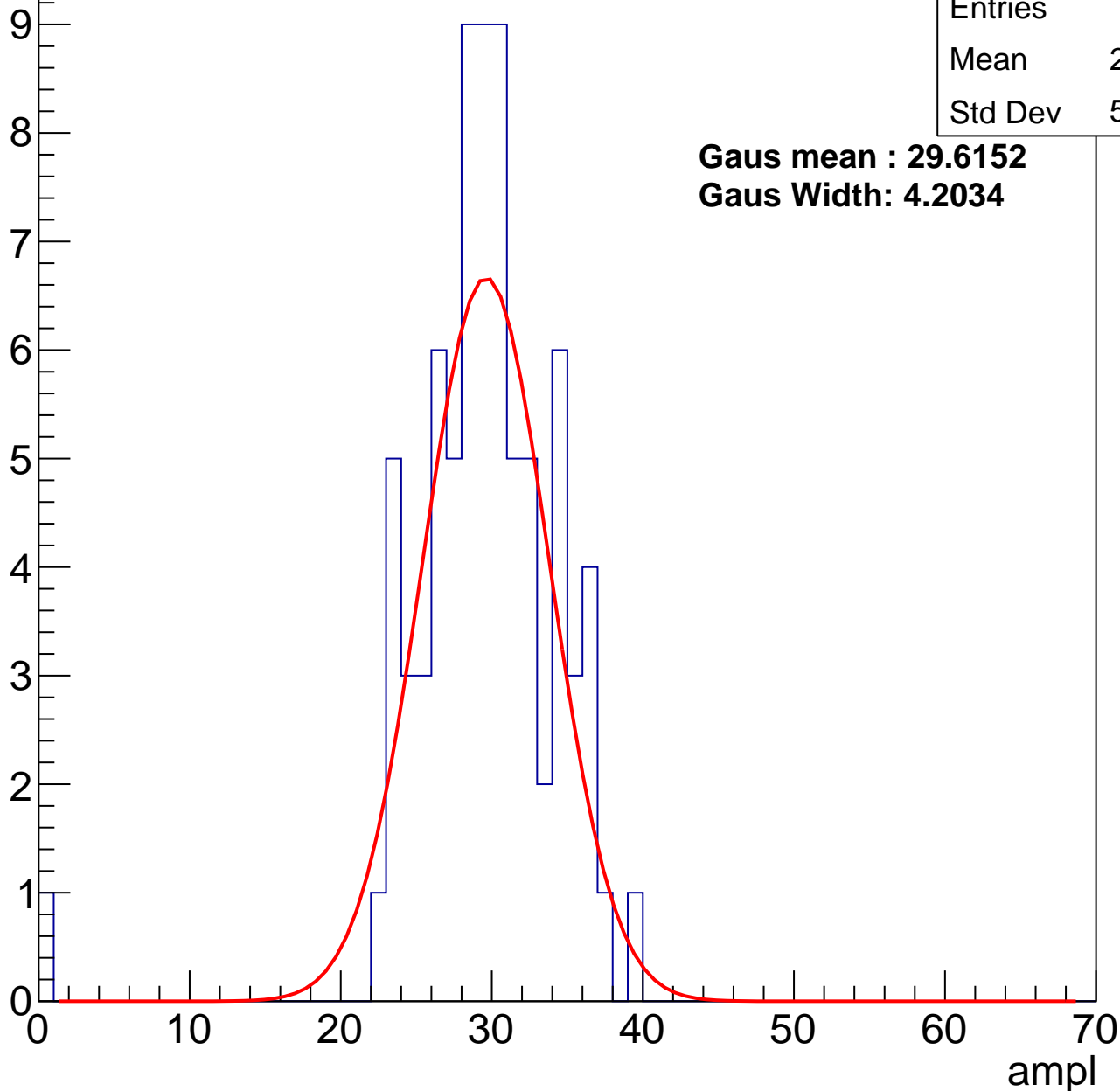
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	29.08
Std Dev	5.063

Gaus mean : 29.6152

Gaus Width: 4.2034



B1L101S, U15-ch42, adc1

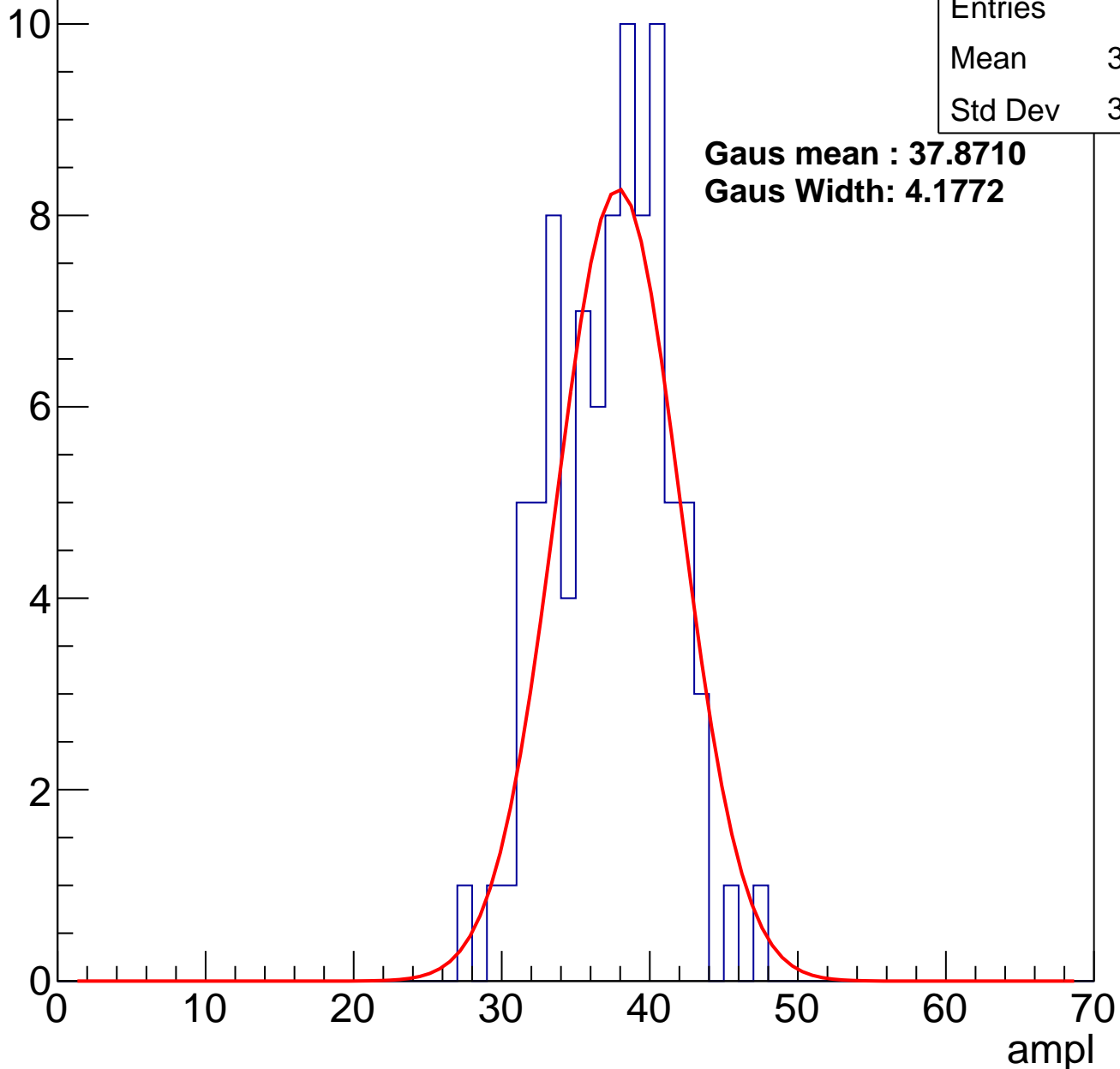
calib_packv5_042523_0143.root, FC#0, port D2

Entries	89
Mean	36.92
Std Dev	3.858

Gaus mean : 37.8710

Gaus Width: 4.1772

Entry



B1L101S, U15-ch42, adc2

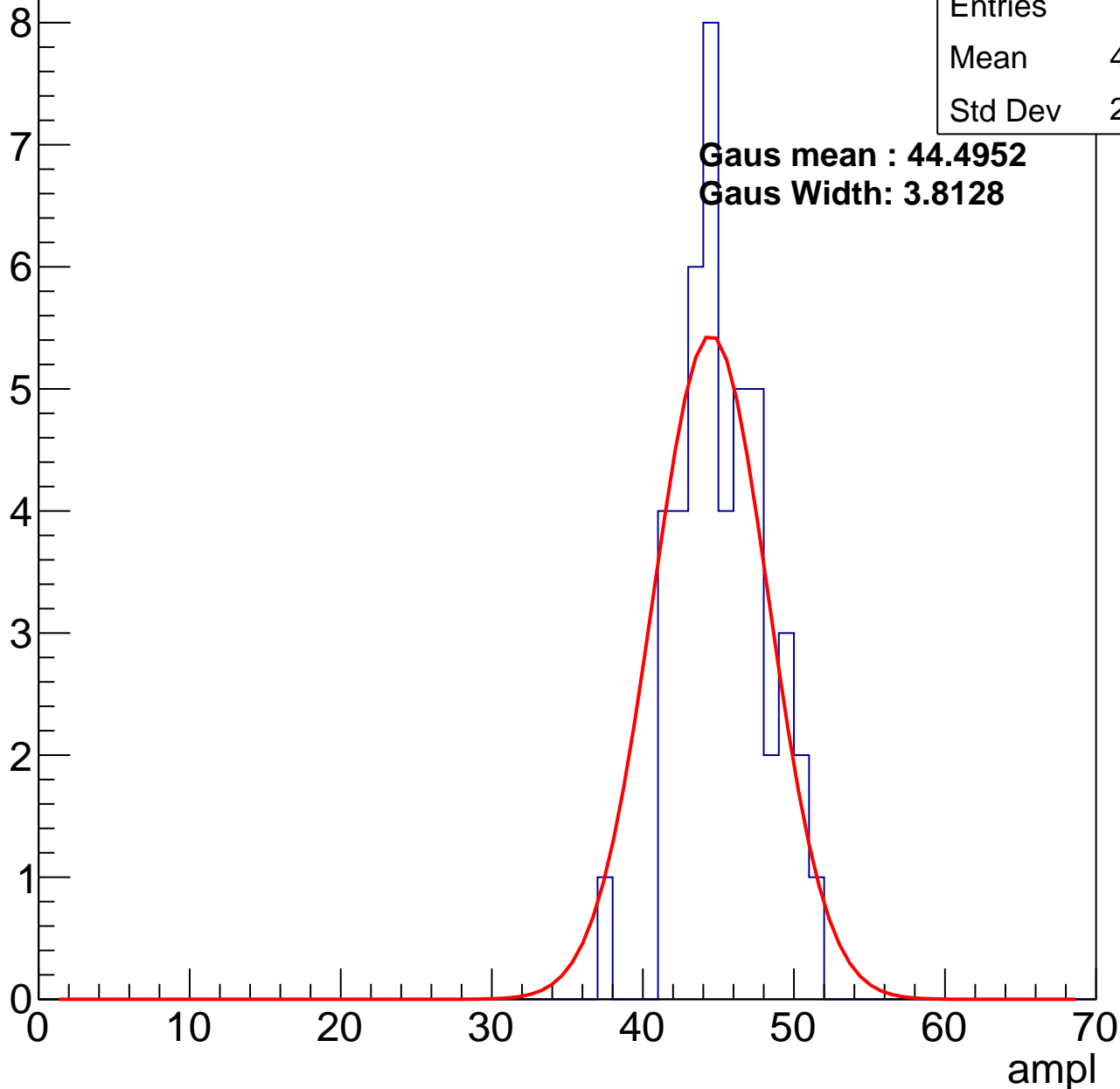
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	44.84
Std Dev	2.875

Gaus mean : 44.4952

Gaus Width: 3.8128

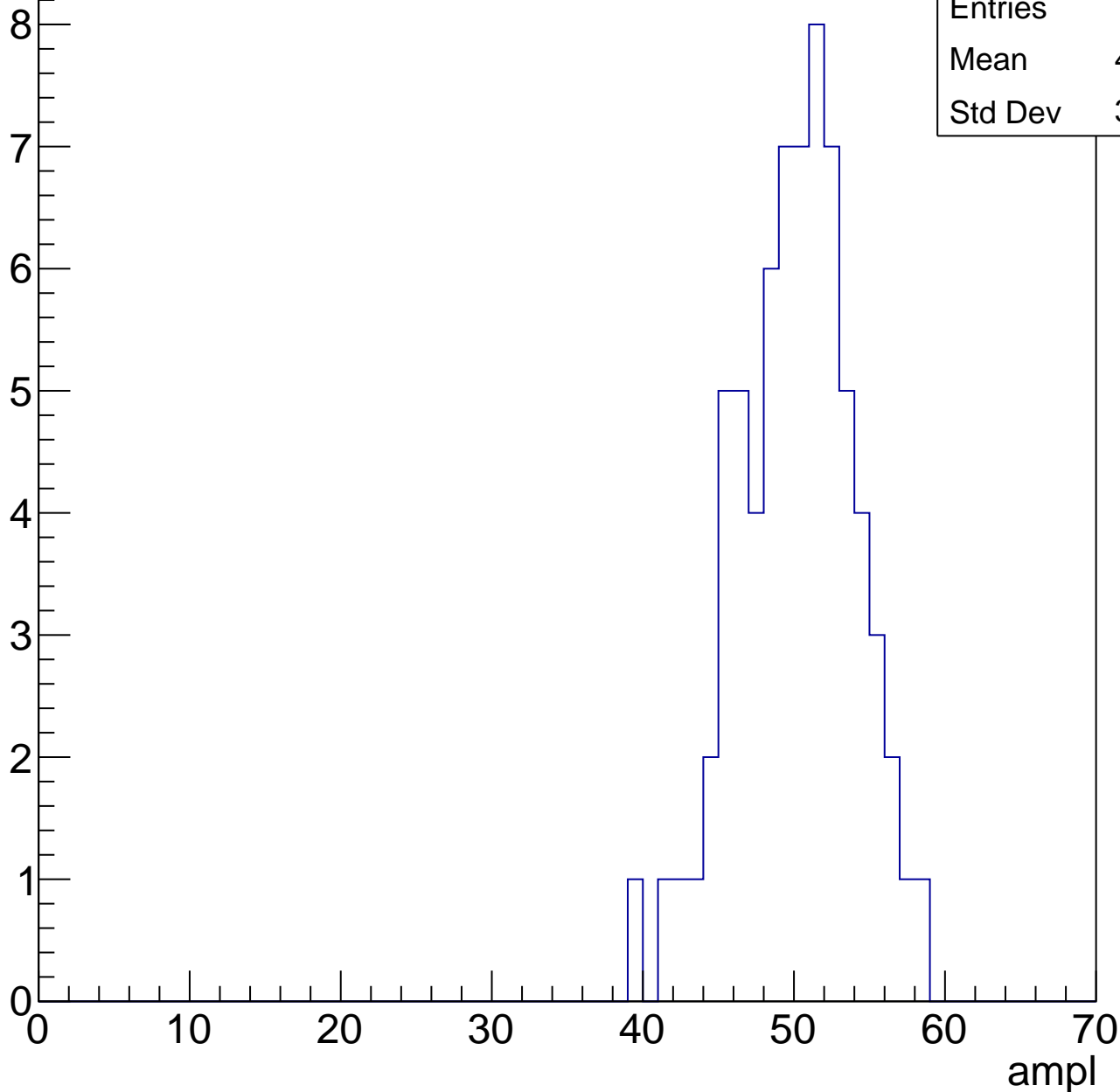


B1L101S, U15-ch42, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

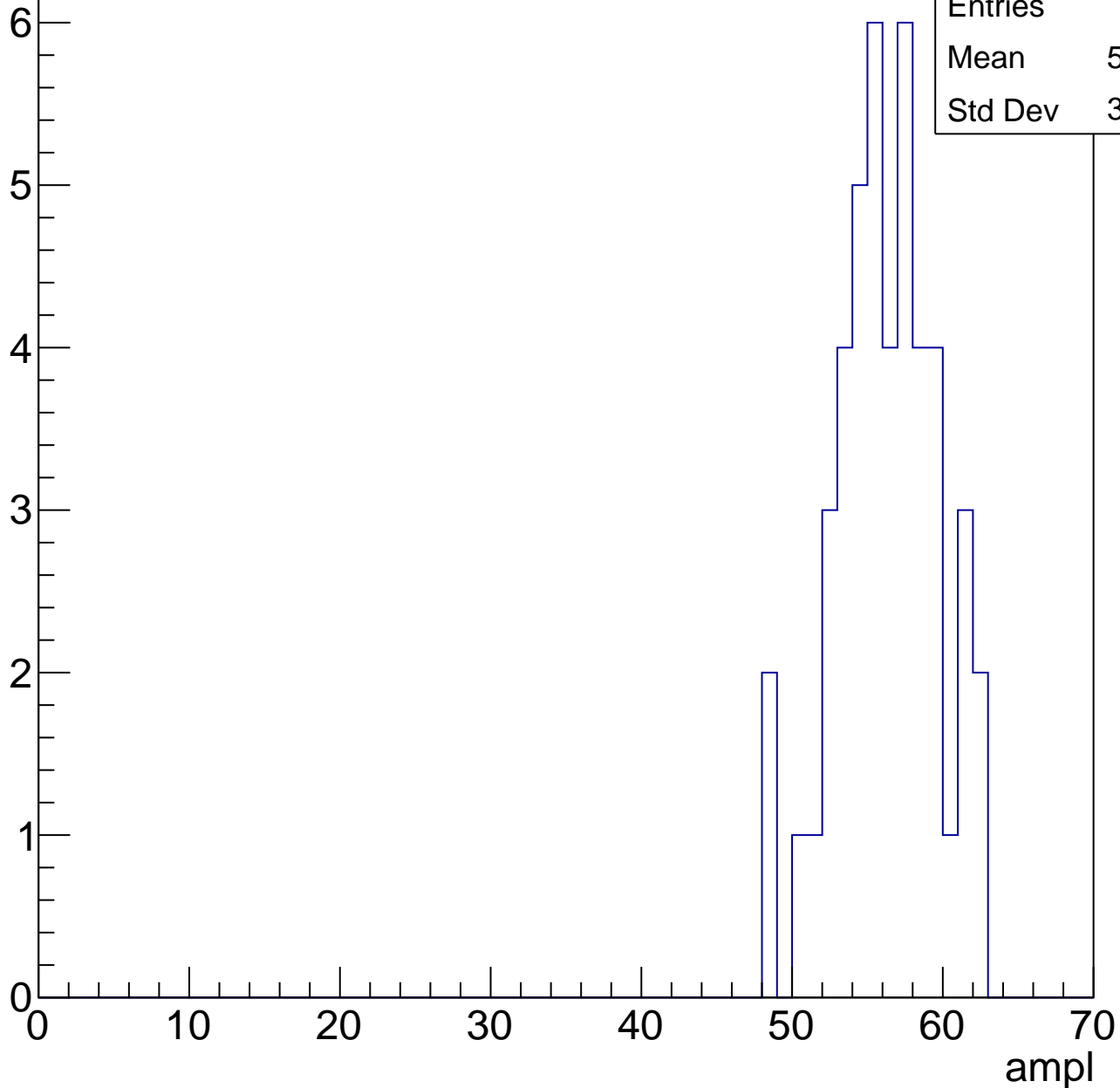
Entries	71
Mean	49.61
Std Dev	3.851



B1L101S, U15-ch42, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch42, adc5

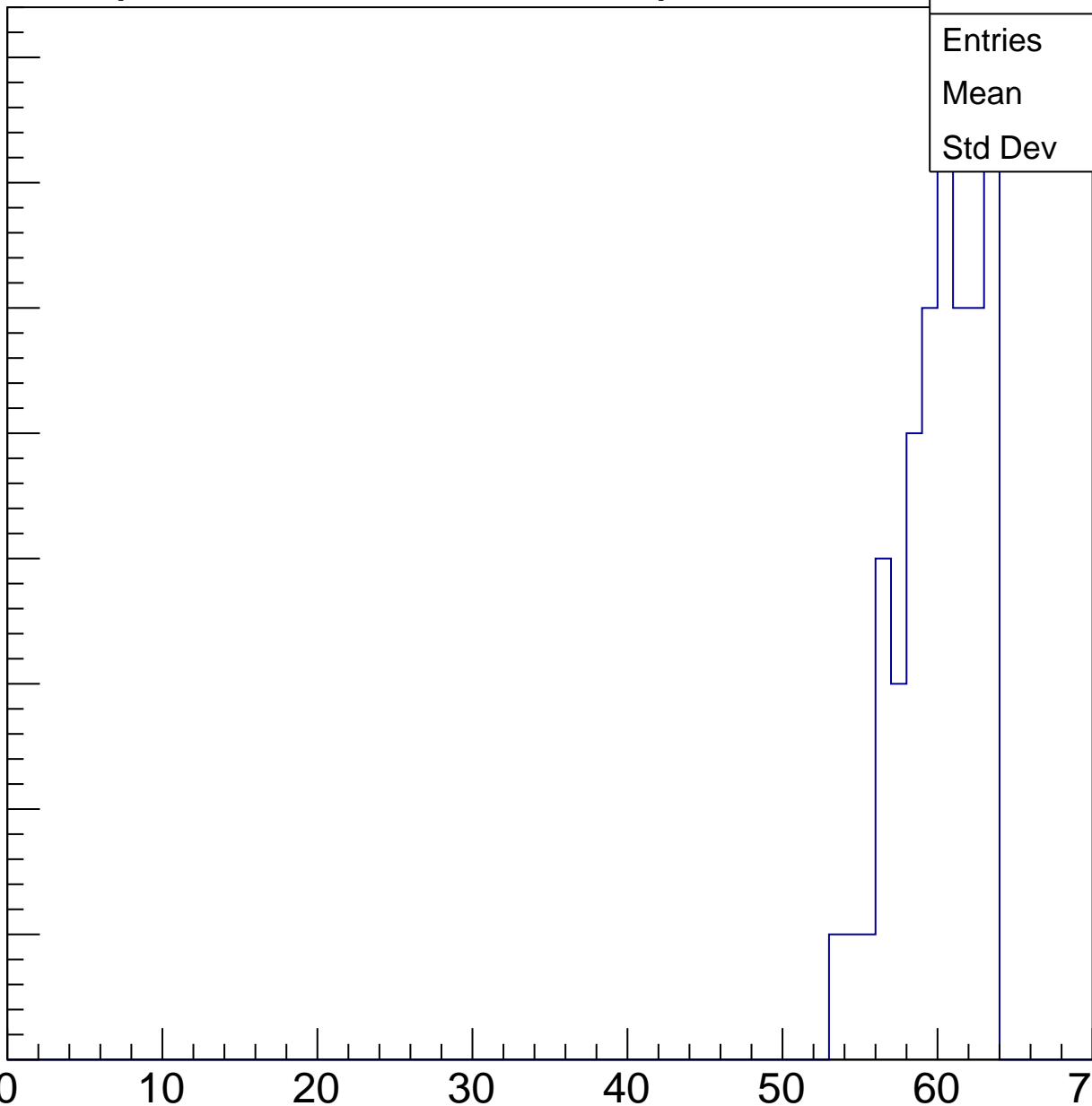
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.65
Std Dev	2.568

ampl



B1L101S, U15-ch42, adc6

calib_packv5_042523_0143.root, FC#0, port D2

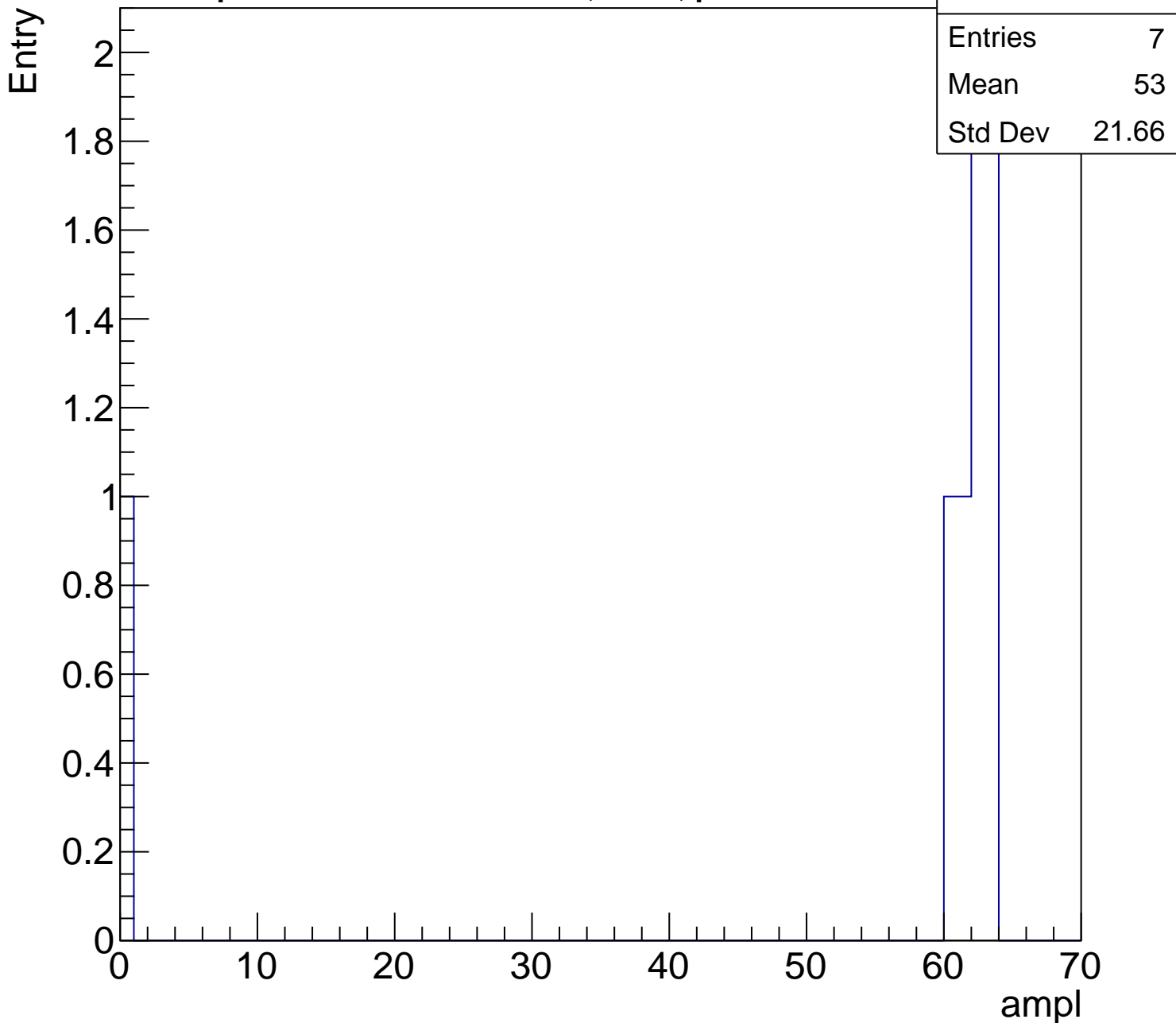
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	53
Std Dev	21.66

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch42, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch43, adc0

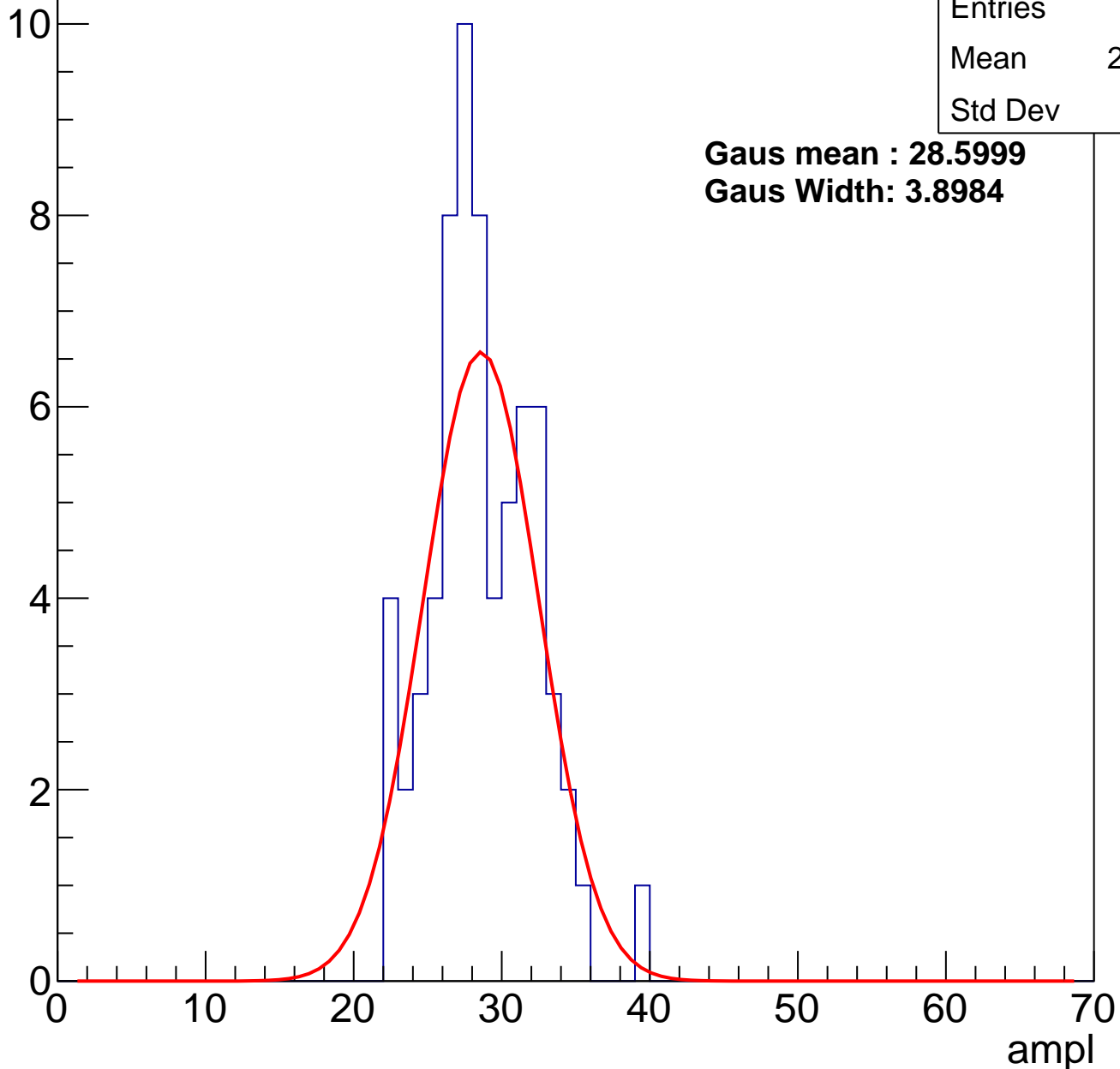
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	28.25
Std Dev	3.47

Gaus mean : 28.5999

Gaus Width: 3.8984

Entry



B1L101S, U15-ch43, adc1

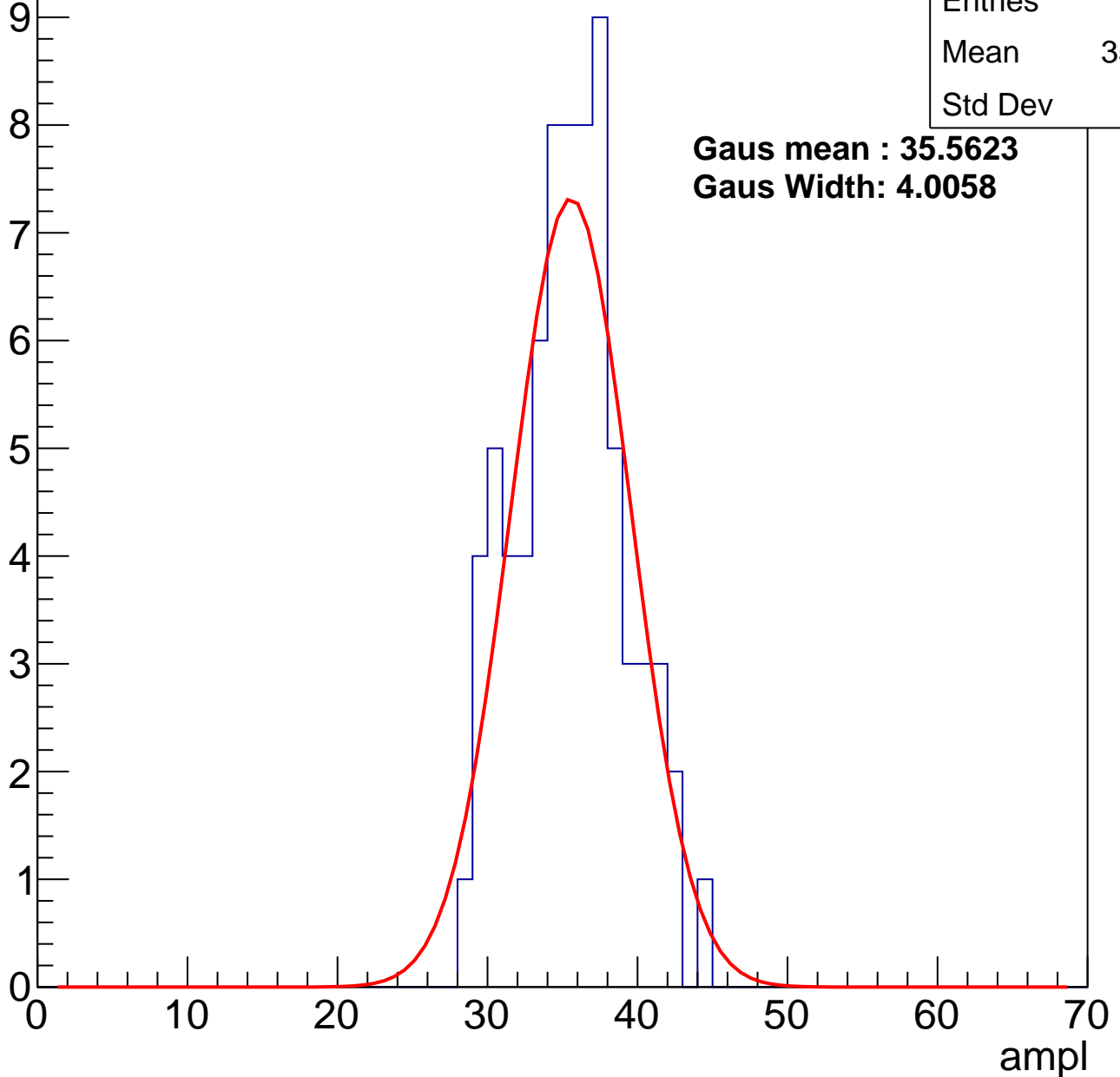
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.07
Std Dev	3.58

Gaus mean : 35.5623

Gaus Width: 4.0058



B1L101S, U15-ch43, adc2

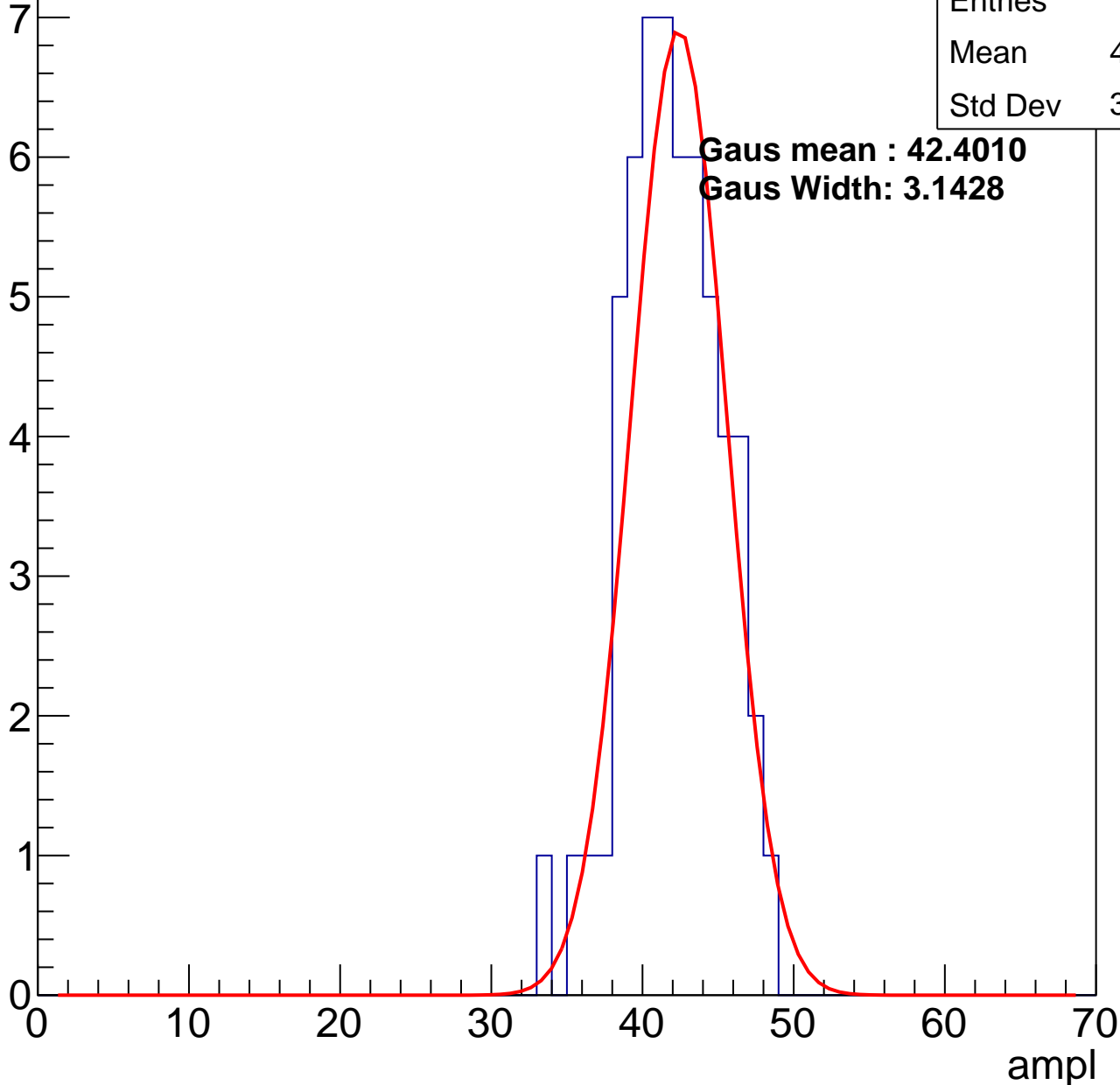
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.54
Std Dev	3.135

Gaus mean : 42.4010

Gaus Width: 3.1428

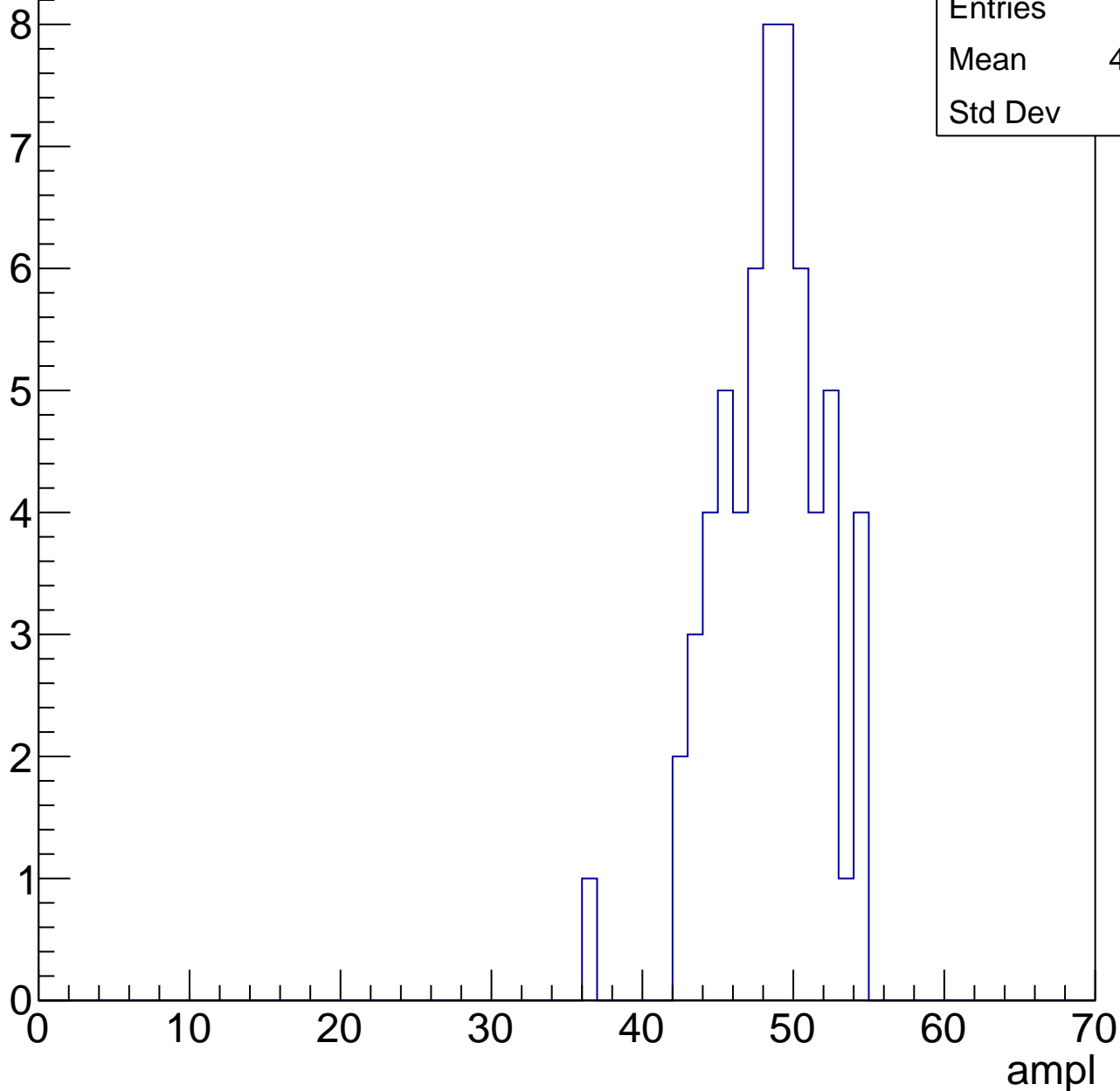


B1L101S, U15-ch43, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	47.95
Std Dev	3.48

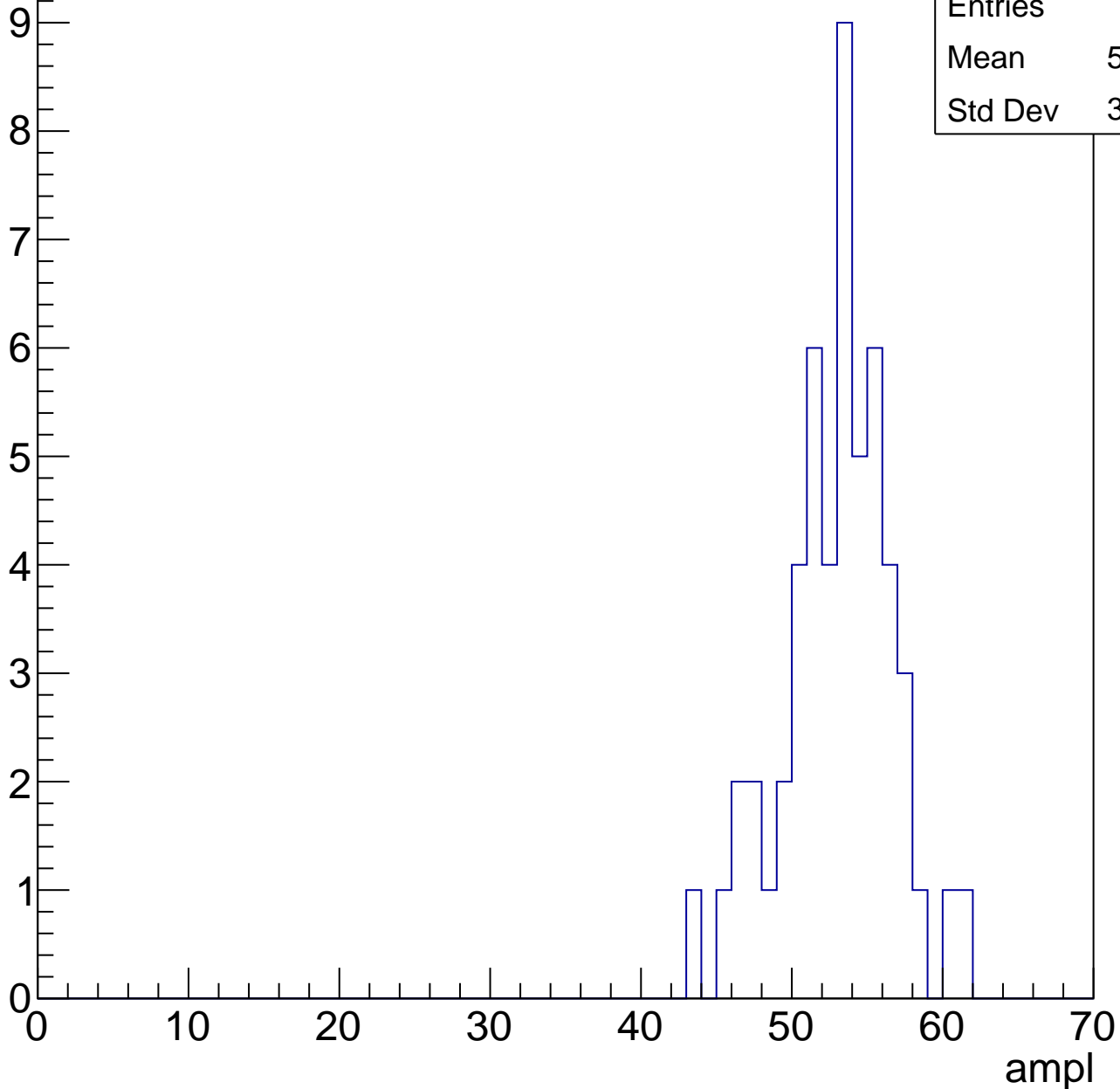


B1L101S, U15-ch43, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	52.55
Std Dev	3.622

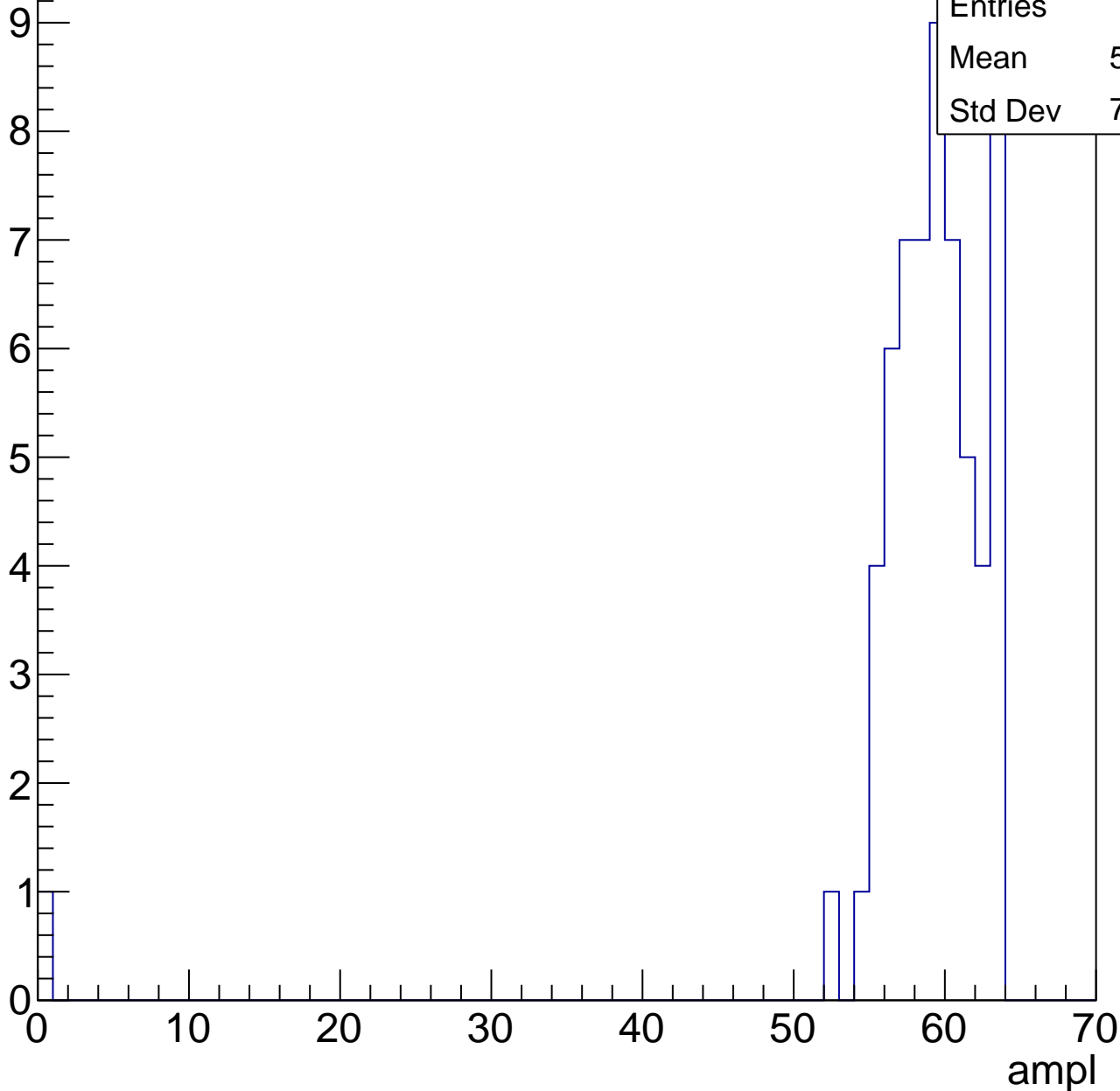


B1L101S, U15-ch43, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	57.92
Std Dev	7.988

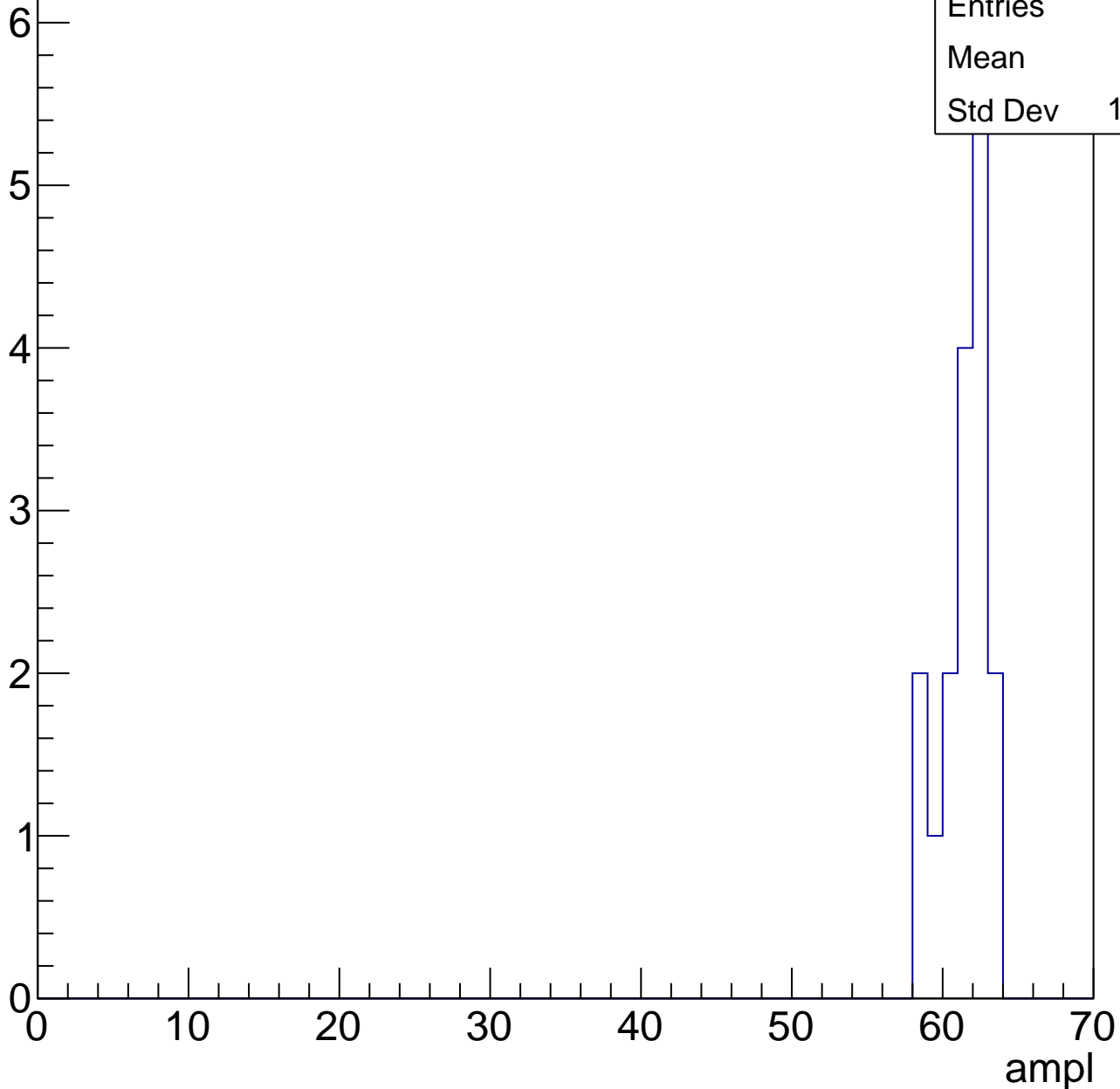


B1L101S, U15-ch43, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	17
Mean	61
Std Dev	1.495



B1L101S, U15-ch43, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch44, adc0

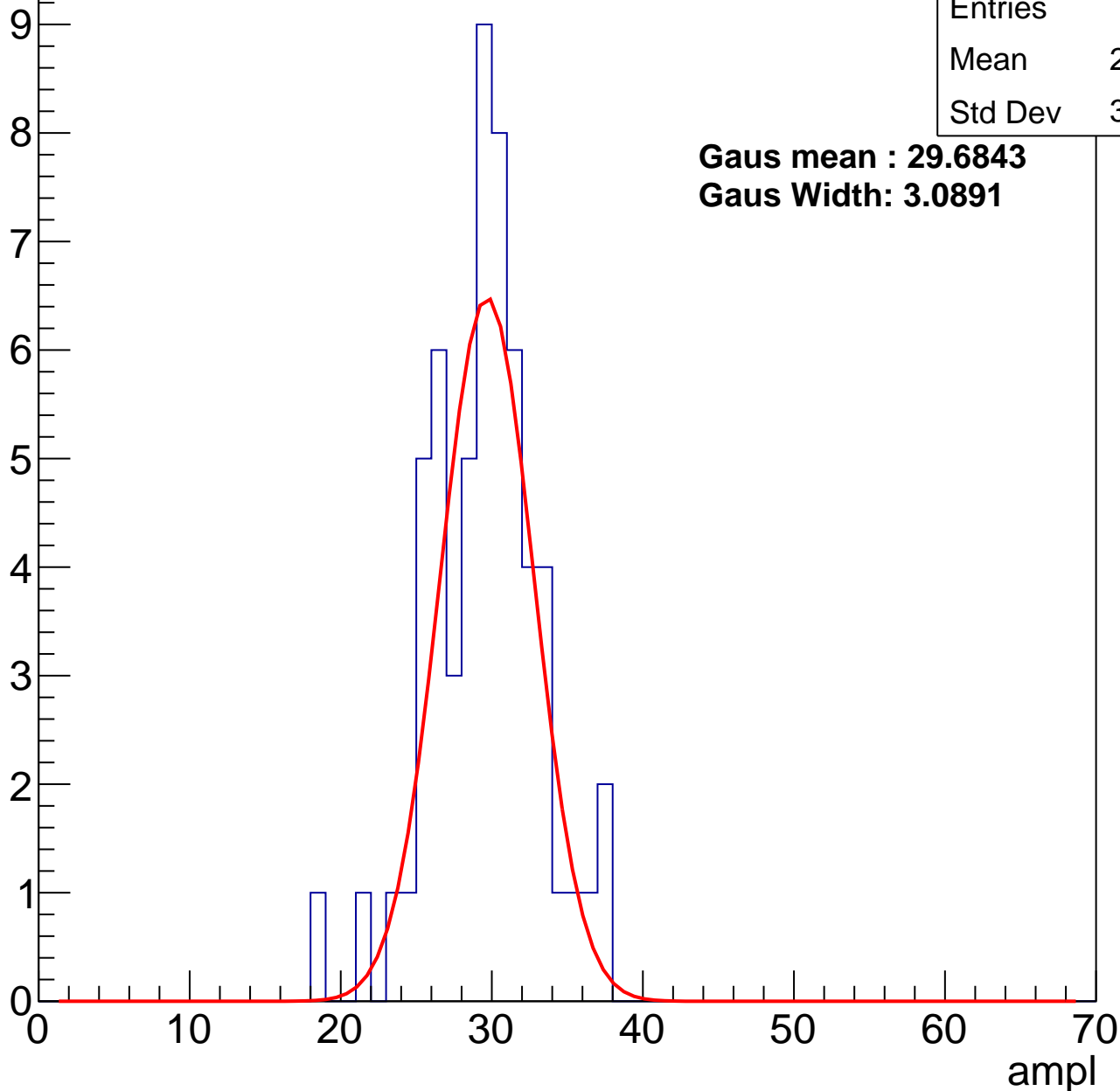
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	29.05
Std Dev	3.605

Gaus mean : 29.6843

Gaus Width: 3.0891



B1L101S, U15-ch44, adc1

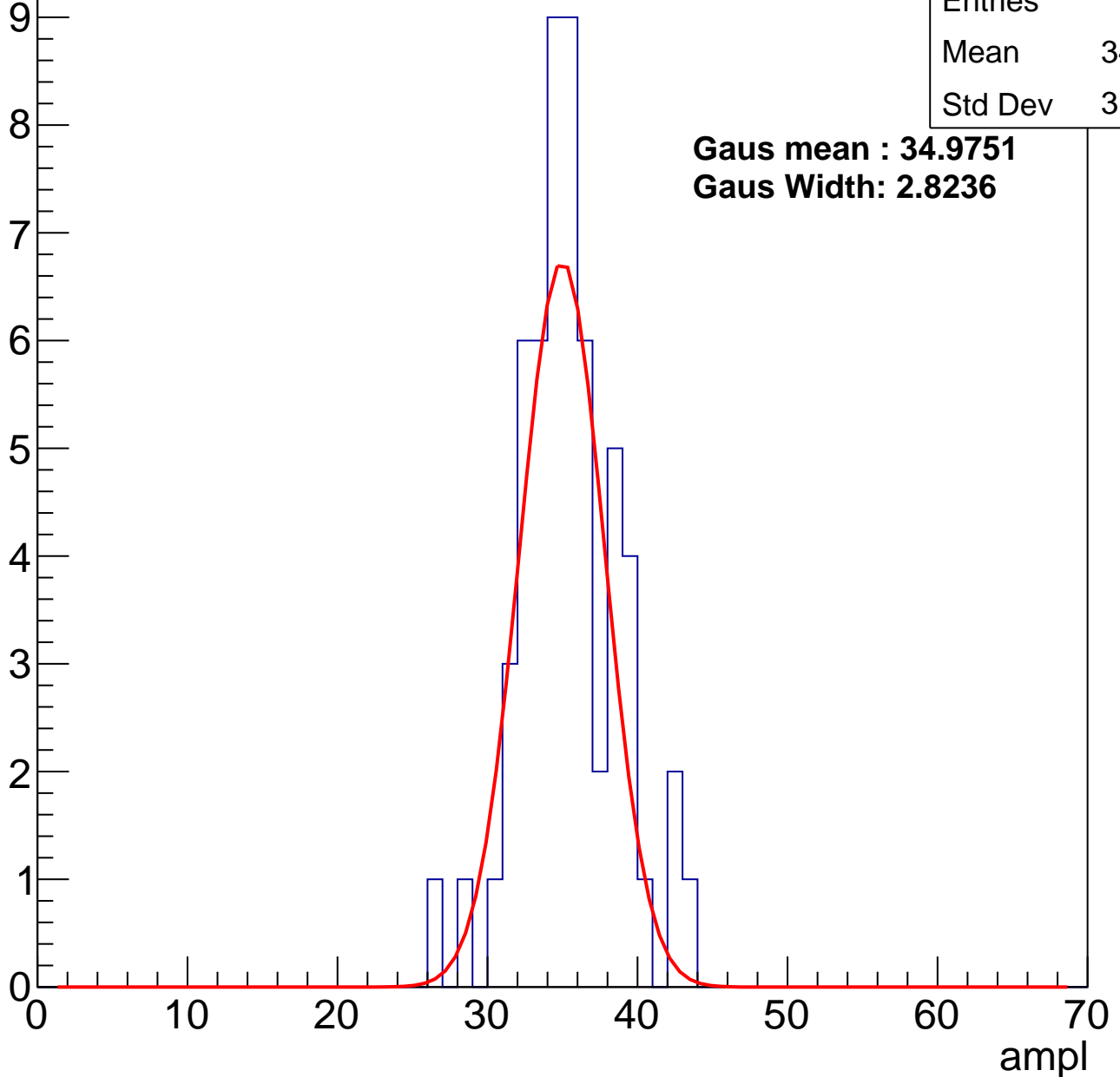
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	34.93
Std Dev	3.244

Gaus mean : 34.9751

Gaus Width: 2.8236



B1L101S, U15-ch44, adc2

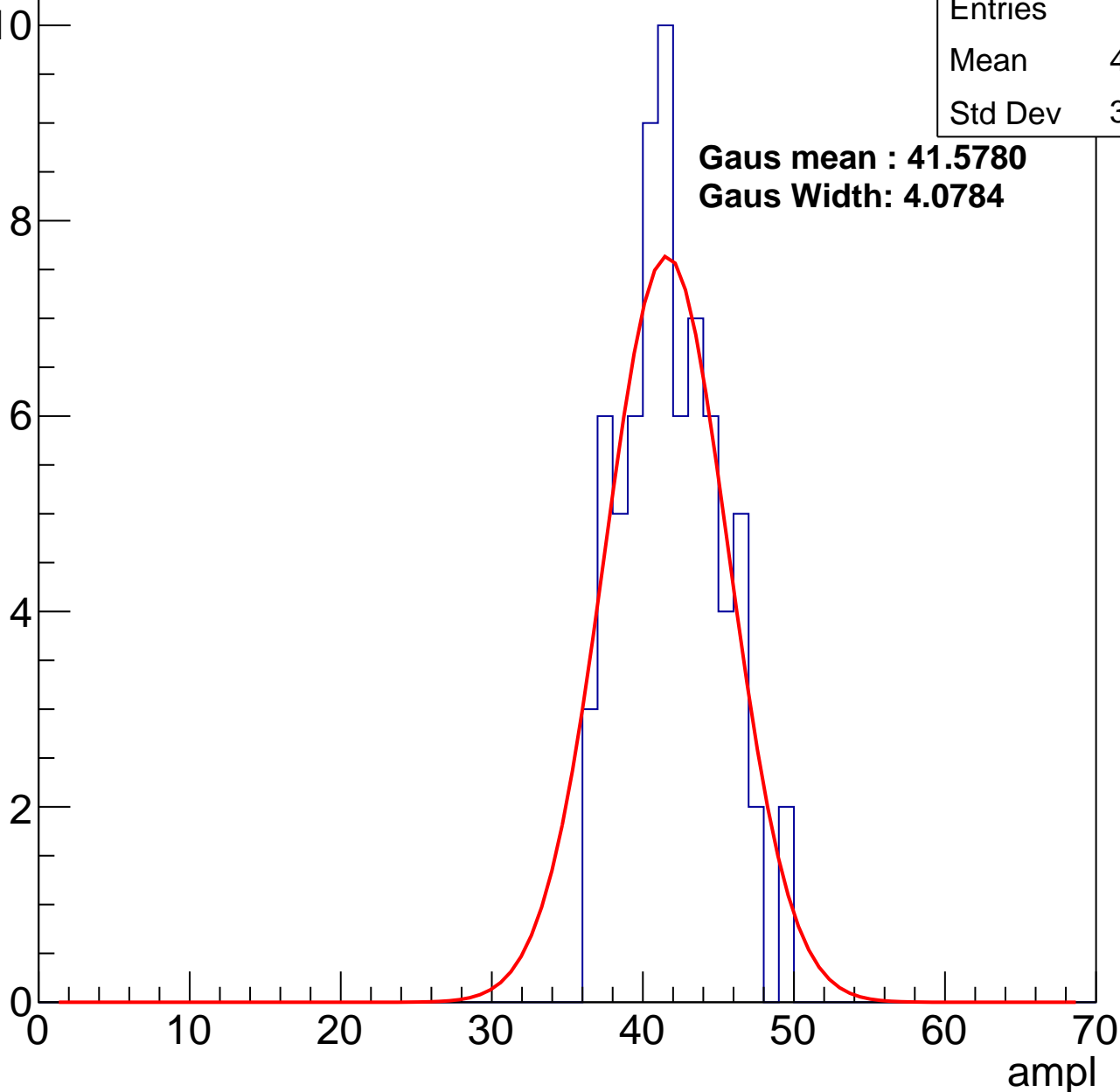
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	41.45
Std Dev	3.166

Gaus mean : 41.5780

Gaus Width: 4.0784



B1L101S, U15-ch44, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	48.41
Std Dev	3.254

Entry

10

8

6

4

2

0

0

10

20

30

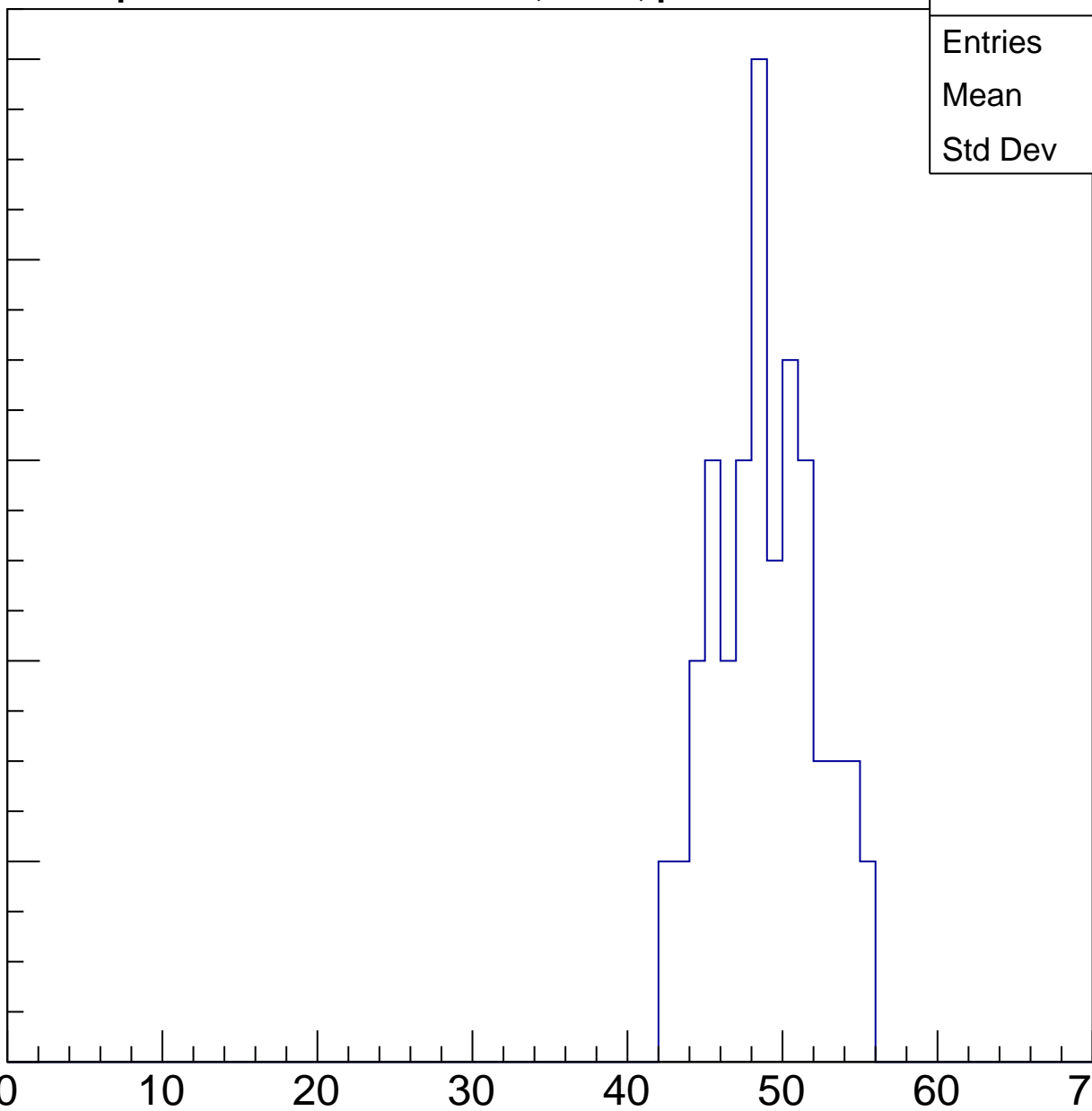
40

50

60

70

ampl

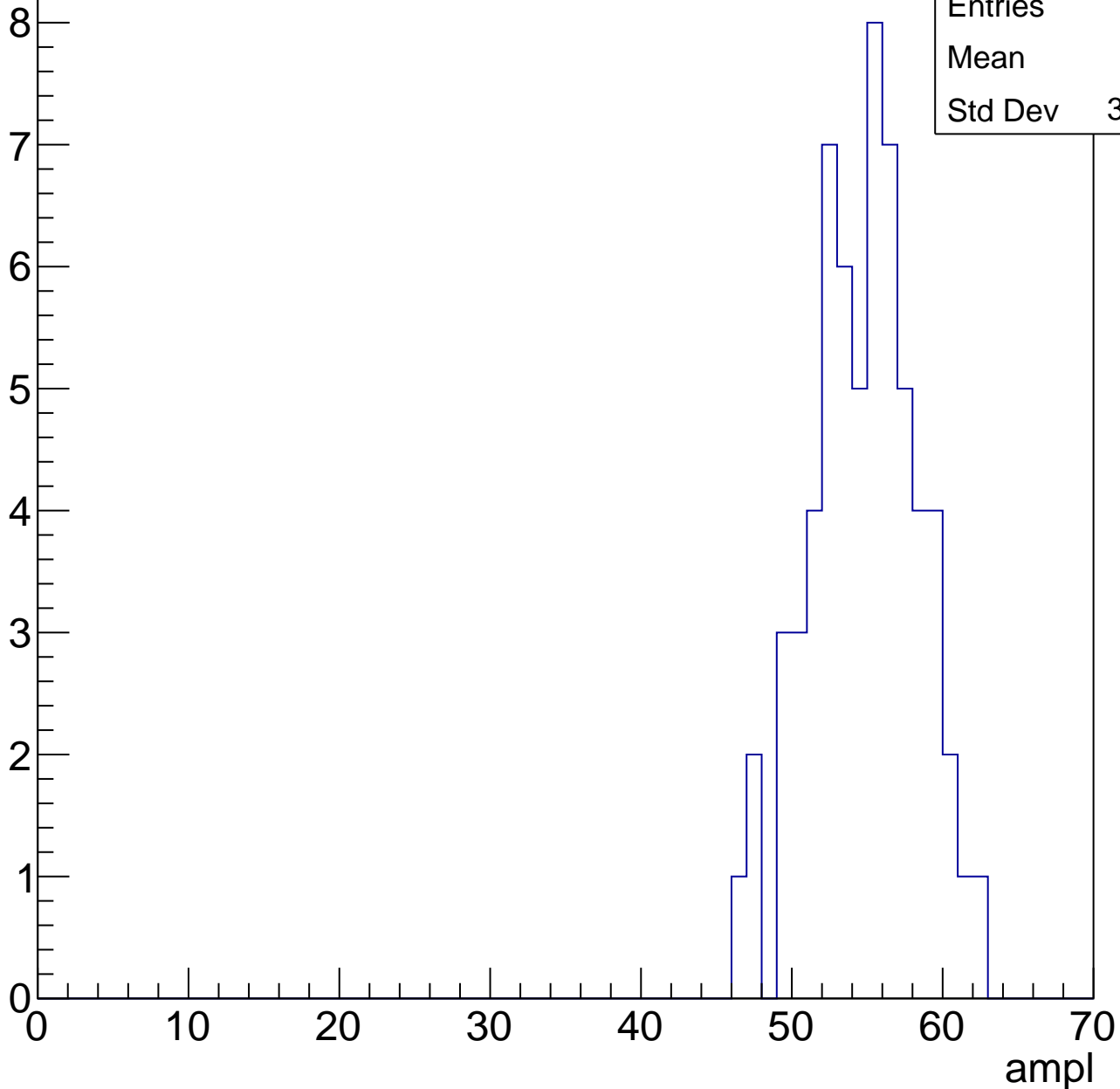


B1L101S, U15-ch44, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	54.3
Std Dev	3.517

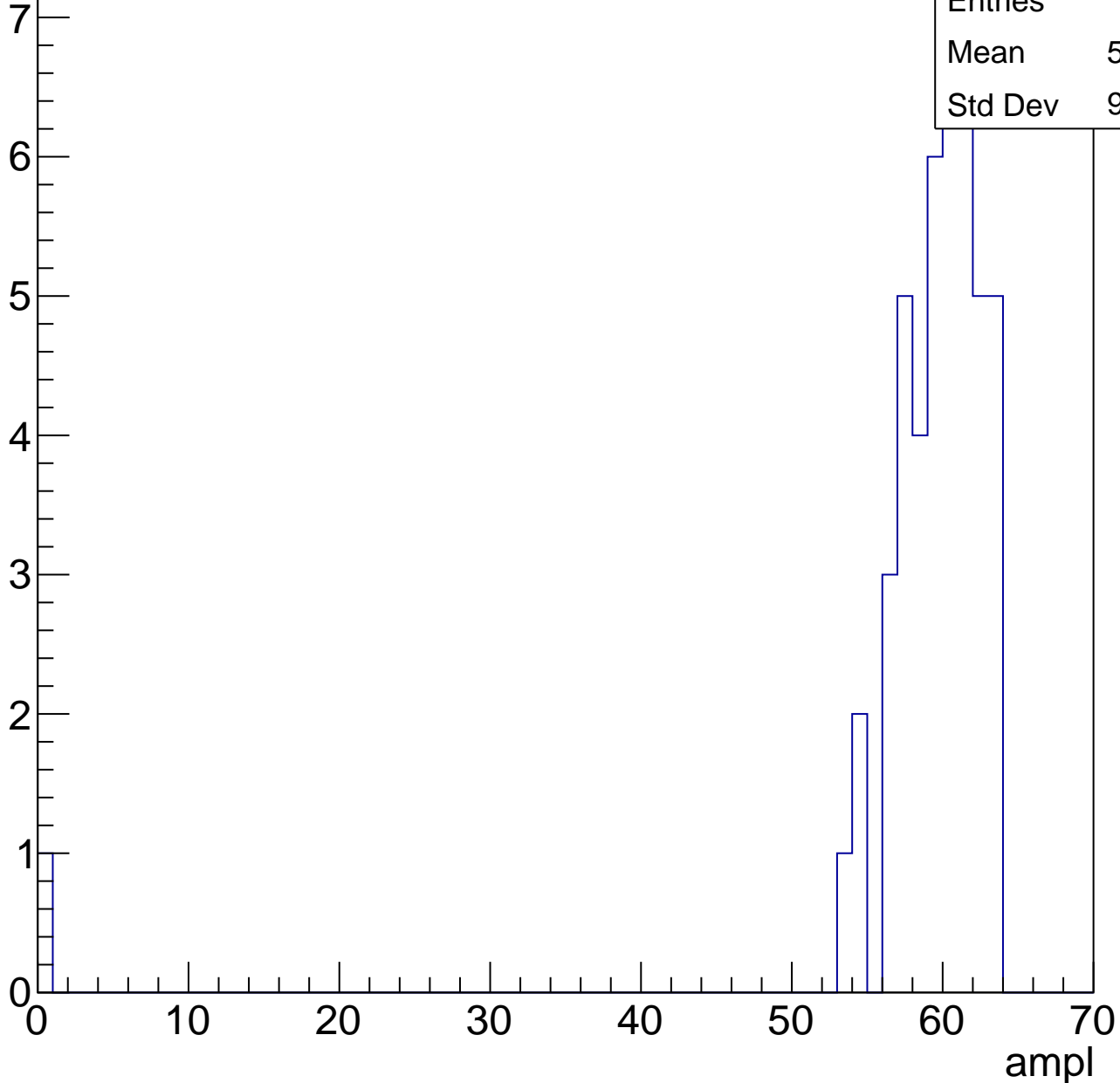


B1L101S, U15-ch44, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.09
Std Dev	9.016

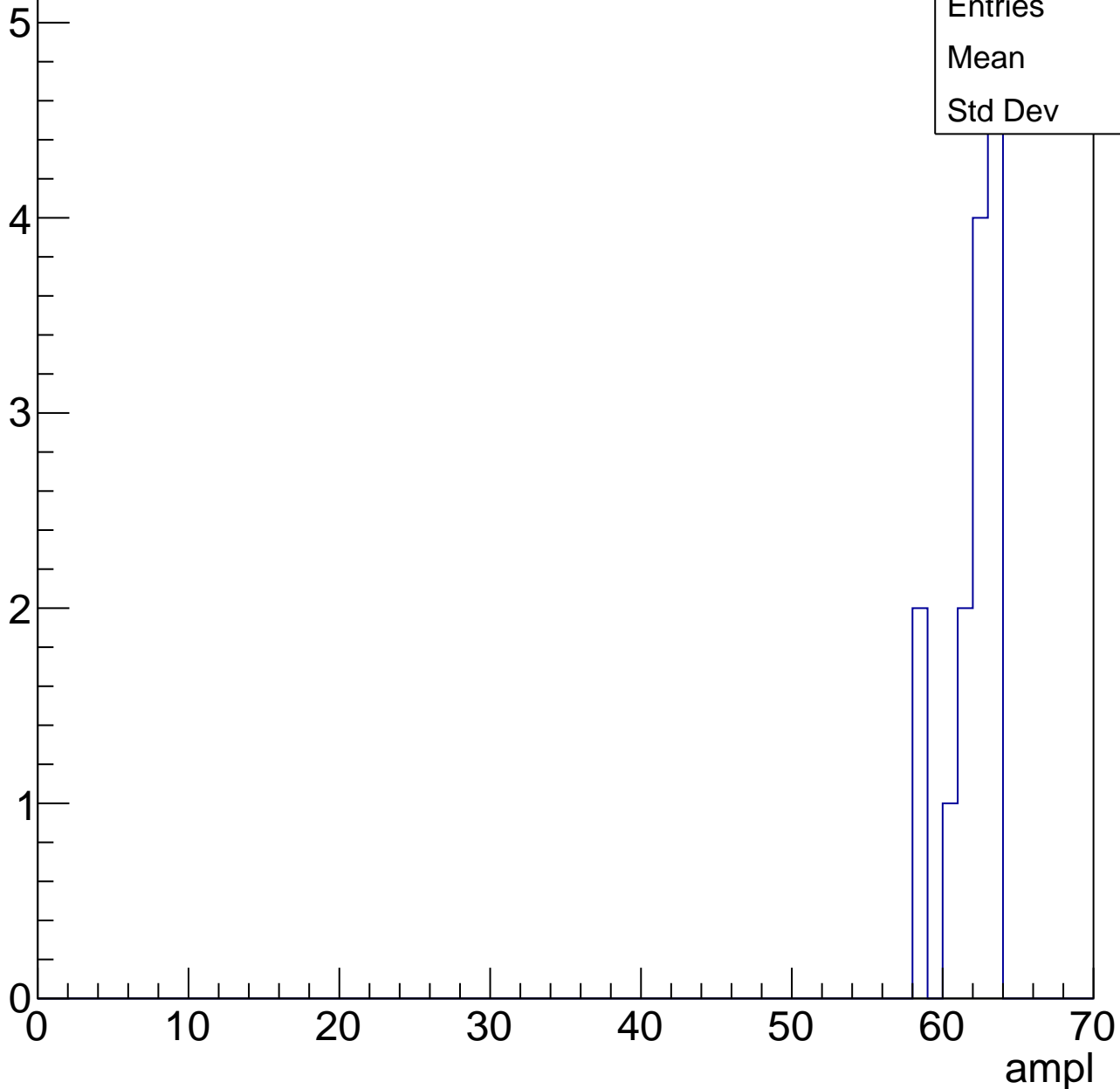


B1L101S, U15-ch44, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.5
Std Dev	1.68



B1L101S, U15-ch44, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U15-ch45, adc0

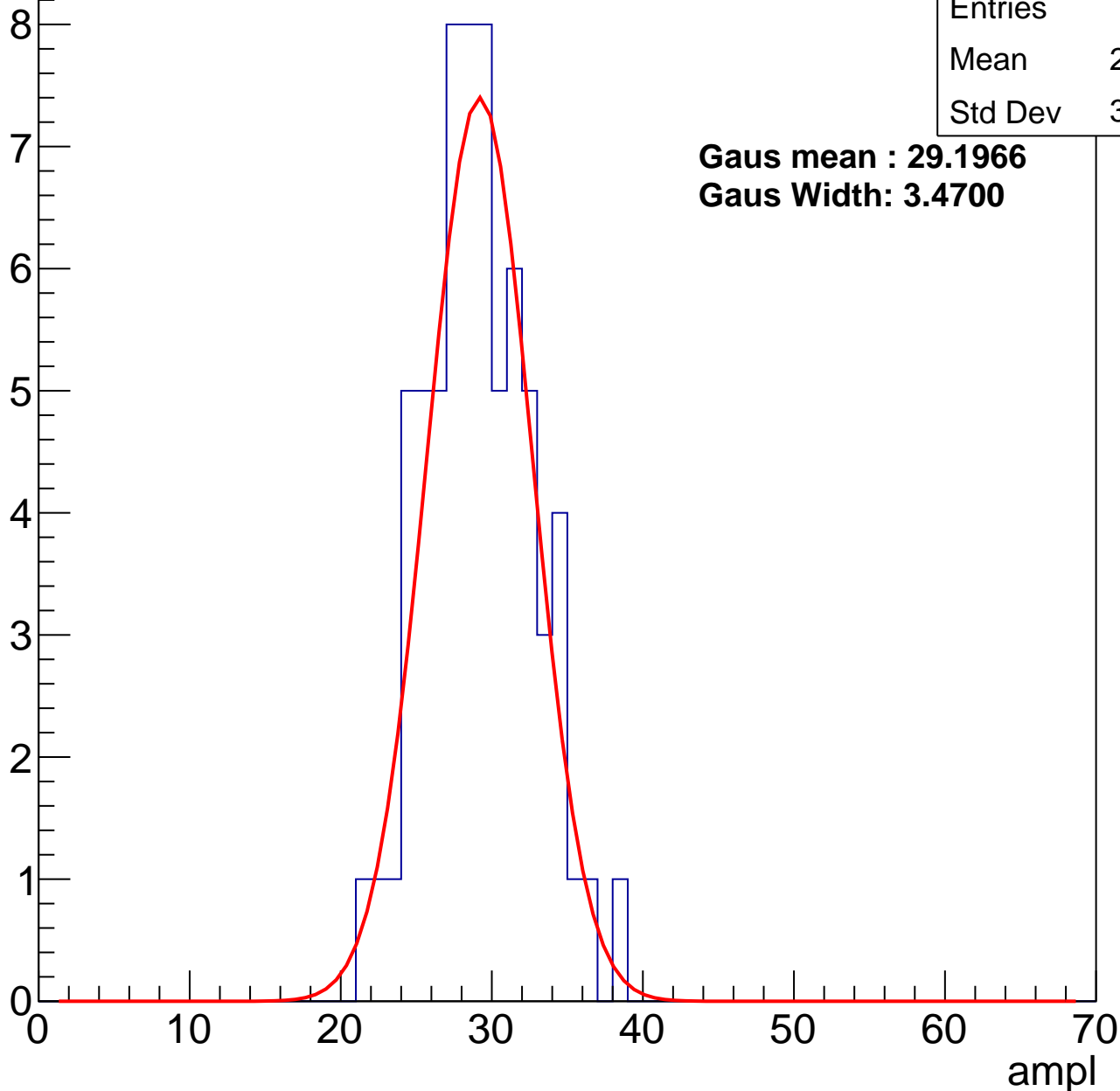
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	28.72
Std Dev	3.476

Gaus mean : 29.166

Gaus Width: 3.4700



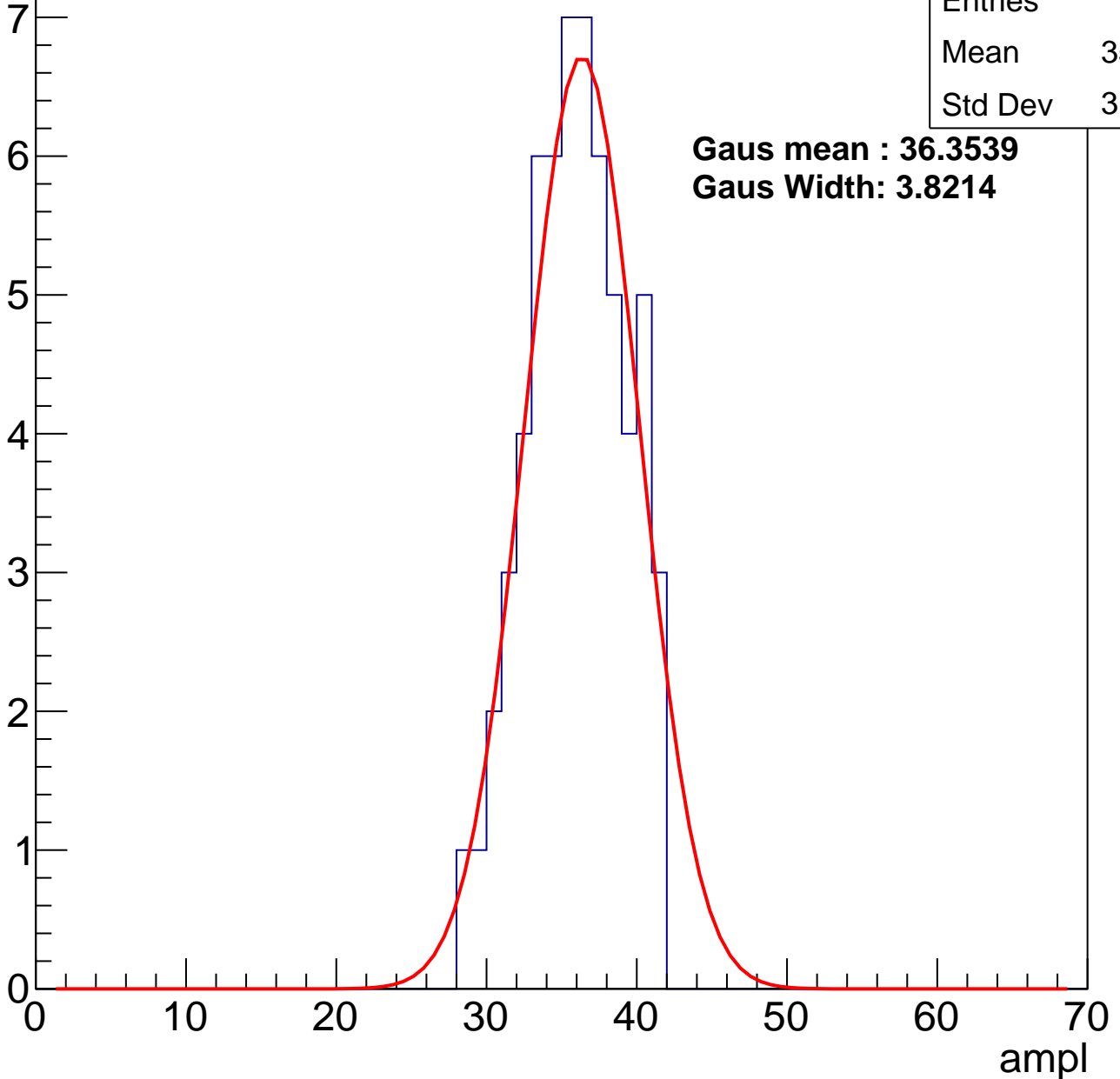
B1L101S, U15-ch45, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.47
Std Dev	3.186

Gaus mean : 36.3539
Gaus Width: 3.8214



B1L101S, U15-ch45, adc2

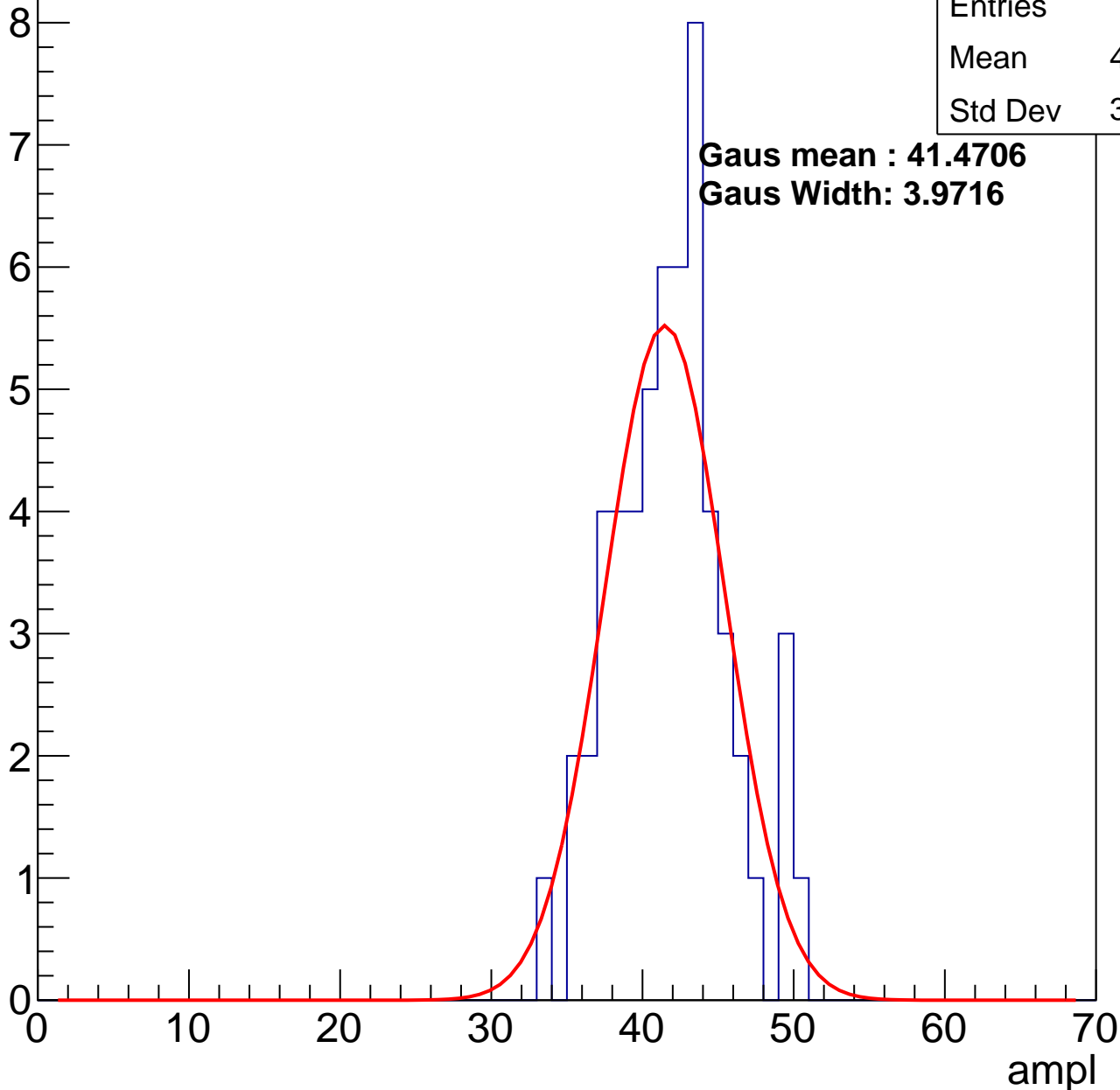
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.43
Std Dev	3.727

Gaus mean : 41.4706

Gaus Width: 3.9716

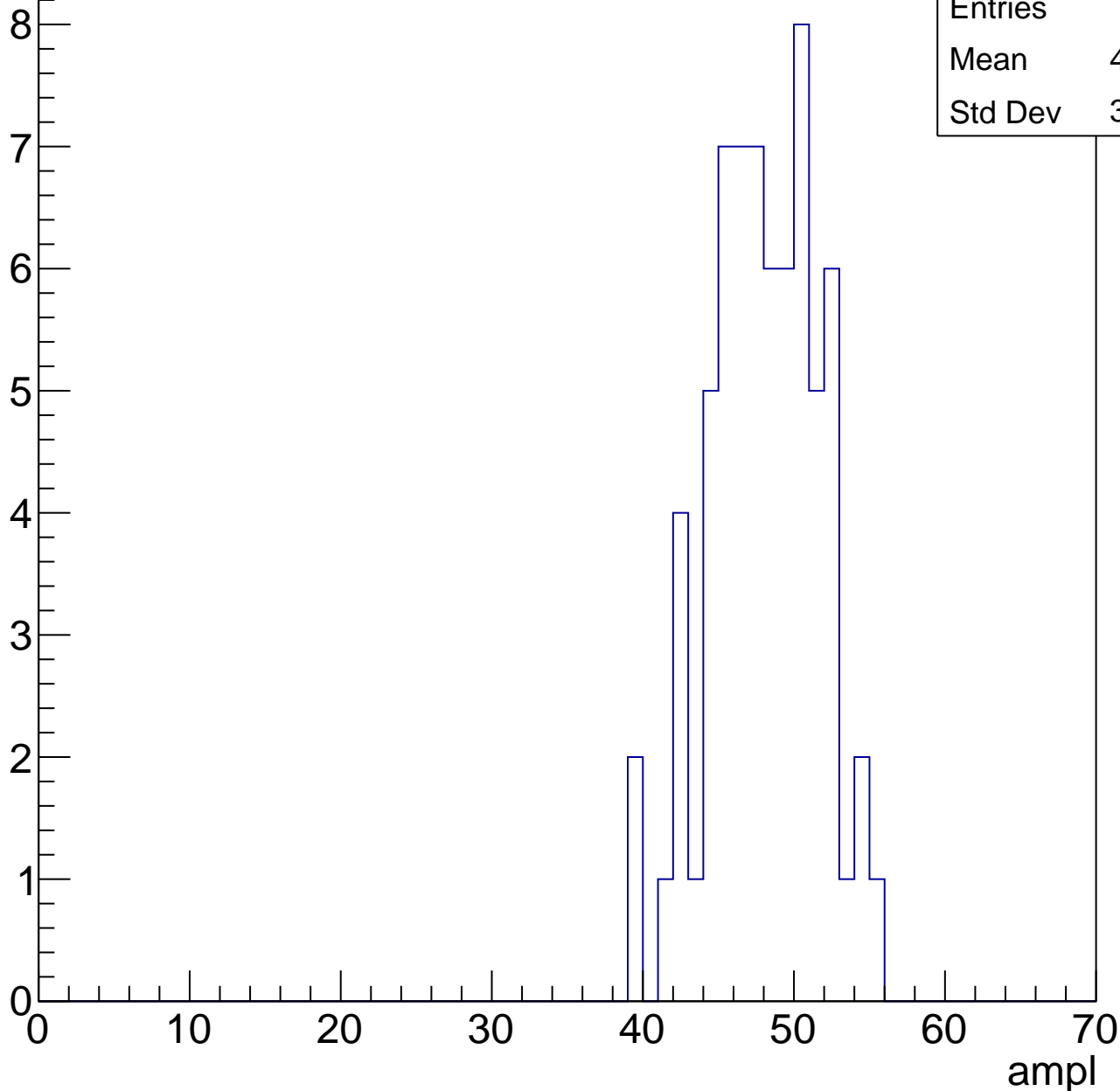


B1L101S, U15-ch45, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

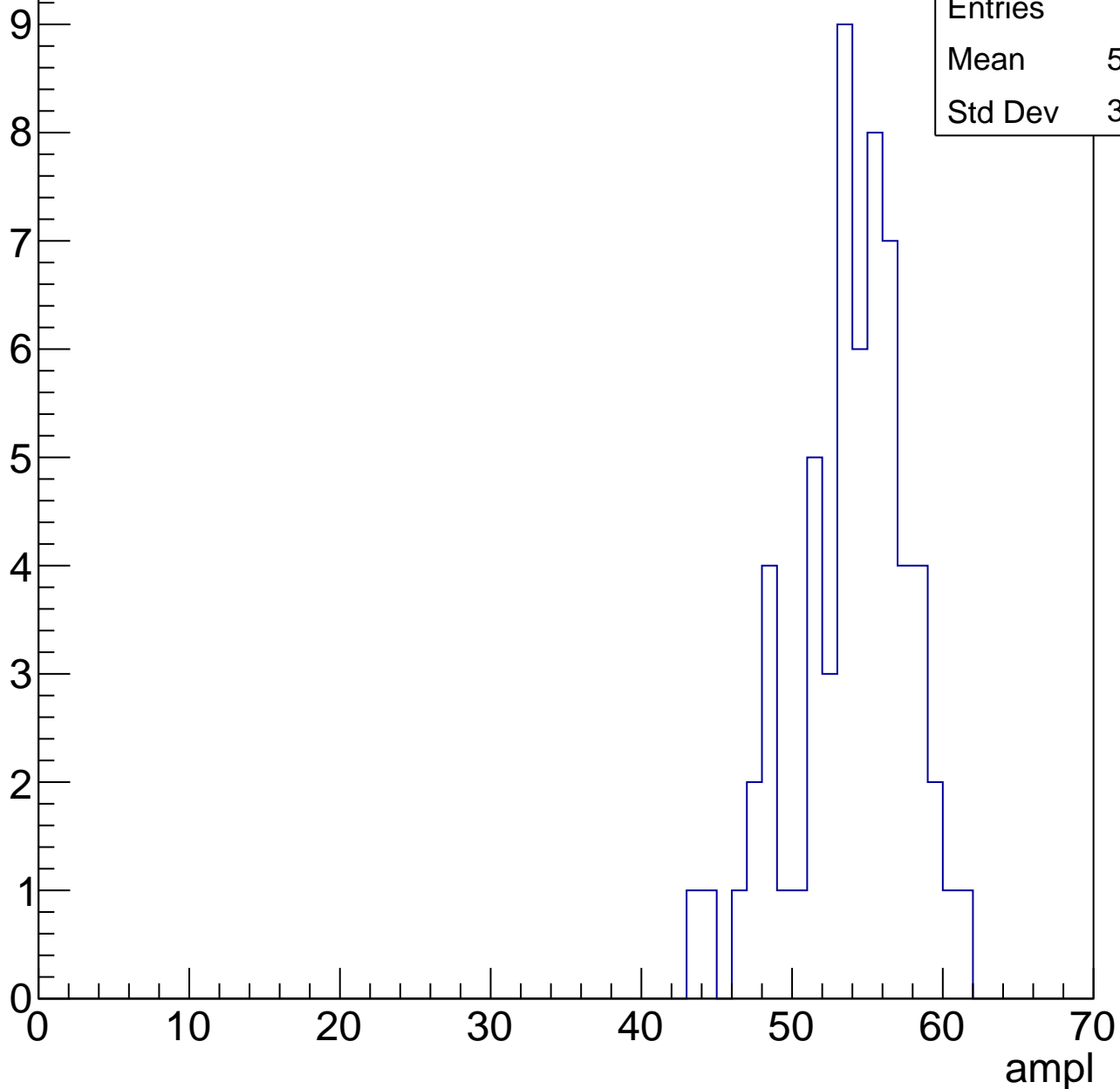
Entries	69
Mean	47.55
Std Dev	3.549



B1L101S, U15-ch45, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

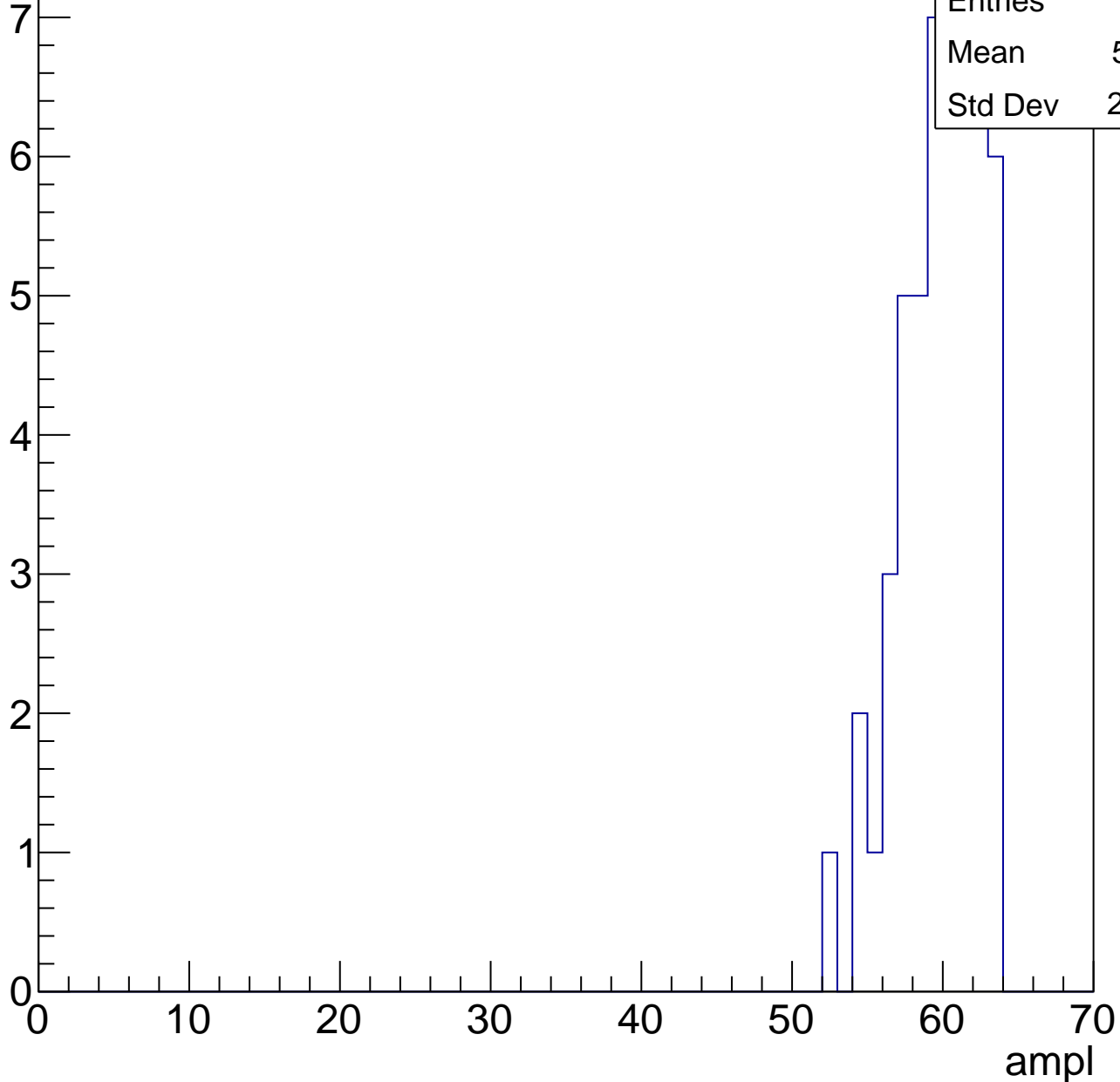


Entries	61
Mean	53.46
Std Dev	3.814

B1L101S, U15-ch45, adc5

calib_packv5_042523_0143.root, FC#0, port D2

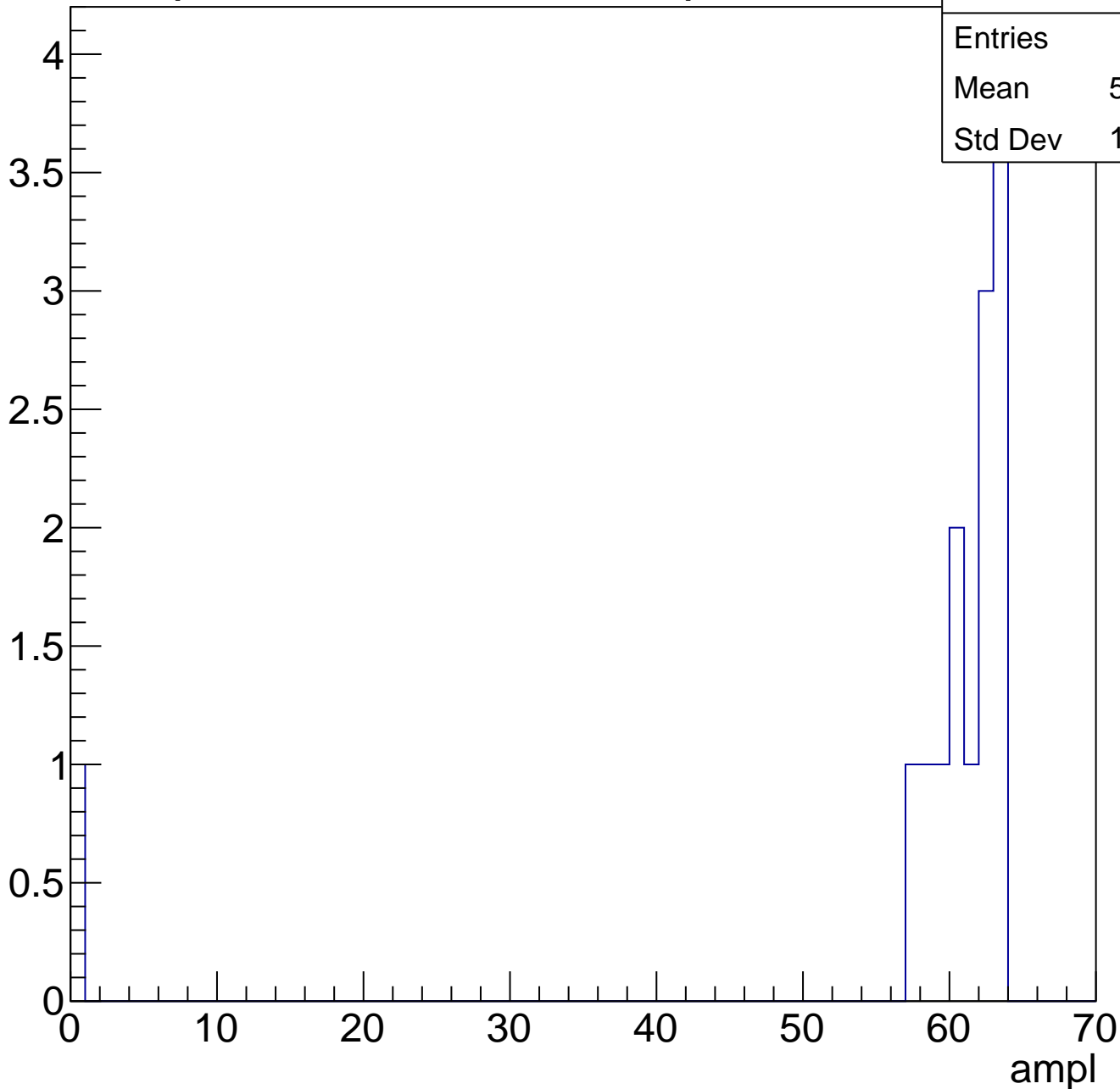
Entry



B1L101S, U15-ch45, adc6

calib_packv5_042523_0143.root, FC#0, port D2

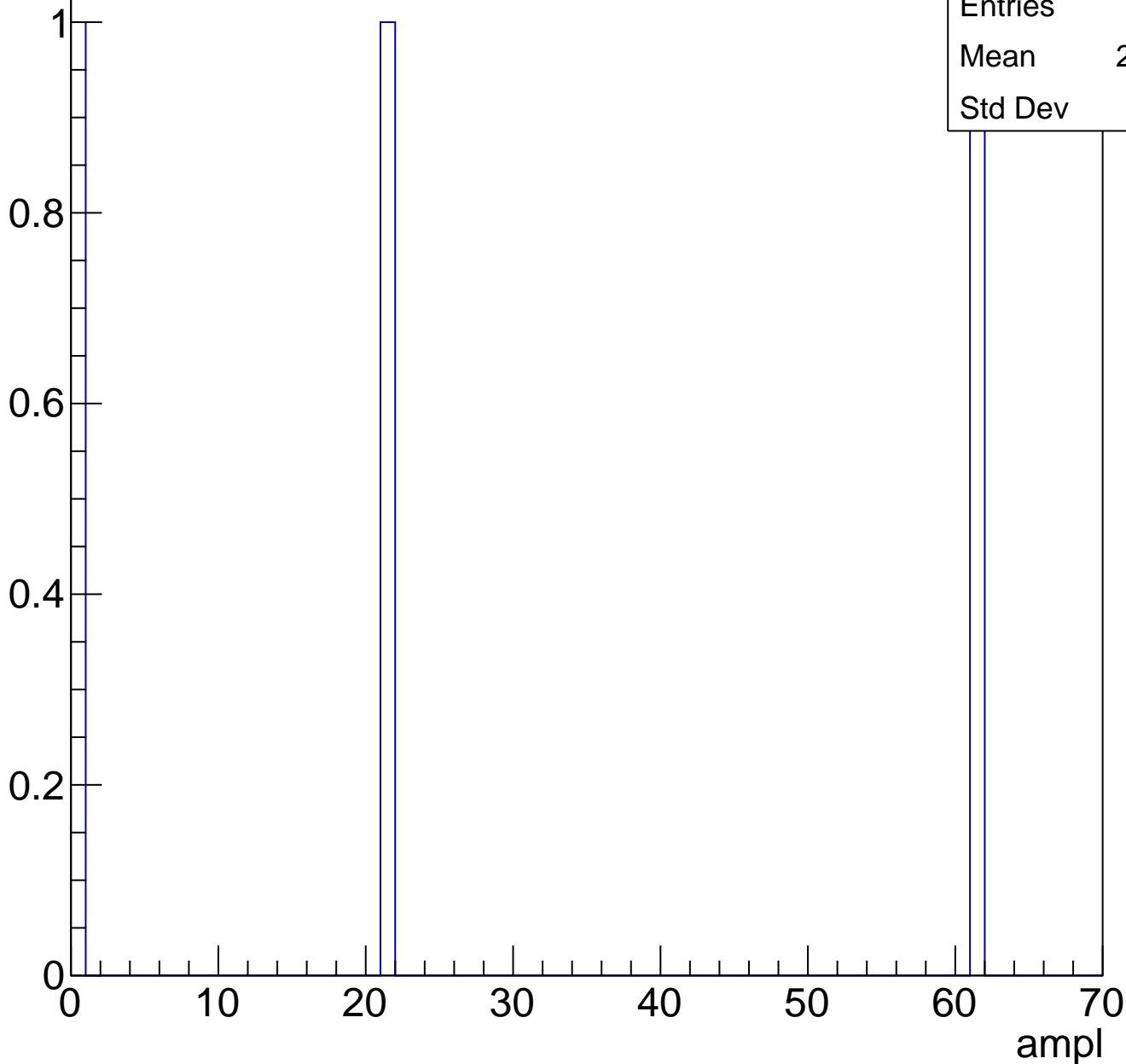
Entry



B1L101S, U15-ch45, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch46, adc0

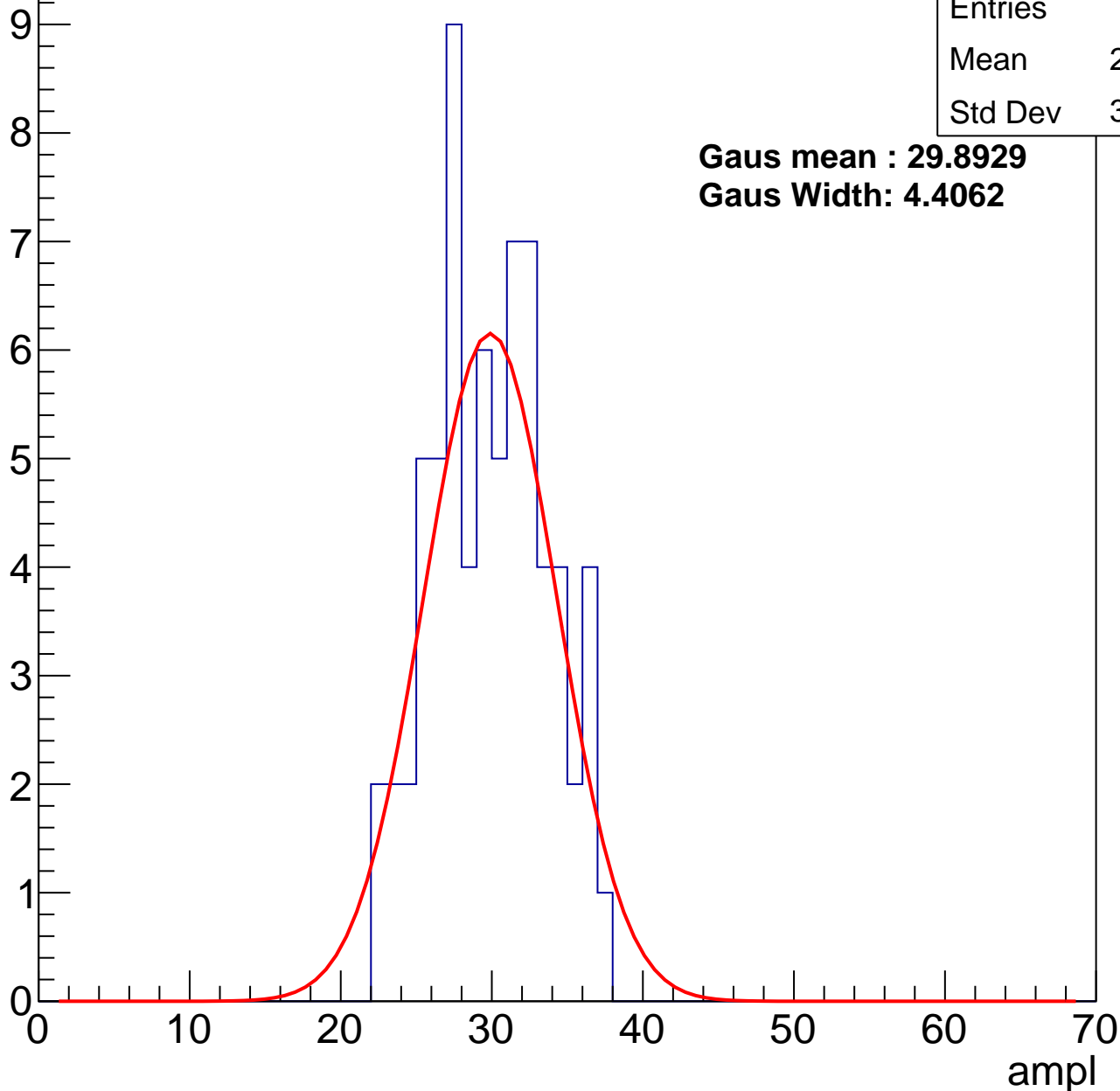
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.45
Std Dev	3.736

Gaus mean : 29.8929

Gaus Width: 4.4062



B1L101S, U15-ch46, adc1

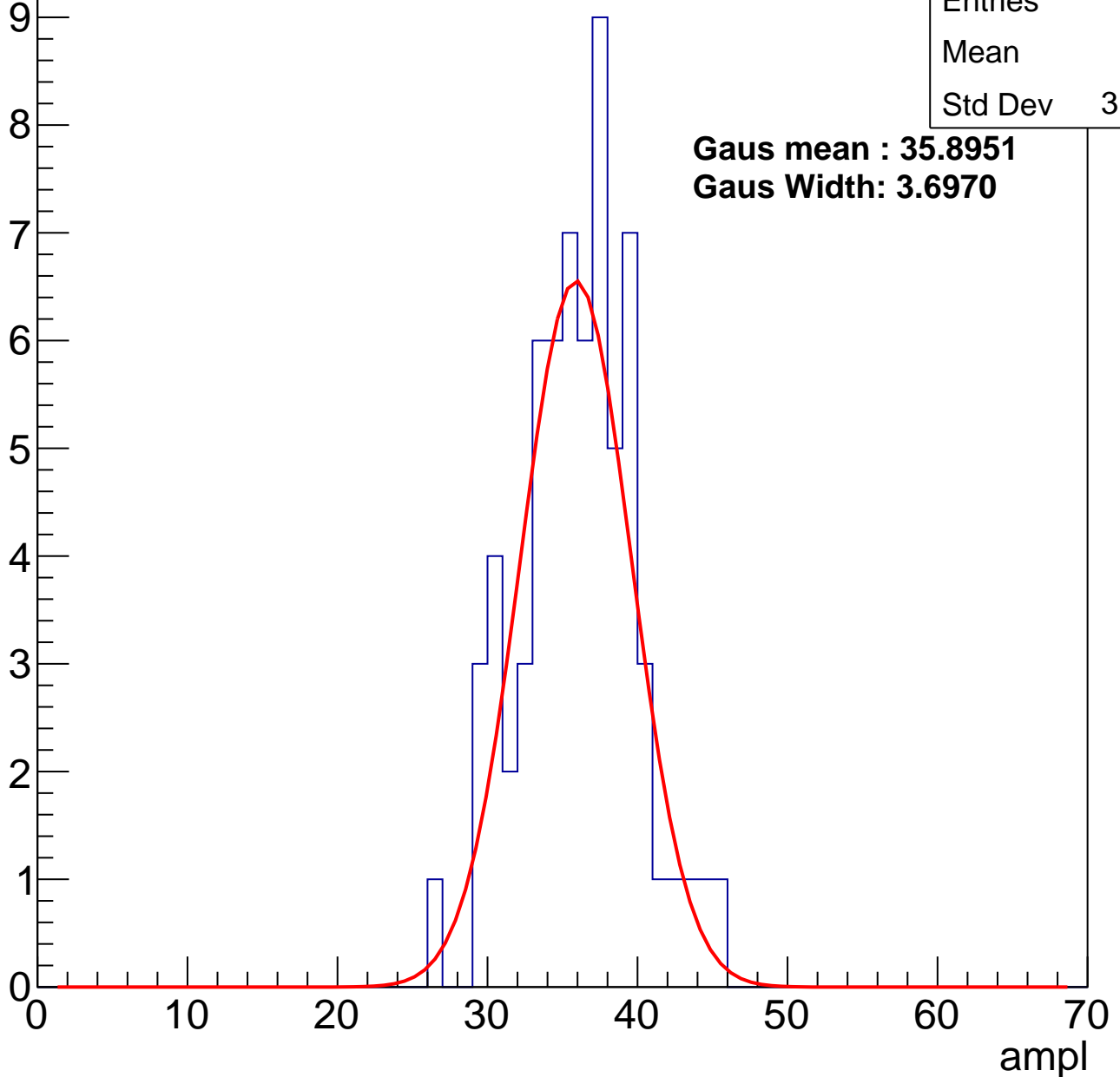
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.6
Std Dev	3.782

Gaus mean : 35.8951

Gaus Width: 3.6970



B1L101S, U15-ch46, adc2

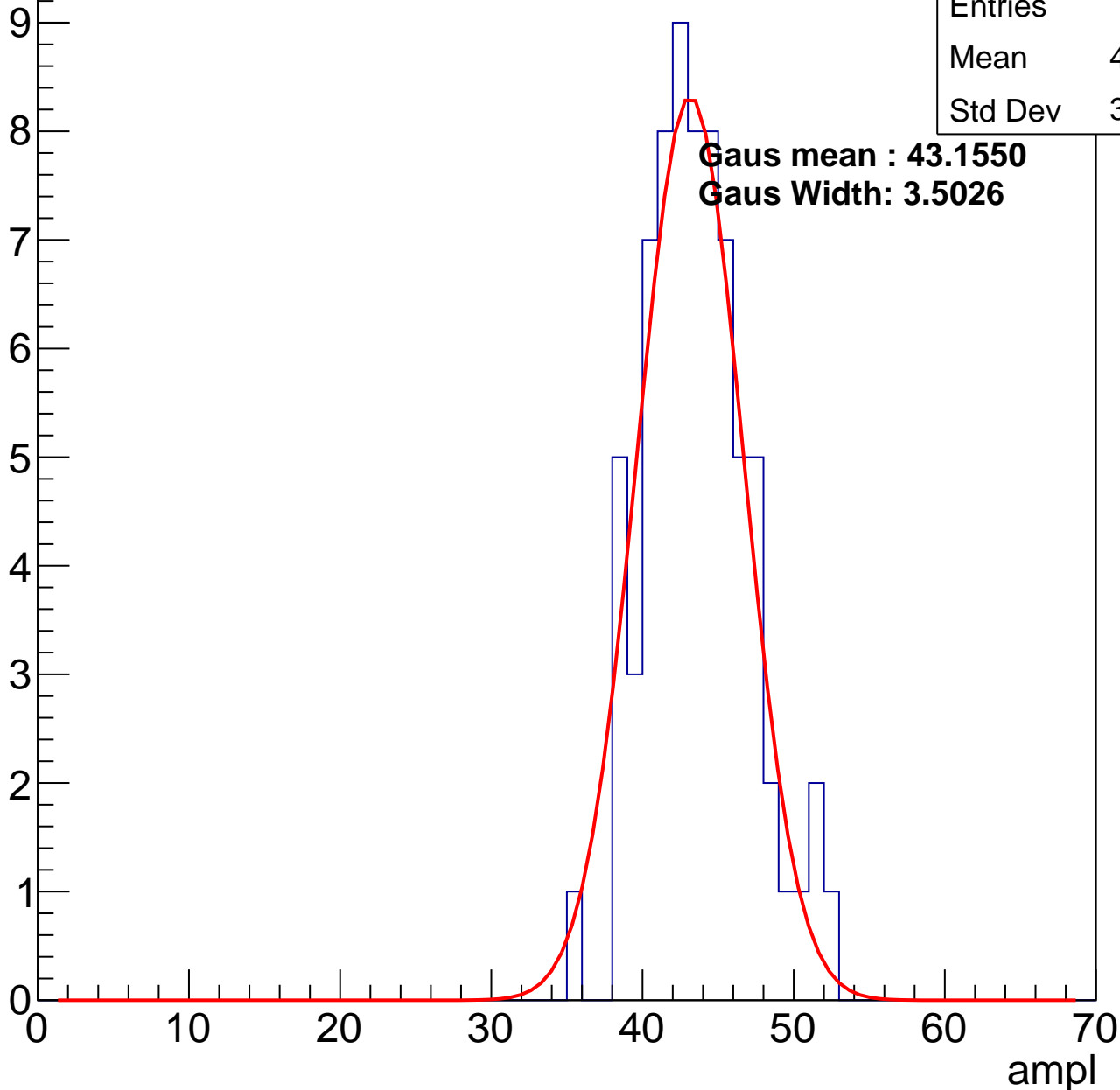
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.19
Std Dev	3.399

Gaus mean : 43.1550

Gaus Width: 3.5026

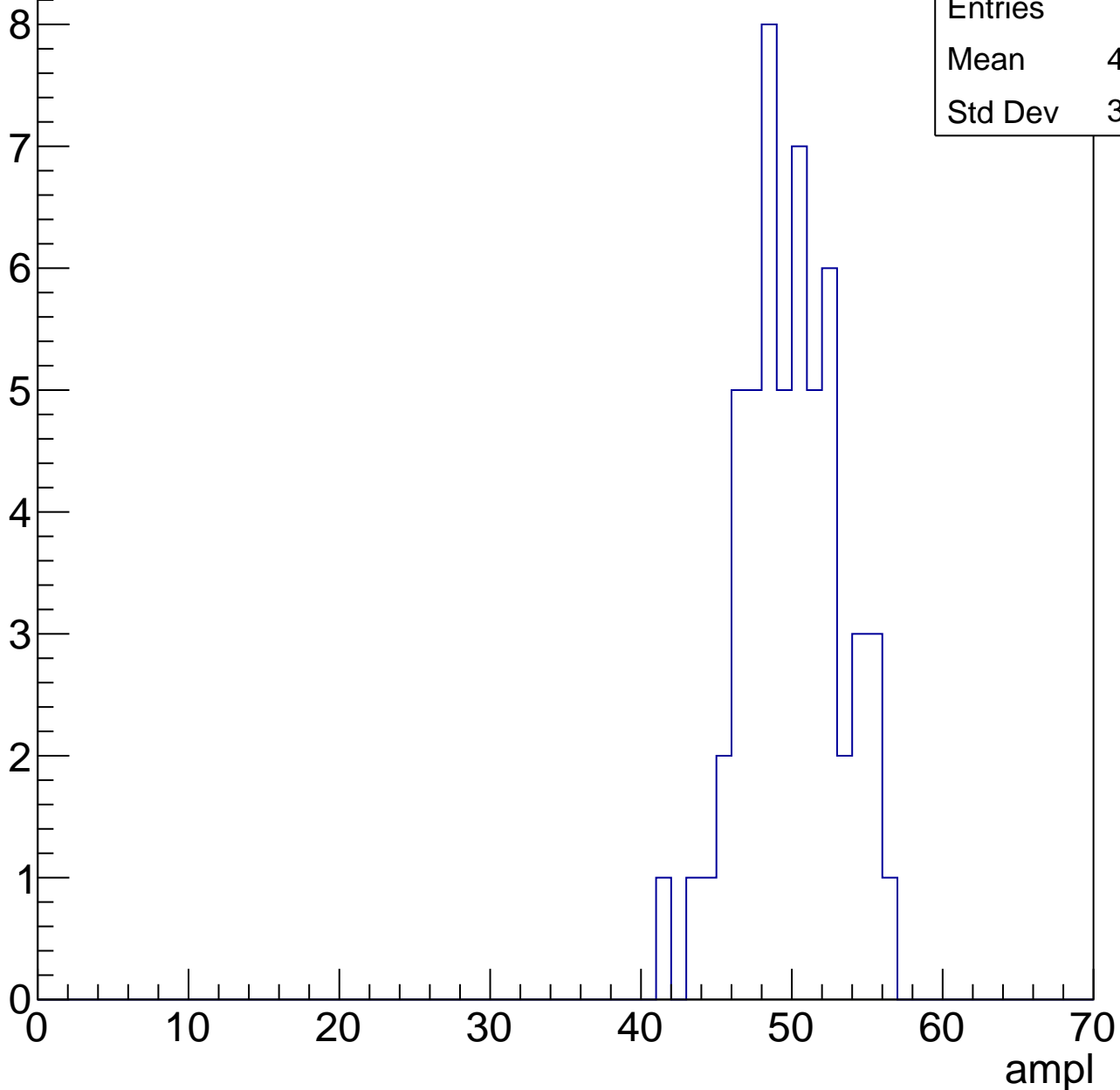


B1L101S, U15-ch46, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	49.42
Std Dev	3.206

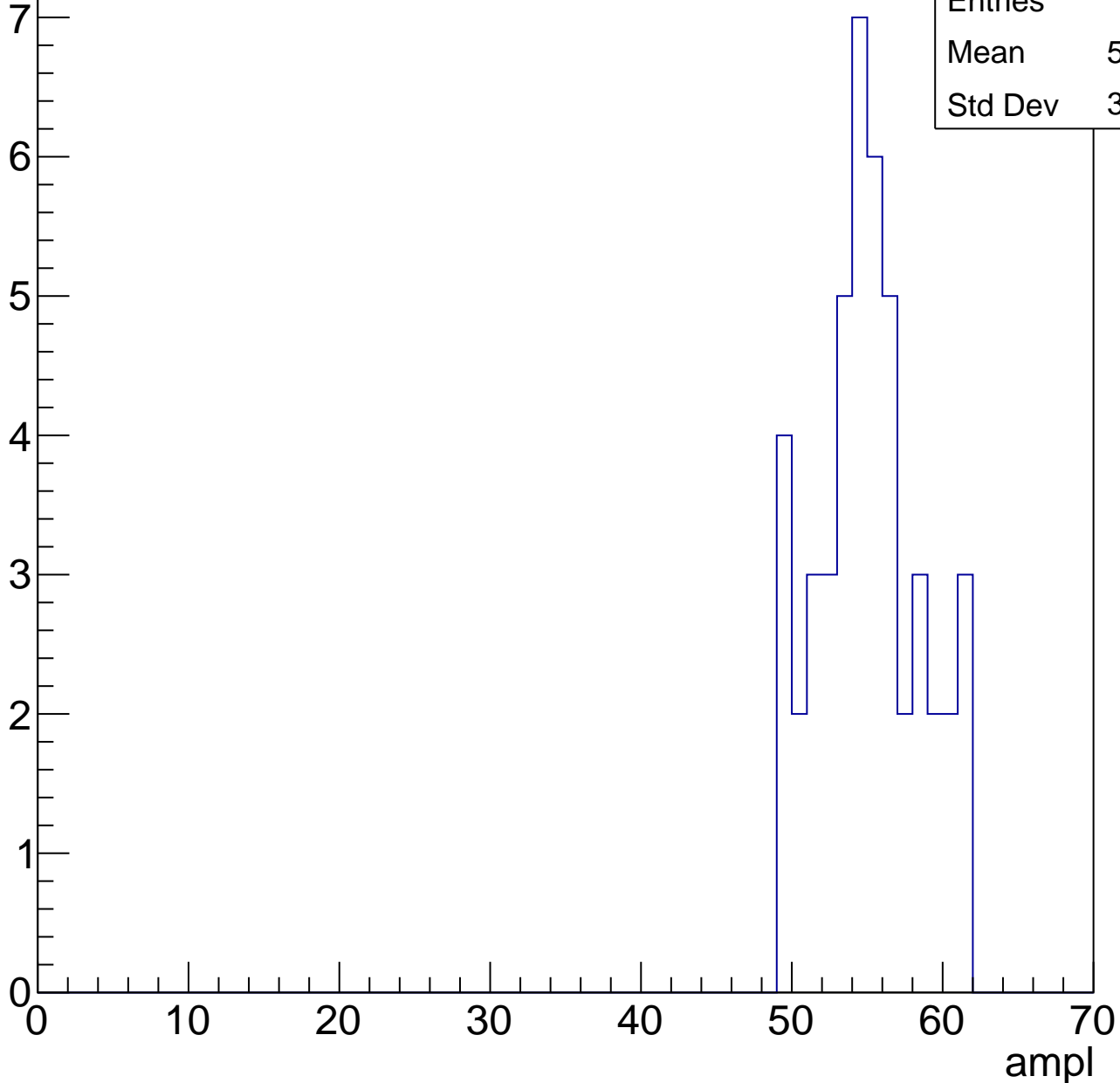


B1L101S, U15-ch46, adc4

calib_packv5_042523_0143.root, FC#0, port D2

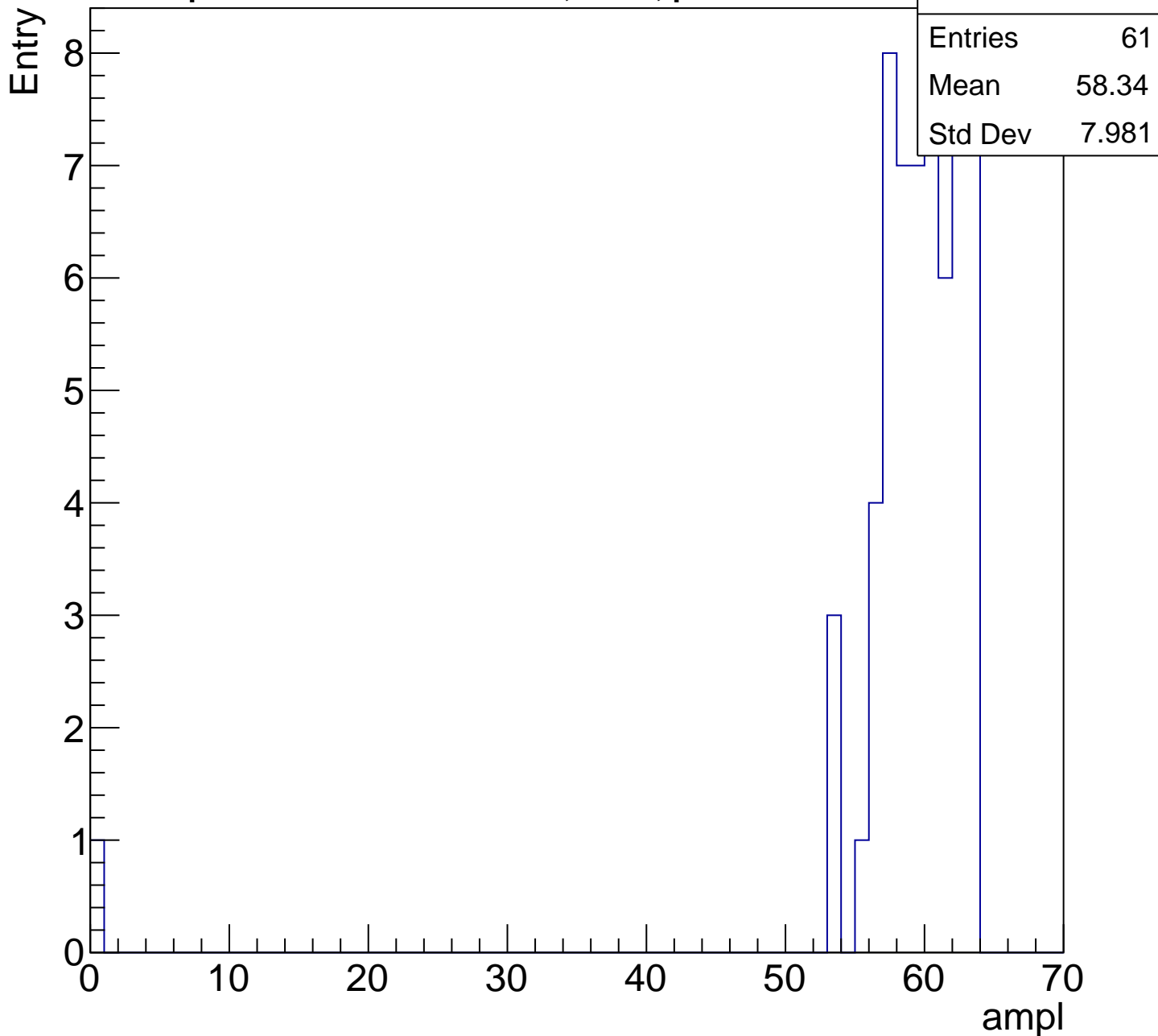
Entry

Entries	47
Mean	54.62
Std Dev	3.323



B1L101S, U15-ch46, adc5

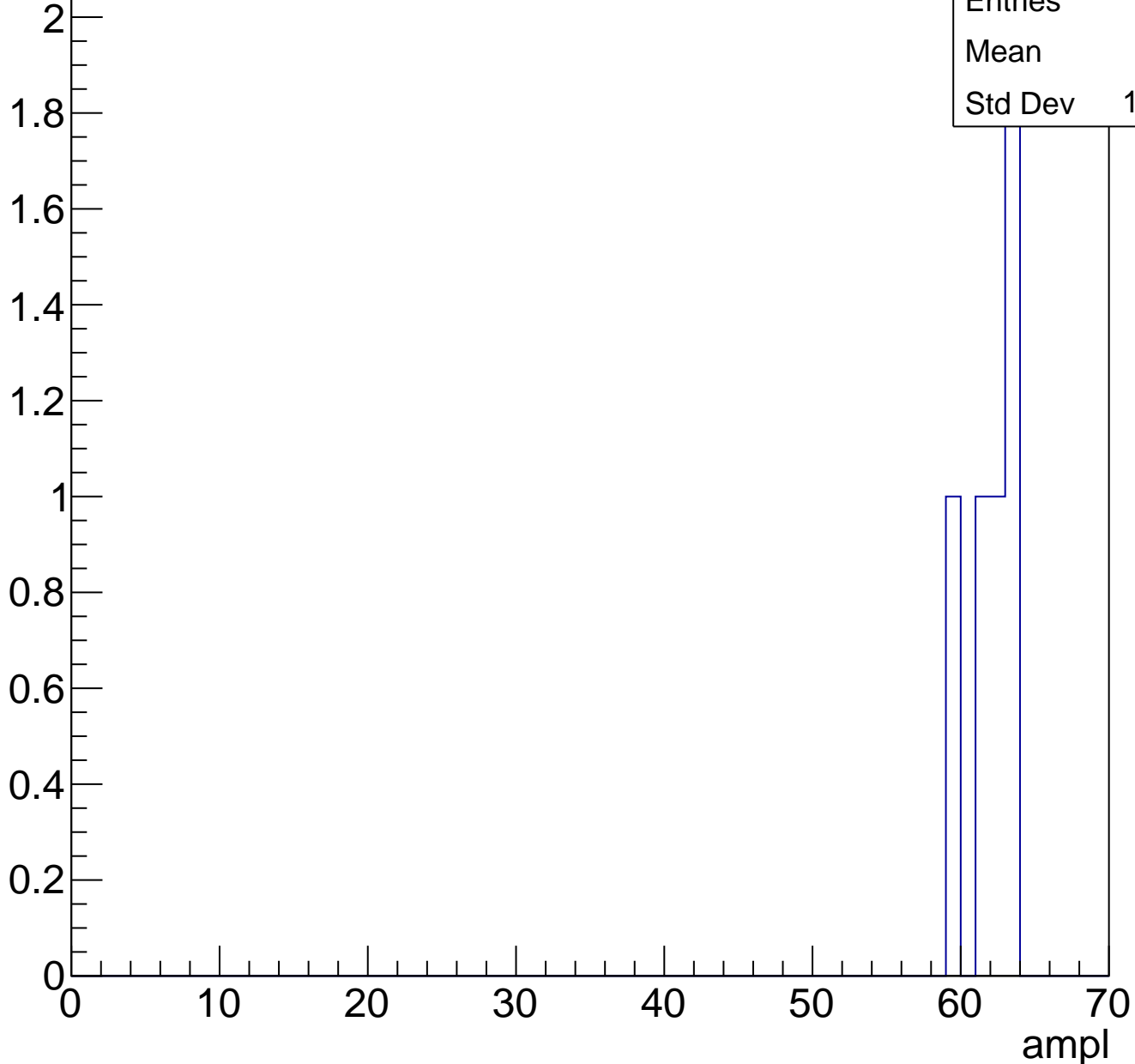
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch46, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch46, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U15-ch47, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	30.62
Std Dev	4.727

Gaus mean : 31.1506

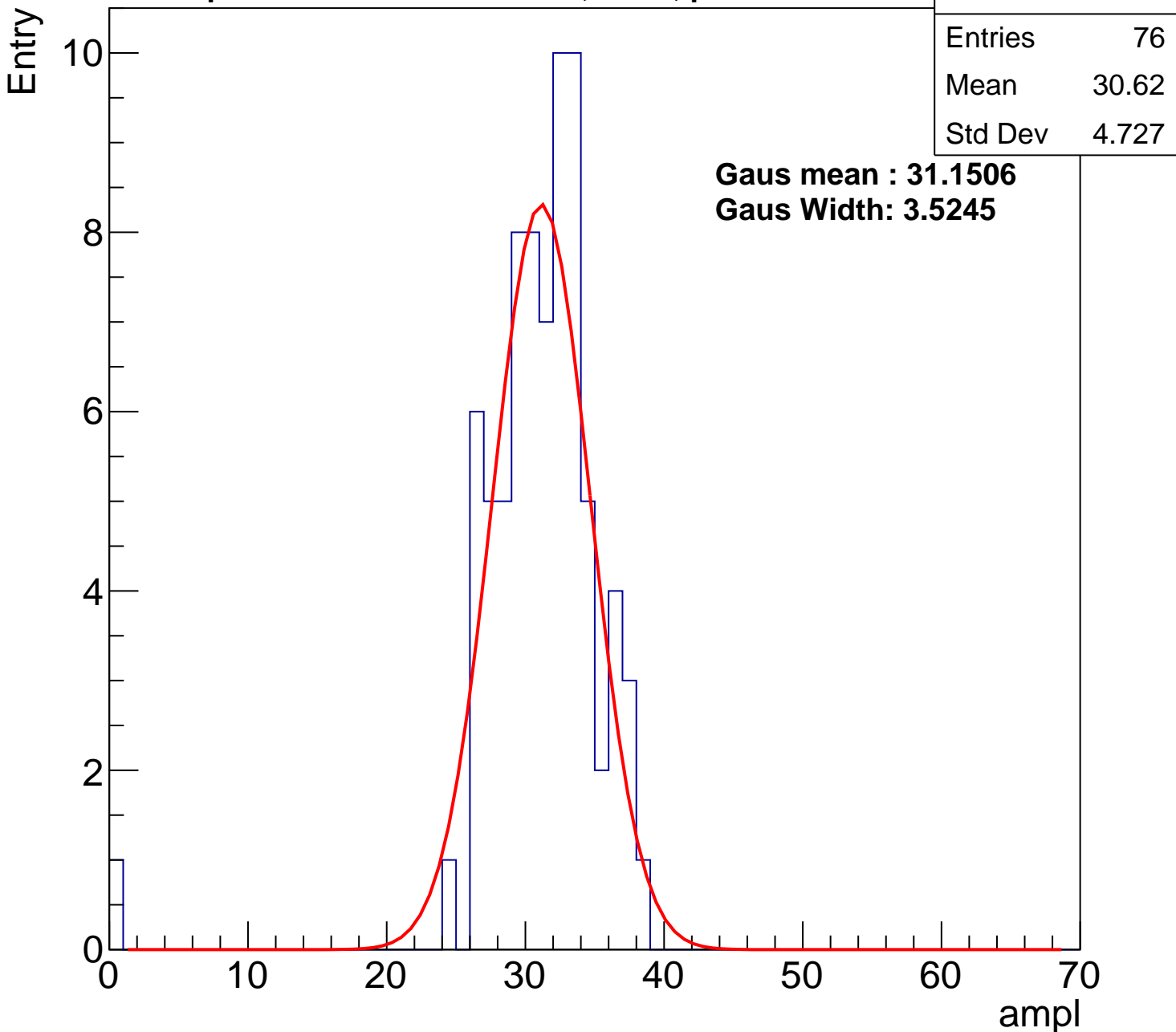
Gaus Width: 3.5245

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch47, adc1

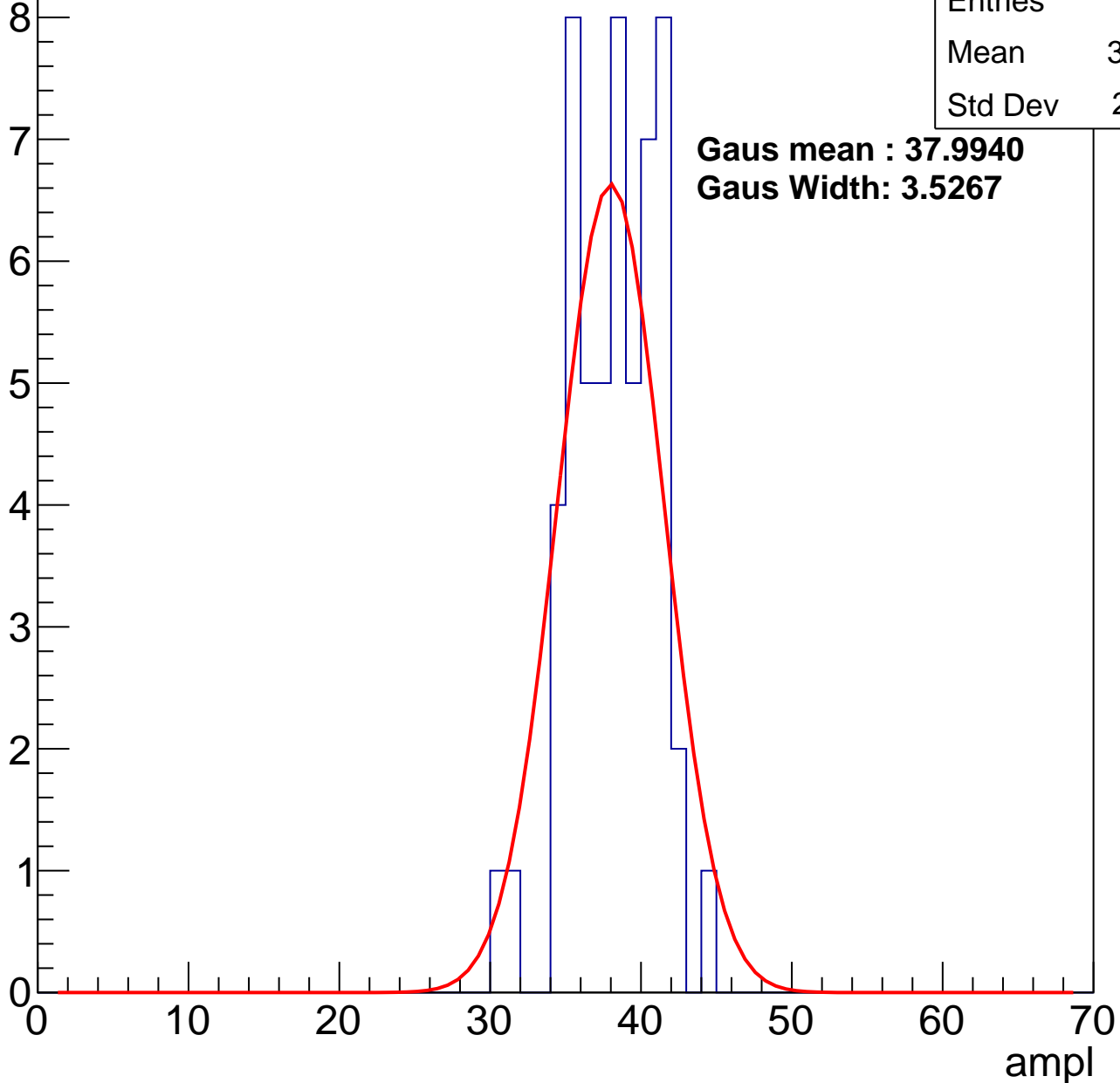
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	37.76
Std Dev	2.841

Gaus mean : 37.9940

Gaus Width: 3.5267



B1L101S, U15-ch47, adc2

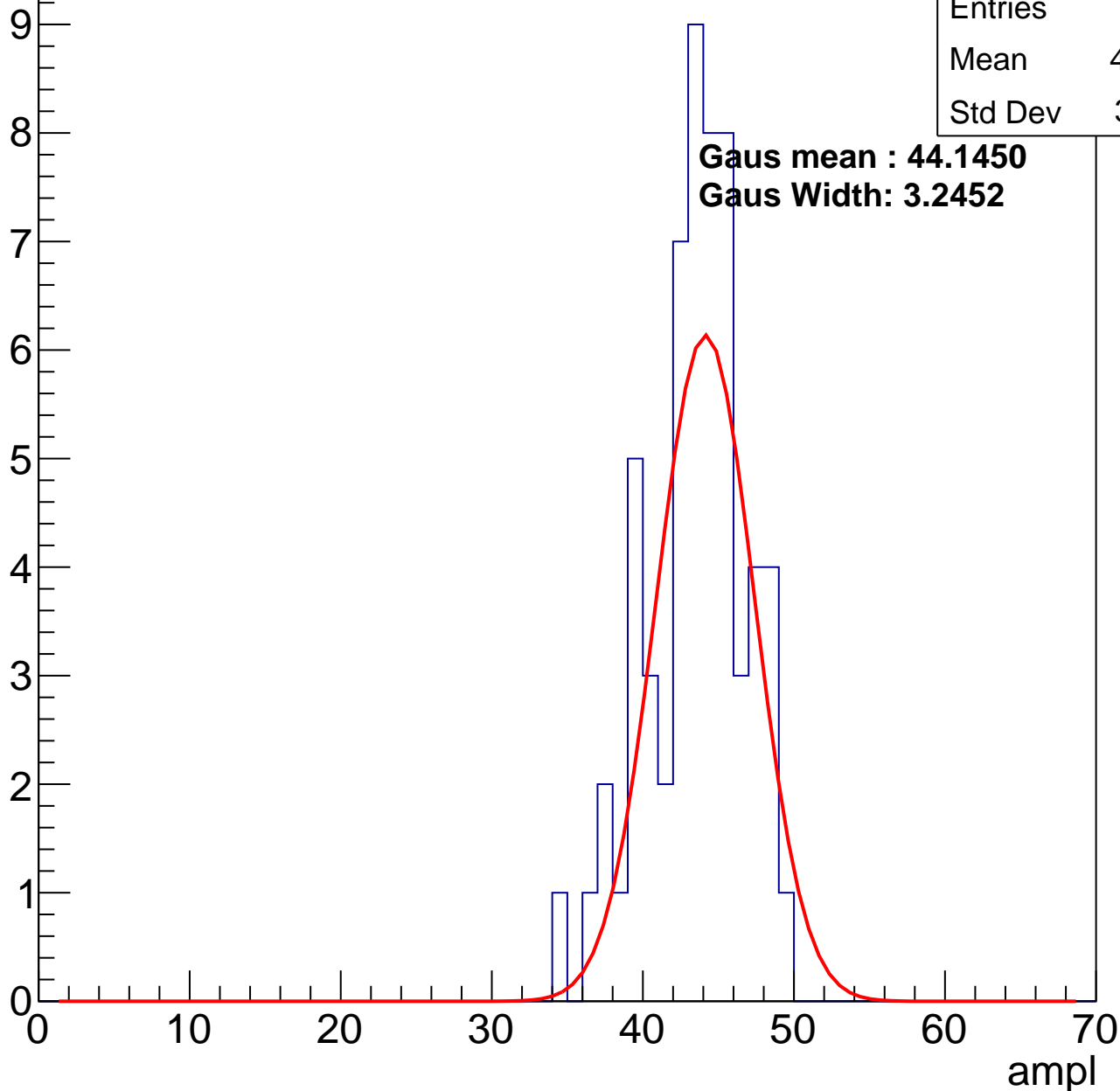
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.03
Std Dev	3.231

Gaus mean : 44.1450

Gaus Width: 3.2452



B1L101S, U15-ch47, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	50.28
Std Dev	3.353

Entry

10

8

6

4

2

0

0

10

20

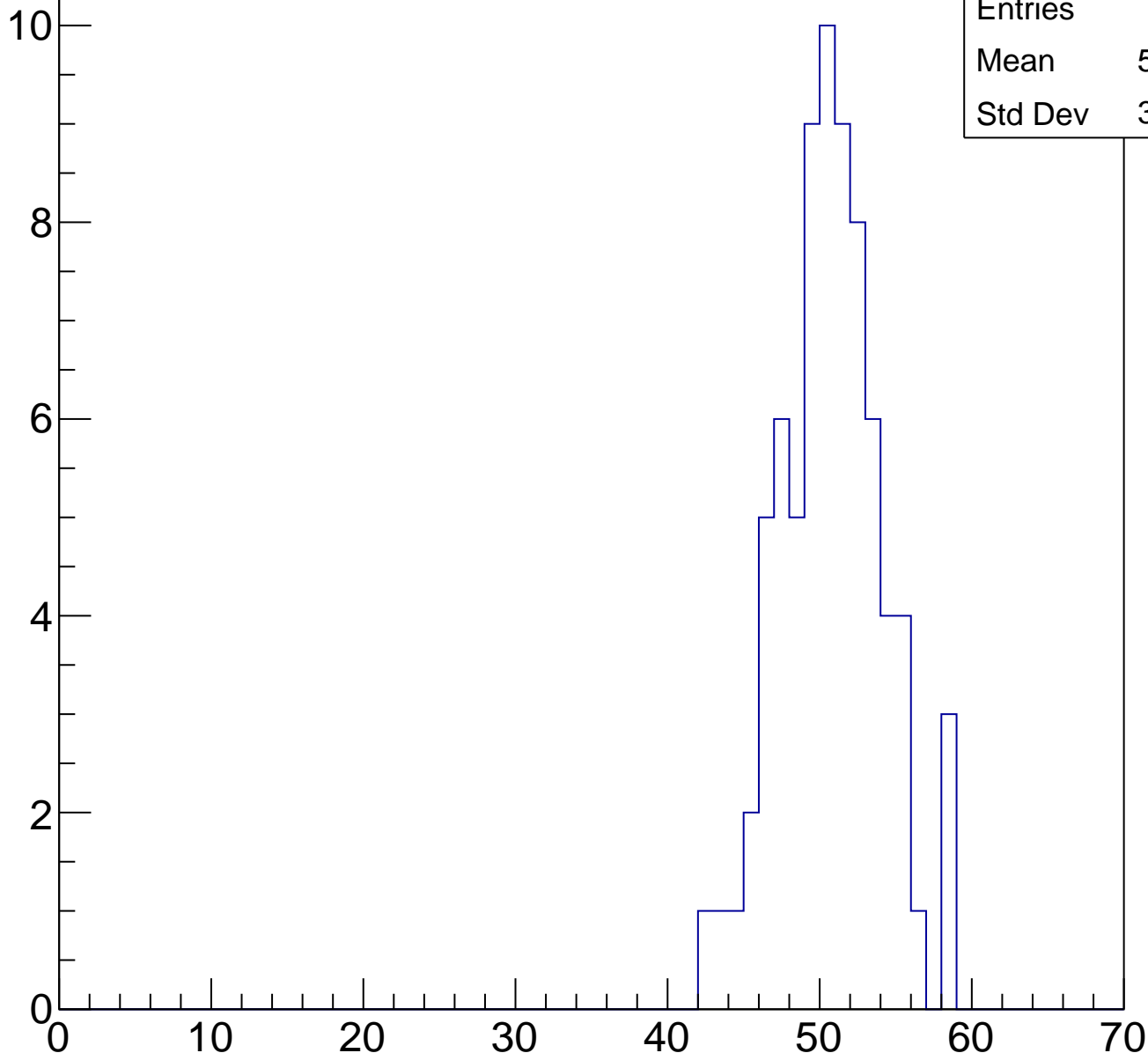
30

40

50

60

ampl

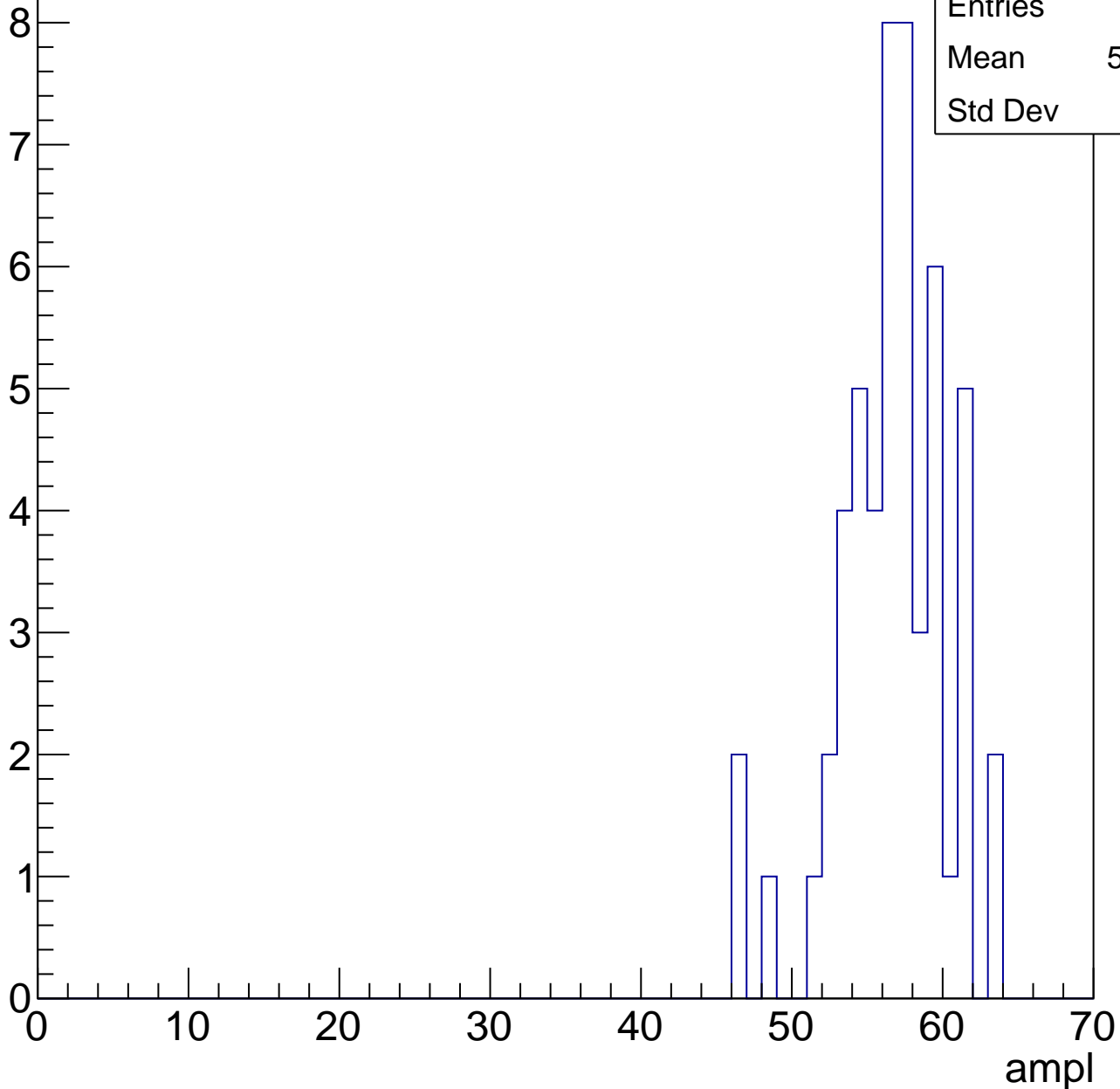


B1L101S, U15-ch47, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	56.15
Std Dev	3.65

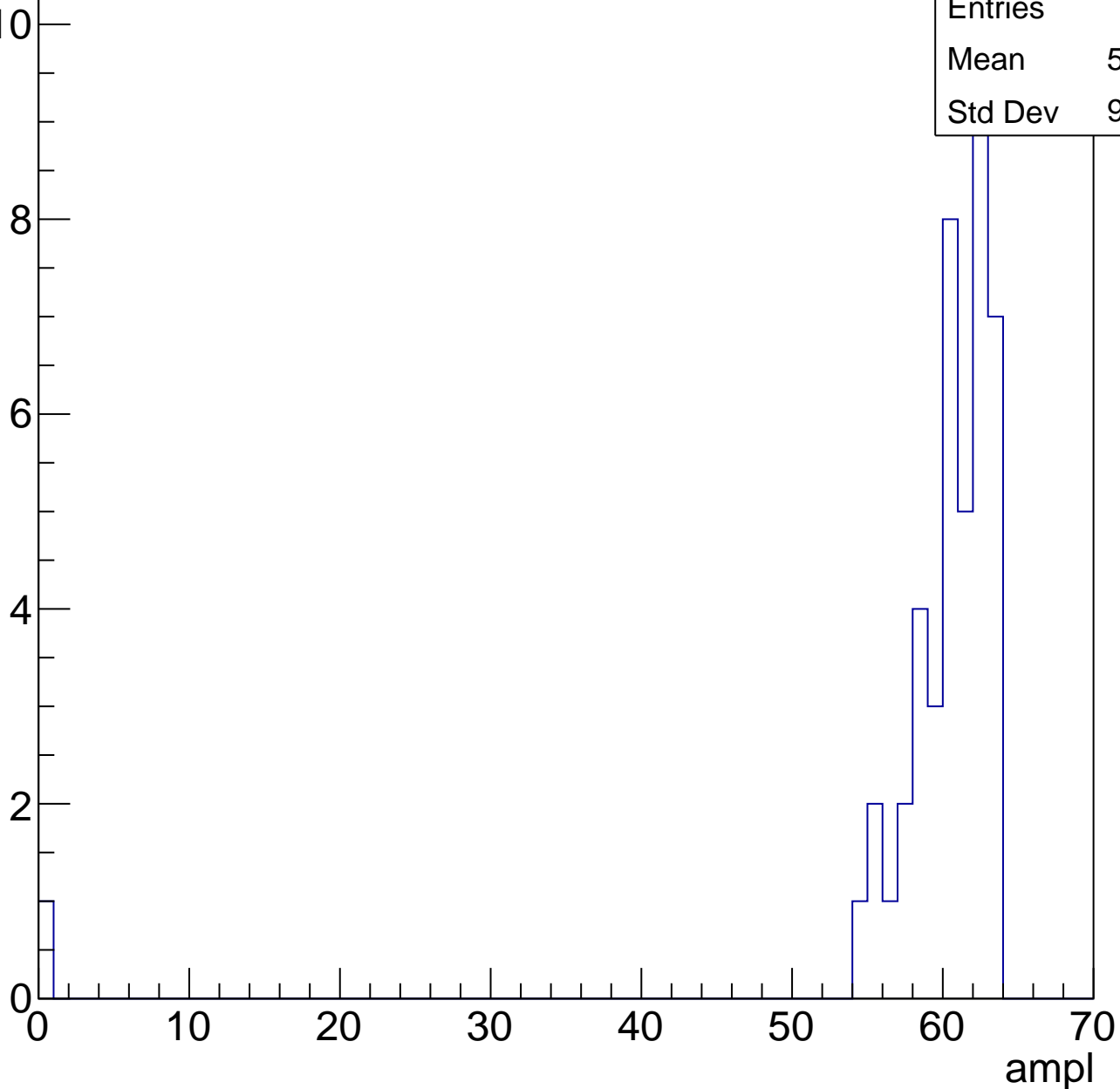


B1L101S, U15-ch47, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	58.84
Std Dev	9.278



B1L101S, U15-ch47, adc6

calib_packv5_042523_0143.root, FC#0, port D2

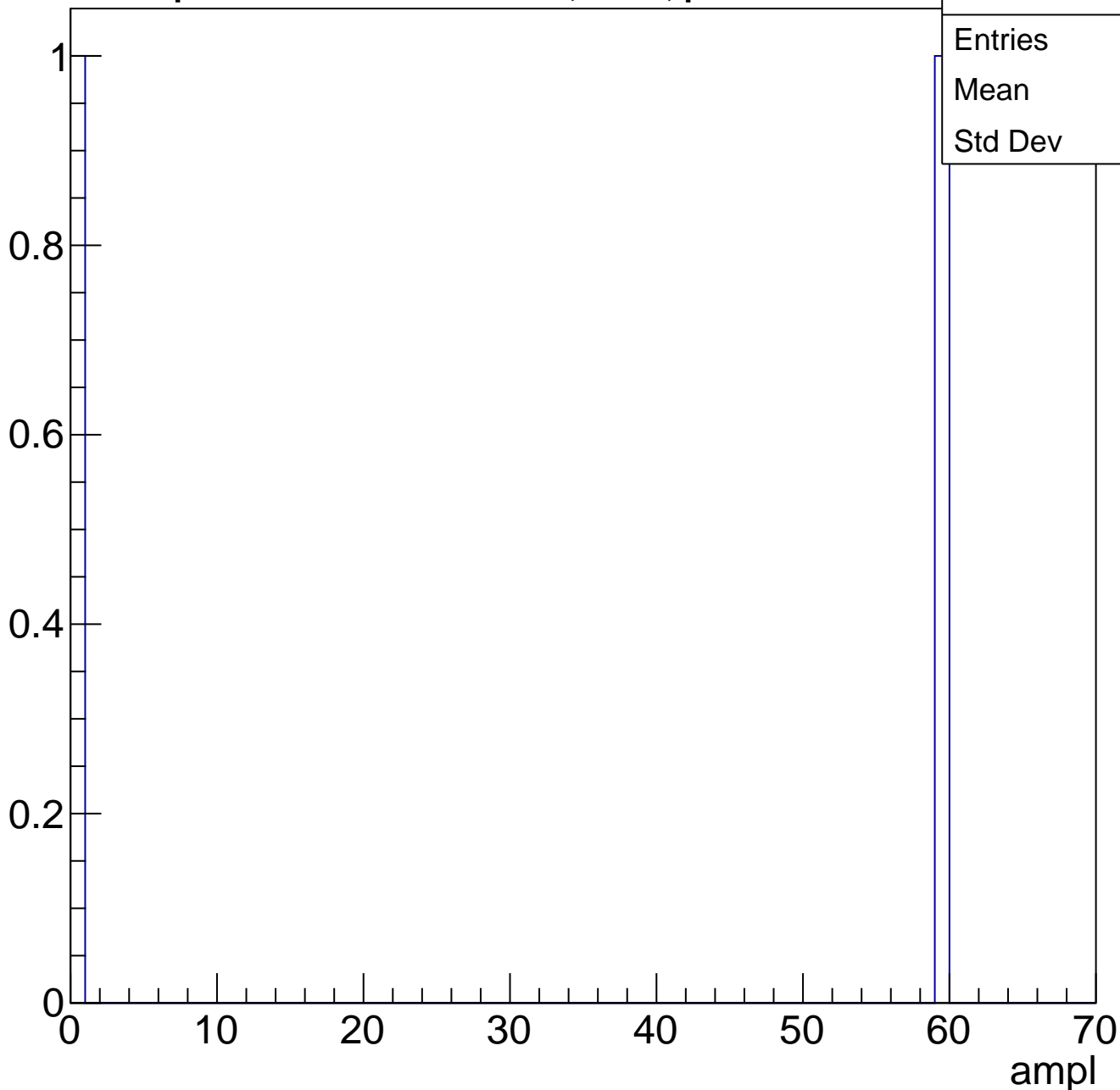
Entry



B1L101S, U15-ch47, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	29.5
Std Dev	29.5

B1L101S, U15-ch48, adc0

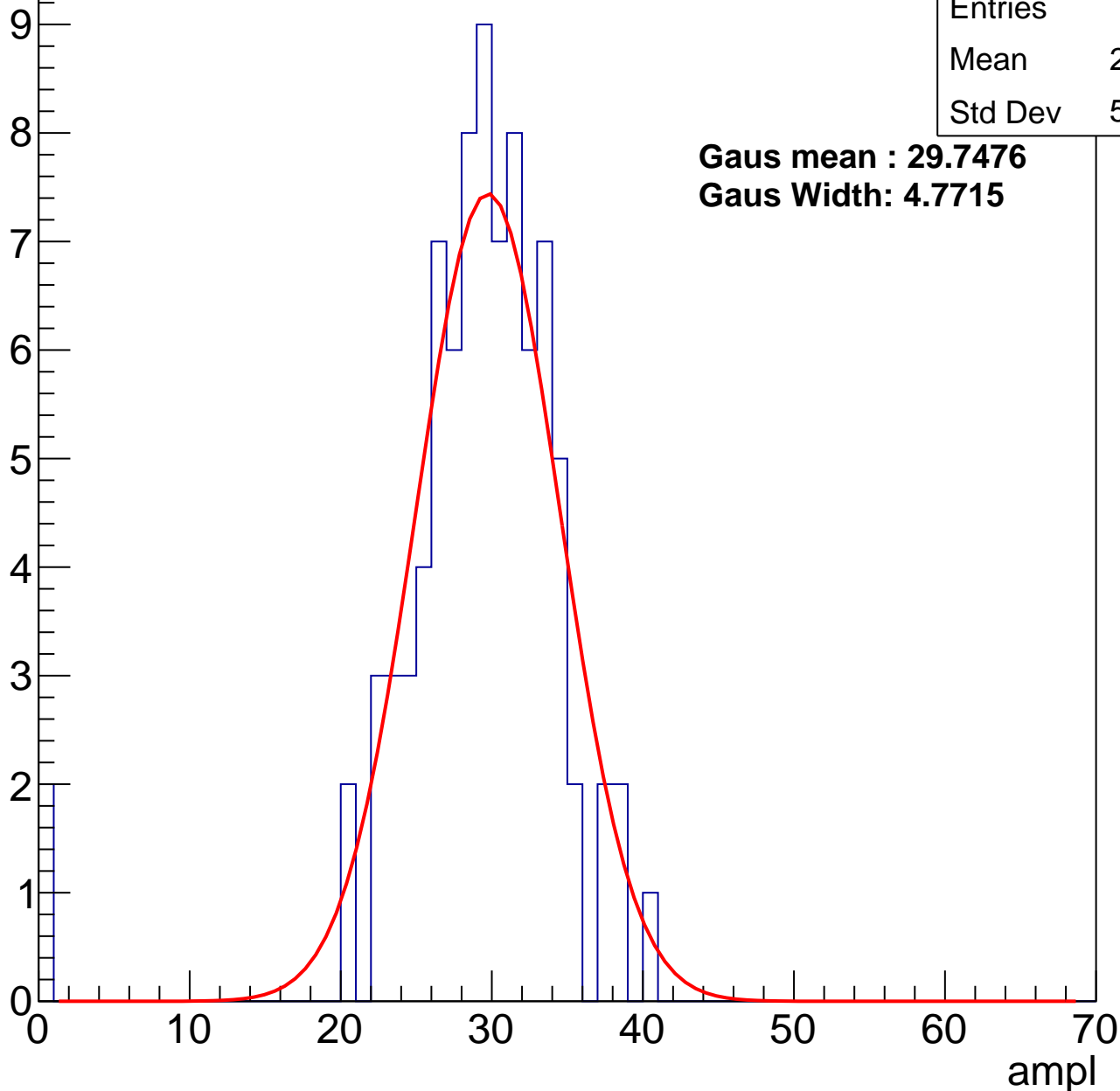
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	28.59
Std Dev	5.999

Gaus mean : 29.7476

Gaus Width: 4.7715



B1L101S, U15-ch48, adc1

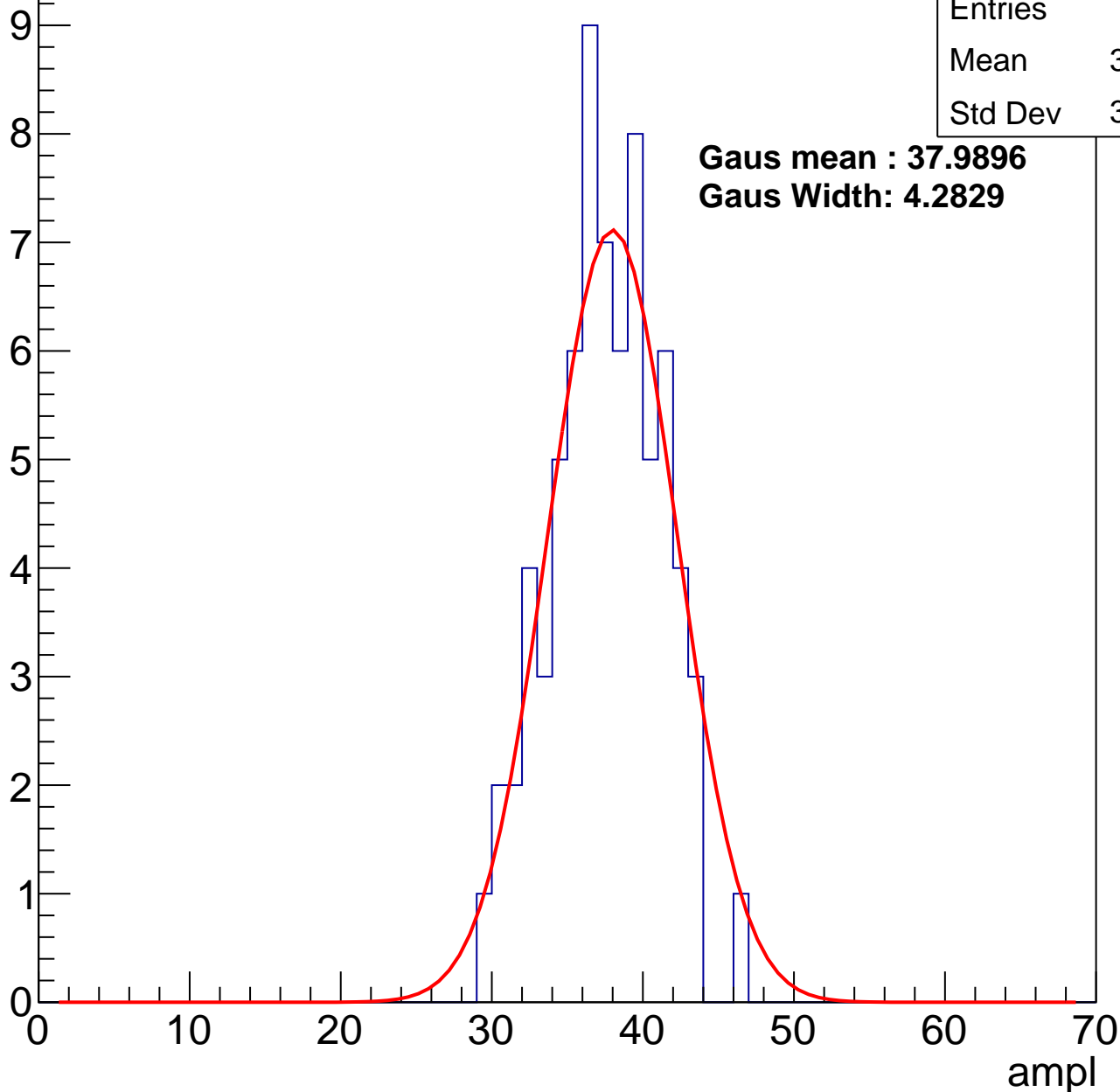
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	37.08
Std Dev	3.589

Gaus mean : 37.9896

Gaus Width: 4.2829



B1L101S, U15-ch48, adc2

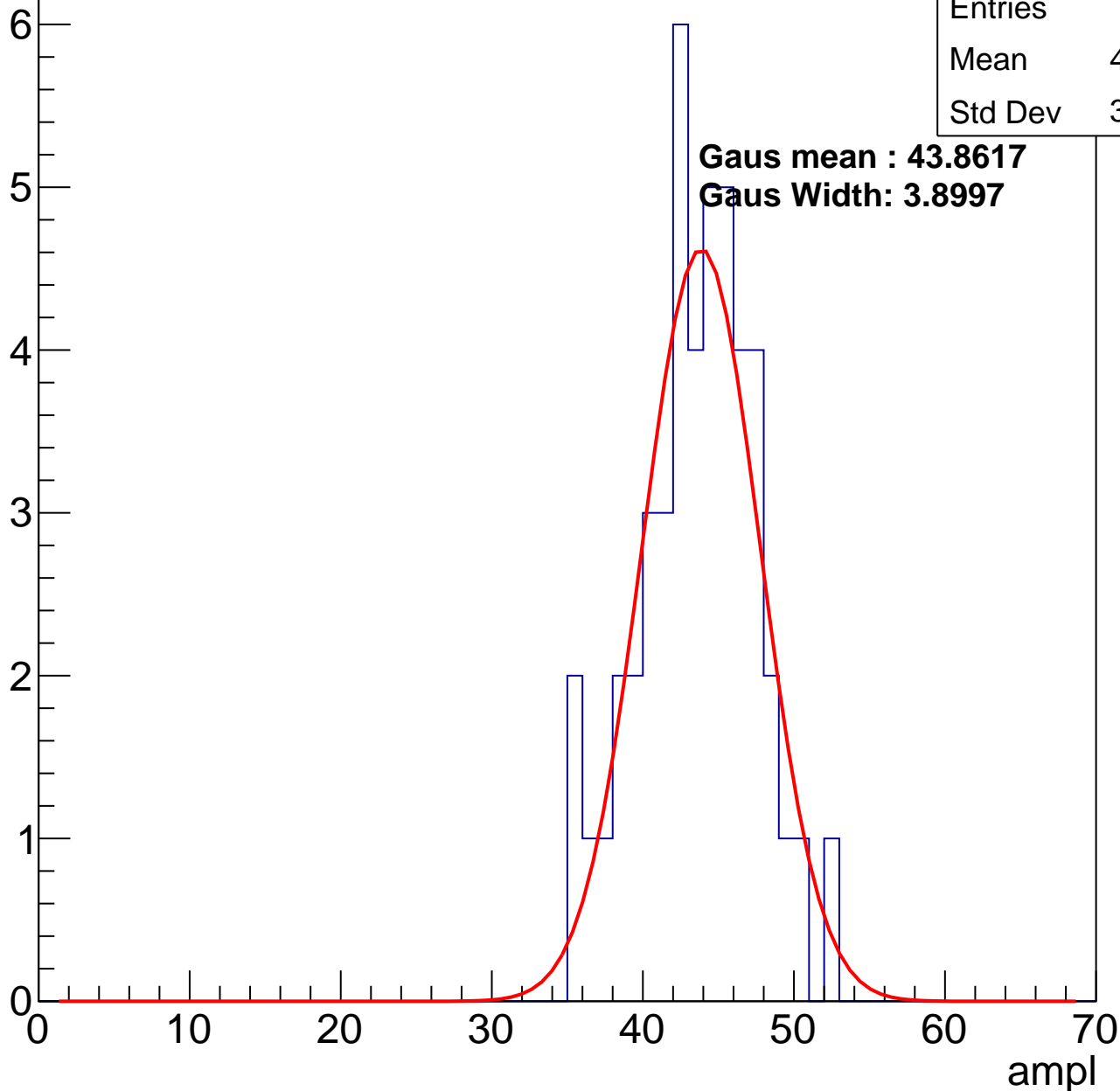
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	43.15
Std Dev	3.804

Gaus mean : 43.8617

Gaus Width: 3.8997

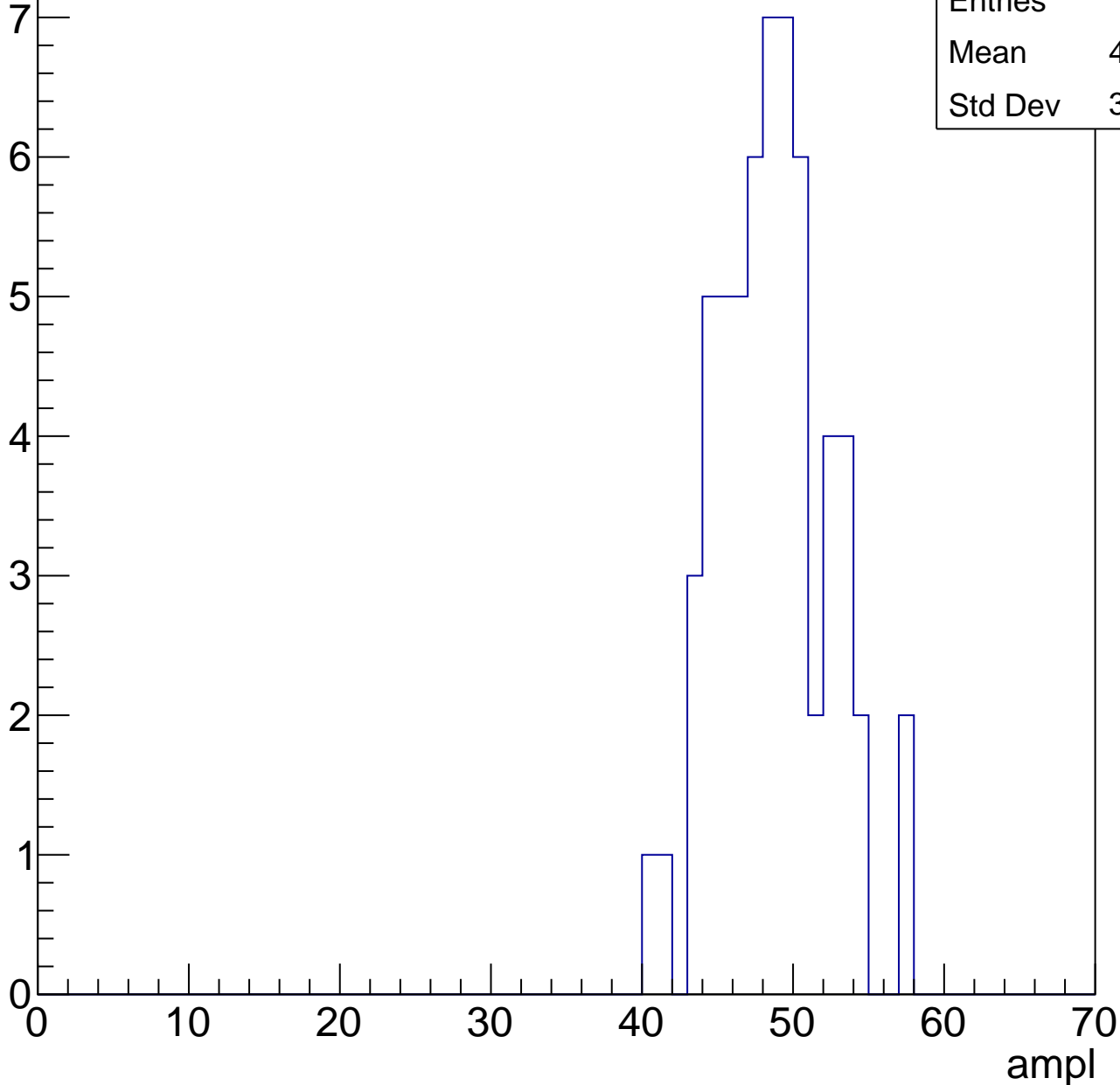


B1L101S, U15-ch48, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.17
Std Dev	3.616

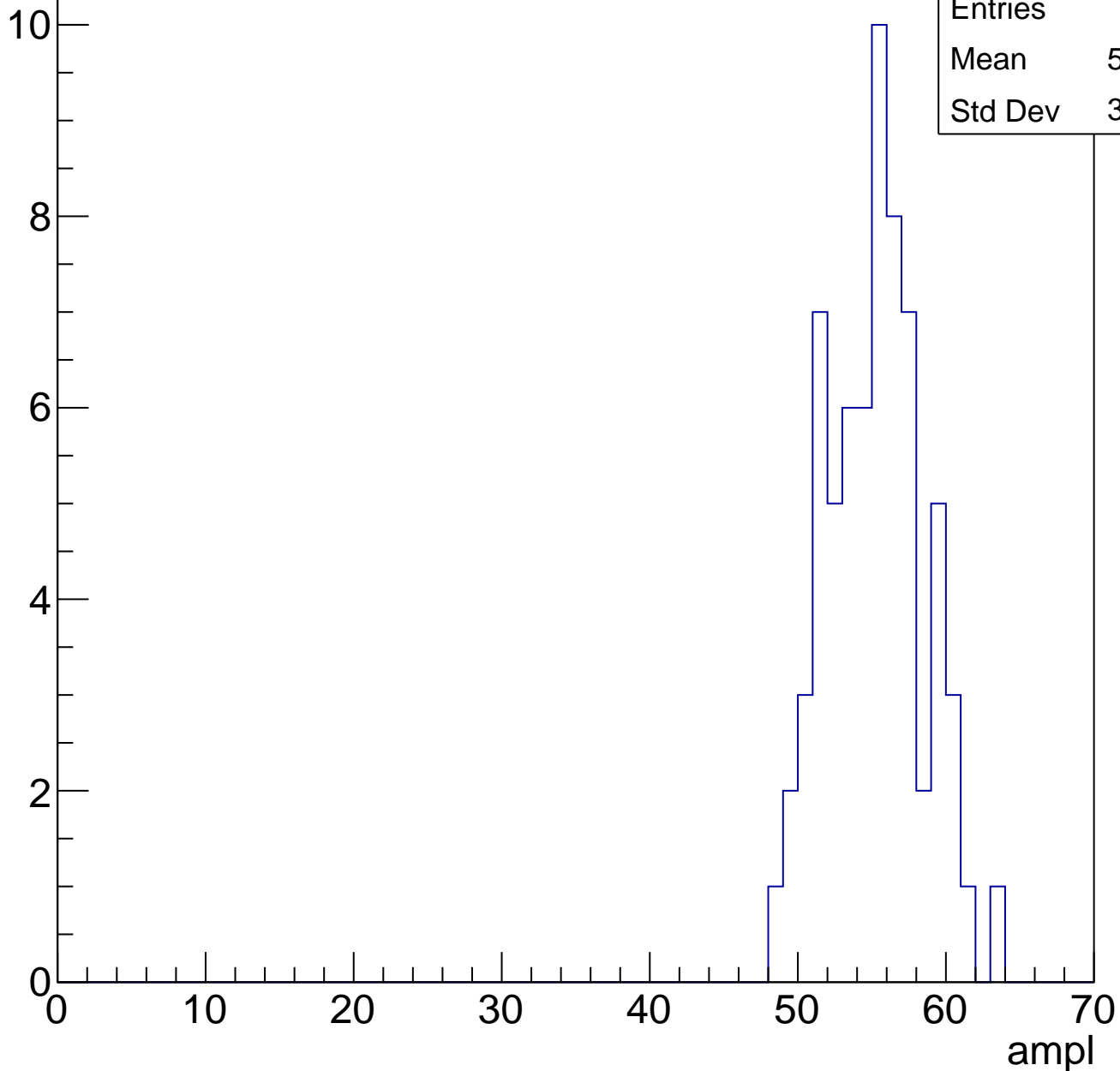


B1L101S, U15-ch48, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	54.73
Std Dev	3.203

Entry



B1L101S, U15-ch48, adc5

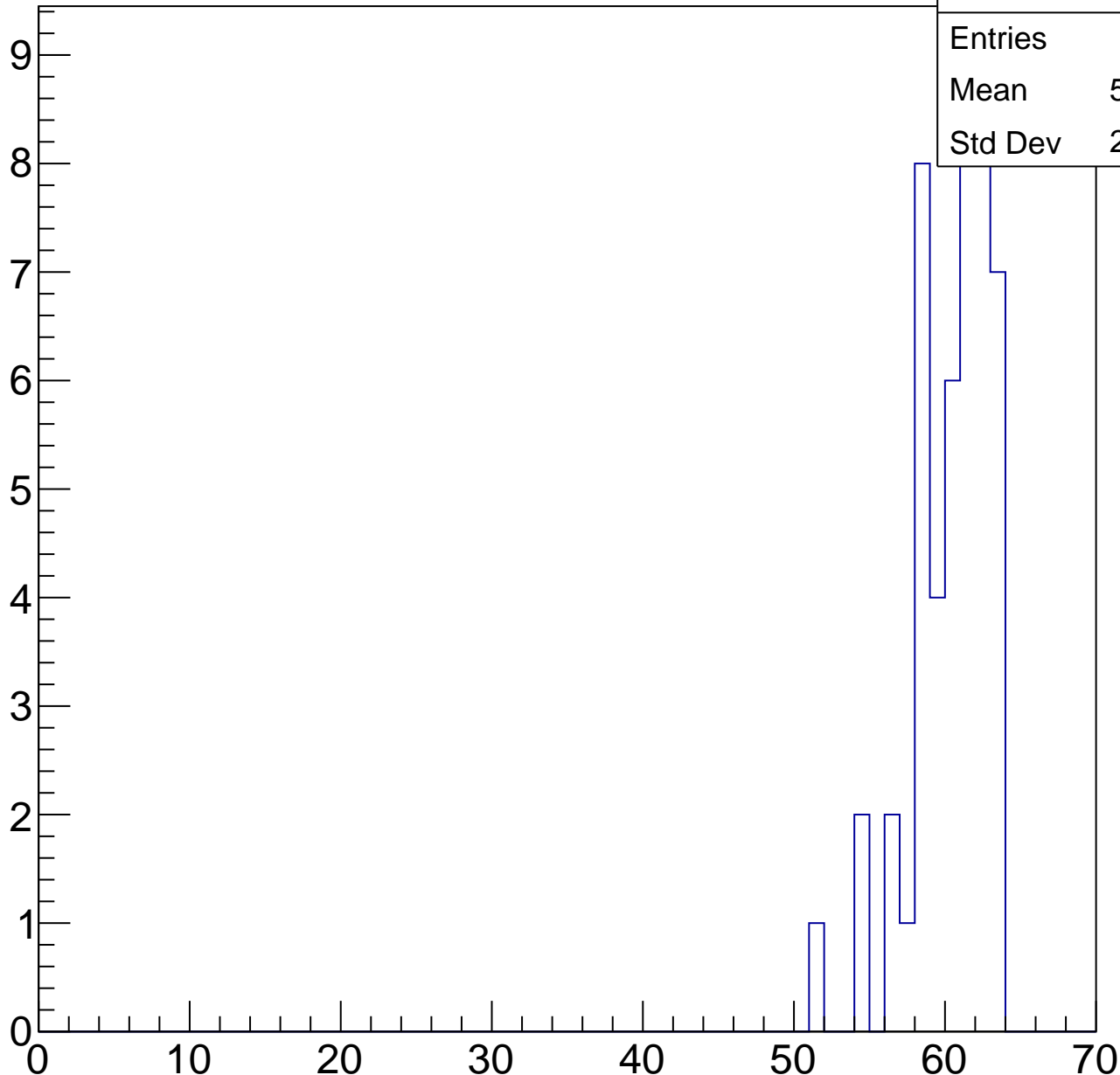
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.92
Std Dev	2.625

ampl



B1L101S, U15-ch48, adc6

calib_packv5_042523_0143.root, FC#0, port D2

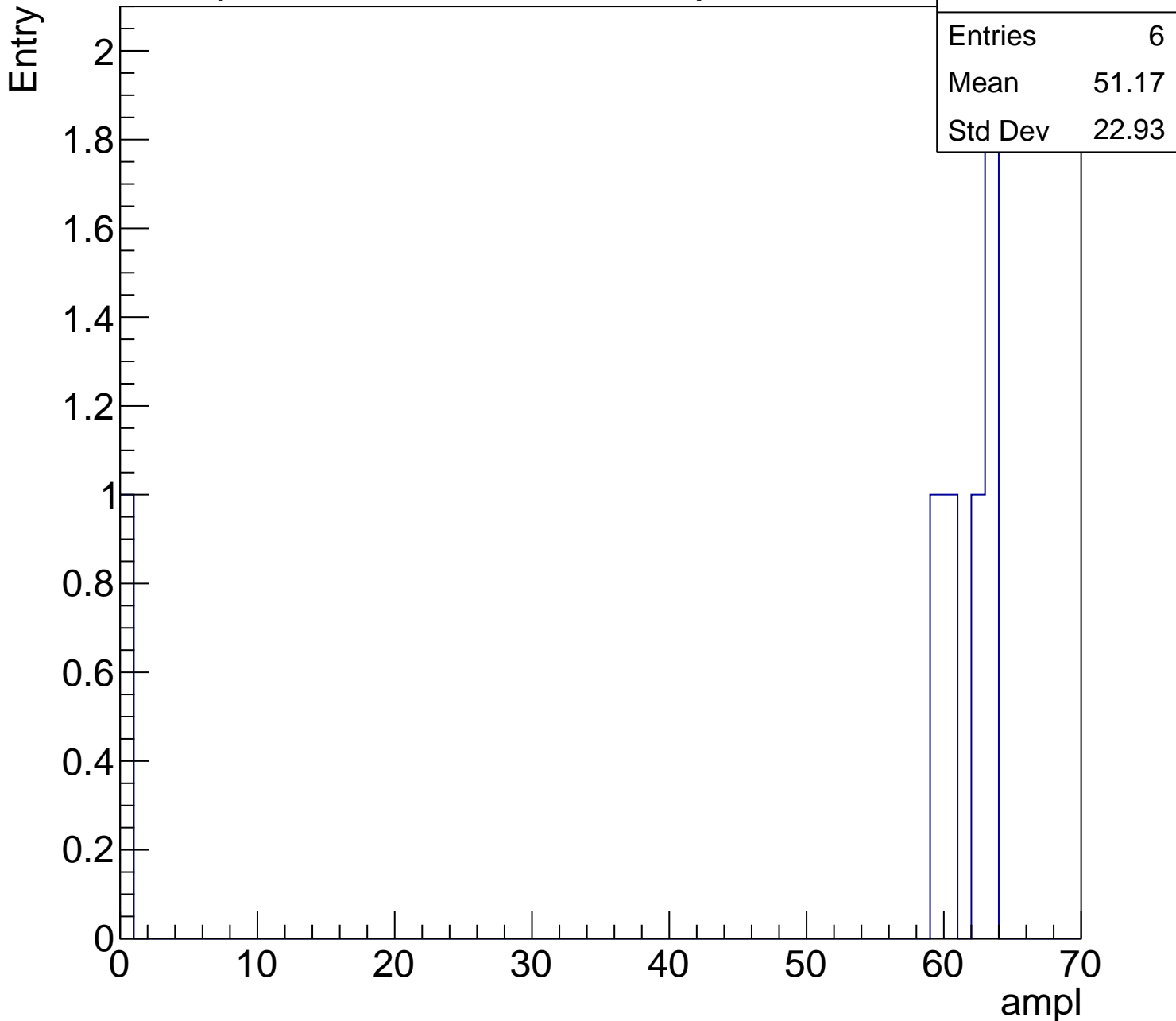
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.17
Std Dev	22.93

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch48, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	17
Std Dev	0

ampl

B1L101S, U15-ch49, adc0

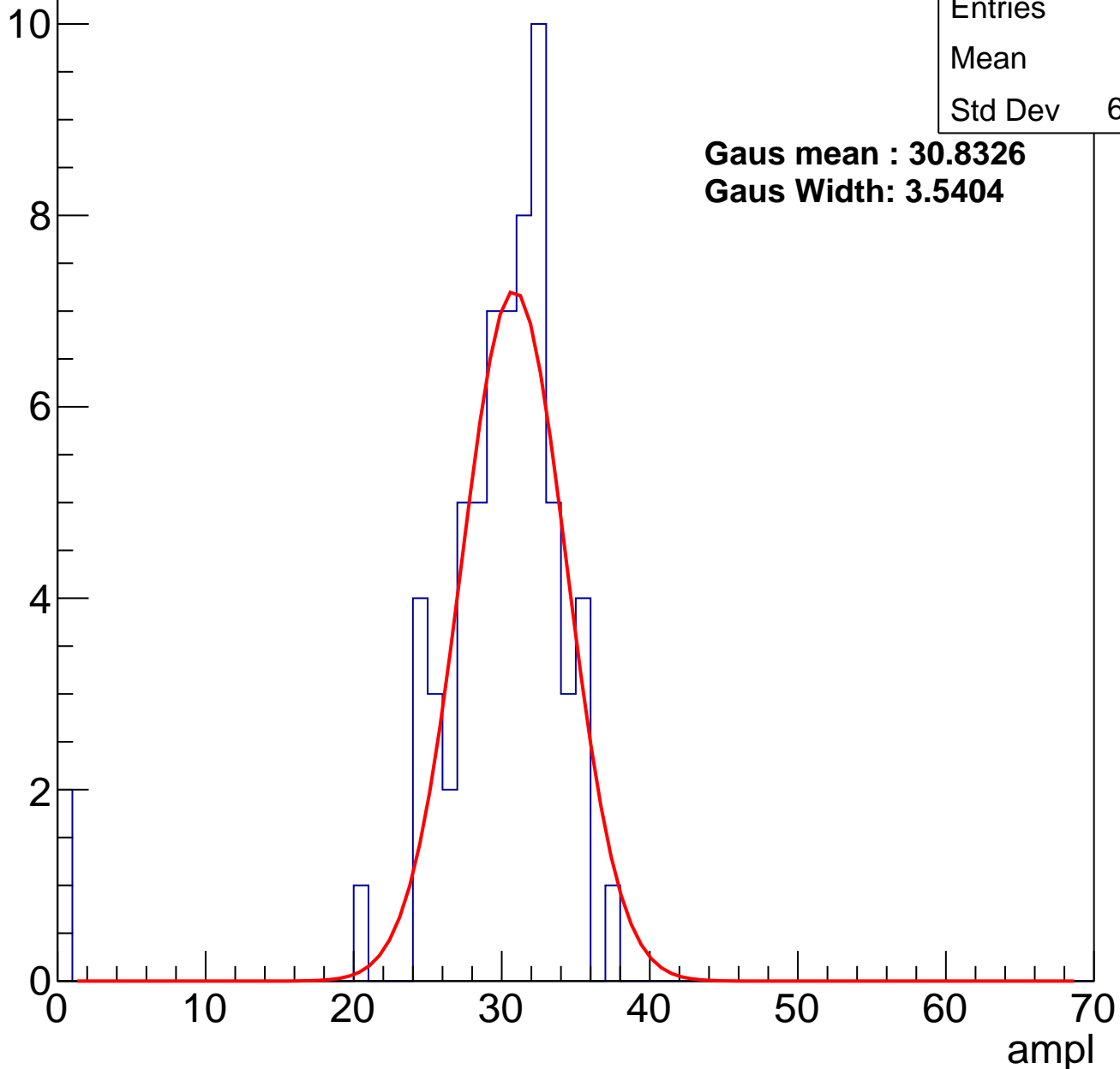
calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	29
Std Dev	6.042

Gaus mean : 30.8326

Gaus Width: 3.5404

Entry



B1L101S, U15-ch49, adc1

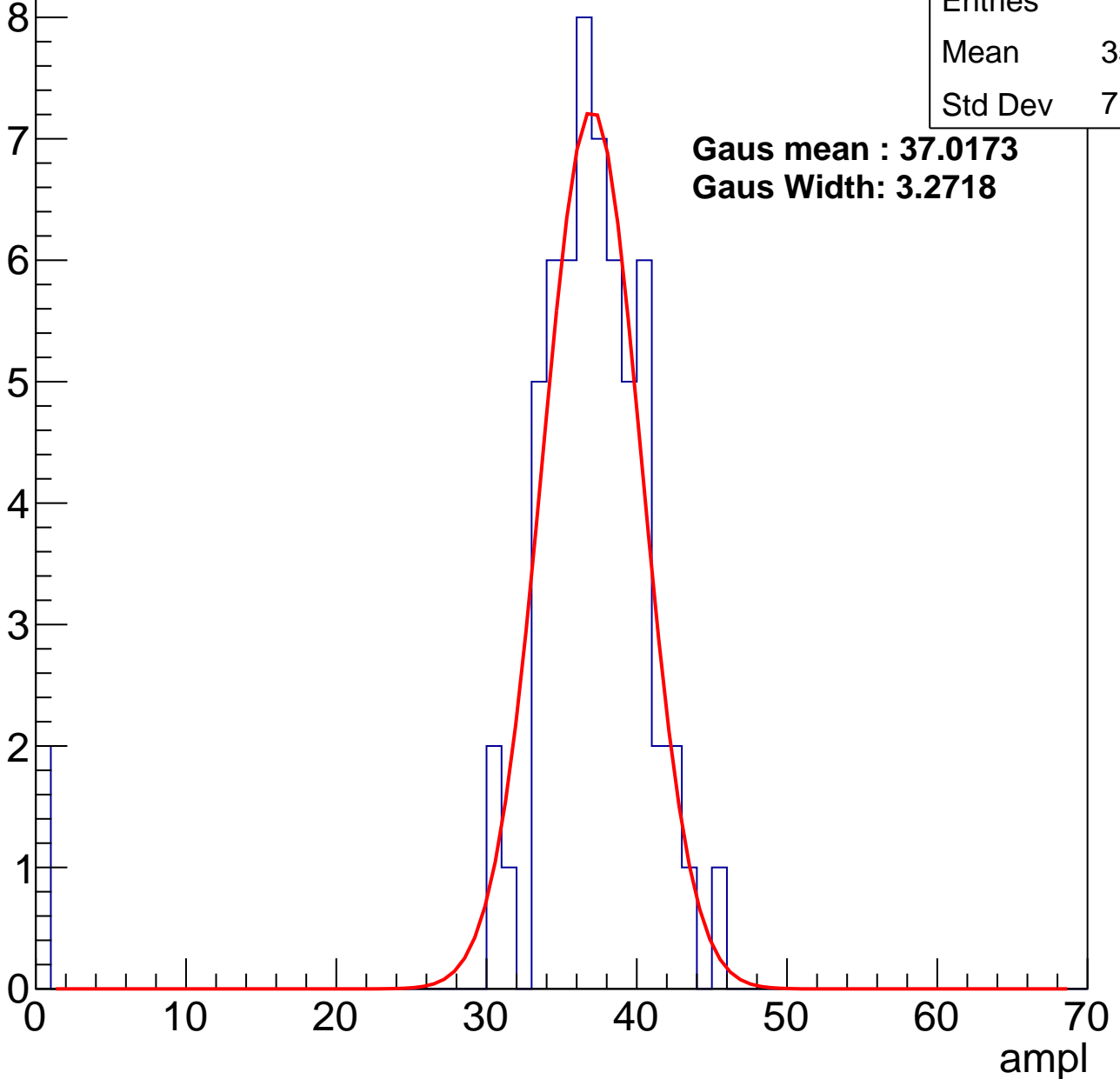
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.57
Std Dev	7.276

Gaus mean : 37.0173

Gaus Width: 3.2718



B1L101S, U15-ch49, adc2

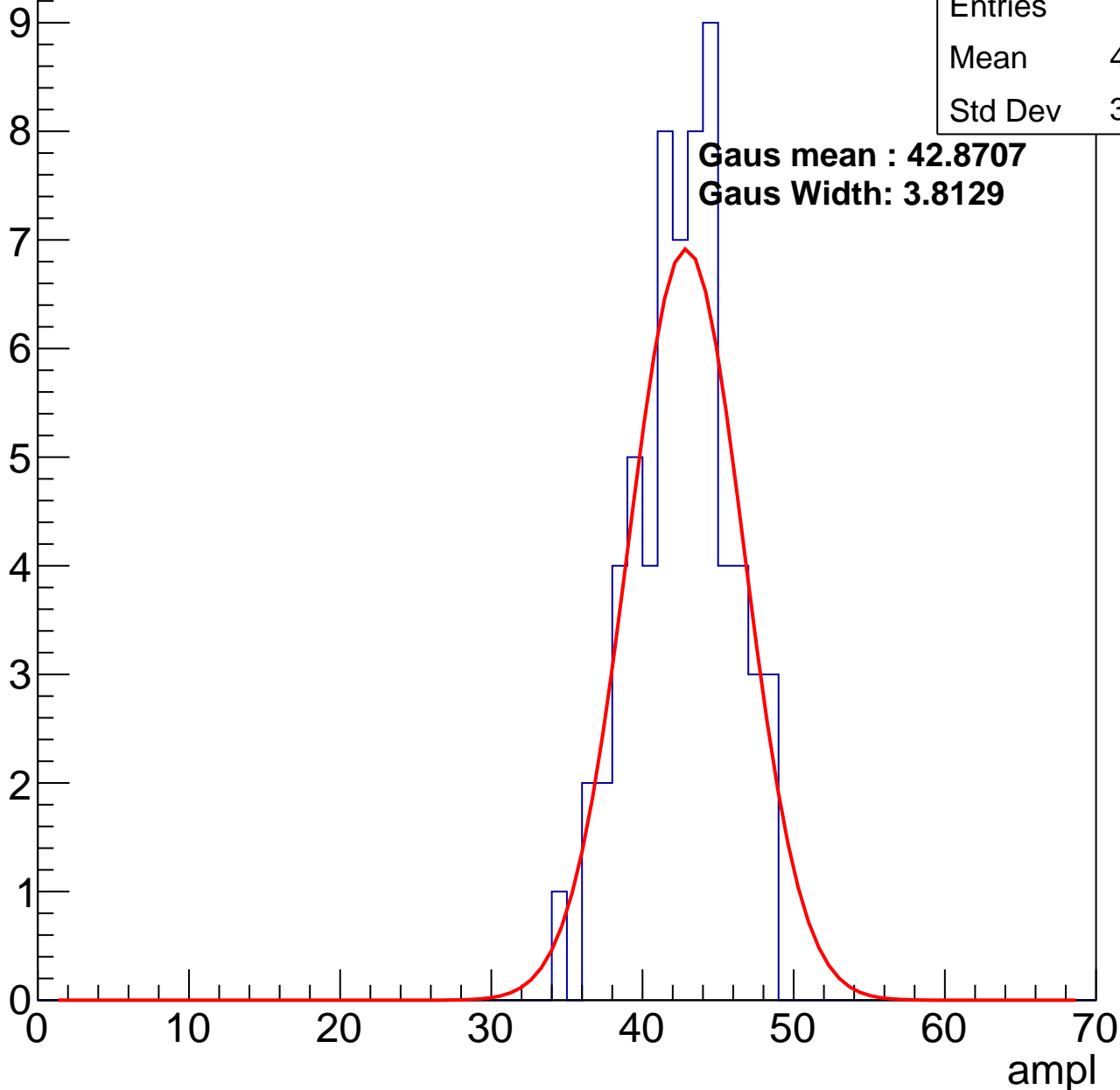
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.16
Std Dev	3.173

Gaus mean : 42.8707

Gaus Width: 3.8129



B1L101S, U15-ch49, adc3

calib_packv5_042523_0143.root, FC#0, port D2

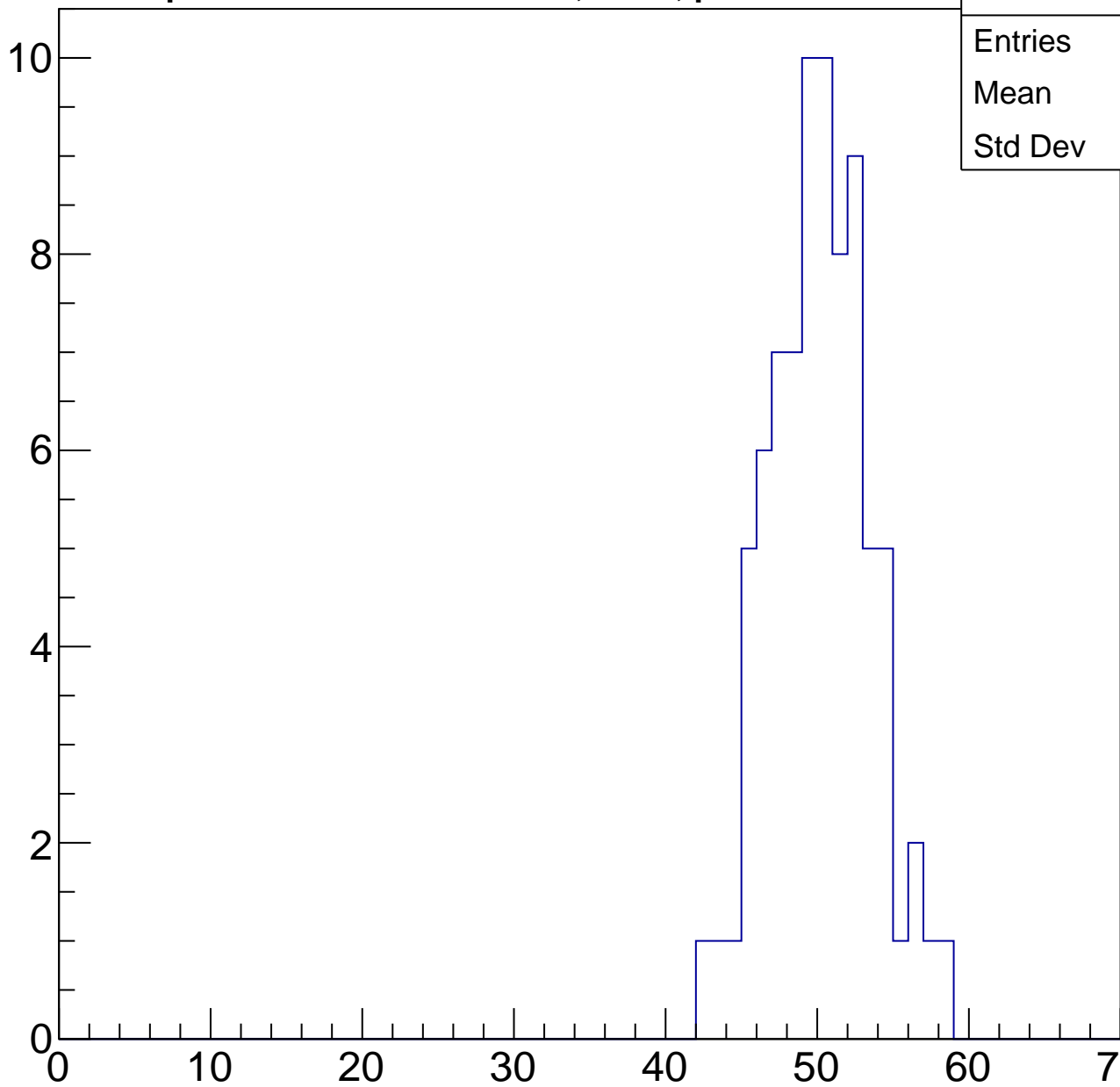
Entries	80
Mean	49.73
Std Dev	3.248

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

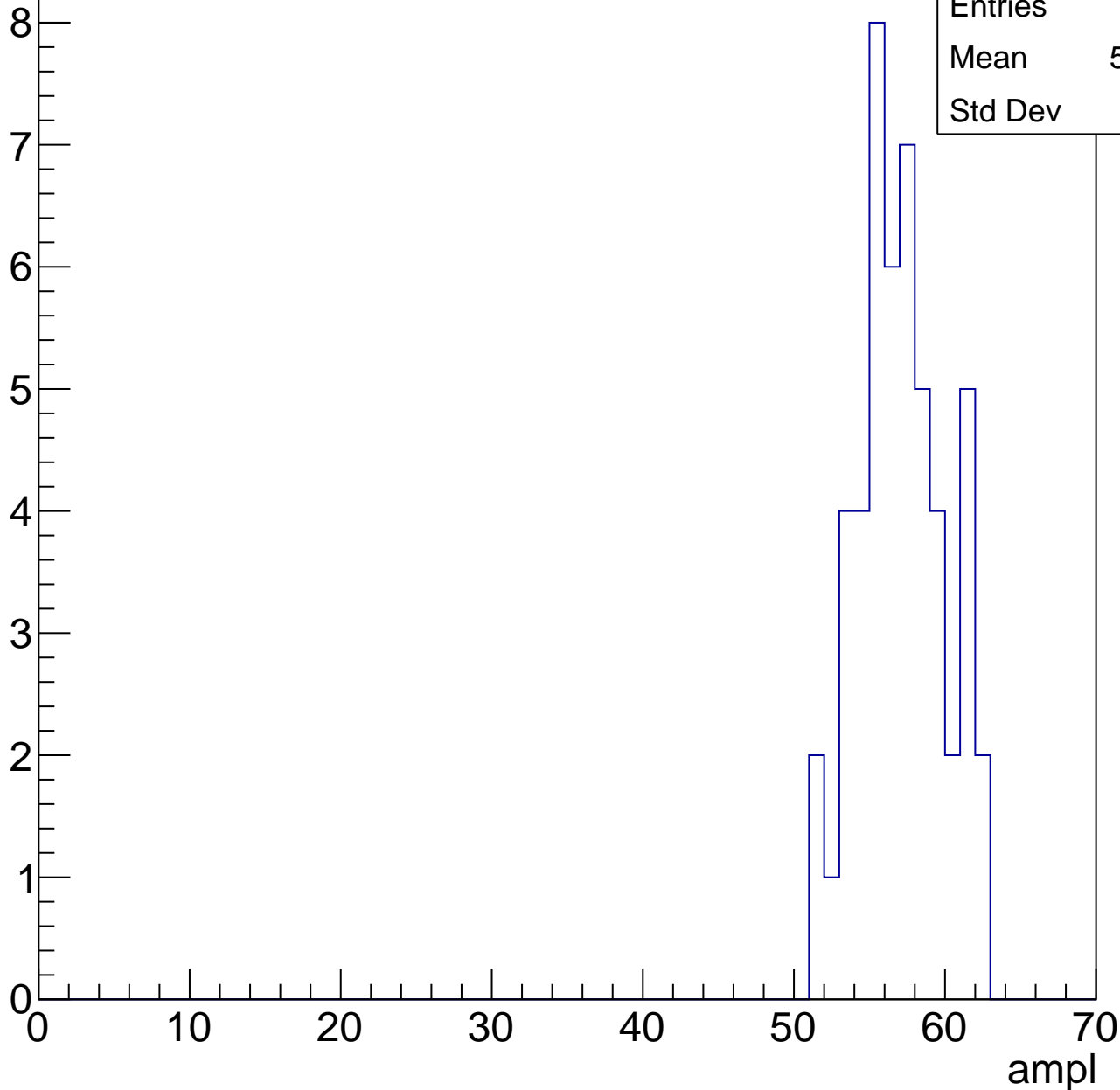


B1L101S, U15-ch49, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

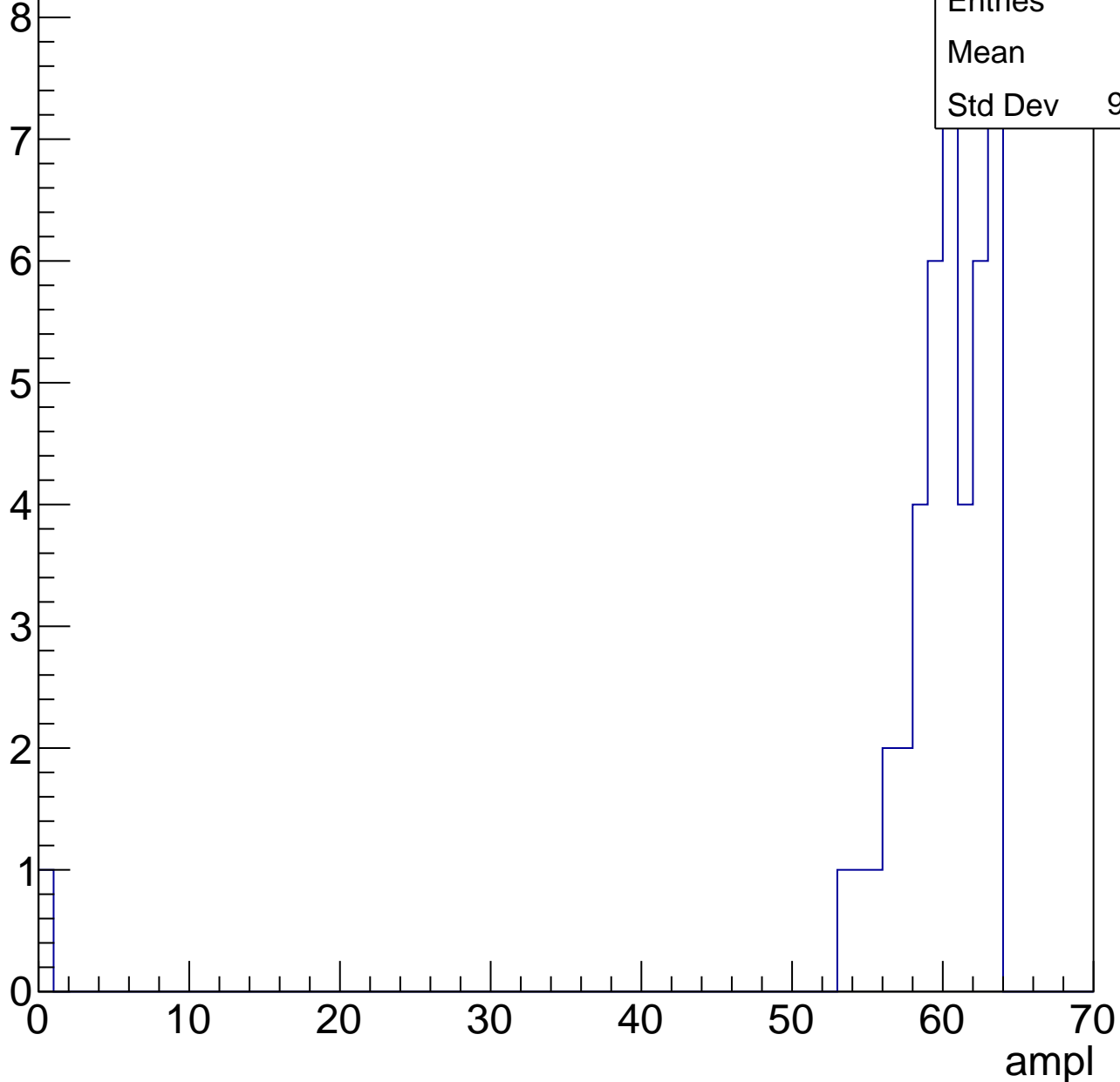
Entries	50
Mean	56.64
Std Dev	2.82



B1L101S, U15-ch49, adc5

calib_packv5_042523_0143.root, FC#0, port D2

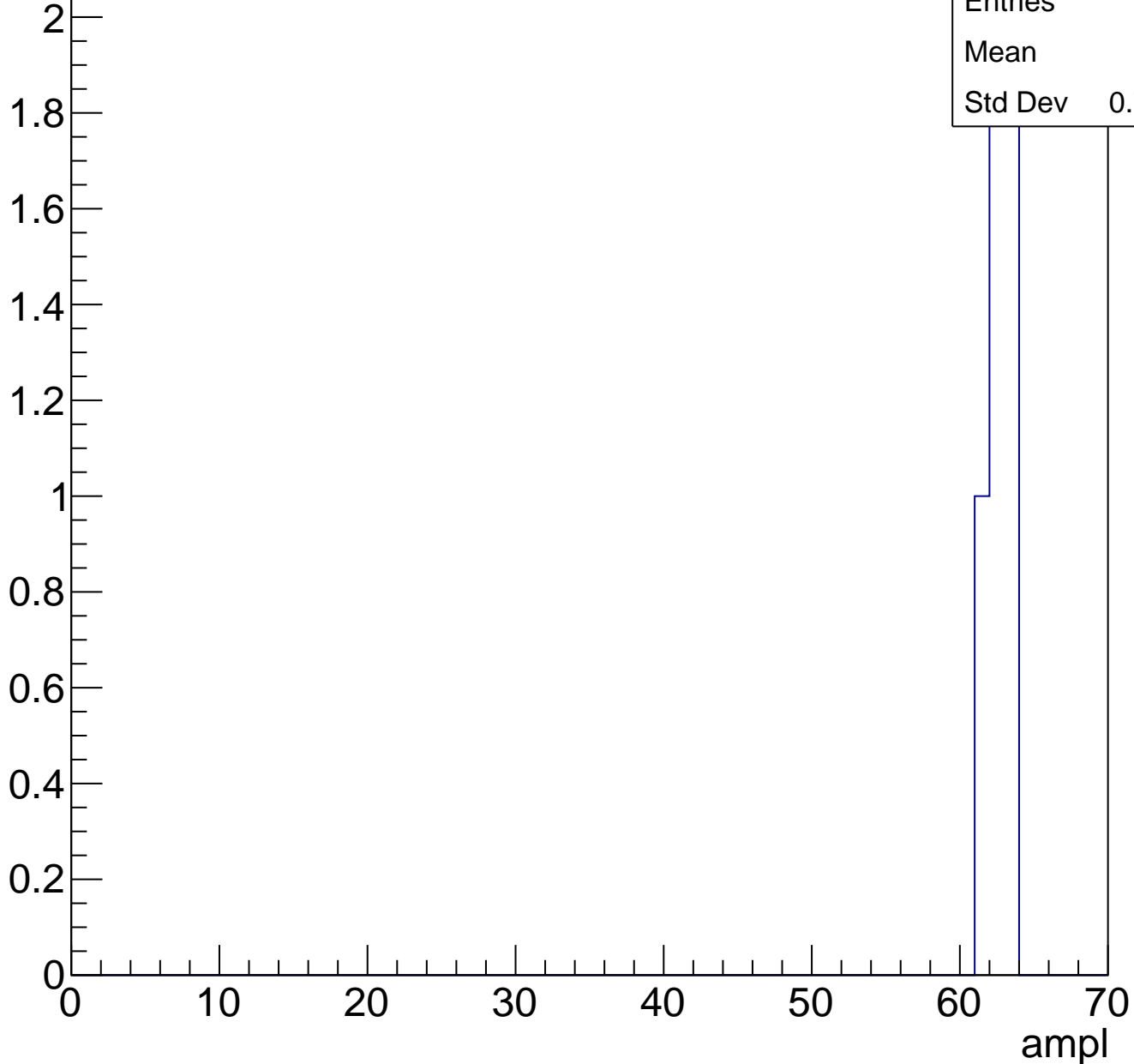
Entry



B1L101S, U15-ch49, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch49, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch50, adc0

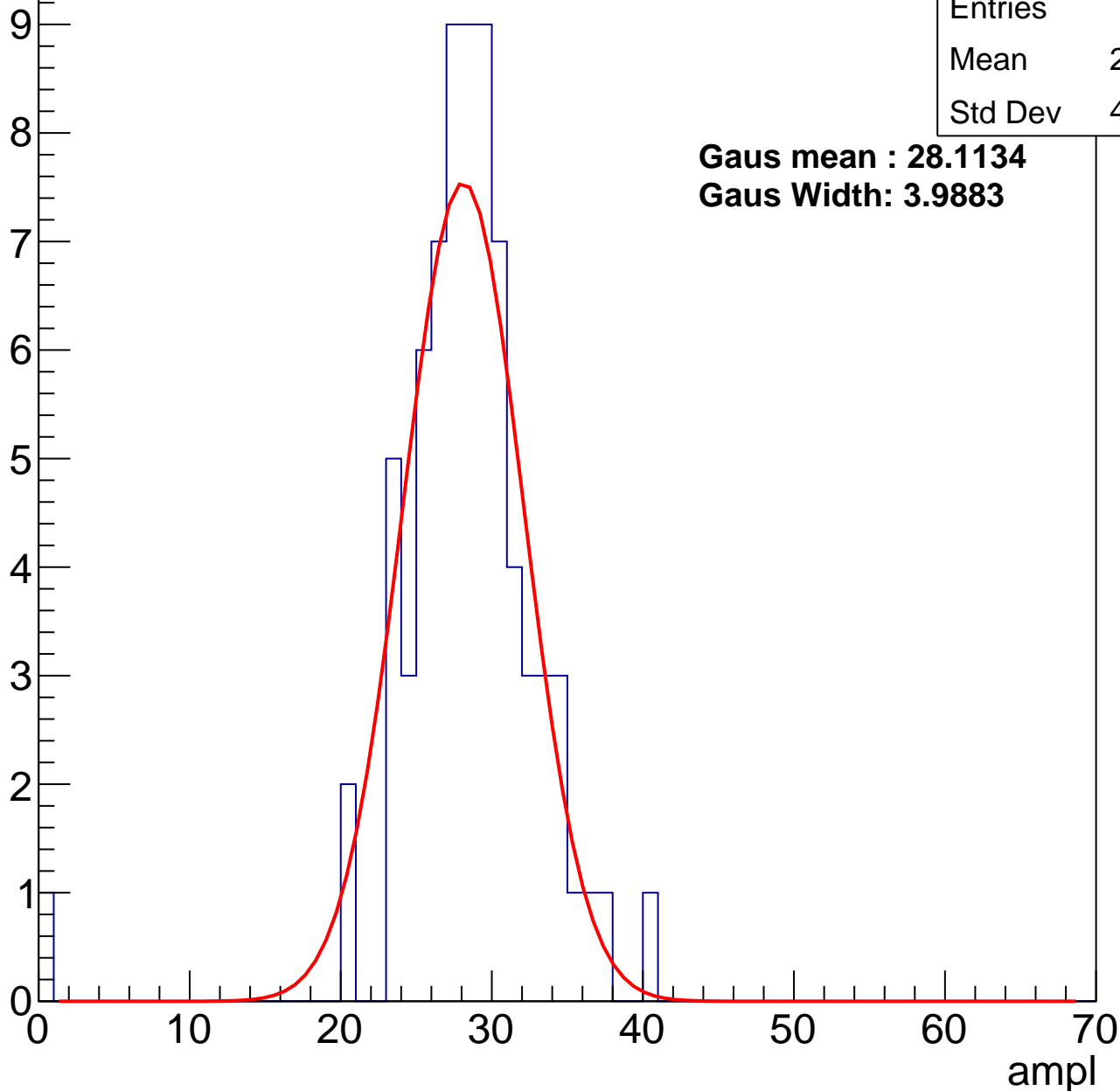
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	27.92
Std Dev	4.923

Gaus mean : 28.1134

Gaus Width: 3.9883



B1L101S, U15-ch50, adc1

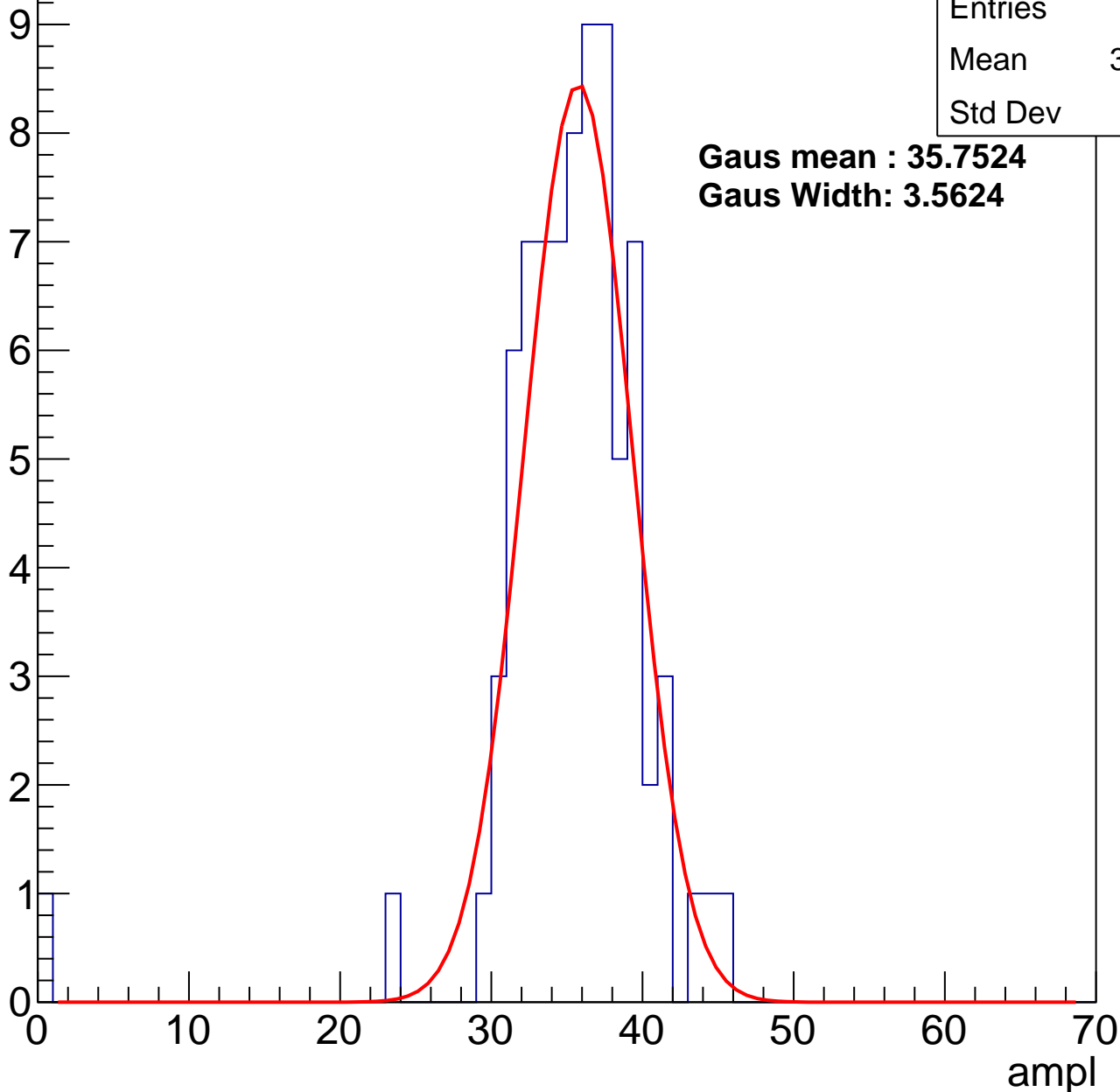
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	34.89
Std Dev	5.37

Gaus mean : 35.7524

Gaus Width: 3.5624



B1L101S, U15-ch50, adc2

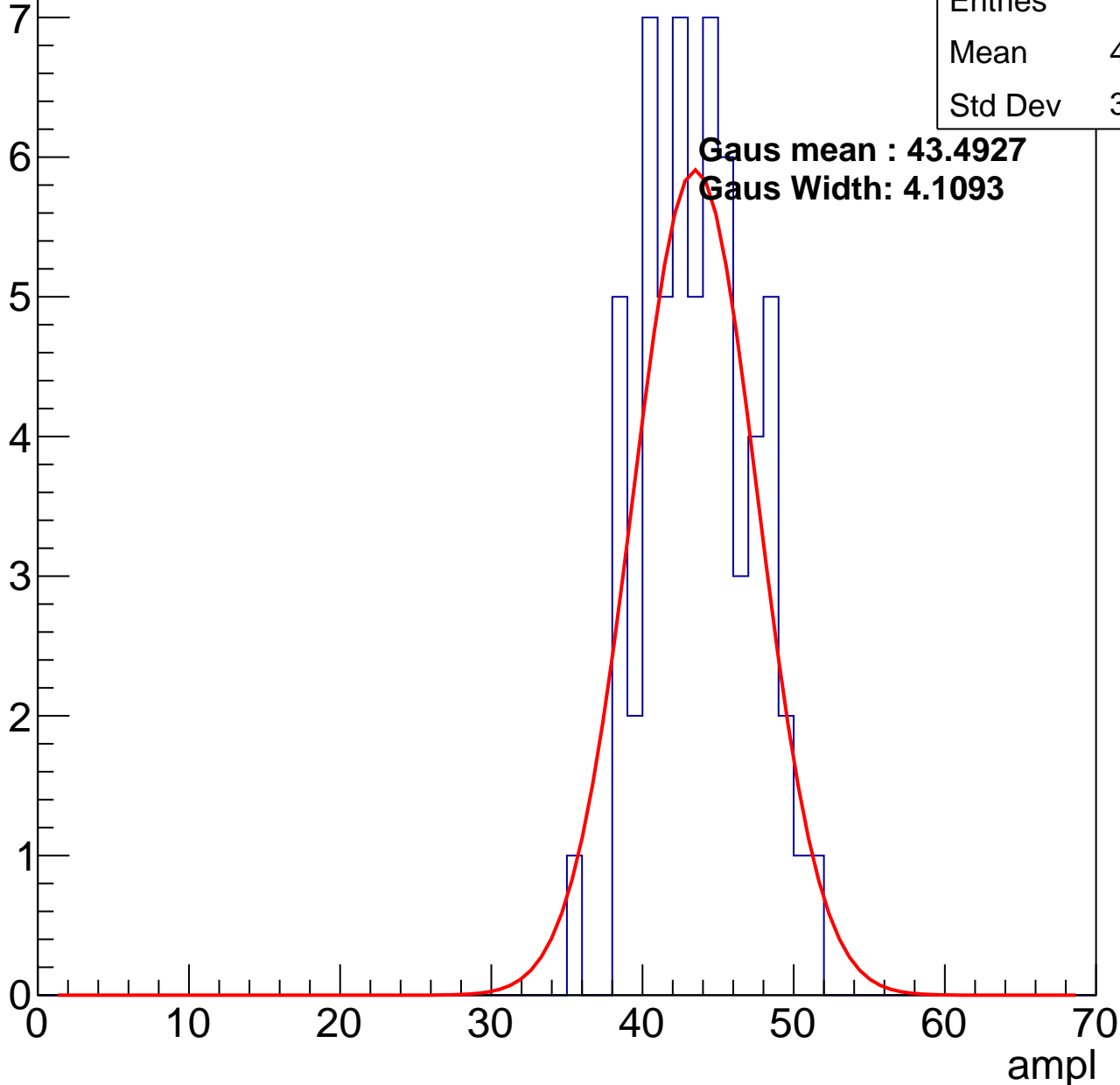
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.28
Std Dev	3.479

Gaus mean : 43.4927

Gaus Width: 4.1093

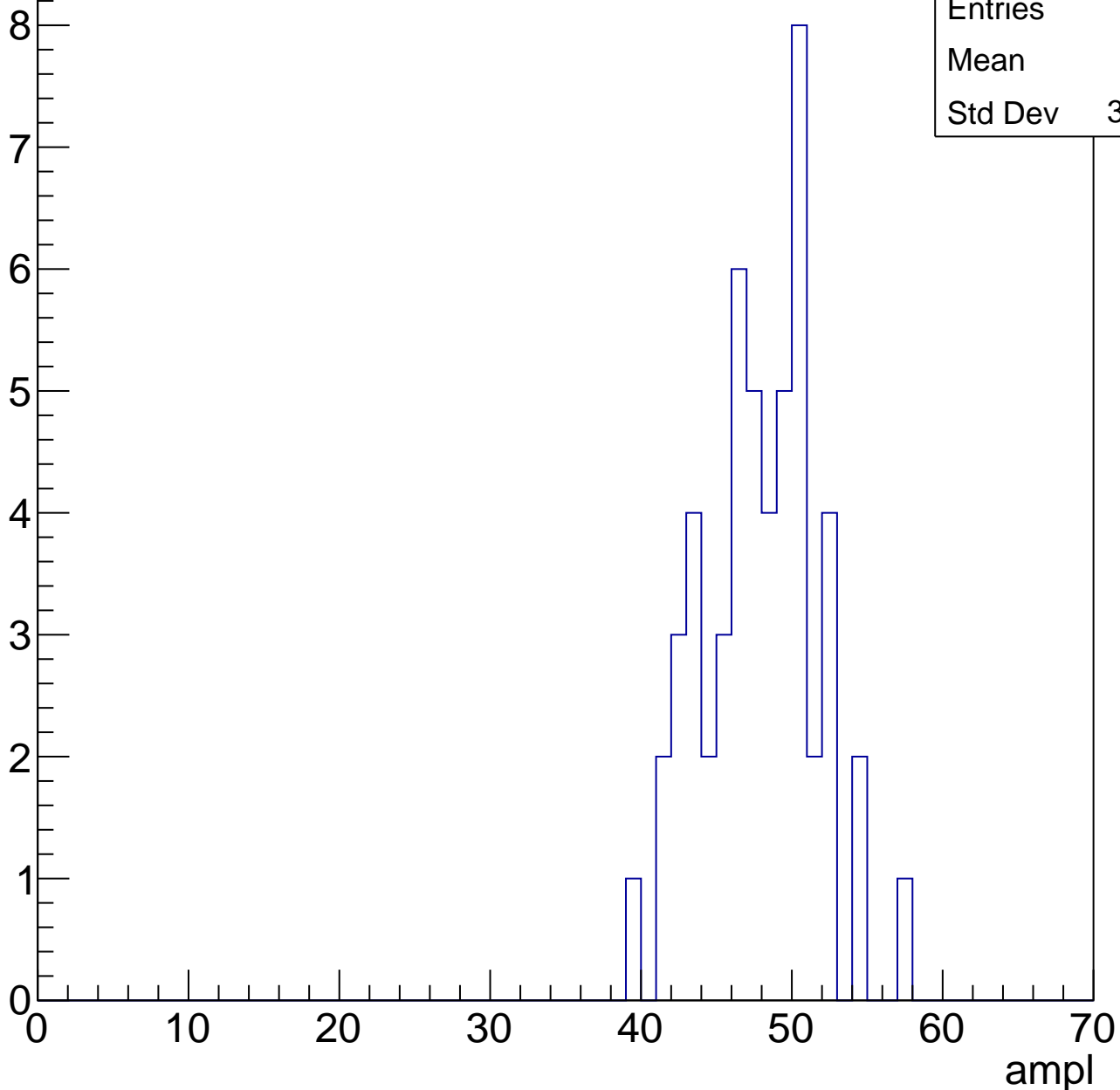


B1L101S, U15-ch50, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	47.4
Std Dev	3.743

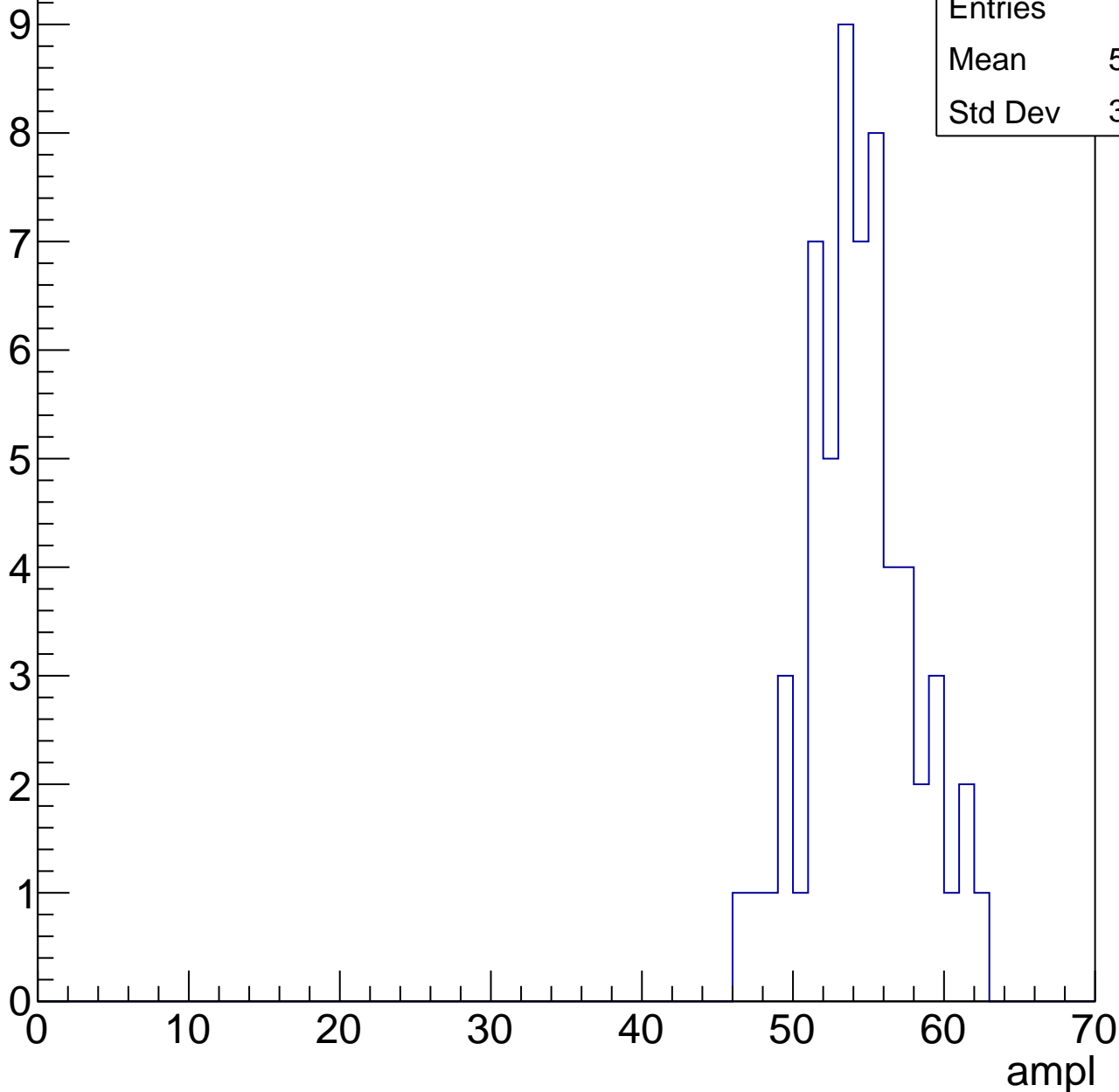


B1L101S, U15-ch50, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

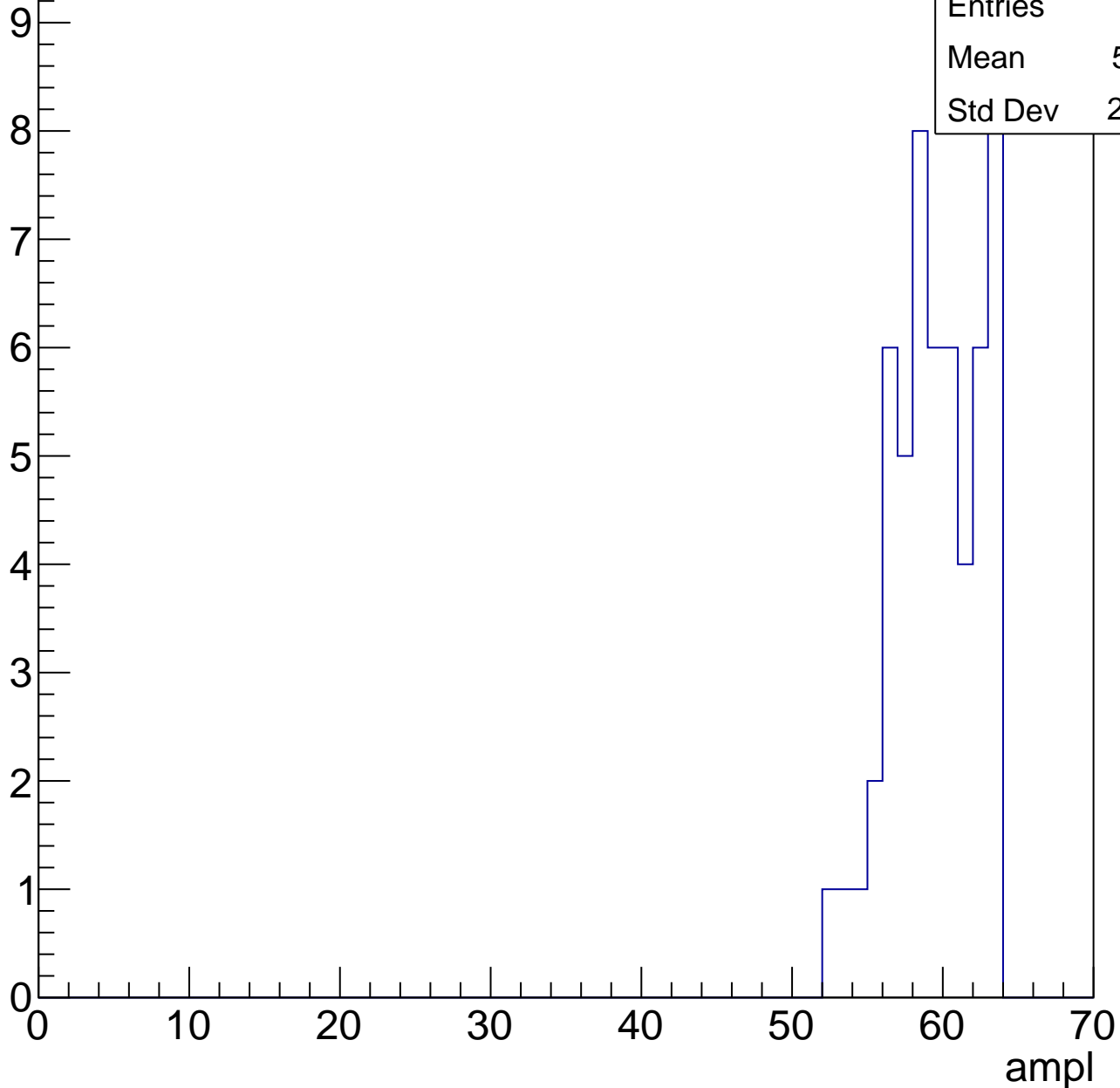
Entries	60
Mean	53.98
Std Dev	3.408



B1L101S, U15-ch50, adc5

calib_packv5_042523_0143.root, FC#0, port D2

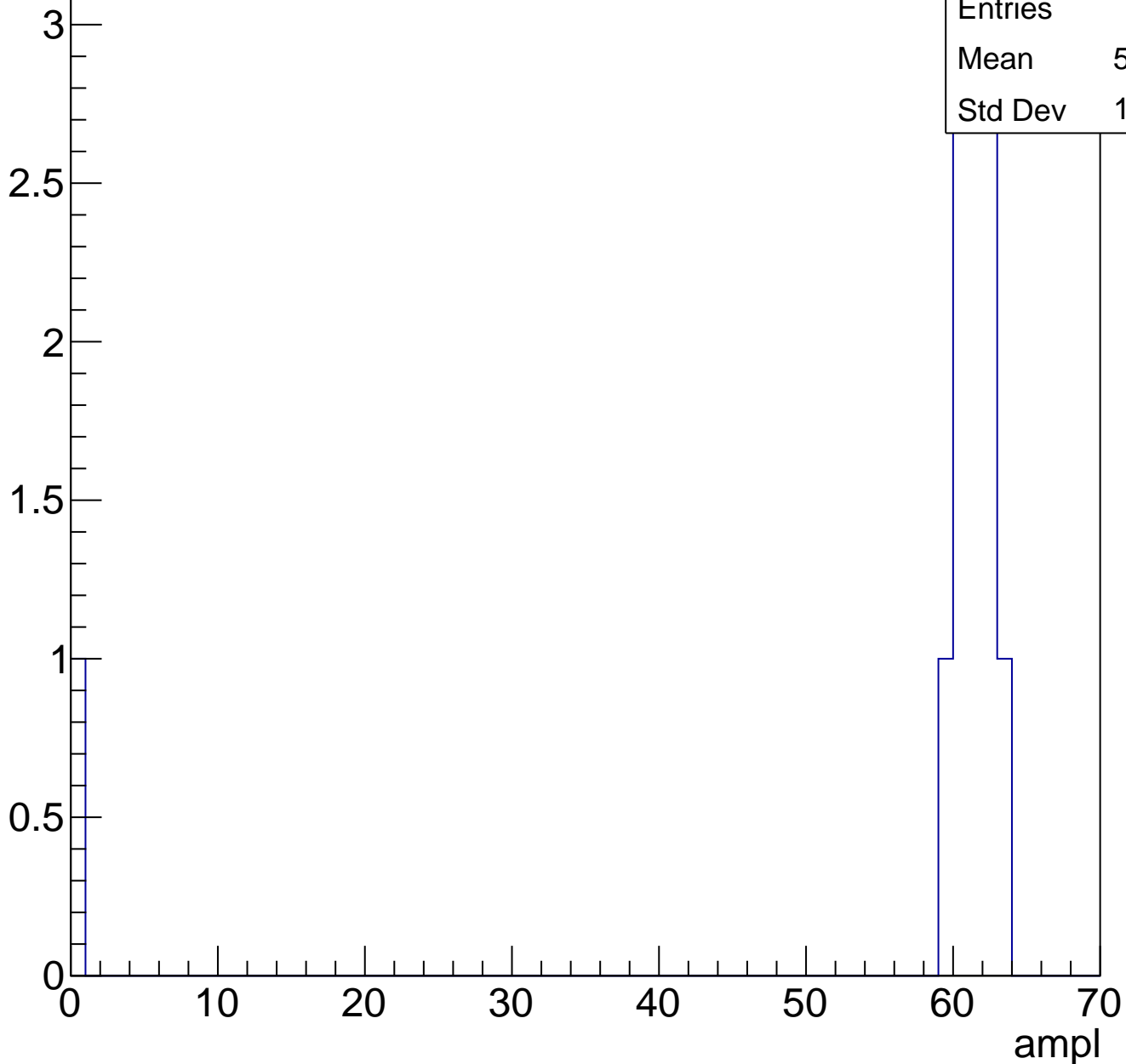
Entry



B1L101S, U15-ch50, adc6

calib_packv5_042523_0143.root, FC#0, port D2

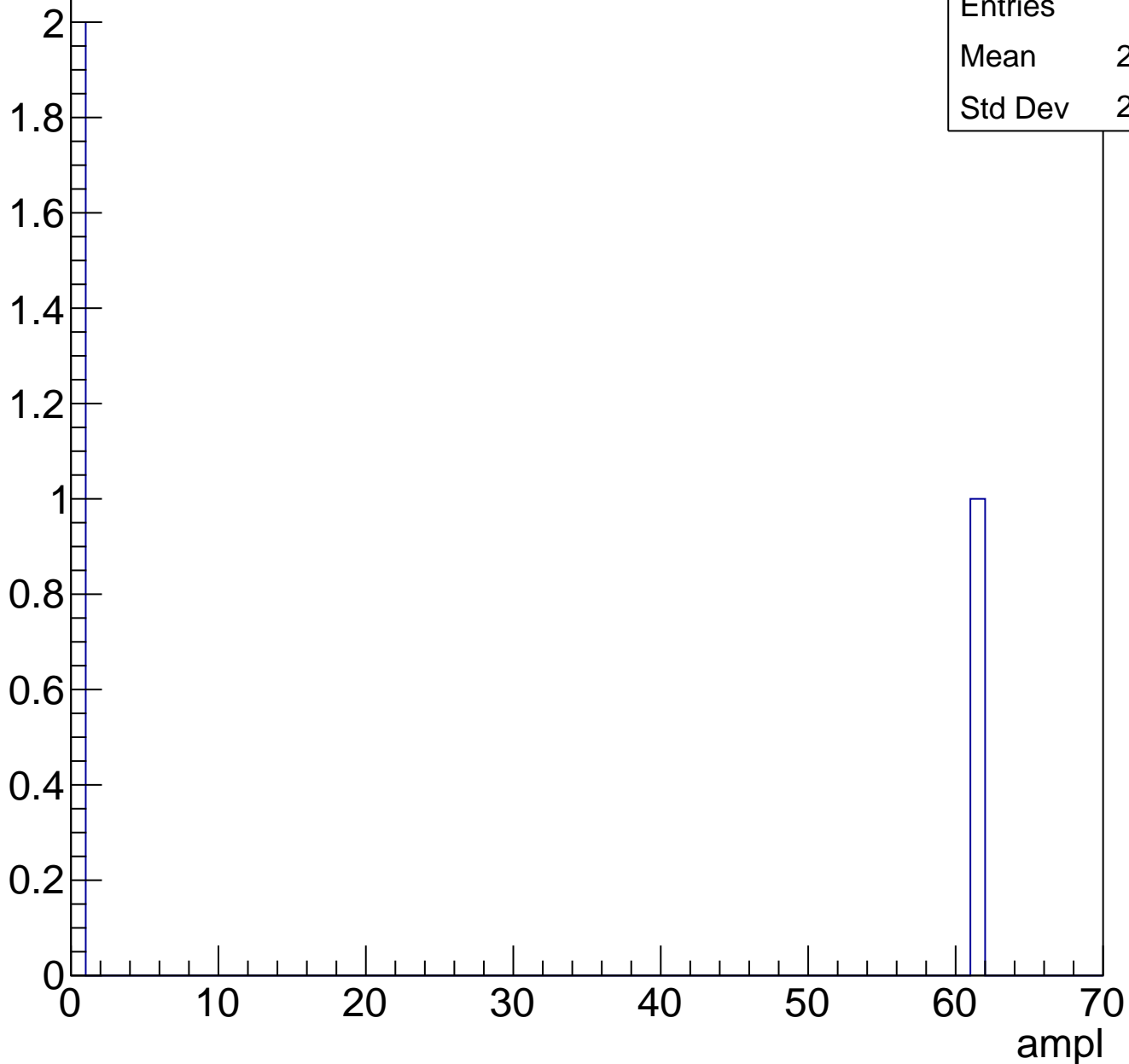
Entry



B1L101S, U15-ch50, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch51, adc0

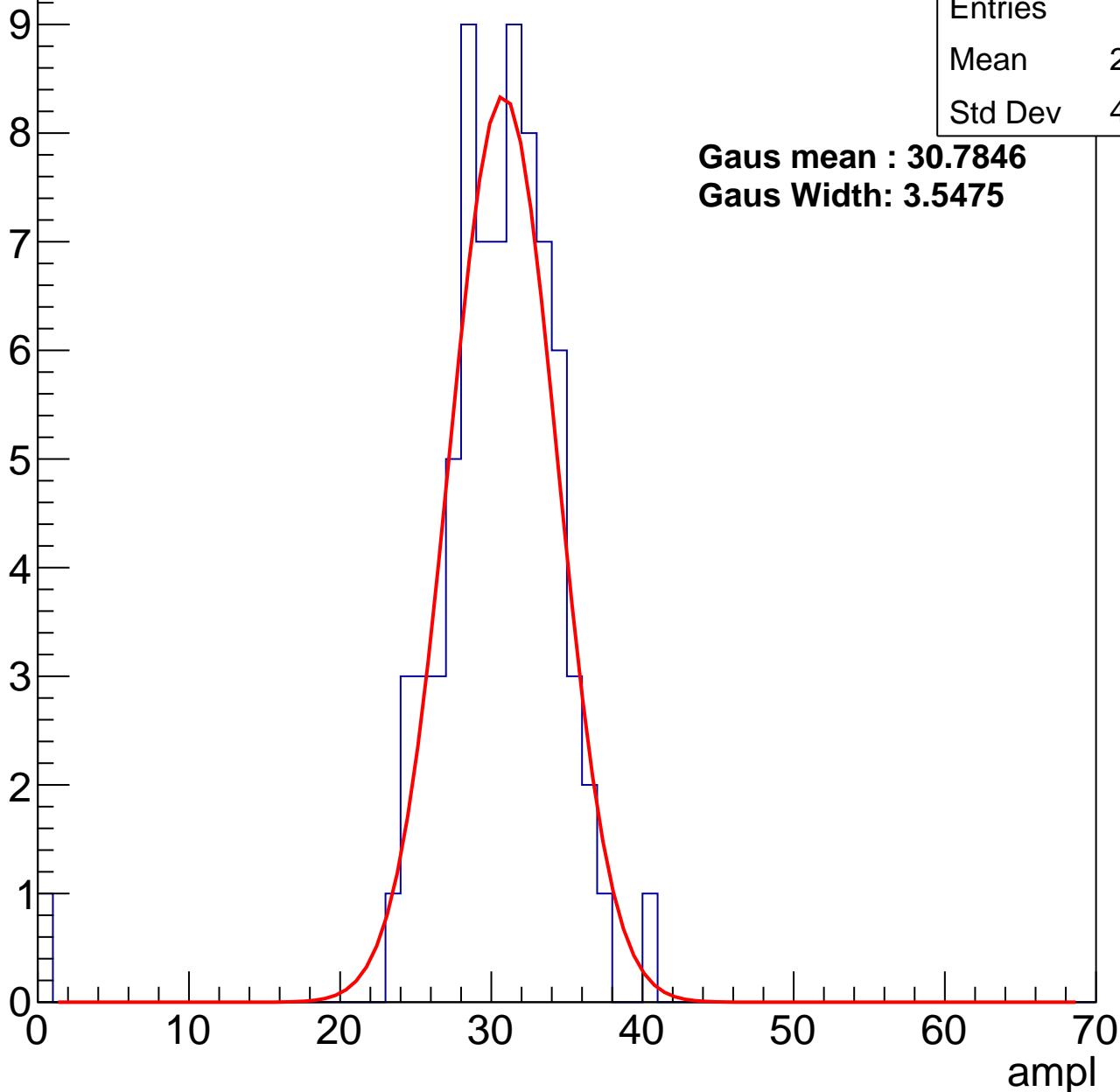
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	29.89
Std Dev	4.817

Gaus mean : 30.7846

Gaus Width: 3.5475



B1L101S, U15-ch51, adc1

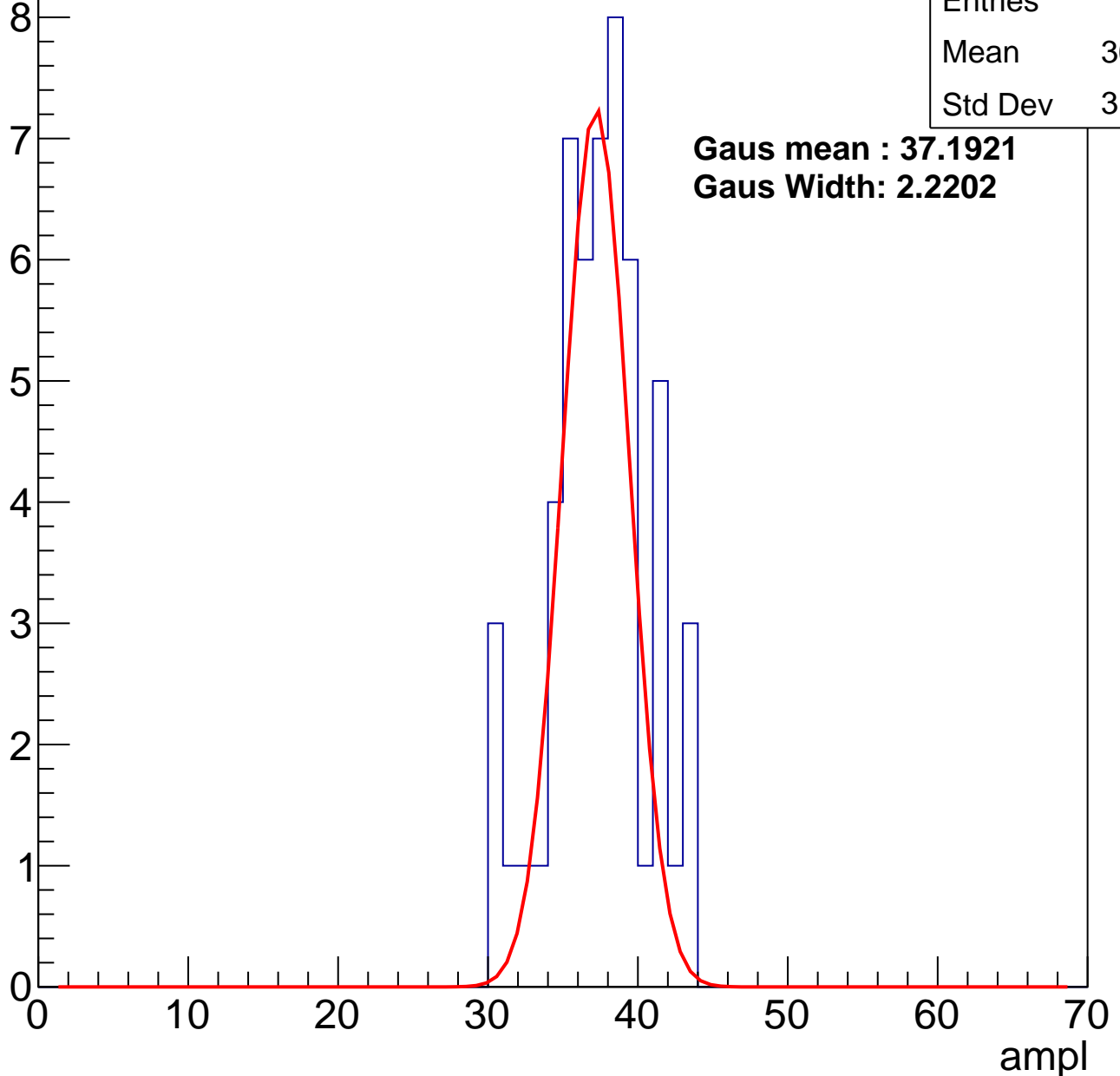
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	36.96
Std Dev	3.185

Gaus mean : 37.1921

Gaus Width: 2.2202



B1L101S, U15-ch51, adc2

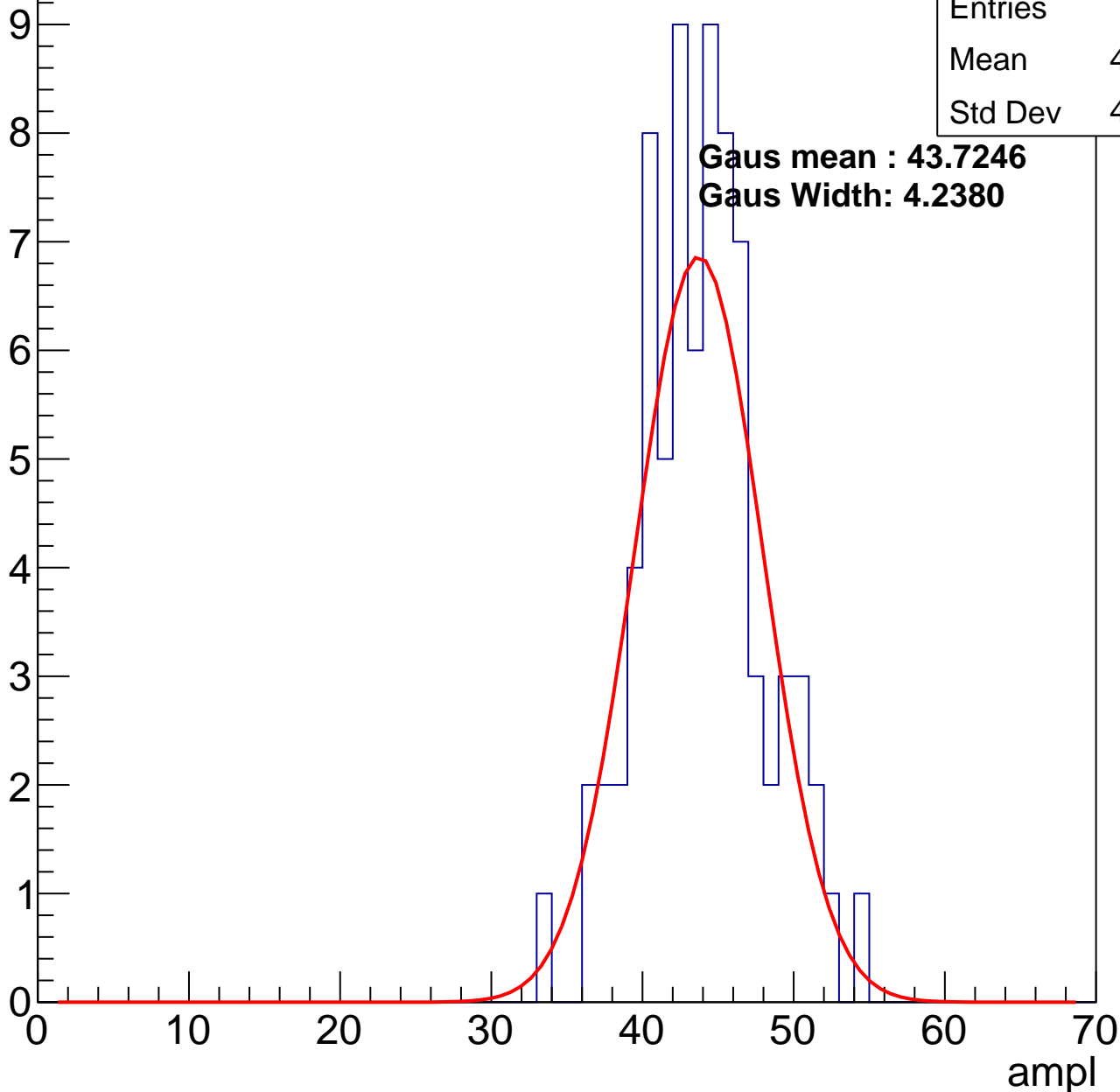
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	43.49
Std Dev	4.009

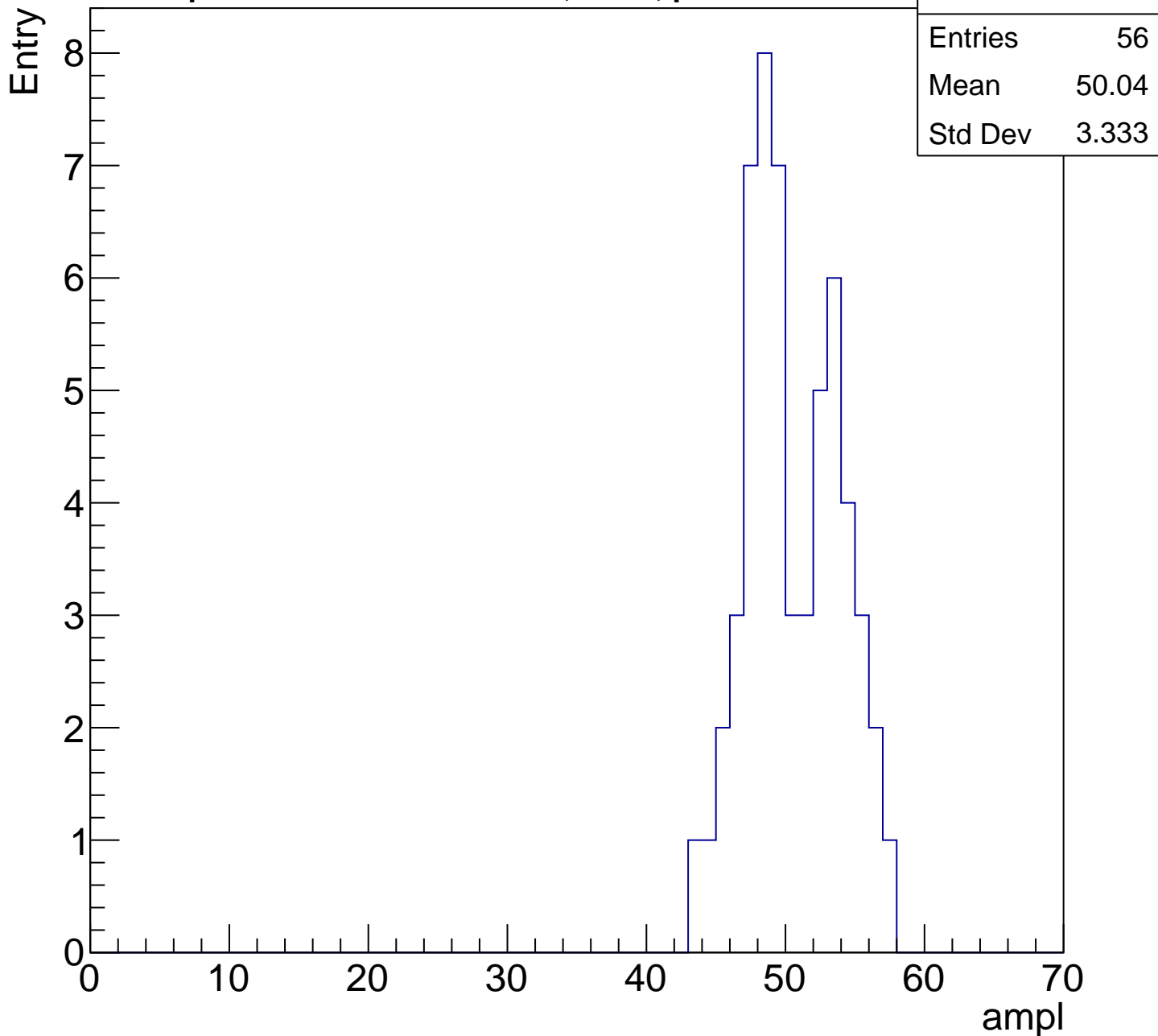
Gaus mean : 43.7246

Gaus Width: 4.2380



B1L101S, U15-ch51, adc3

calib_packv5_042523_0143.root, FC#0, port D2

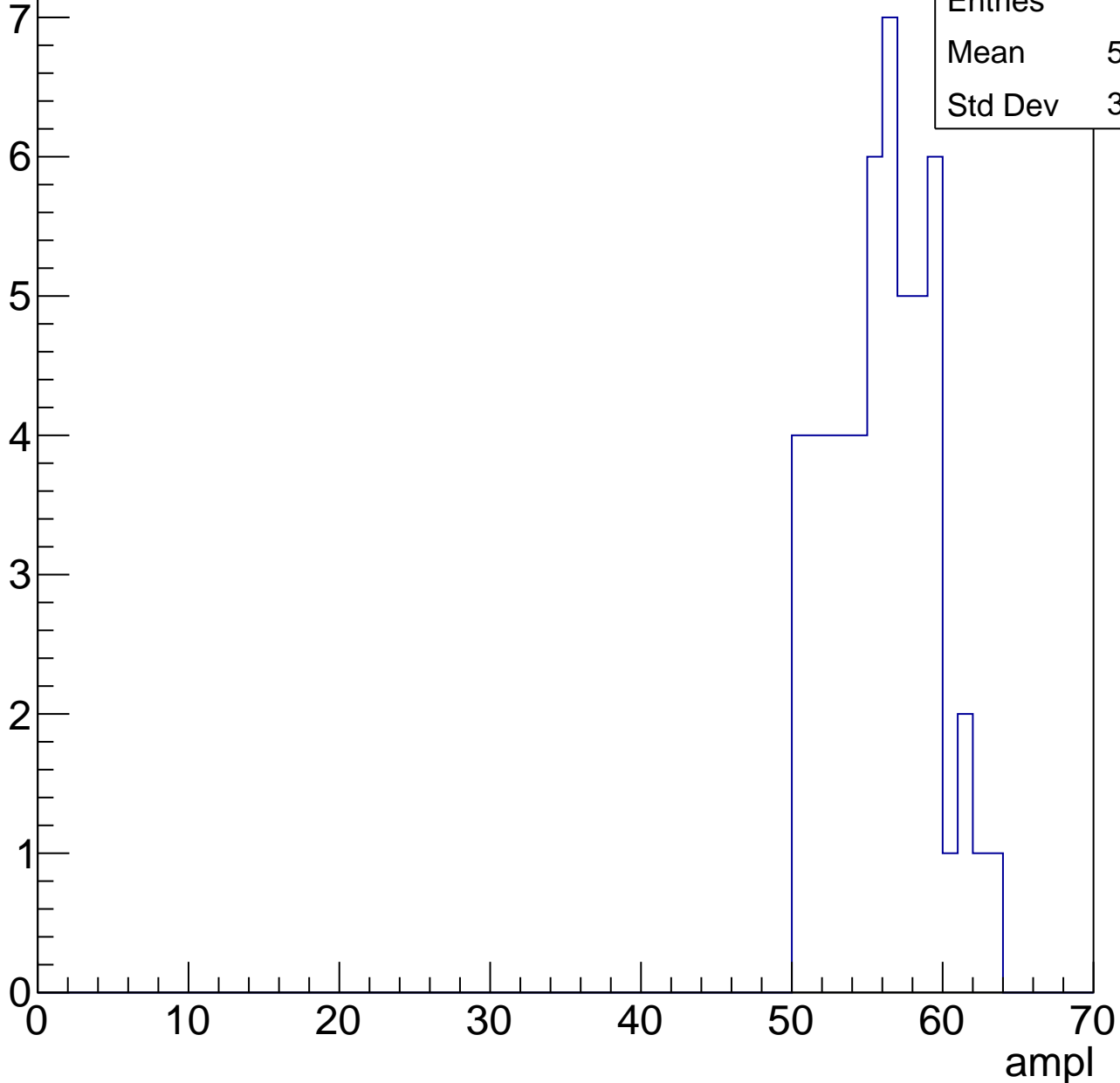


B1L101S, U15-ch51, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

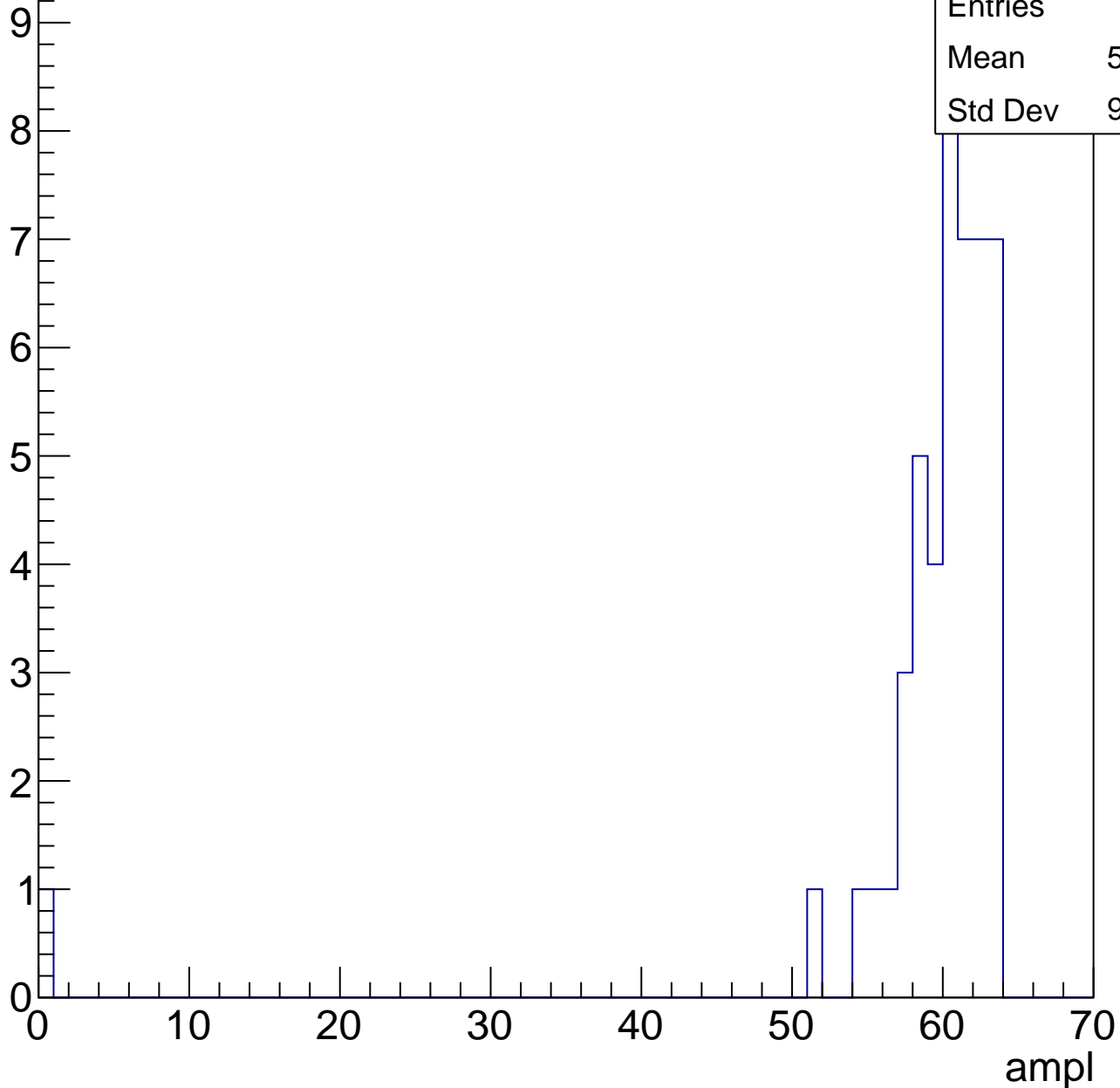
Entries	54
Mean	55.52
Std Dev	3.287



B1L101S, U15-ch51, adc5

calib_packv5_042523_0143.root, FC#0, port D2

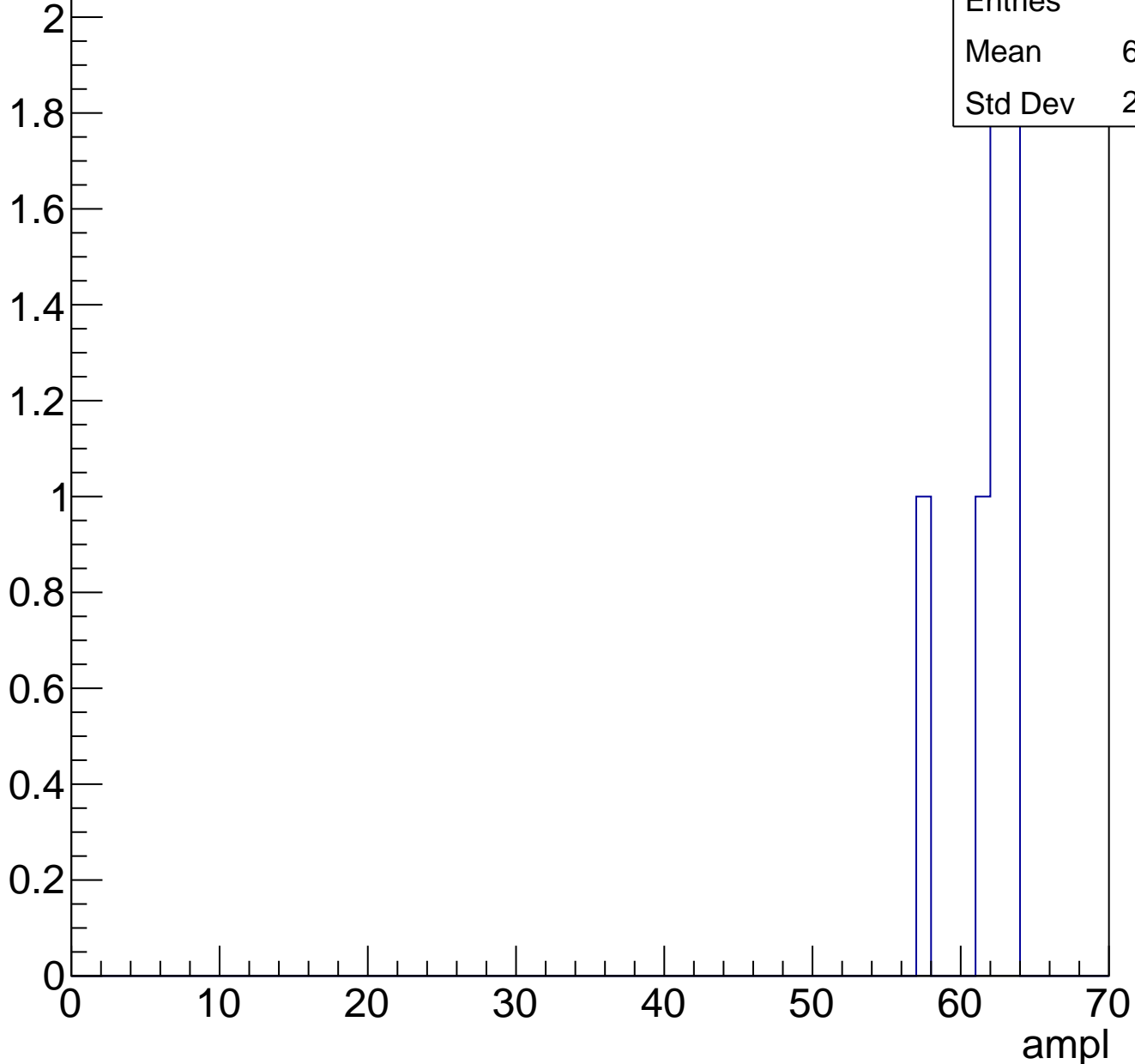
Entry



B1L101S, U15-ch51, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	6
Mean	61.33
Std Dev	2.055

B1L101S, U15-ch51, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch52, adc0

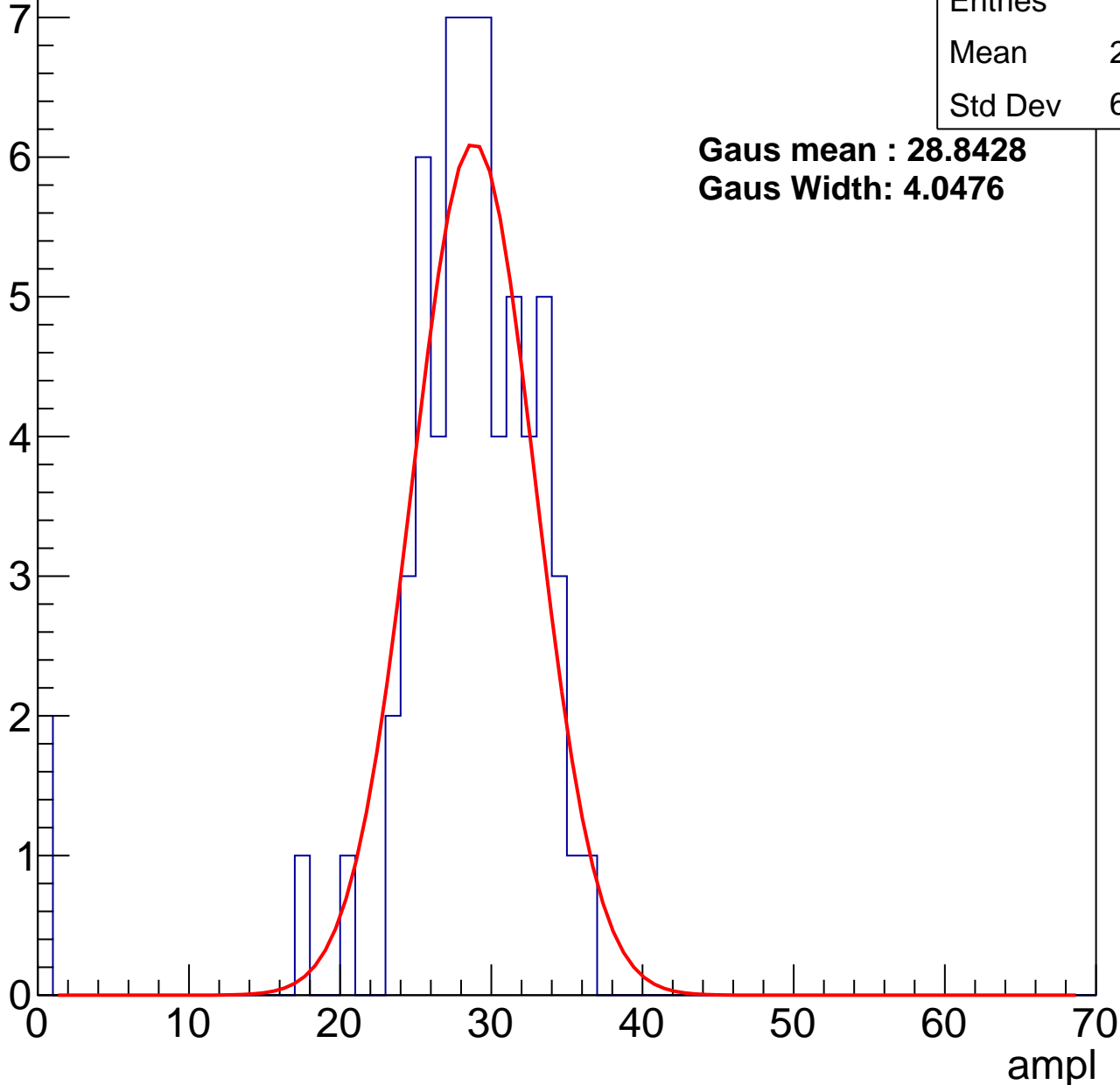
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.59
Std Dev	6.166

Gaus mean : 28.8428

Gaus Width: 4.0476



B1L101S, U15-ch52, adc1

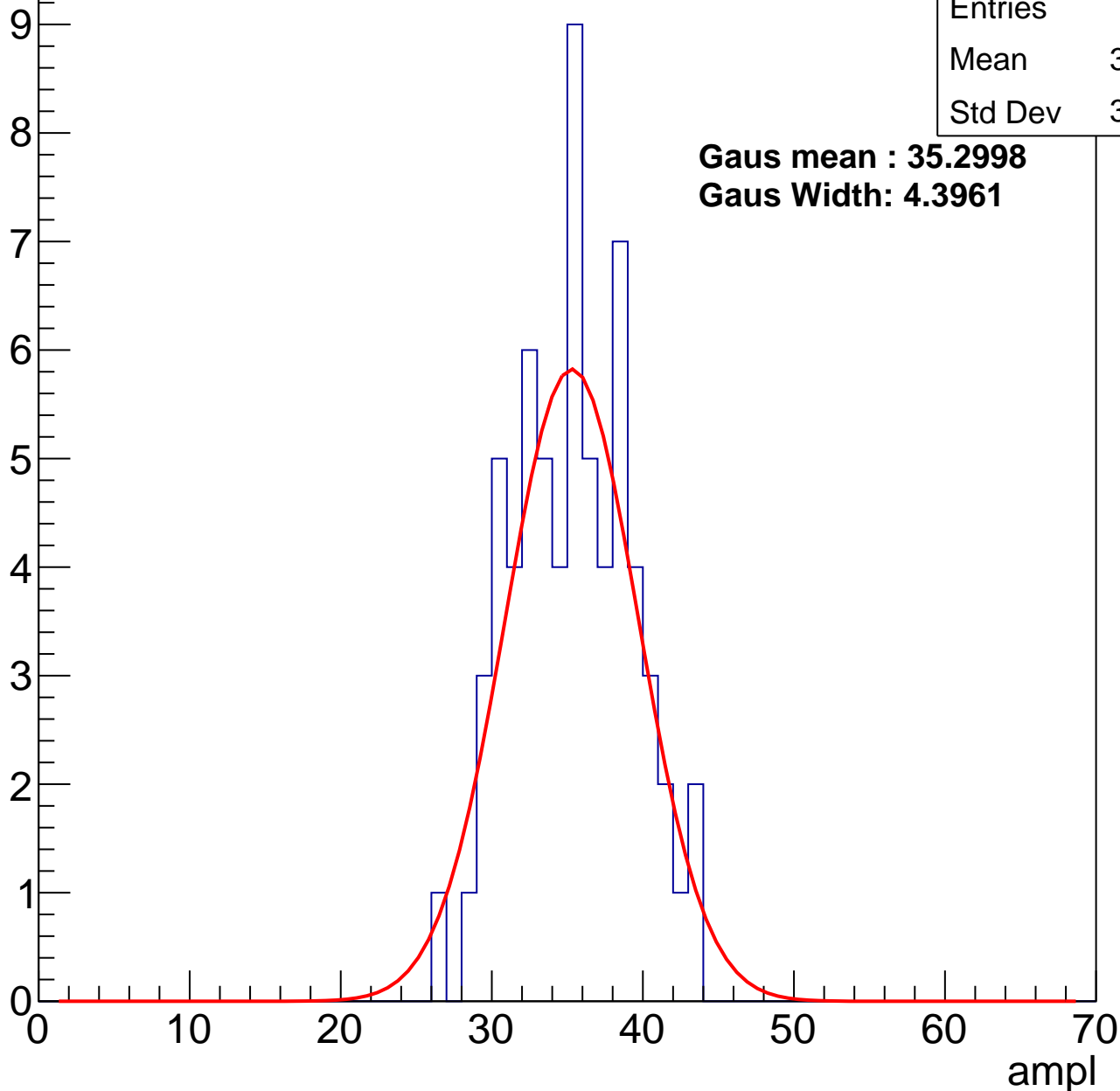
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.89
Std Dev	3.846

Gaus mean : 35.2998

Gaus Width: 4.3961



B1L101S, U15-ch52, adc2

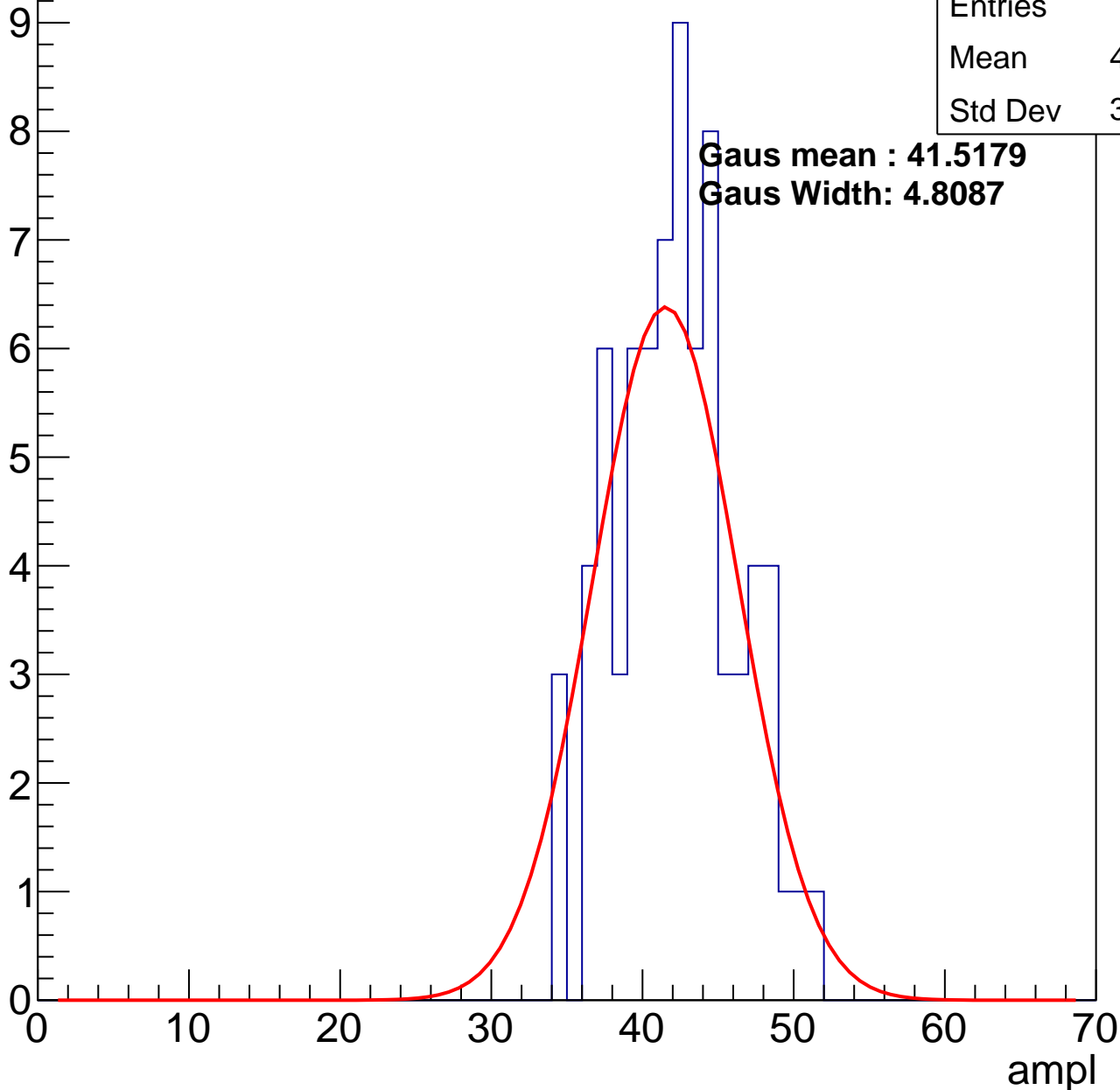
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	41.79
Std Dev	3.944

Gaus mean : 41.5179

Gaus Width: 4.8087

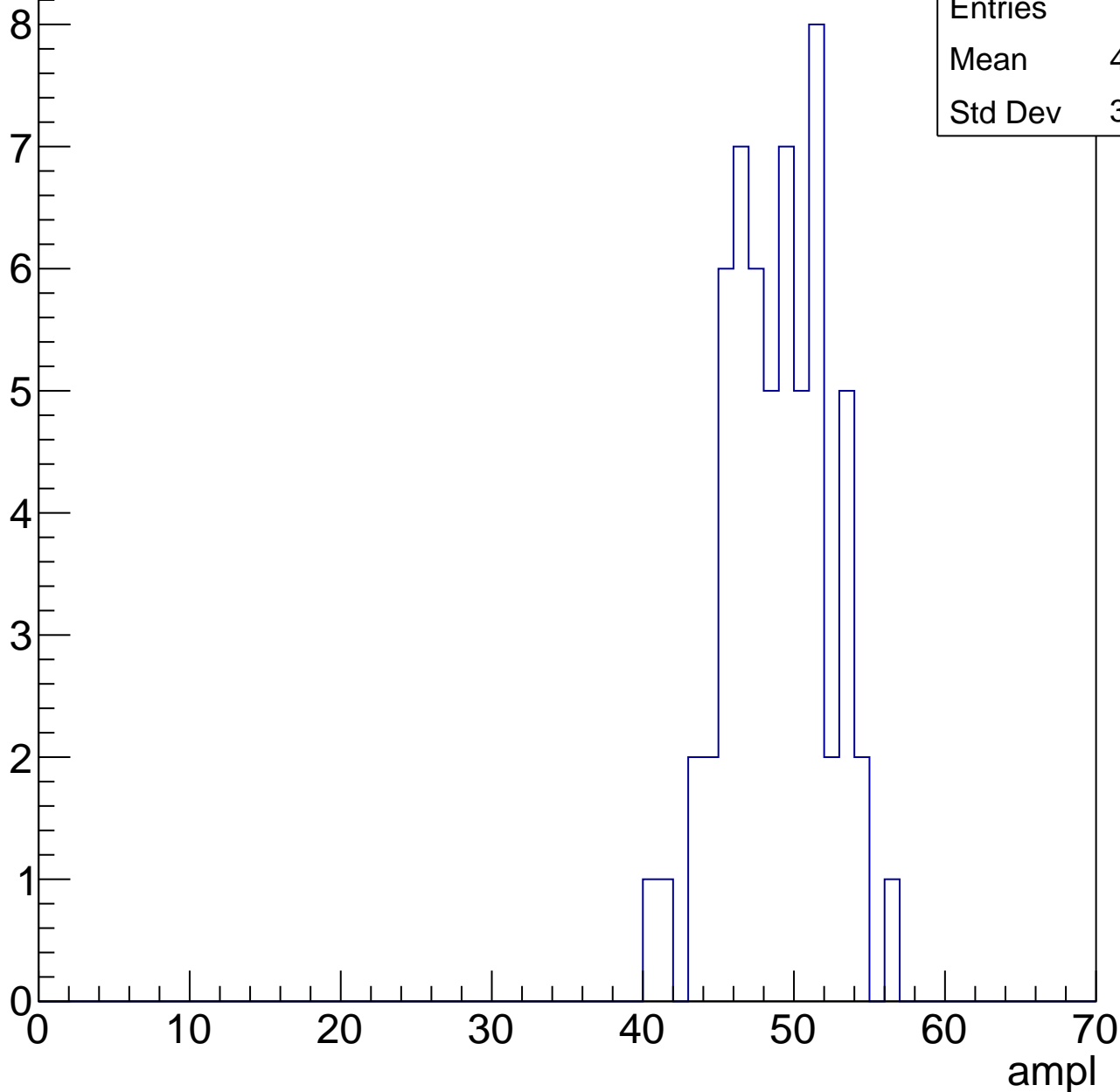


B1L101S, U15-ch52, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	48.38
Std Dev	3.332

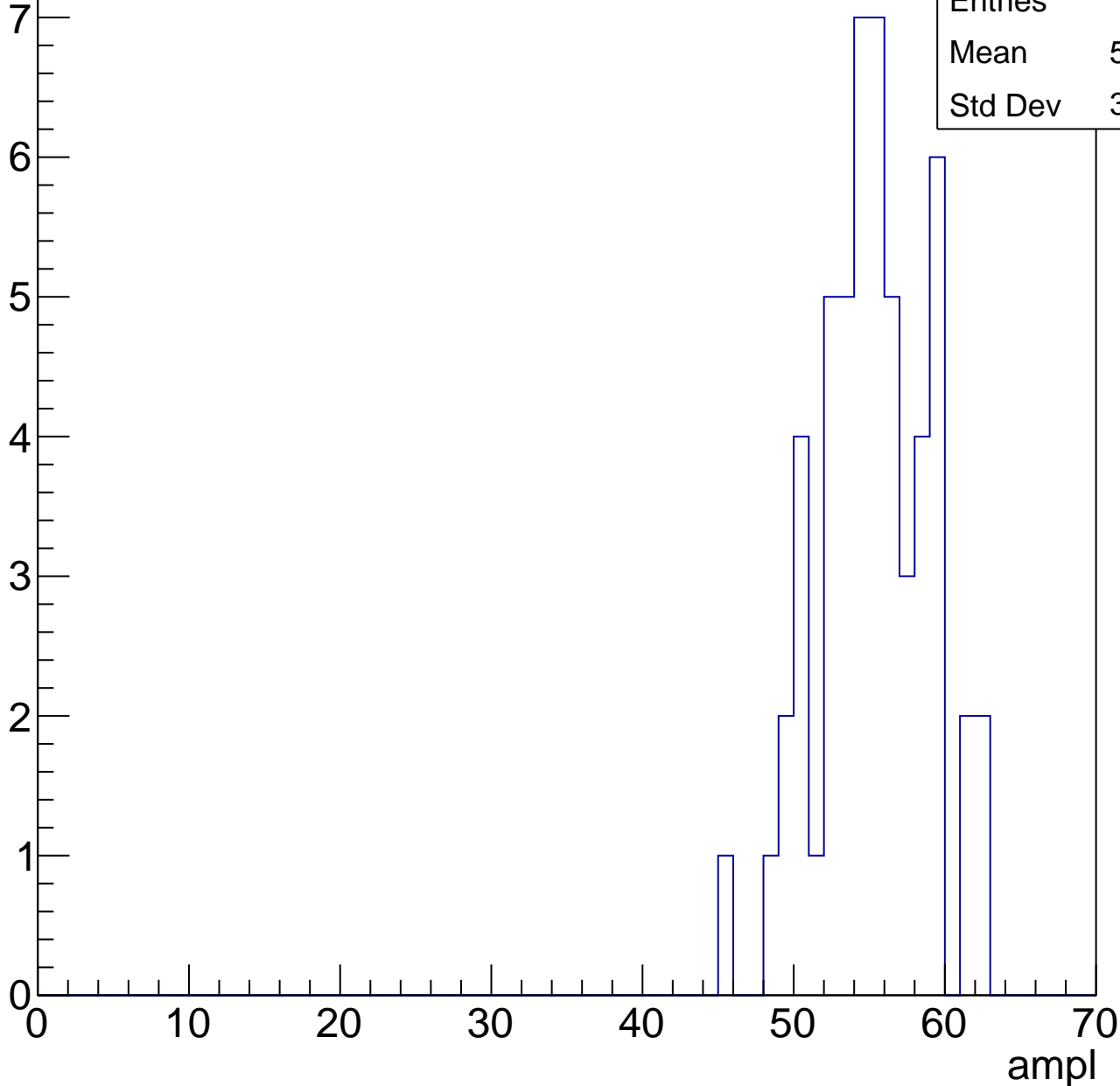


B1L101S, U15-ch52, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

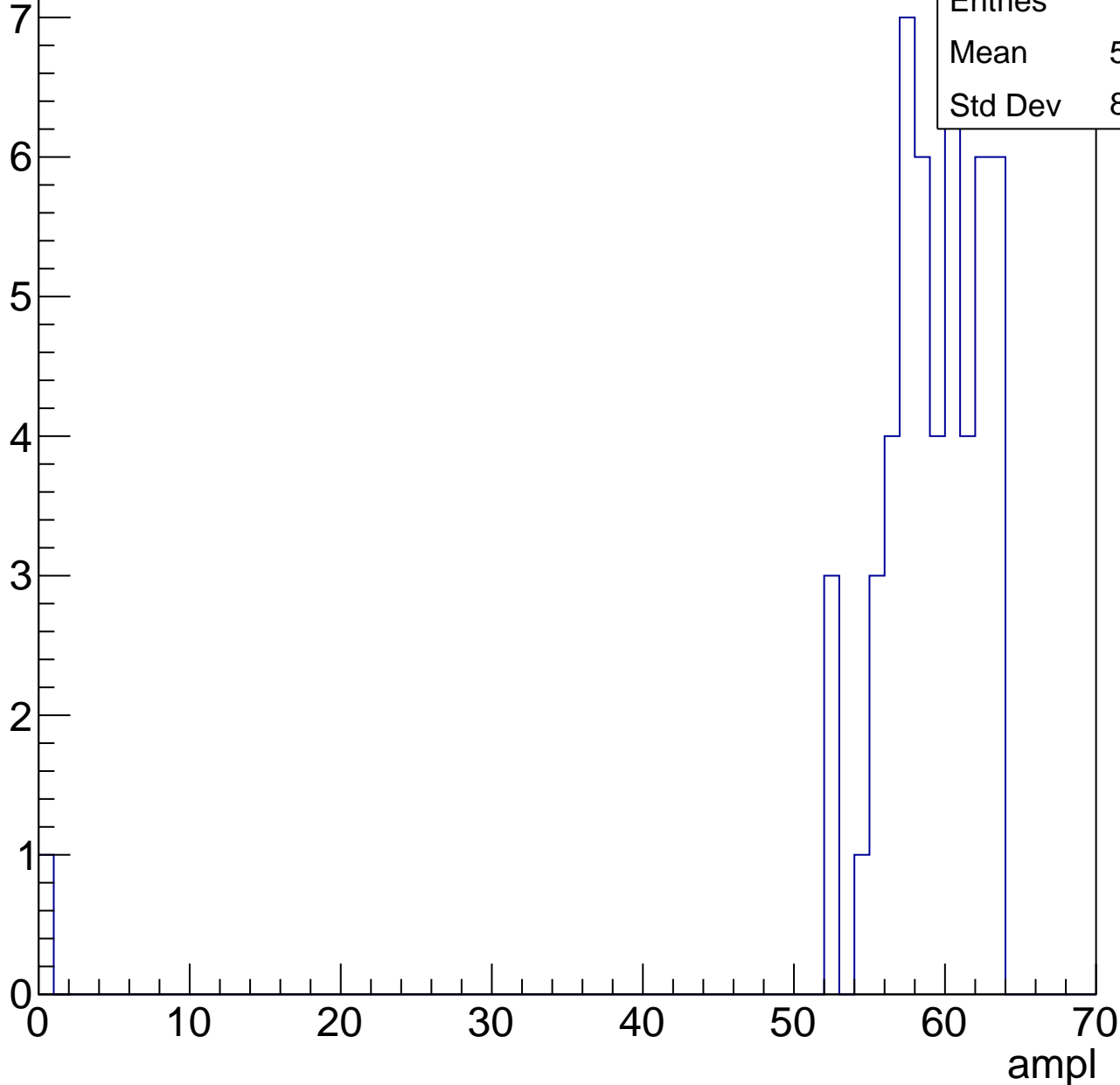
Entries	55
Mean	54.78
Std Dev	3.632



B1L101S, U15-ch52, adc5

calib_packv5_042523_0143.root, FC#0, port D2

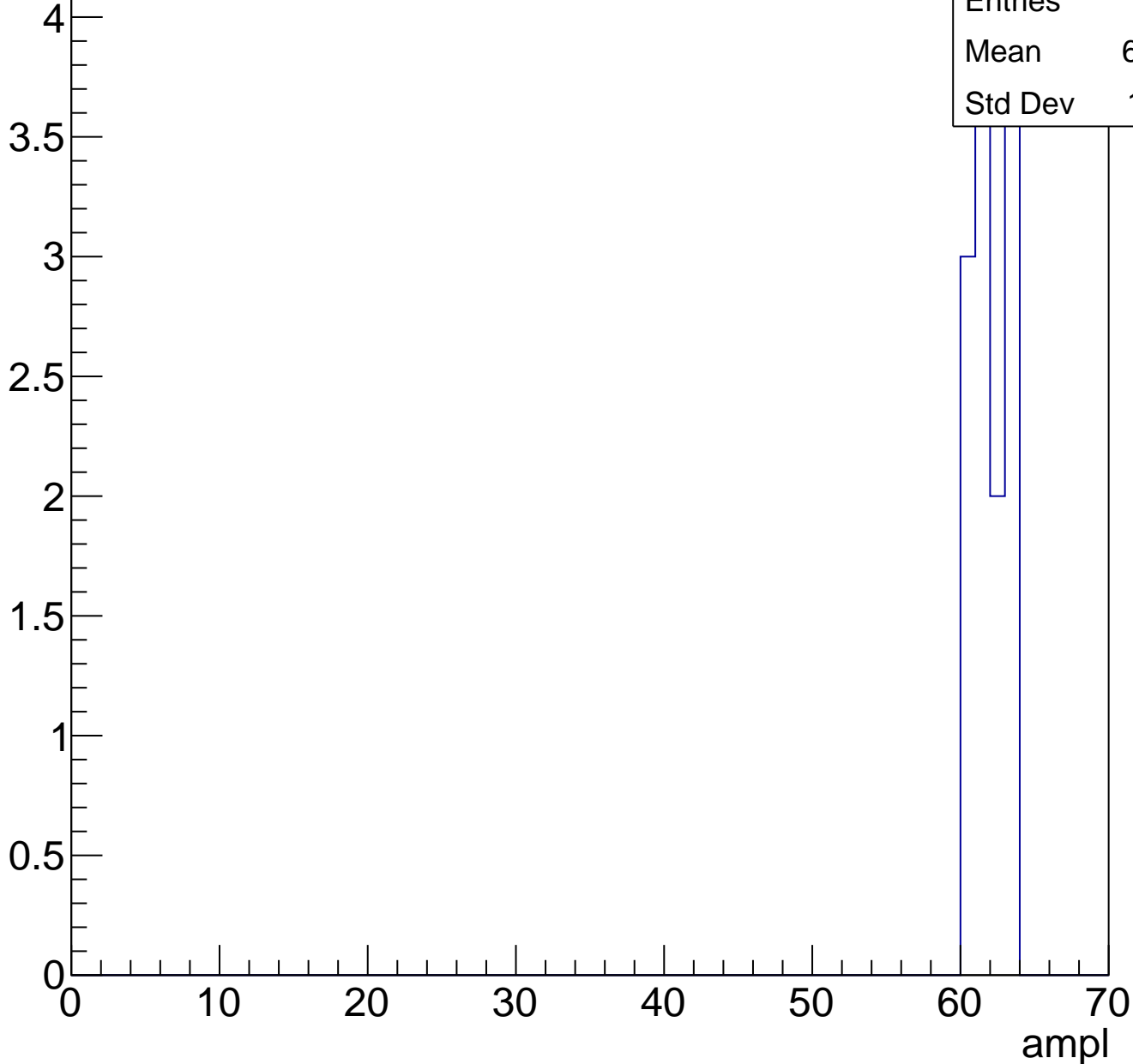
Entry



B1L101S, U15-ch52, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch52, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch53, adc0

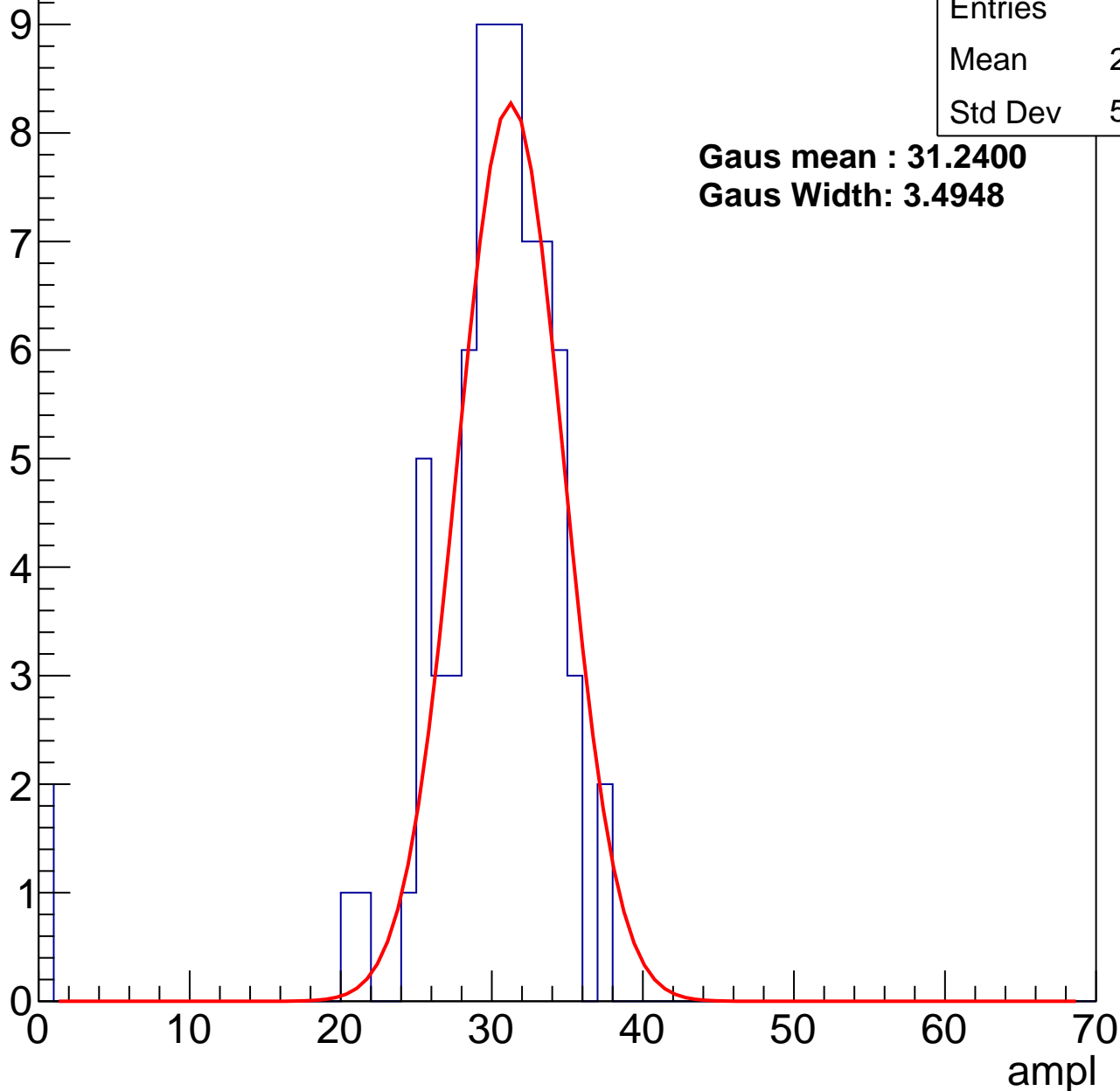
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	29.26
Std Dev	5.902

Gaus mean : 31.2400

Gaus Width: 3.4948



B1L101S, U15-ch53, adc1

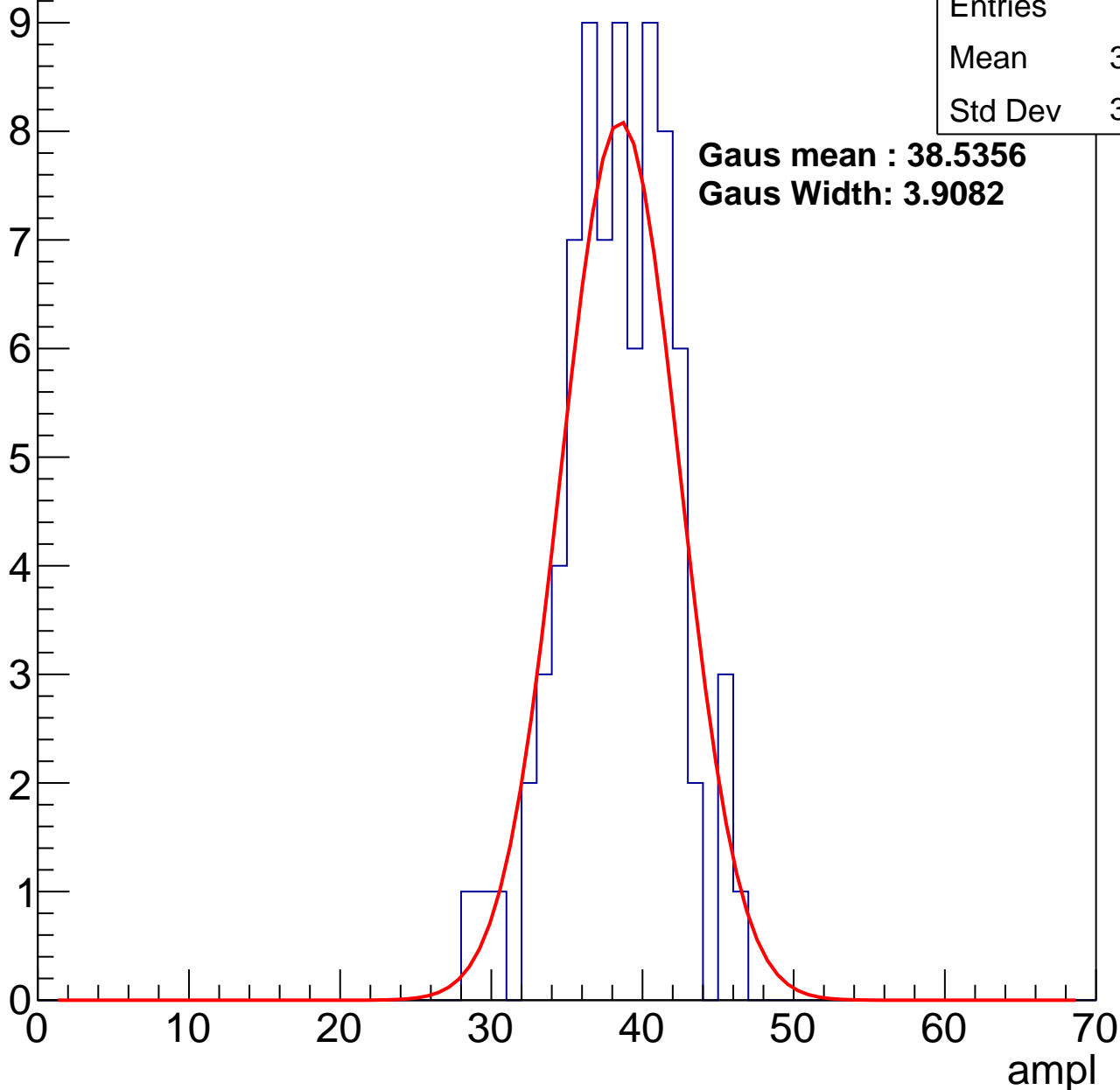
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	37.94
Std Dev	3.616

Gaus mean : 38.5356

Gaus Width: 3.9082



B1L101S, U15-ch53, adc2

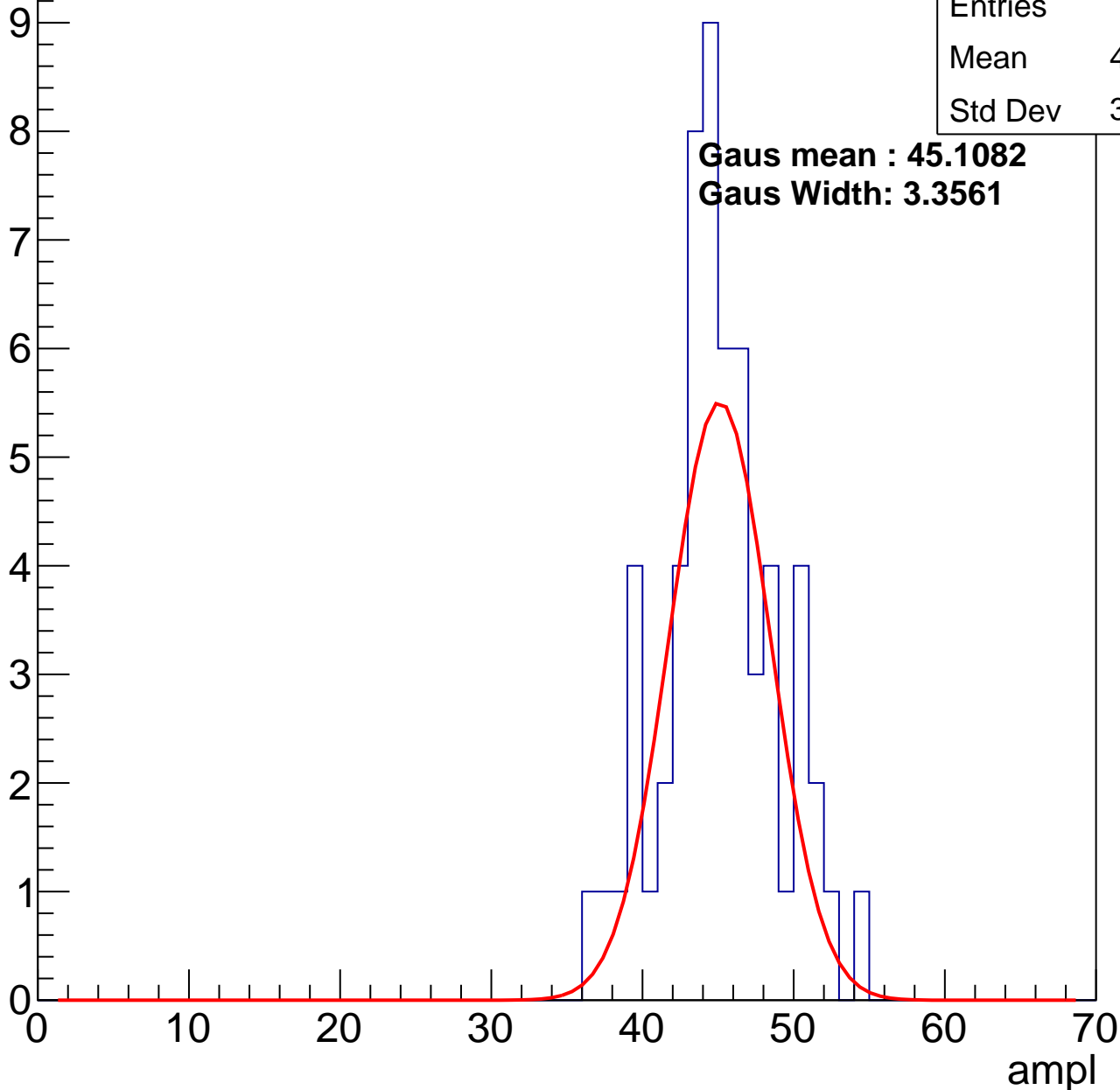
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	44.63
Std Dev	3.764

Gaus mean : 45.1082

Gaus Width: 3.3561

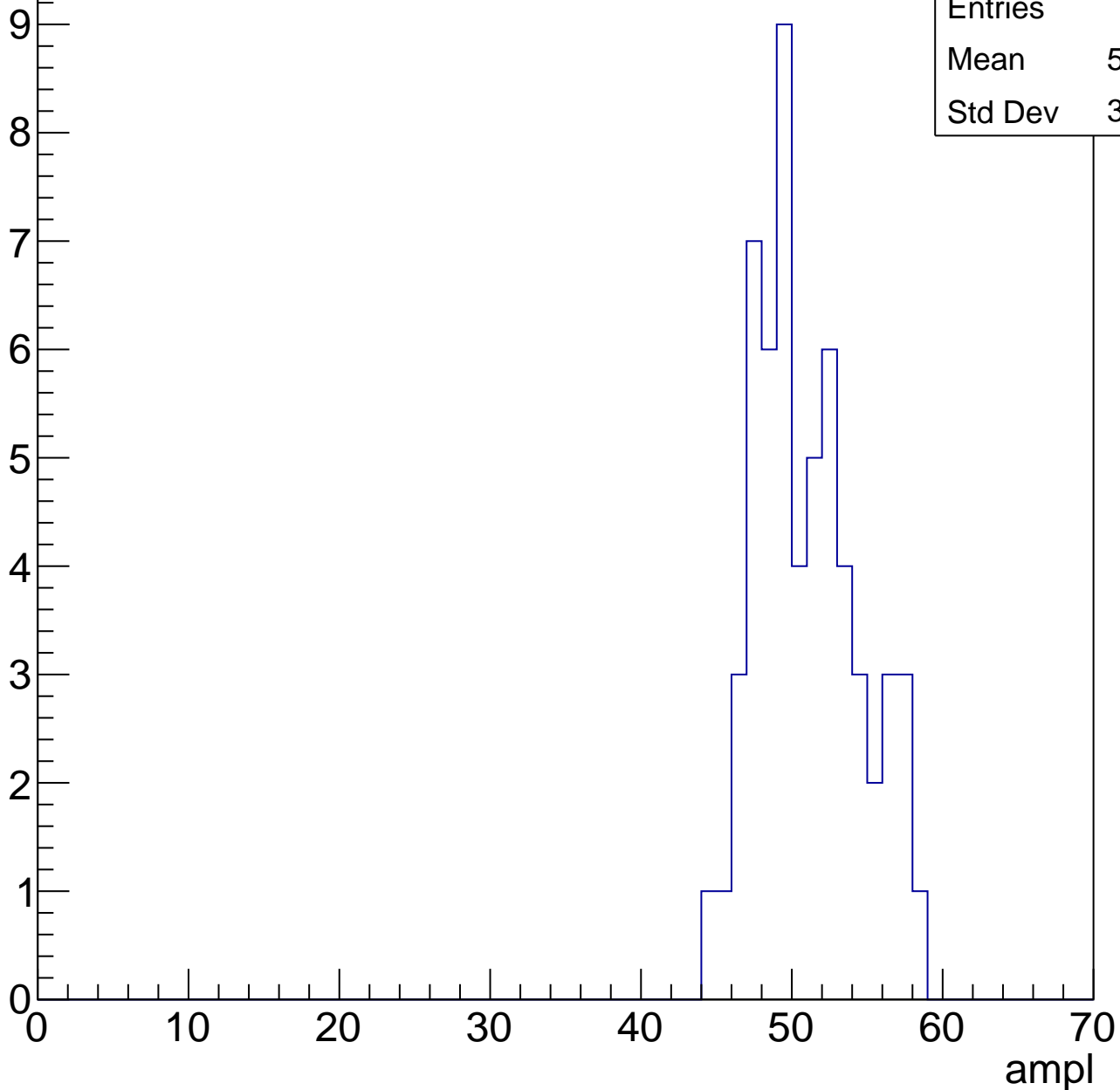


B1L101S, U15-ch53, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.57
Std Dev	3.394

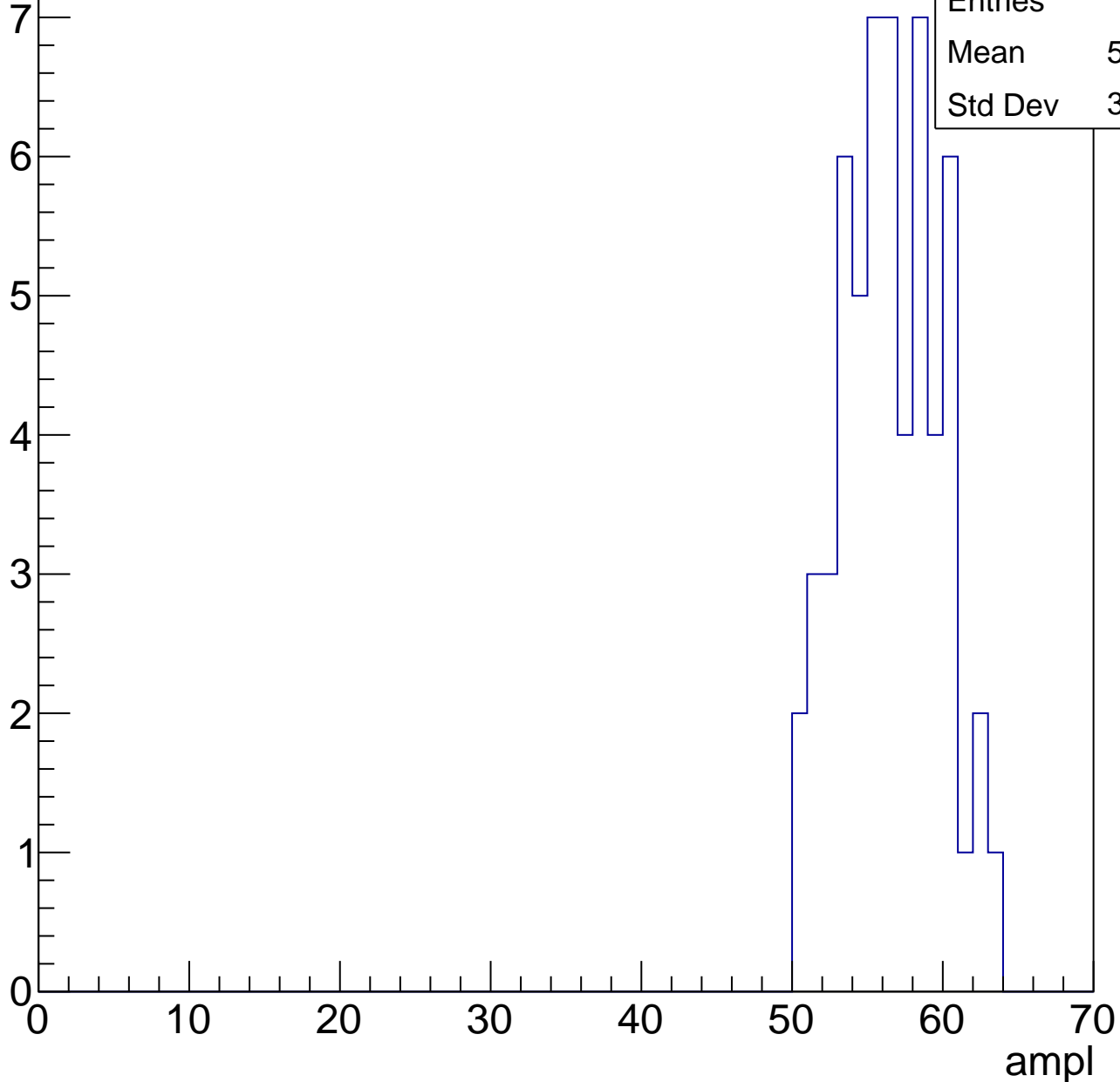


B1L101S, U15-ch53, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.07
Std Dev	3.178

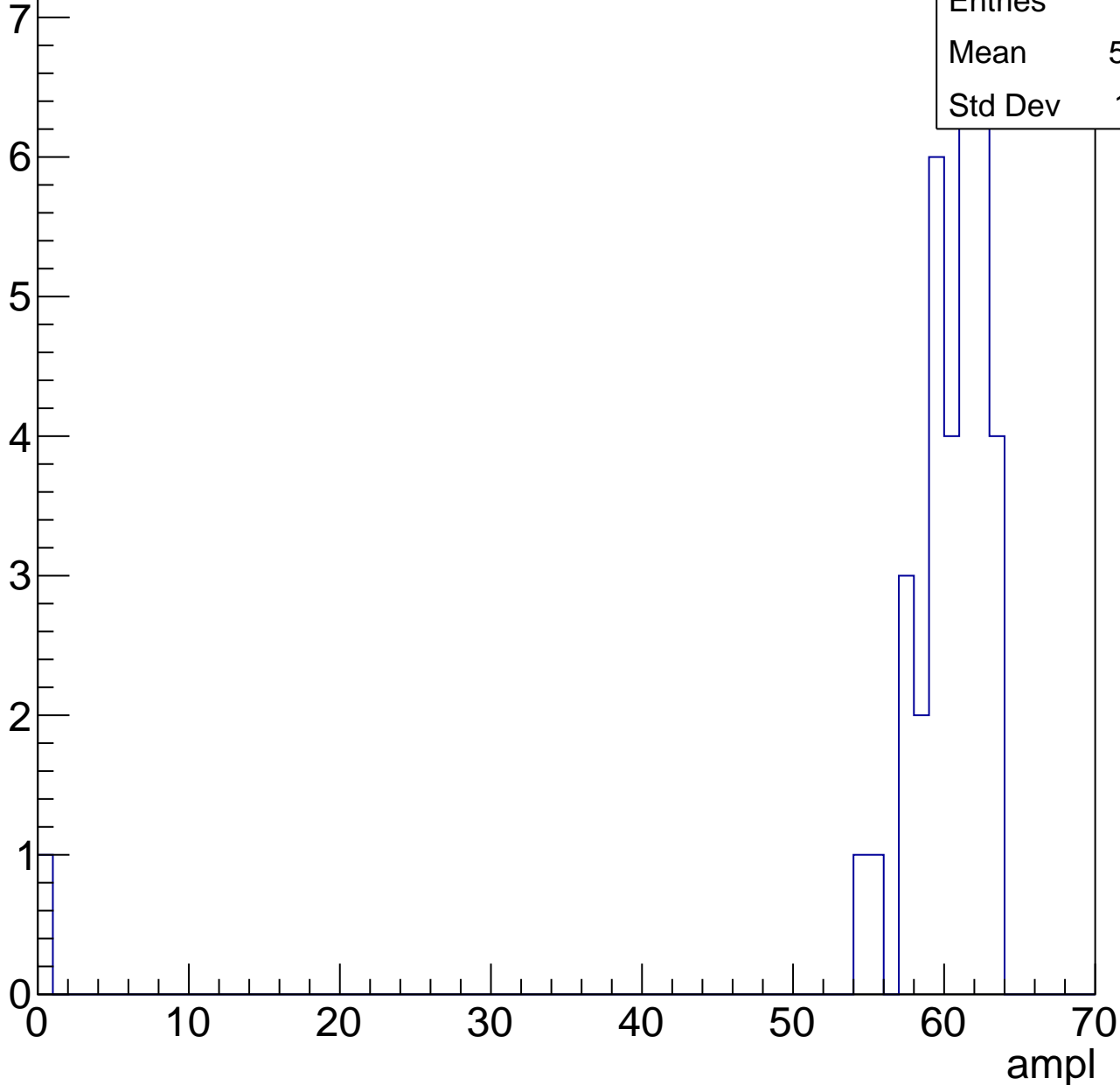


B1L101S, U15-ch53, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	36
Mean	58.42
Std Dev	10.11

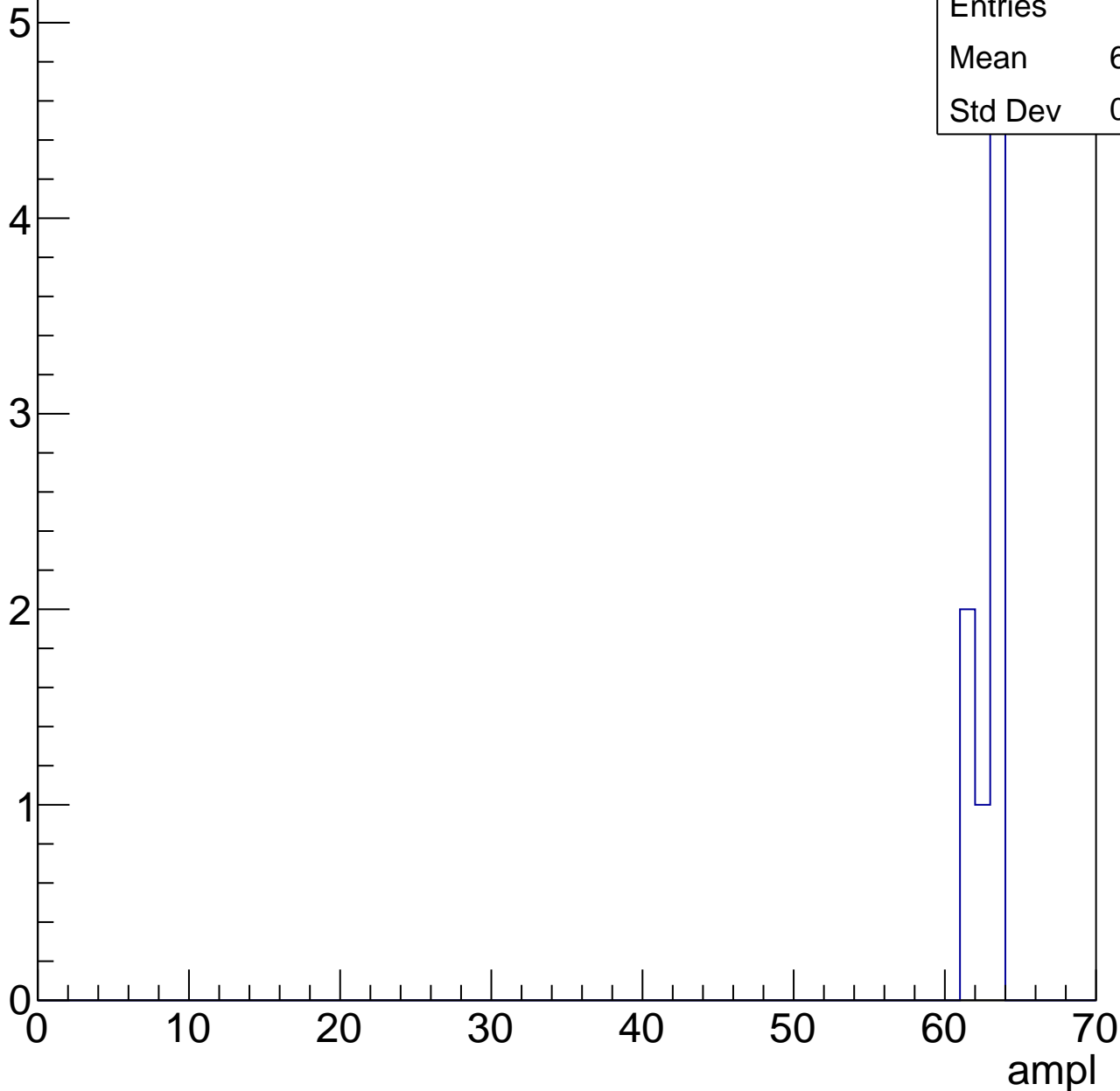


B1L101S, U15-ch53, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	8
Mean	62.38
Std Dev	0.857



B1L101S, U15-ch53, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U15-ch54, adc0

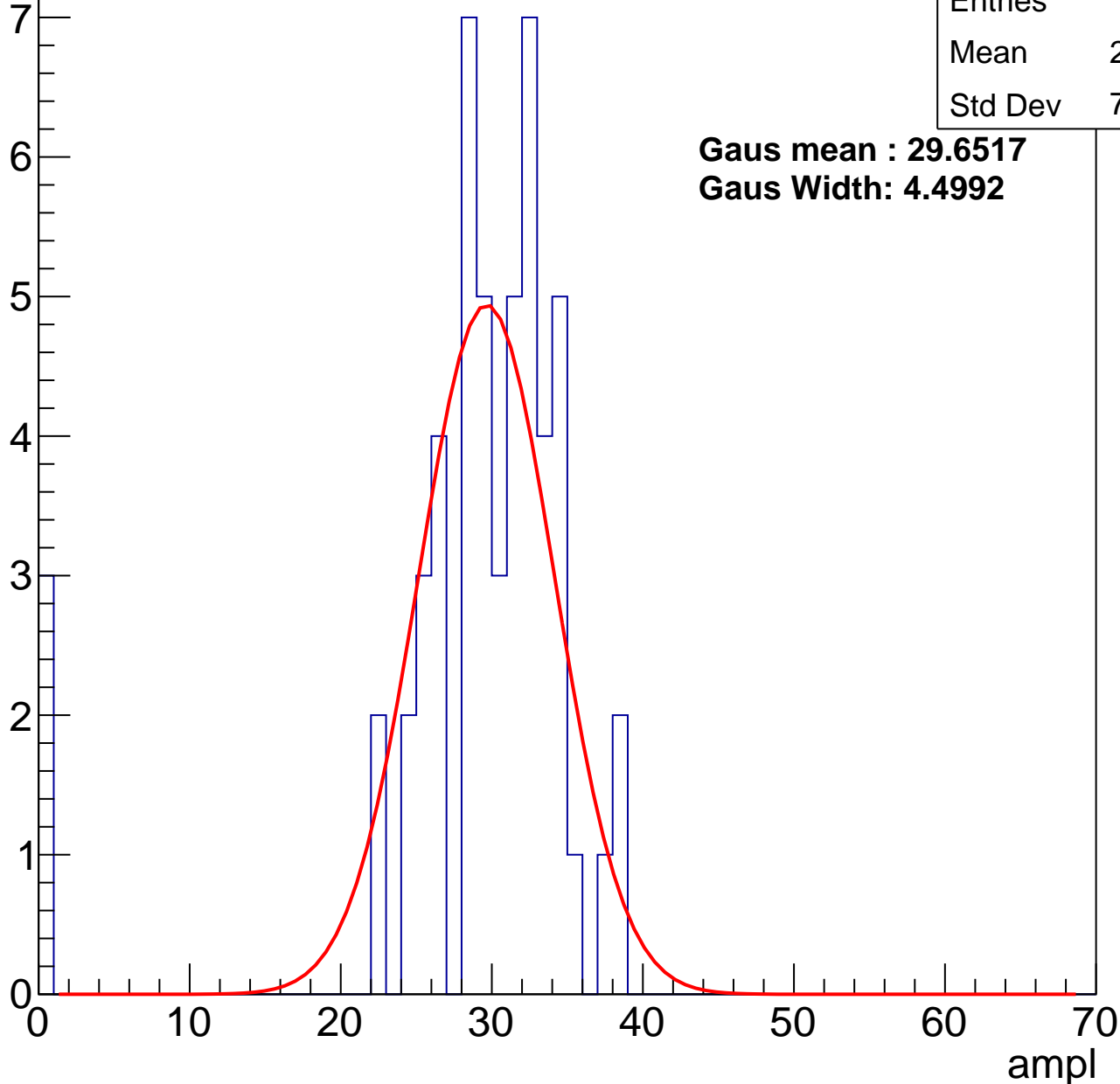
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	28.35
Std Dev	7.789

Gaus mean : 29.6517

Gaus Width: 4.4992



B1L101S, U15-ch54, adc1

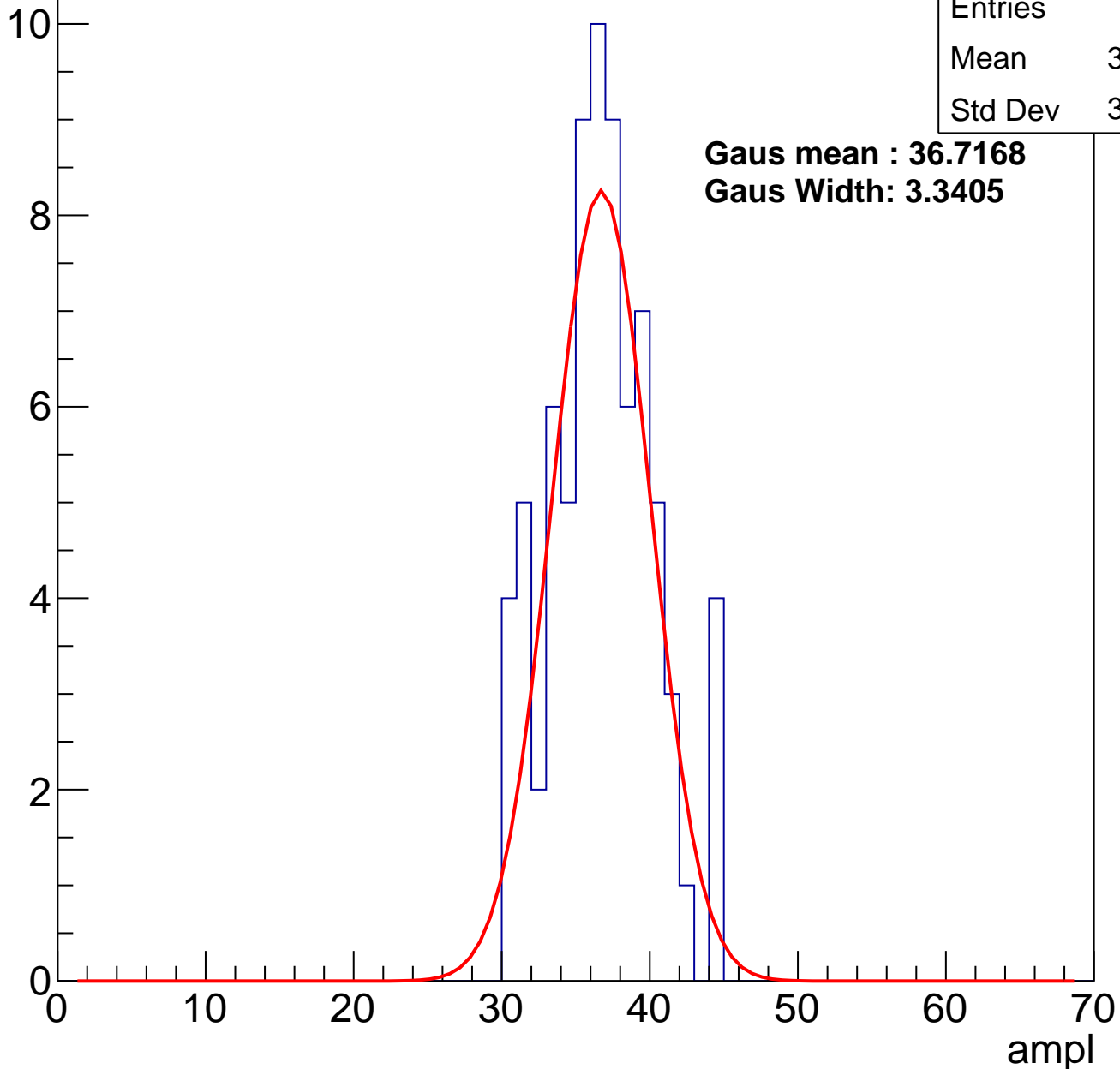
calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	36.28
Std Dev	3.482

Gaus mean : 36.7168

Gaus Width: 3.3405

Entry



B1L101S, U15-ch54, adc2

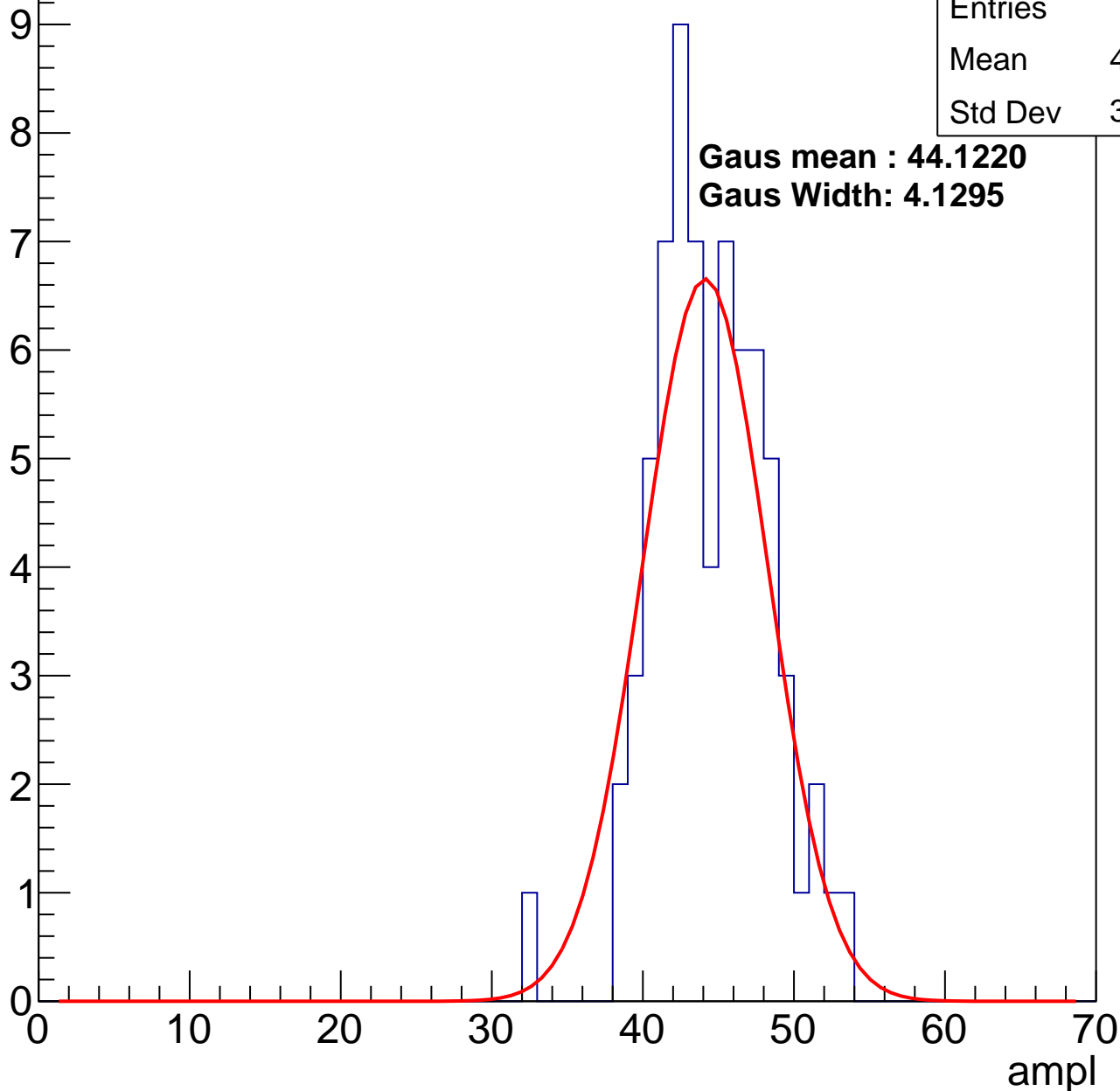
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	44.06
Std Dev	3.772

Gaus mean : 44.1220

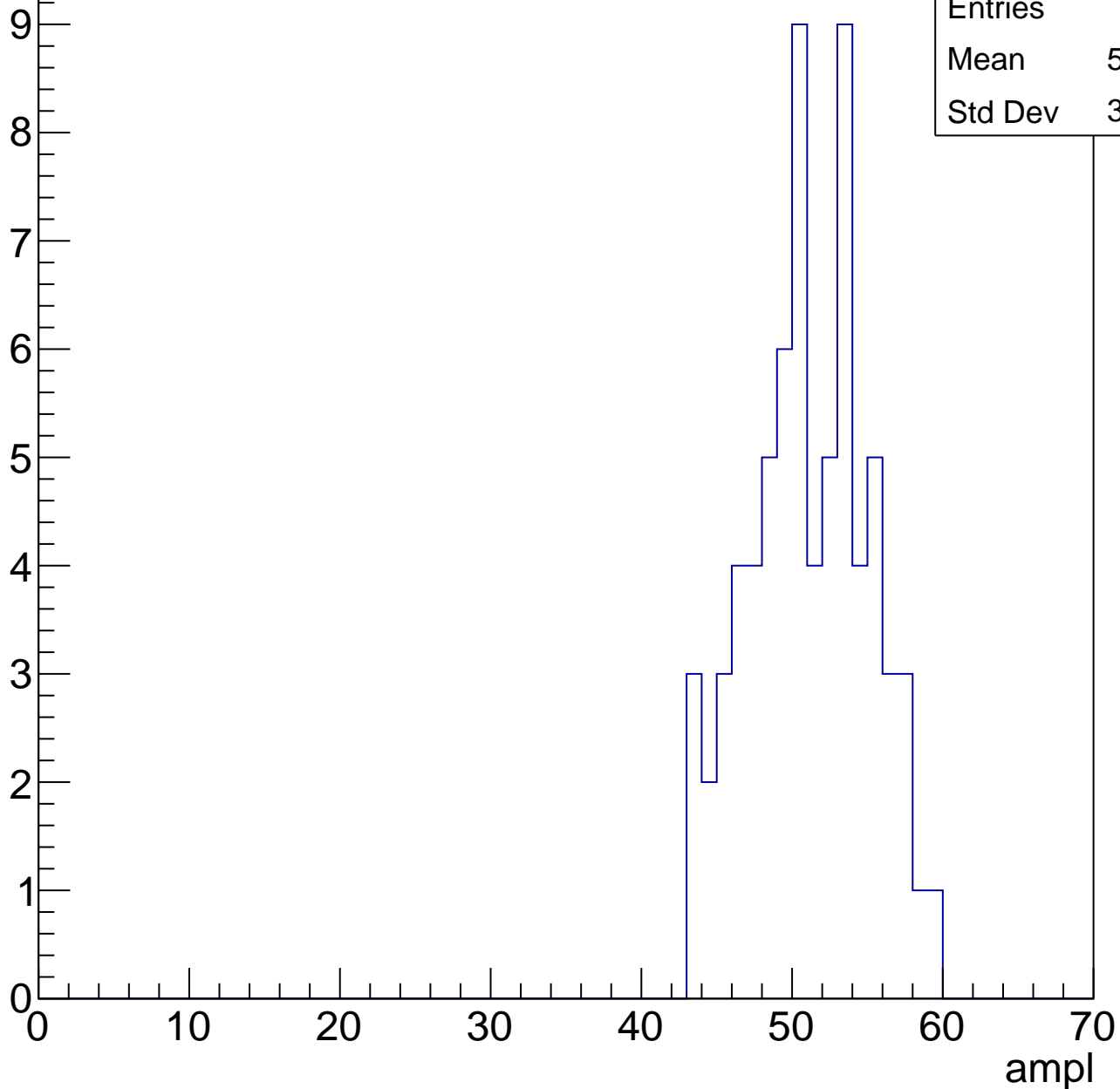
Gaus Width: 4.1295



B1L101S, U15-ch54, adc3

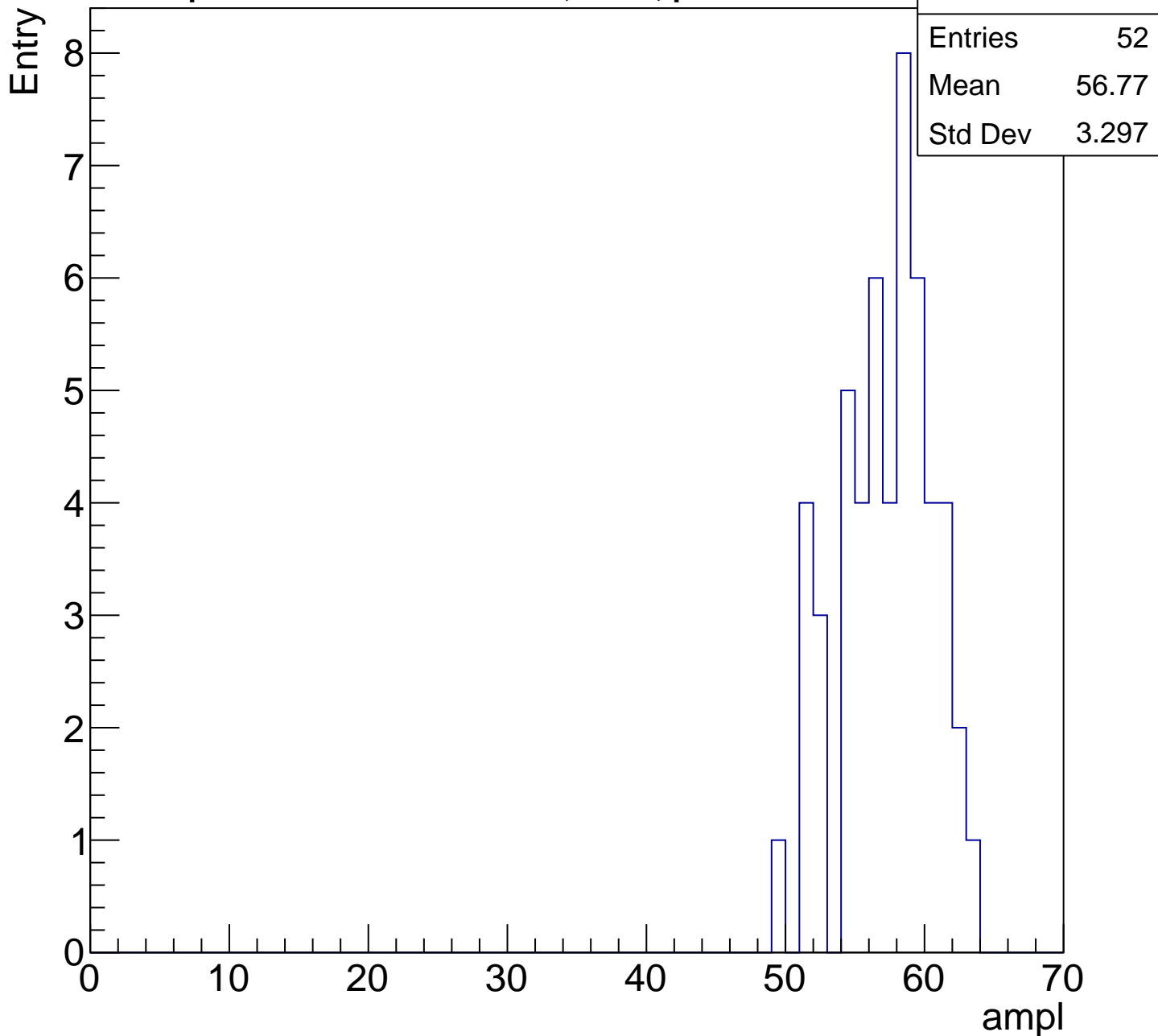
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch54, adc4

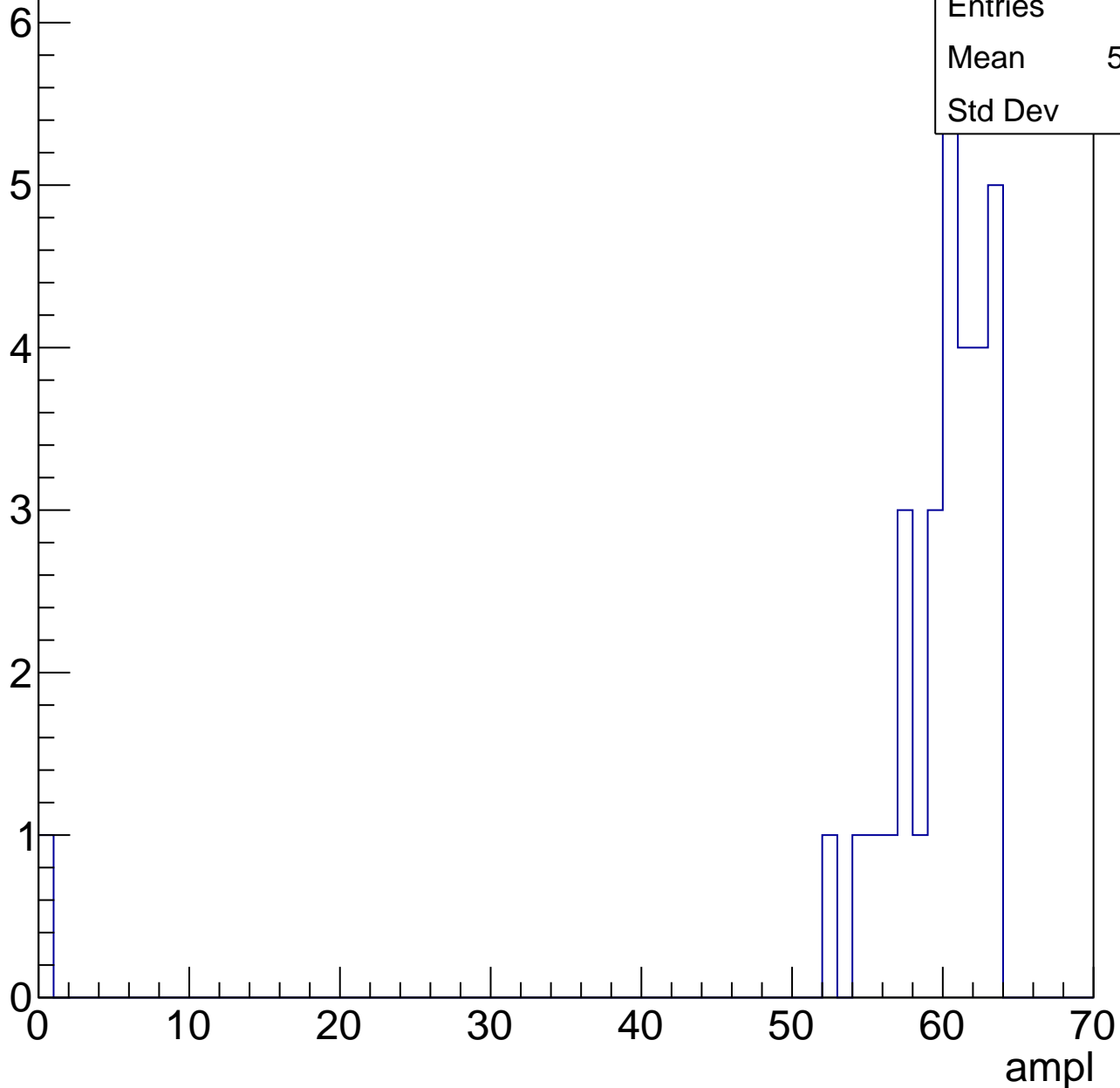
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch54, adc5

calib_packv5_042523_0143.root, FC#0, port D2

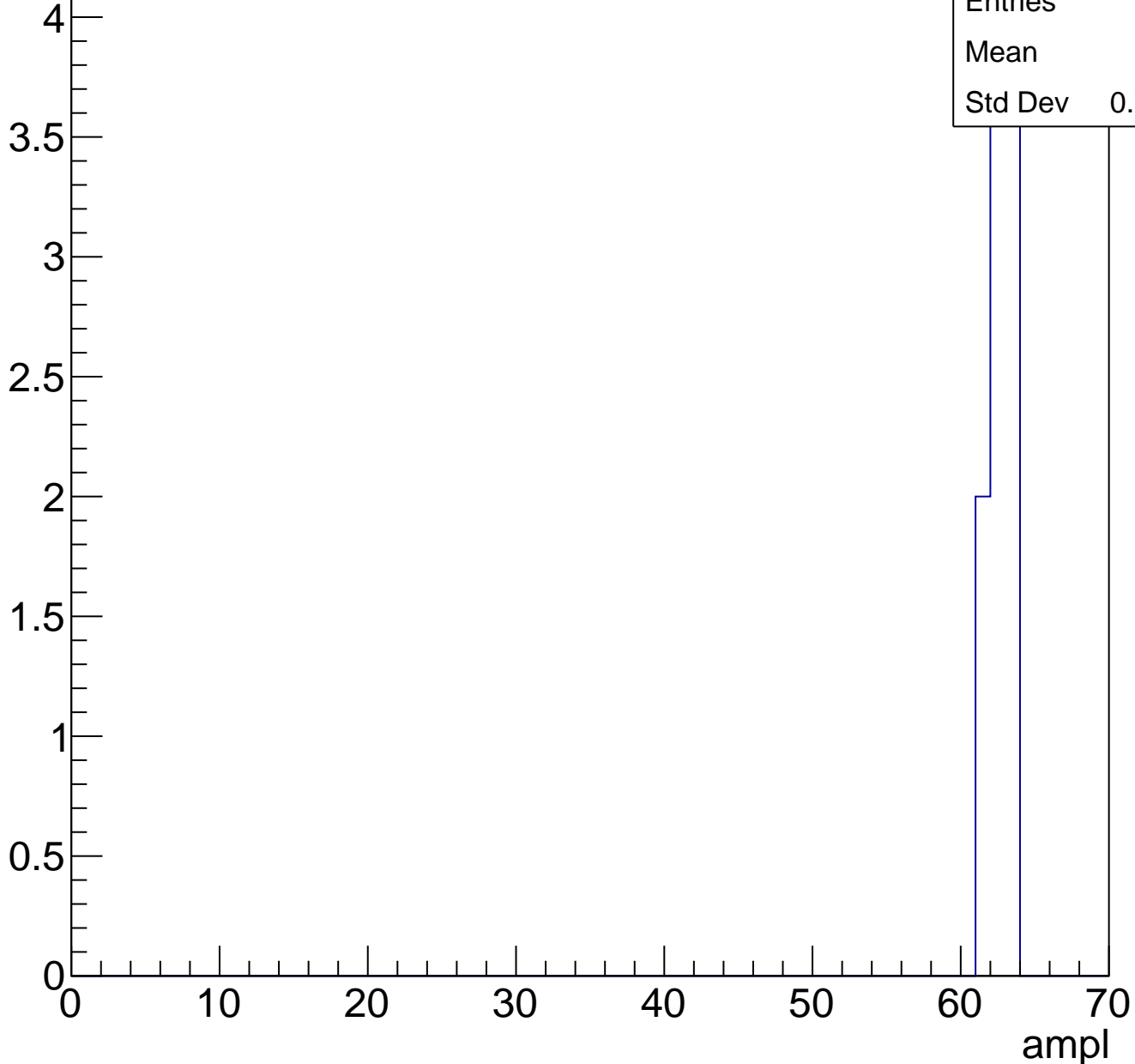
Entry



B1L101S, U15-ch54, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch54, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch55, adc0

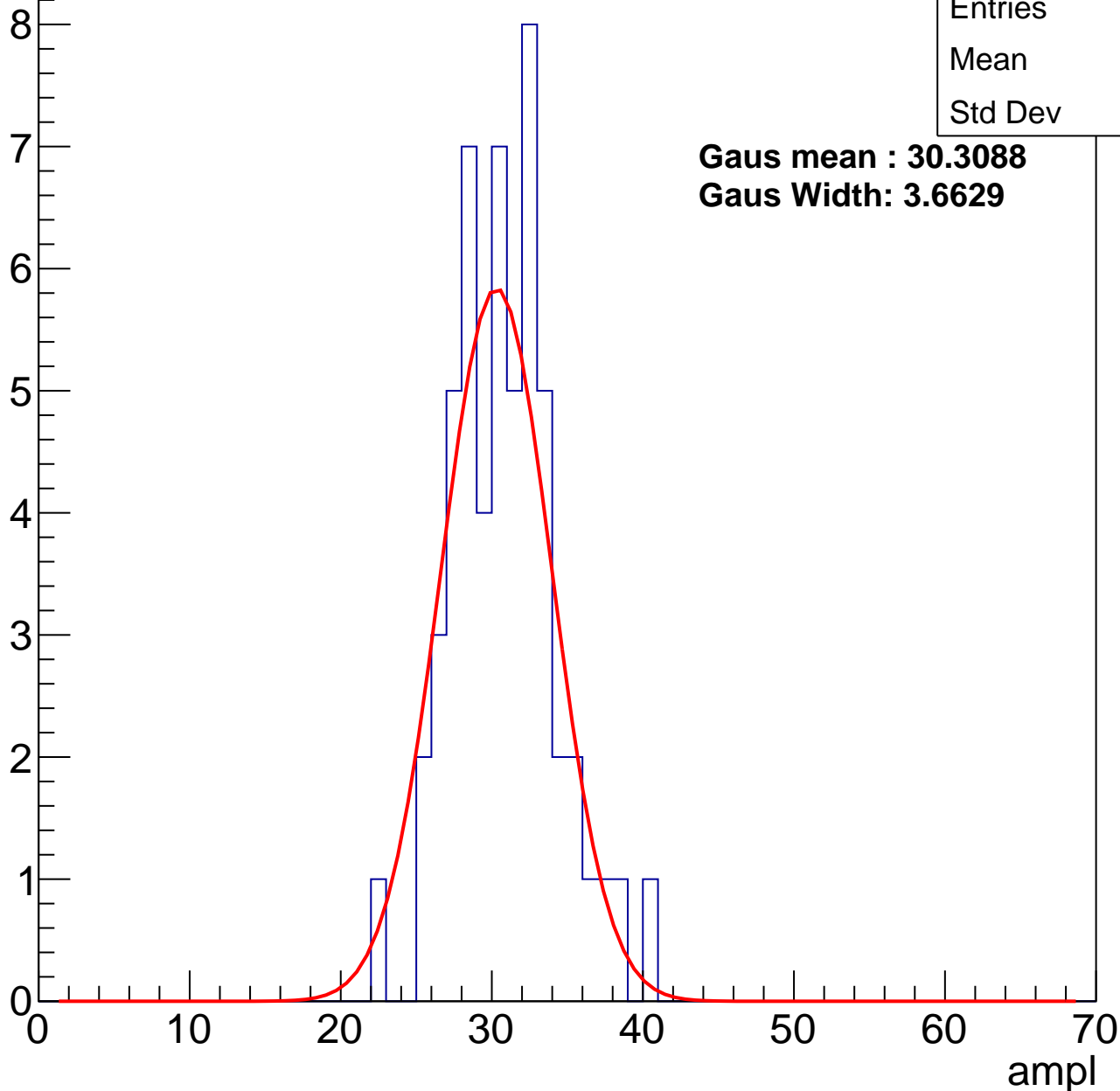
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	30.4
Std Dev	3.42

Gaus mean : 30.3088

Gaus Width: 3.6629



B1L101S, U15-ch55, adc1

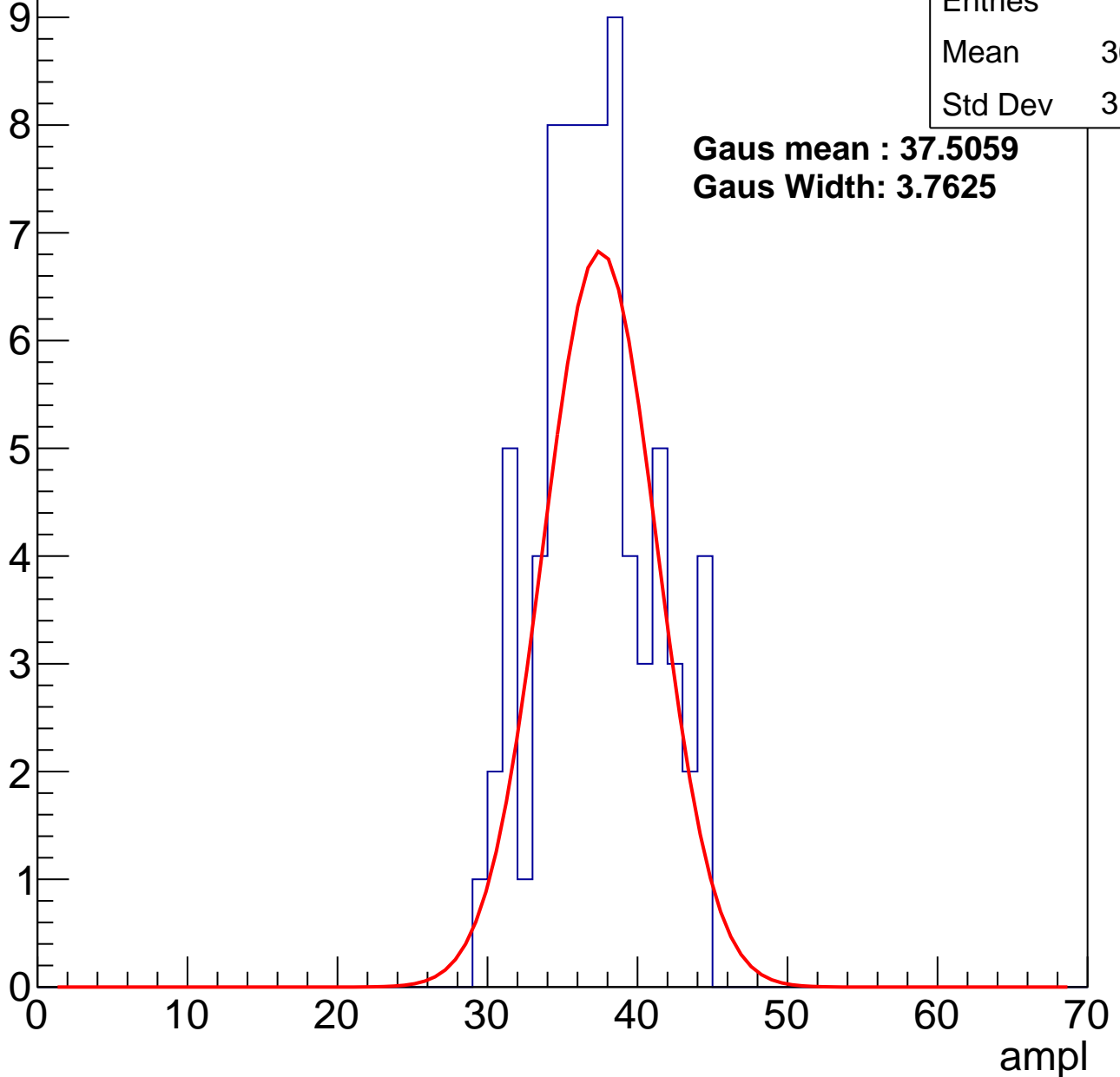
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.73
Std Dev	3.675

Gaus mean : 37.5059

Gaus Width: 3.7625



B1L101S, U15-ch55, adc2

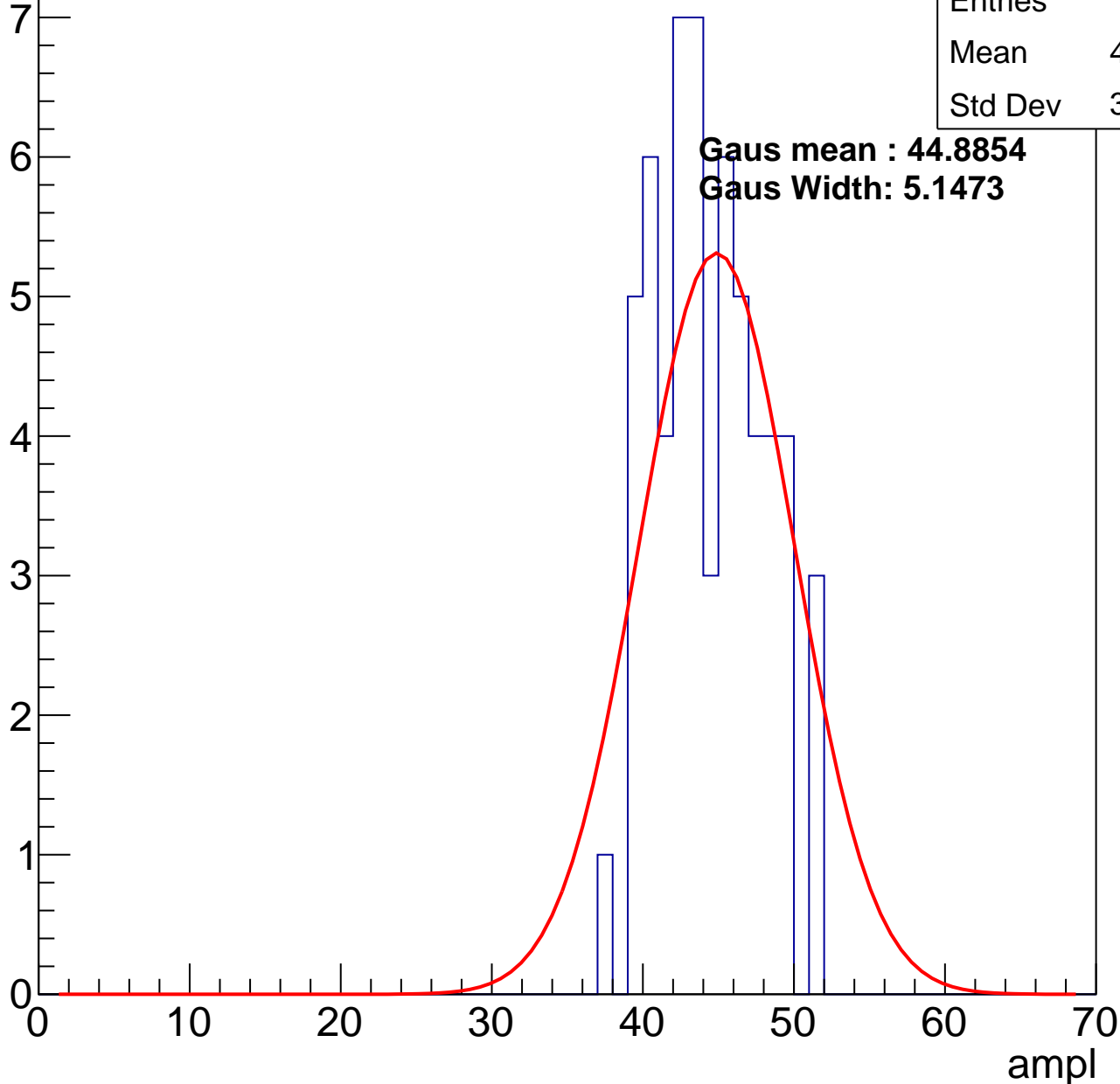
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	43.93
Std Dev	3.478

Gaus mean : 44.8854

Gaus Width: 5.1473

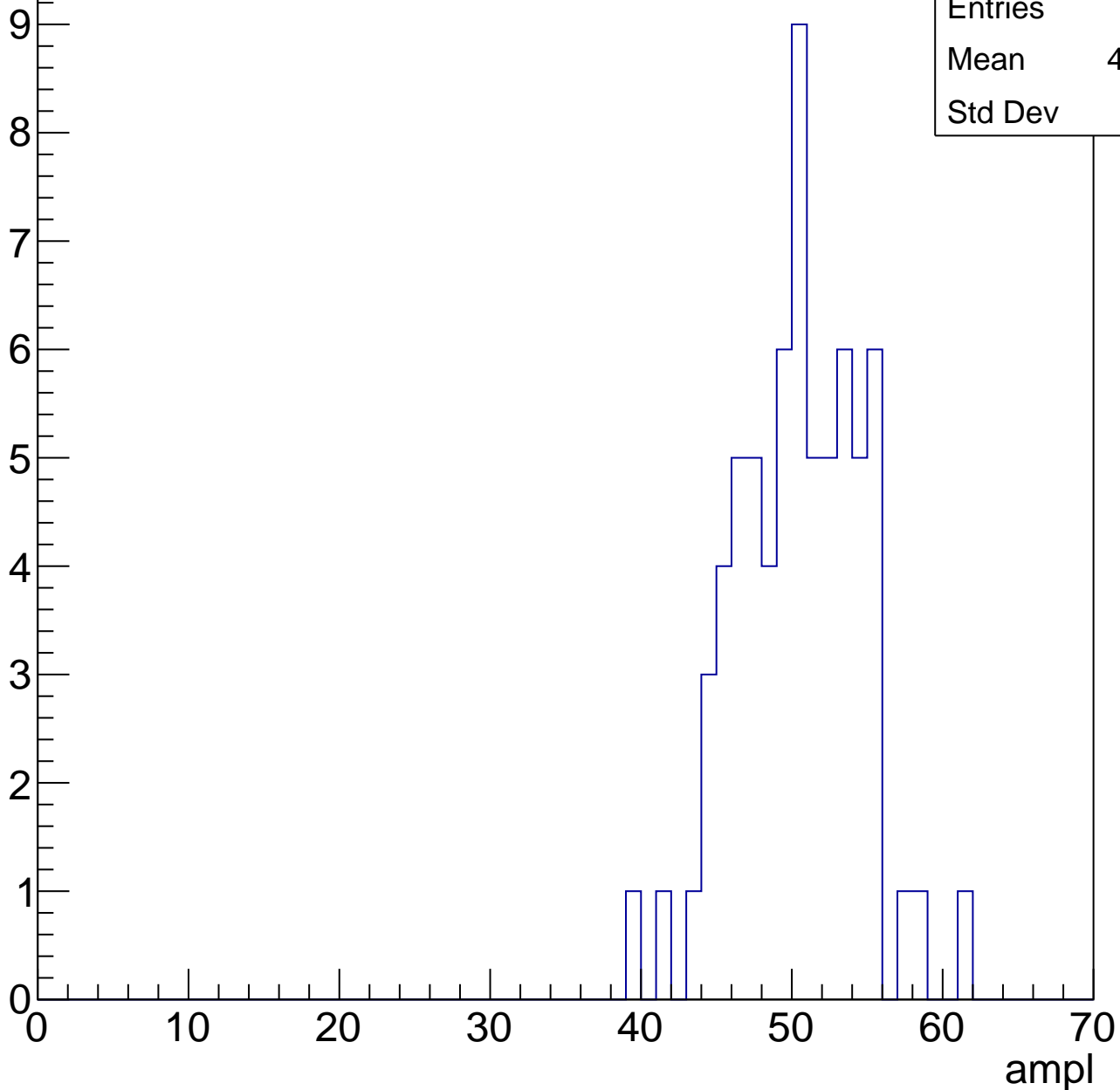


B1L101S, U15-ch55, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	49.93
Std Dev	4.08

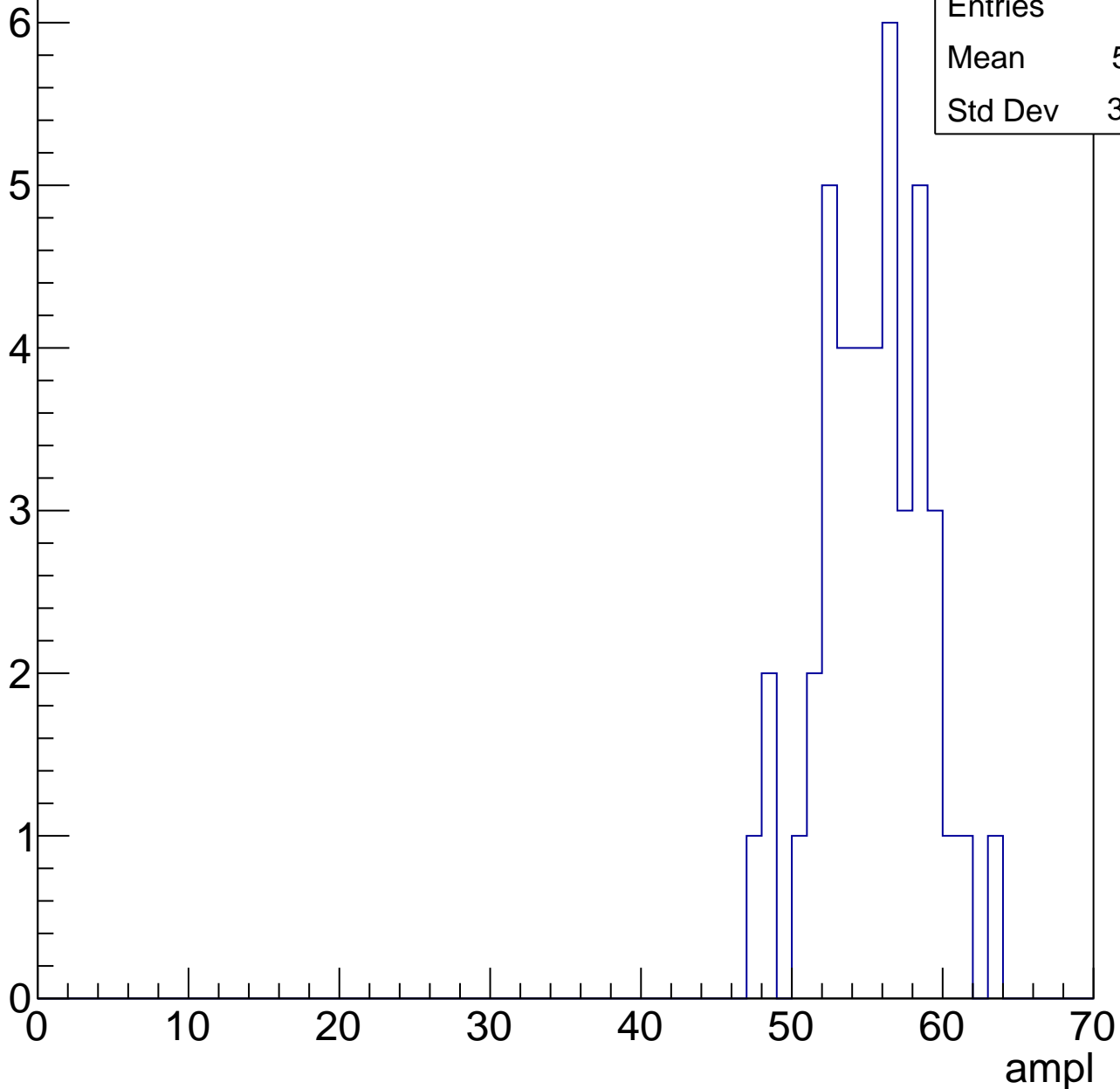


B1L101S, U15-ch55, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

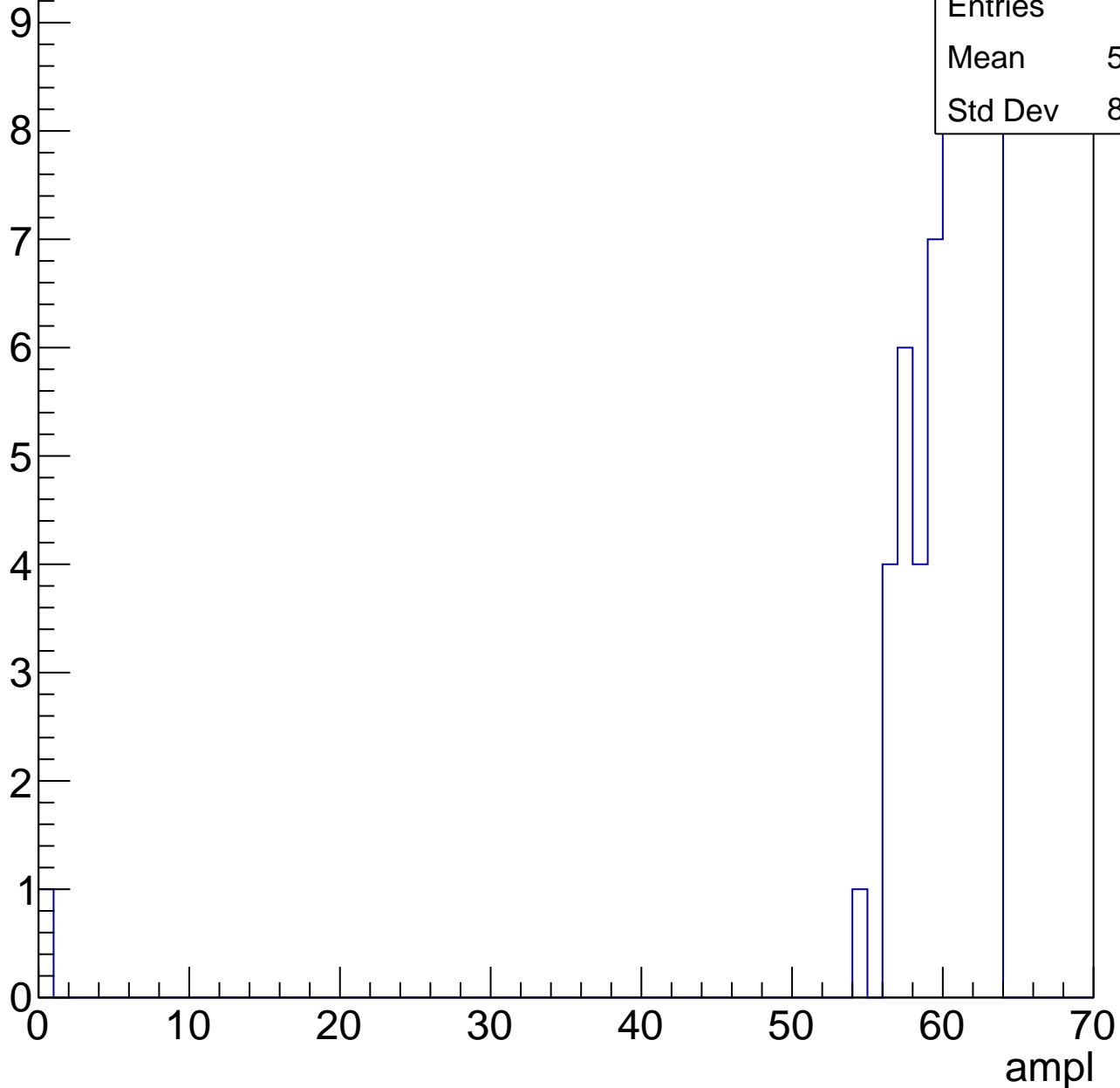
Entries	43
Mean	54.91
Std Dev	3.476



B1L101S, U15-ch55, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch55, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch55, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch56, adc0

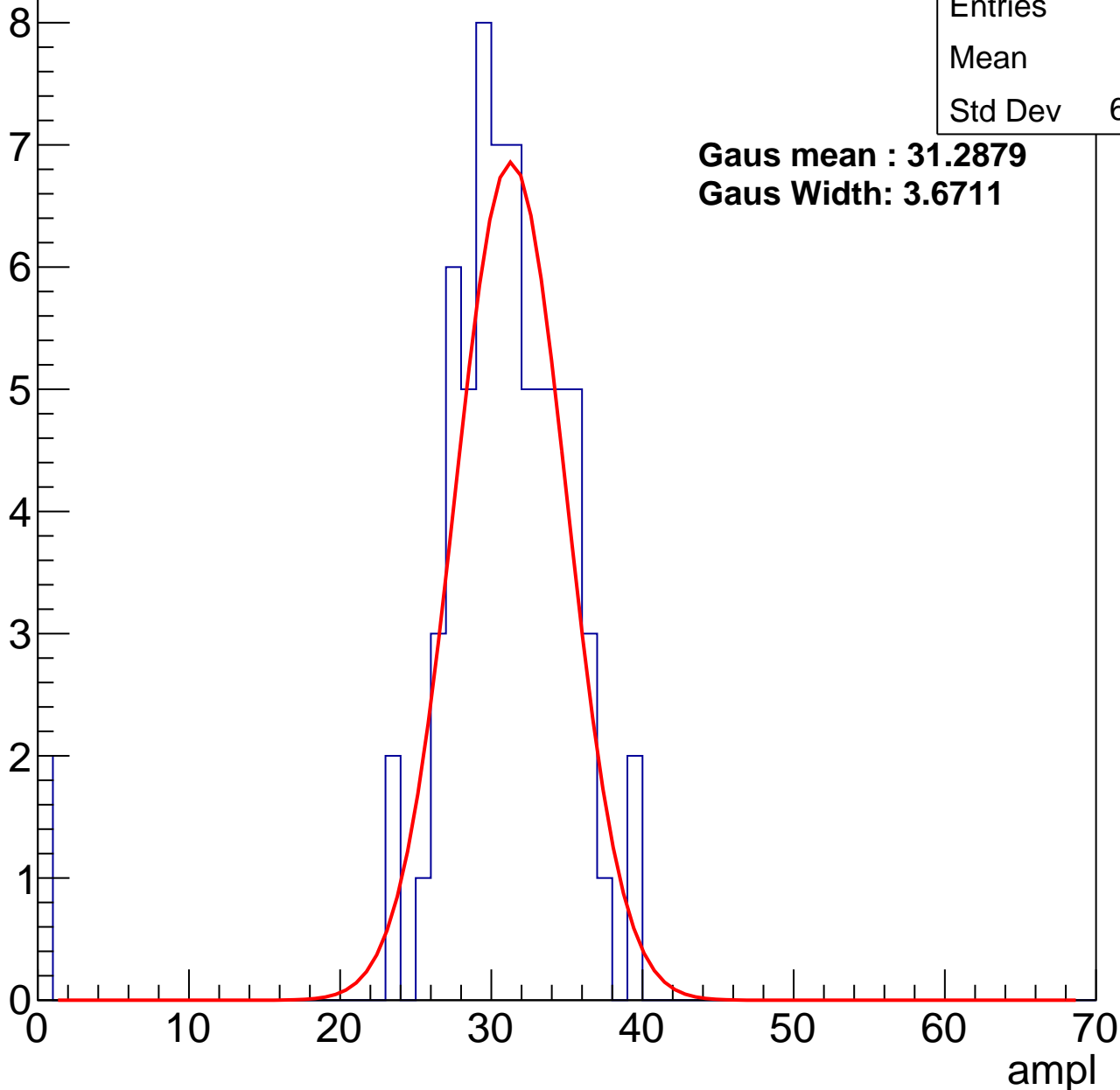
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.9
Std Dev	6.287

Gaus mean : 31.2879

Gaus Width: 3.6711



B1L101S, U15-ch56, adc1

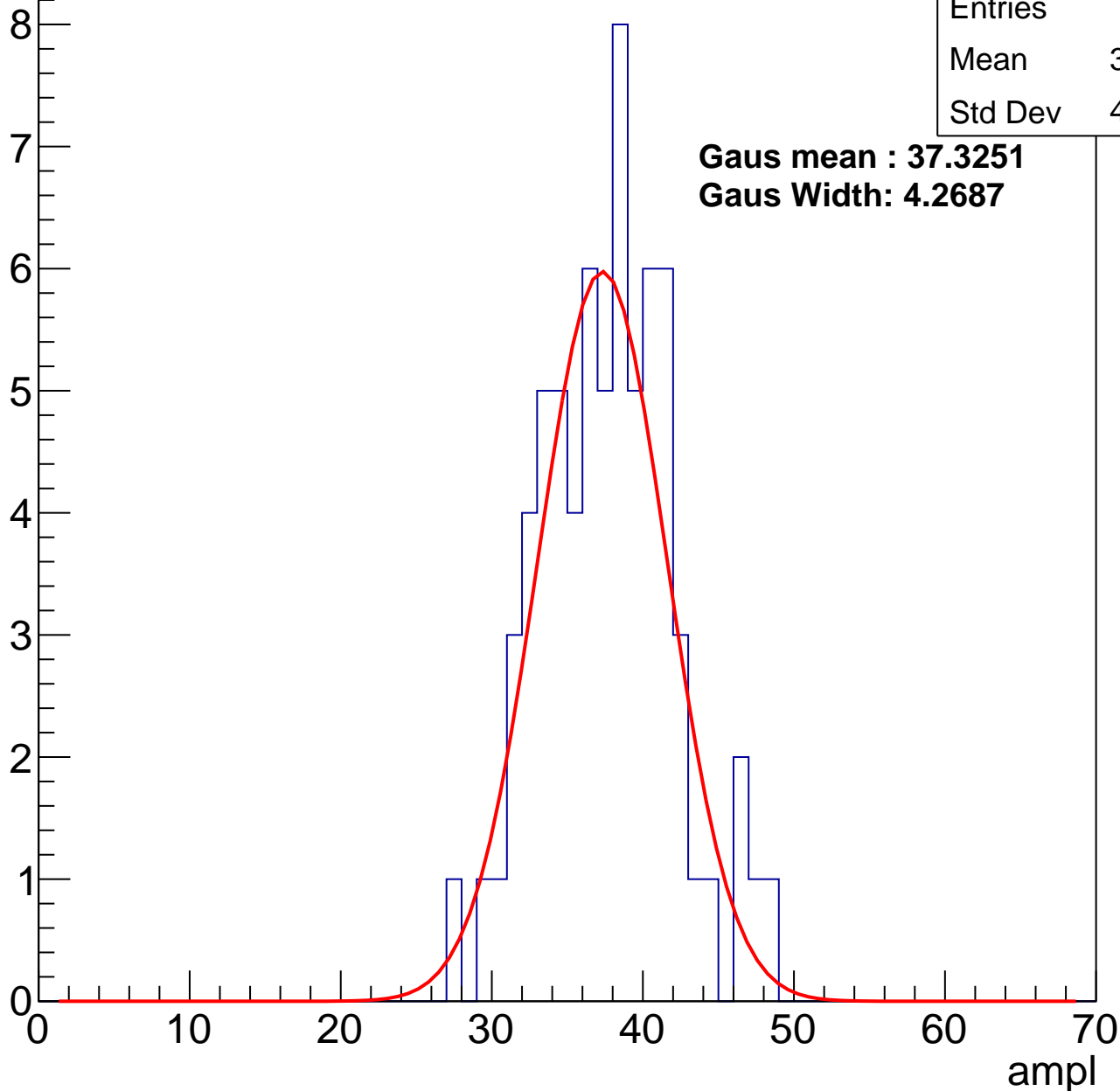
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	37.22
Std Dev	4.317

Gaus mean : 37.3251

Gaus Width: 4.2687



B1L101S, U15-ch56, adc2

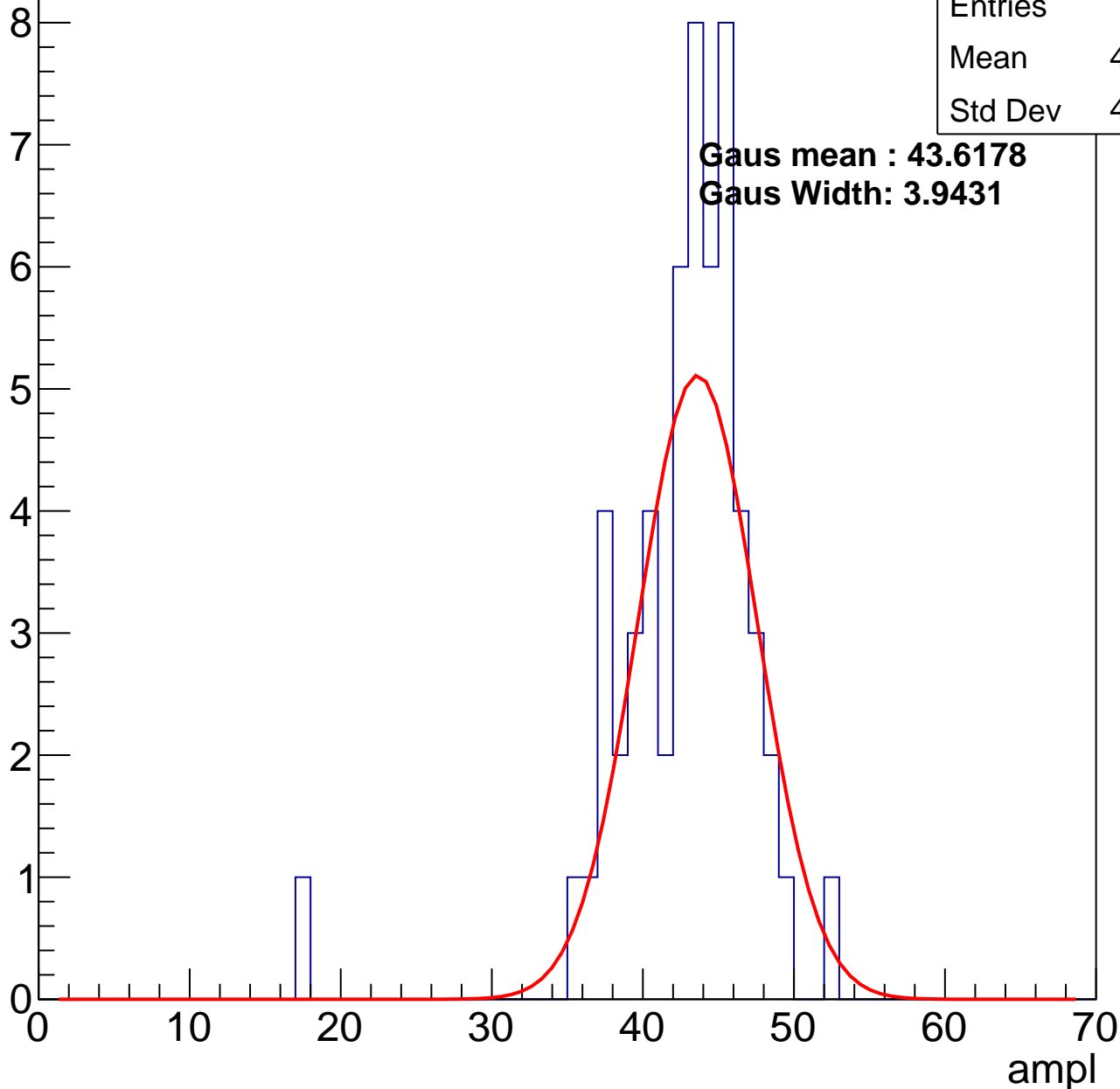
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.33
Std Dev	4.857

Gaus mean : 43.6178

Gaus Width: 3.9431

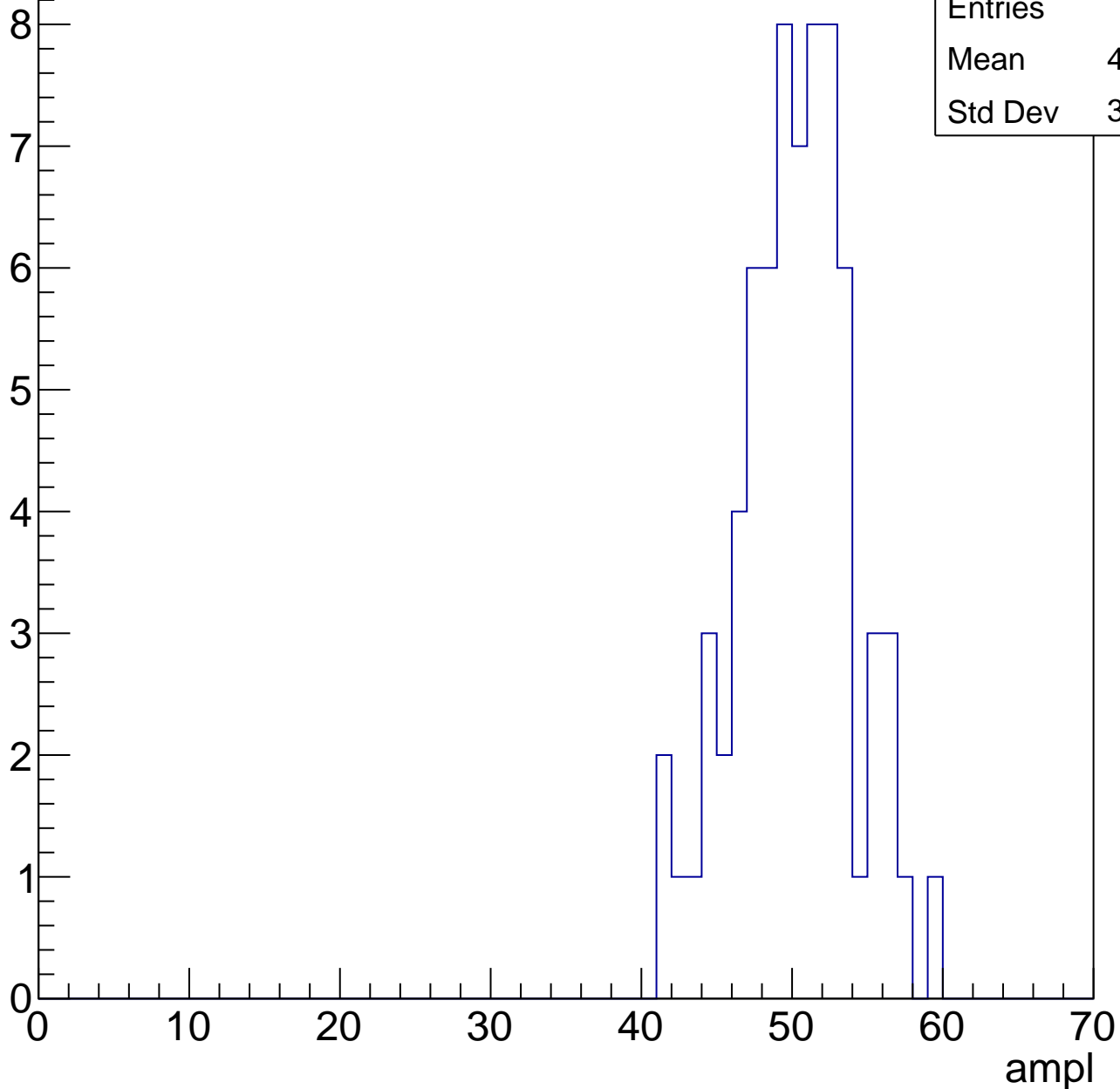


B1L101S, U15-ch56, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.72
Std Dev	3.754

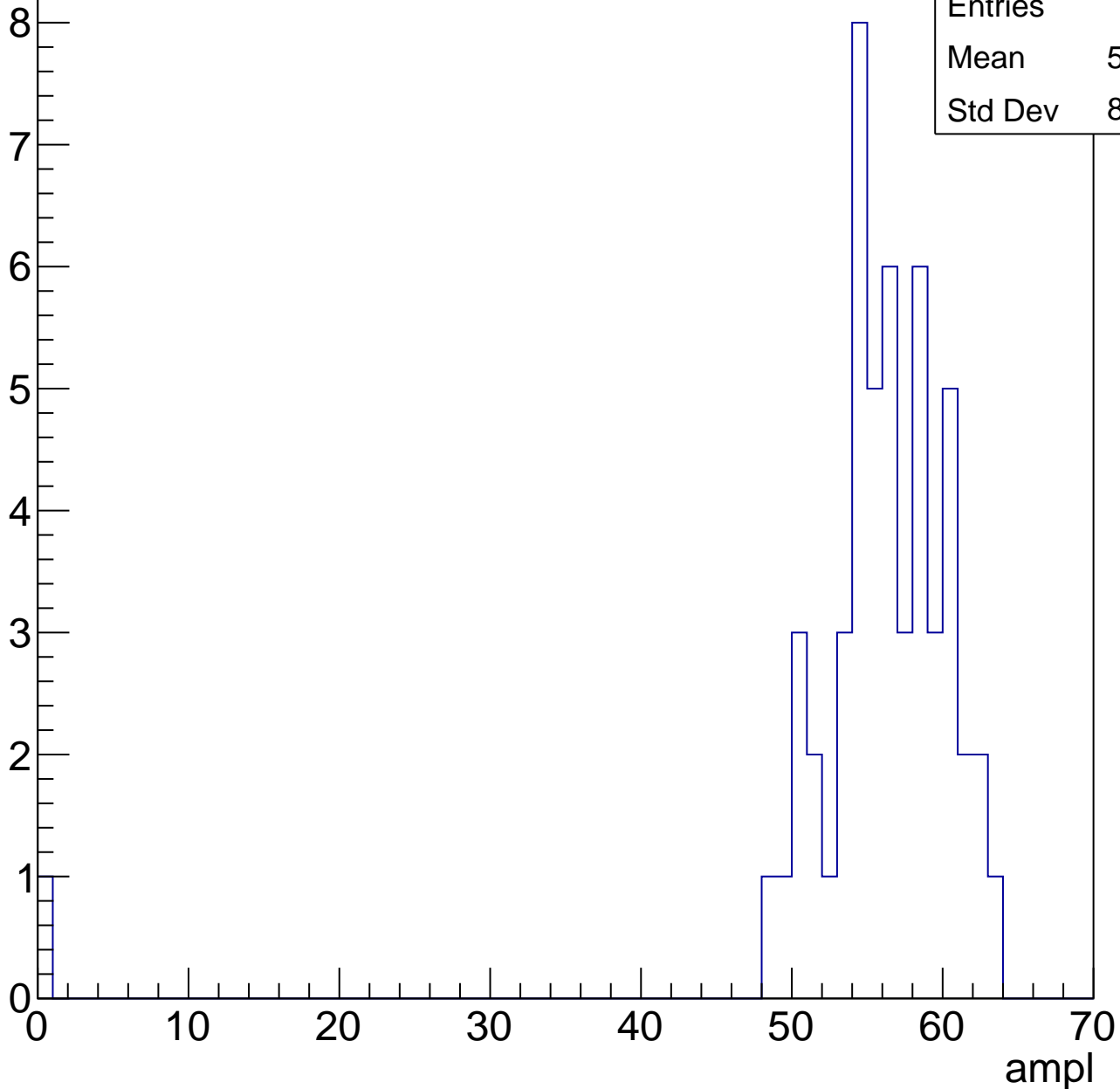


B1L101S, U15-ch56, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	54.87
Std Dev	8.382



B1L101S, U15-ch56, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

6

5

4

3

2

1

0

Entries	39
Mean	59.38
Std Dev	2.704

0

10

20

30

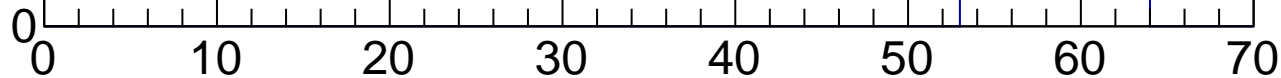
40

50

60

70

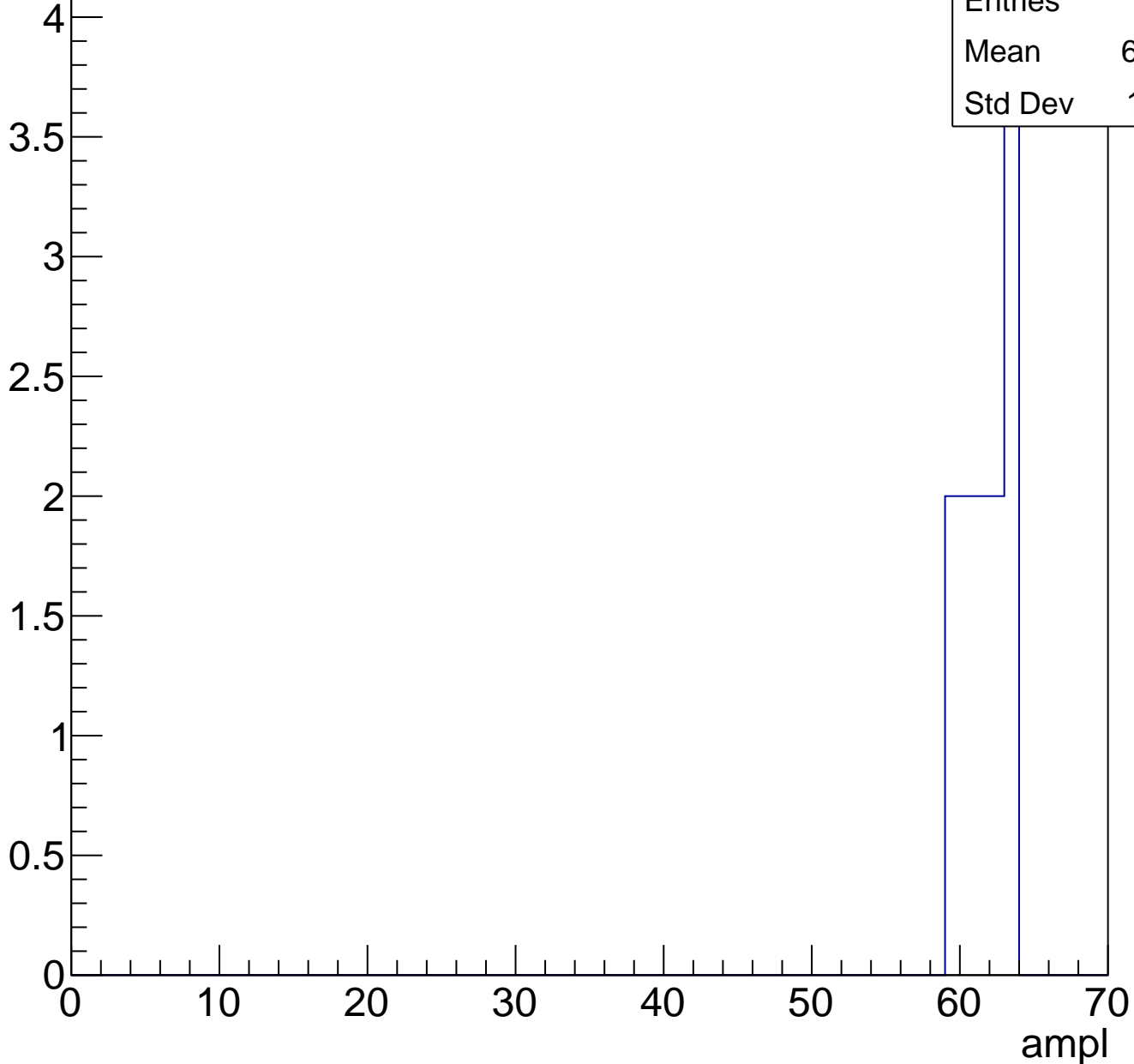
ampl



B1L101S, U15-ch56, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch56, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L101S, U15-ch57, adc0

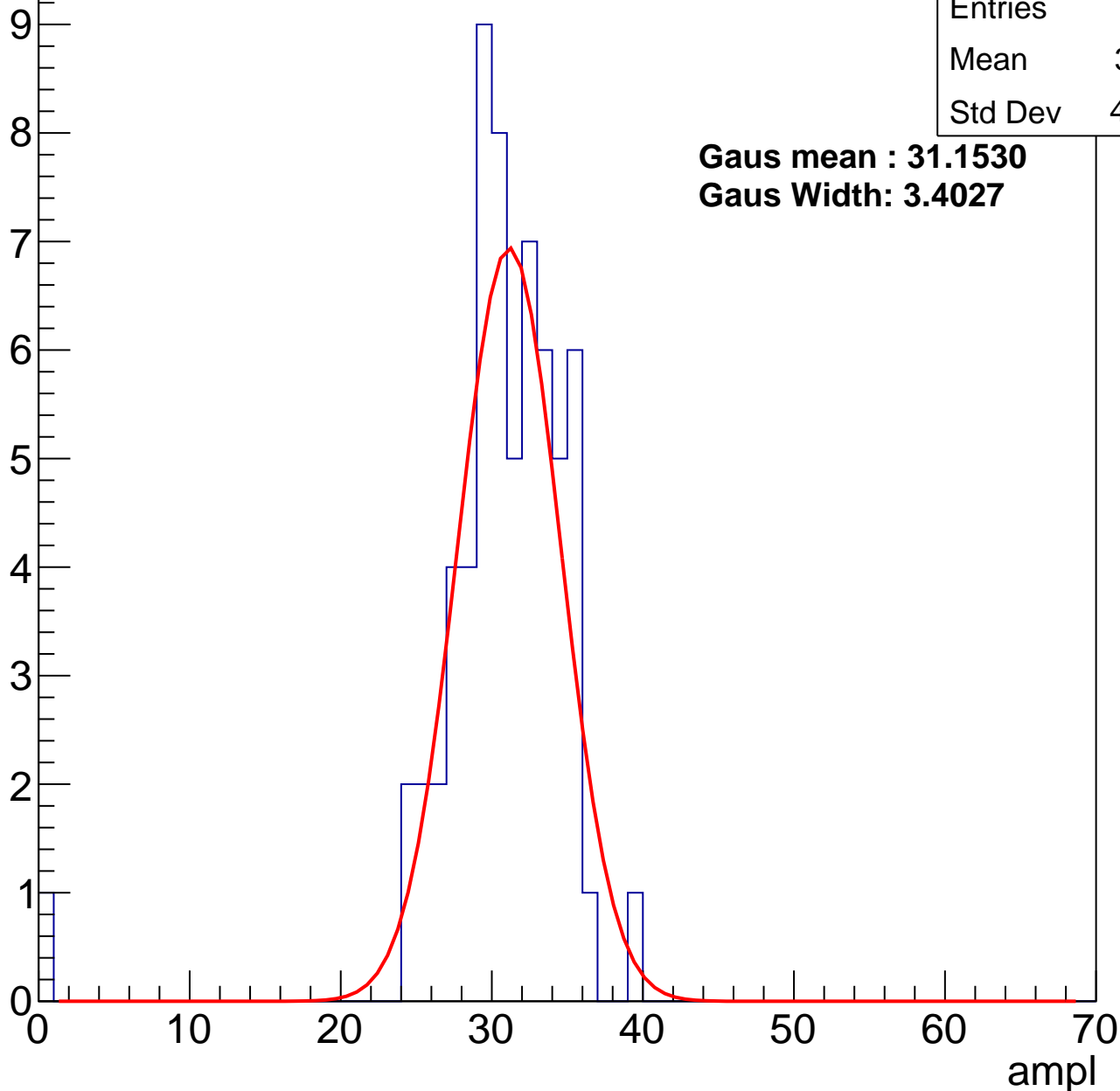
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	30.21
Std Dev	4.954

Gaus mean : 31.1530

Gaus Width: 3.4027

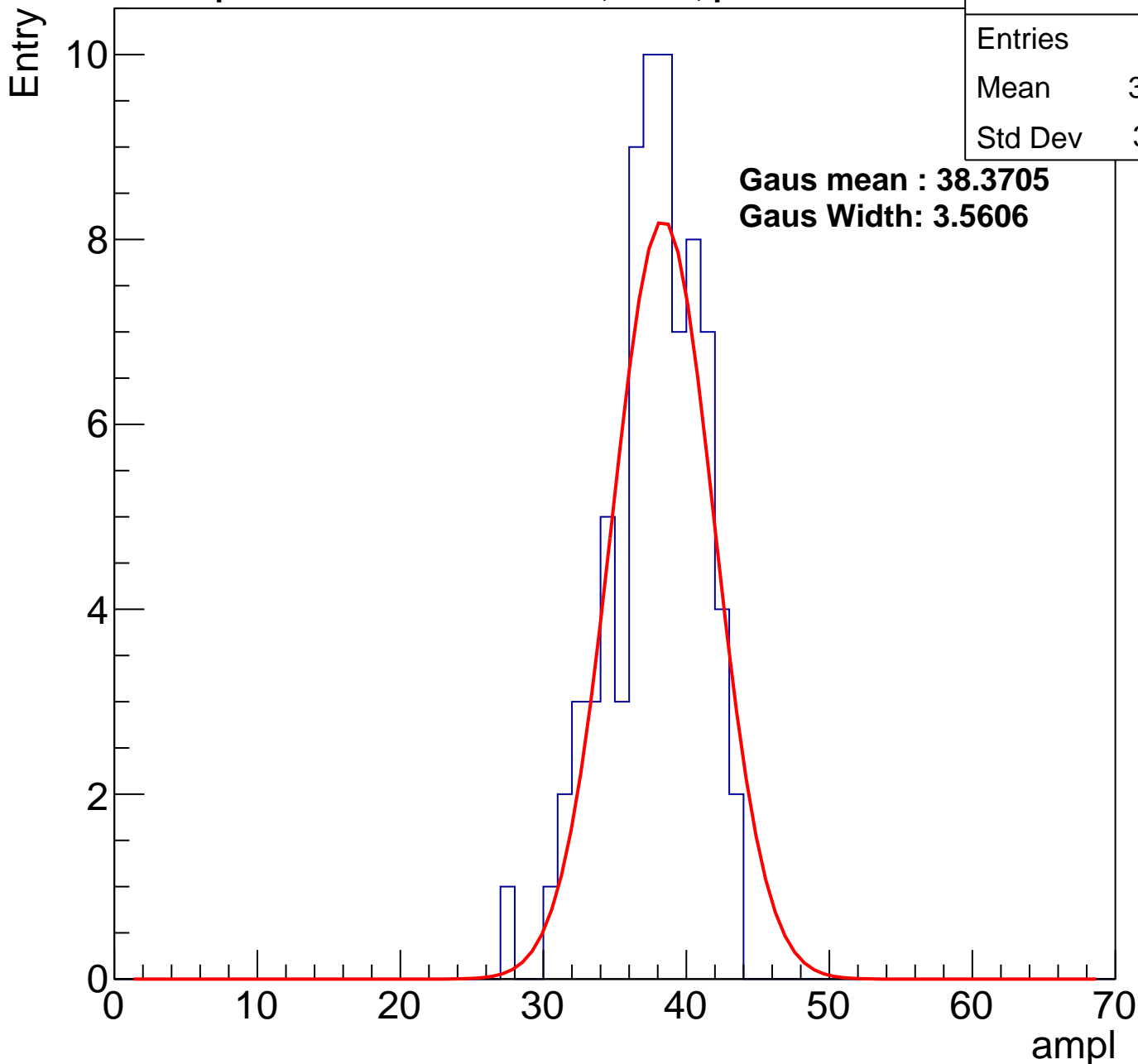


B1L101S, U15-ch57, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	75
Mean	37.29
Std Dev	3.261

Gaus mean : 38.3705
Gaus Width: 3.5606



B1L101S, U15-ch57, adc2

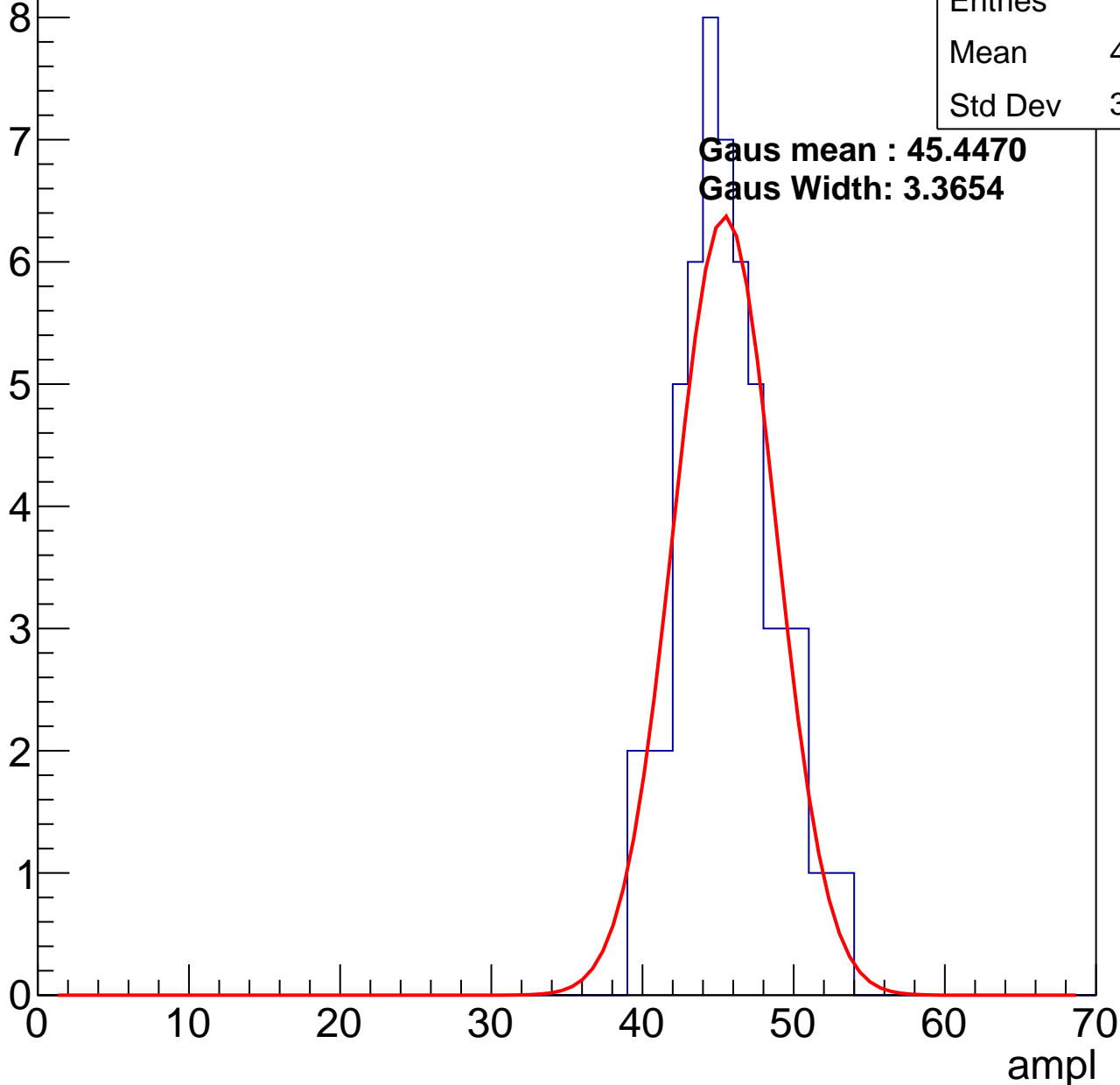
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	45.15
Std Dev	3.176

Gaus mean : 45.4470

Gaus Width: 3.3654

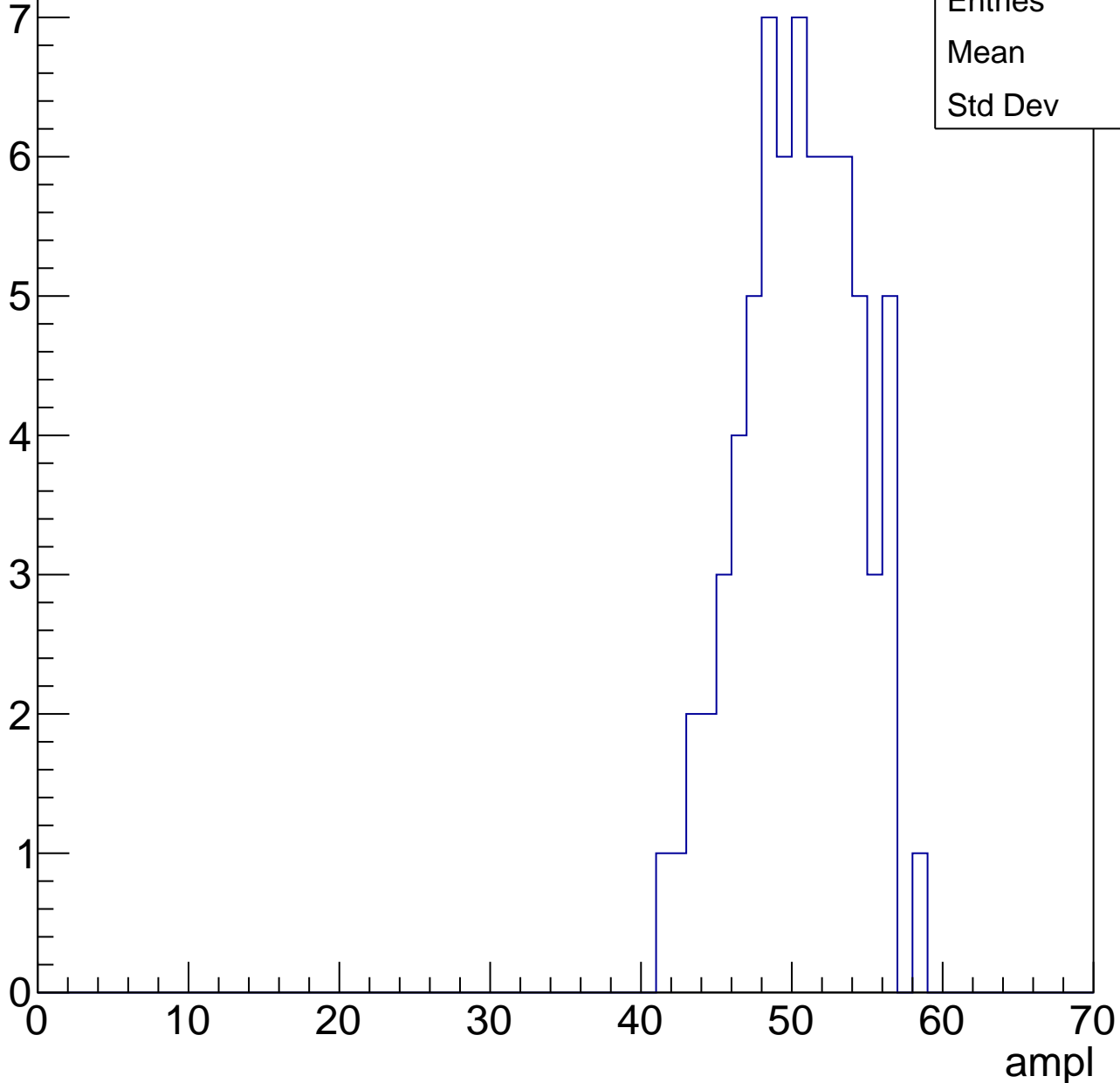


B1L101S, U15-ch57, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

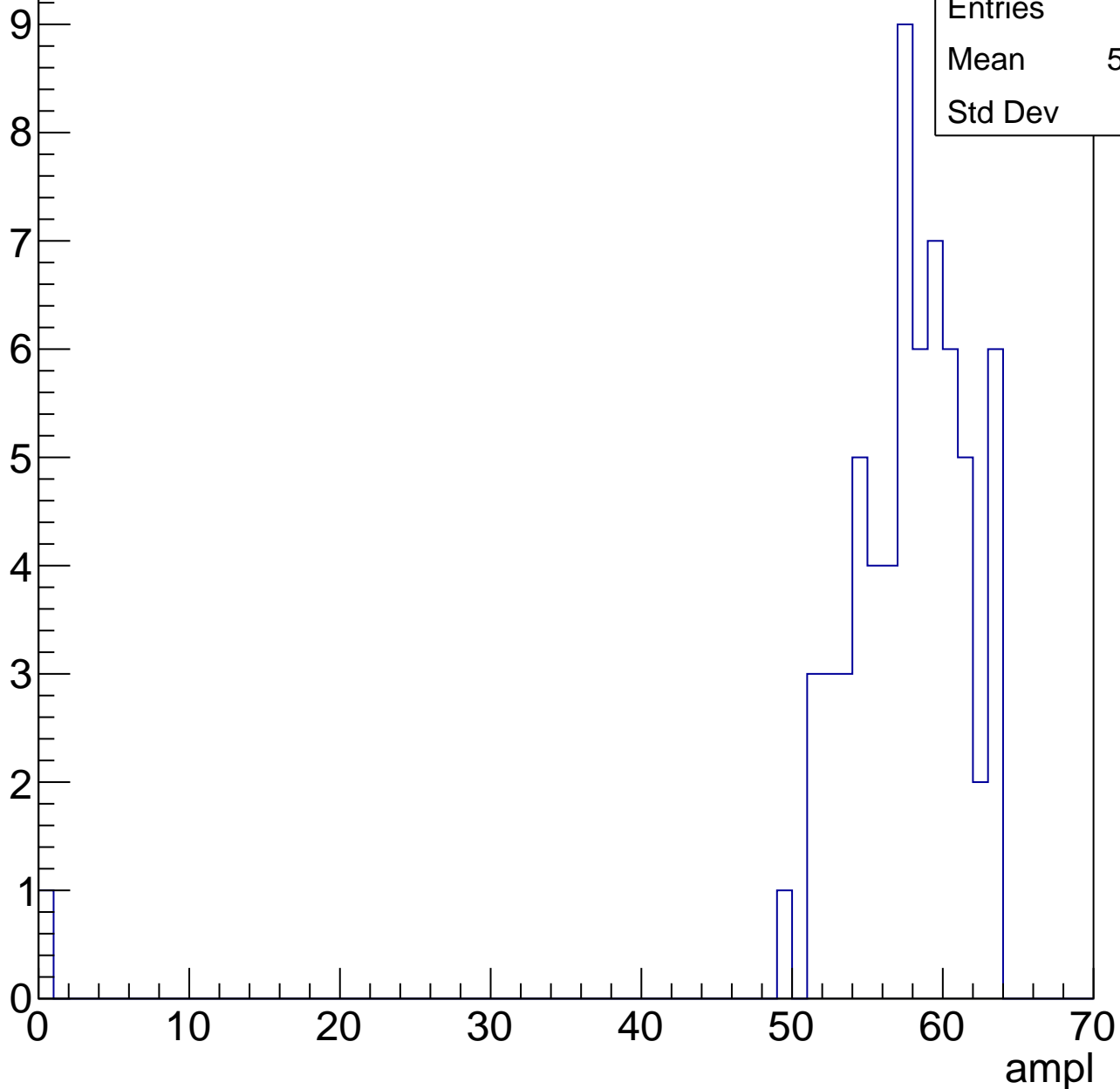
Entries	70
Mean	50
Std Dev	3.81



B1L101S, U15-ch57, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

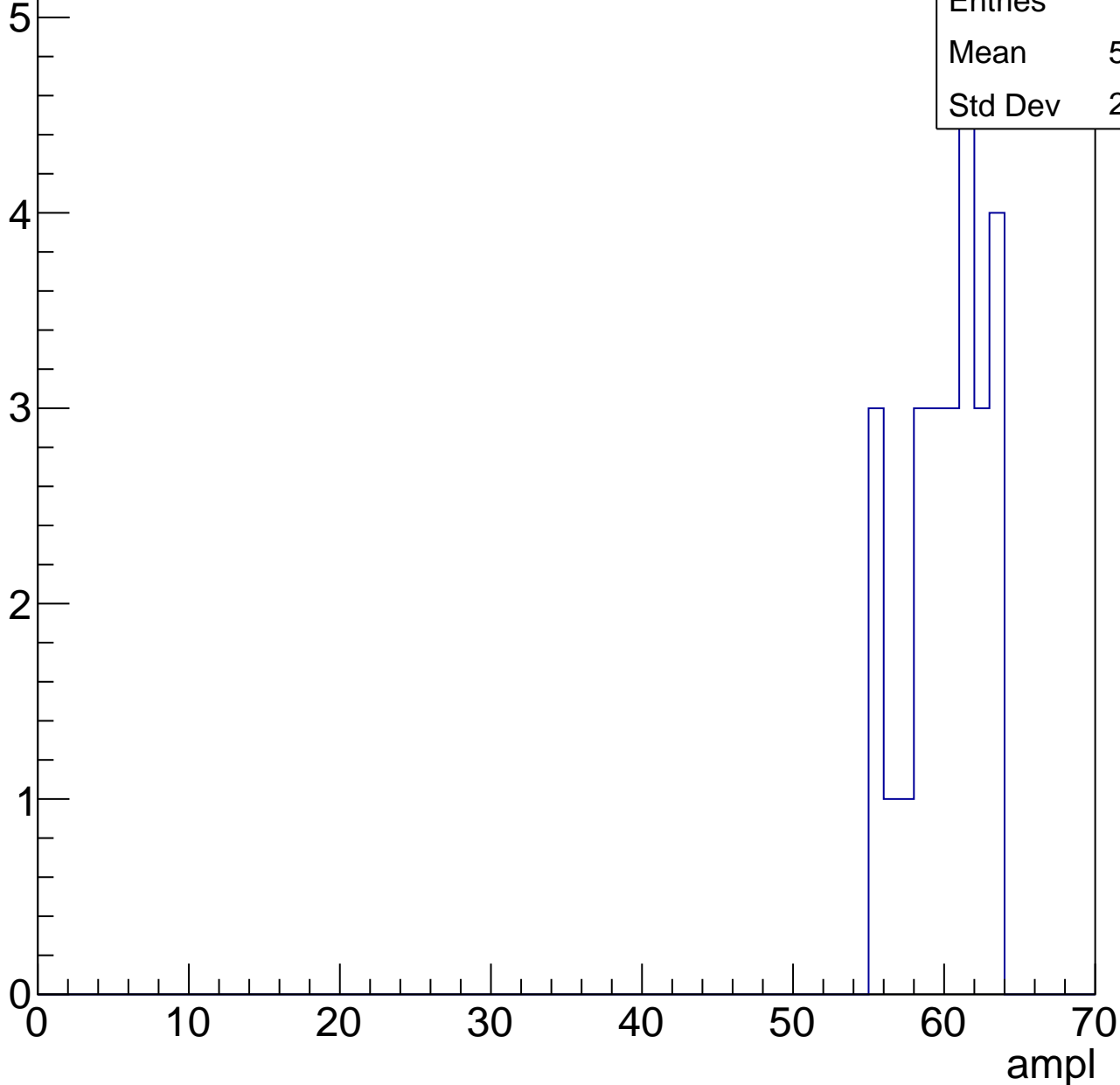


B1L101S, U15-ch57, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	26
Mean	59.69
Std Dev	2.523

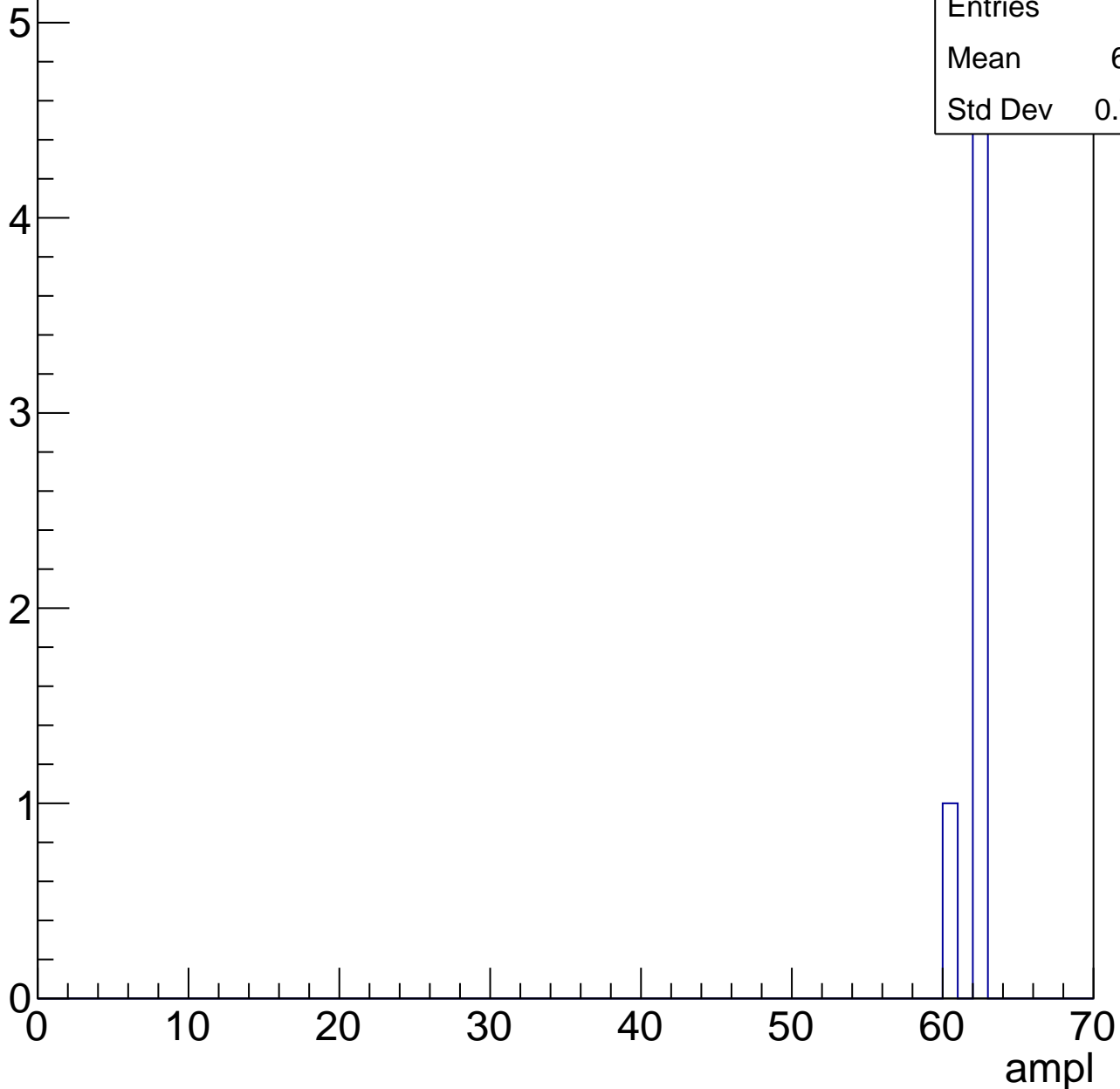


B1L101S, U15-ch57, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	6
Mean	61.67
Std Dev	0.7454



B1L101S, U15-ch57, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch58, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	80
Mean	30.14
Std Dev	6.095

Gaus mean : 31.8992

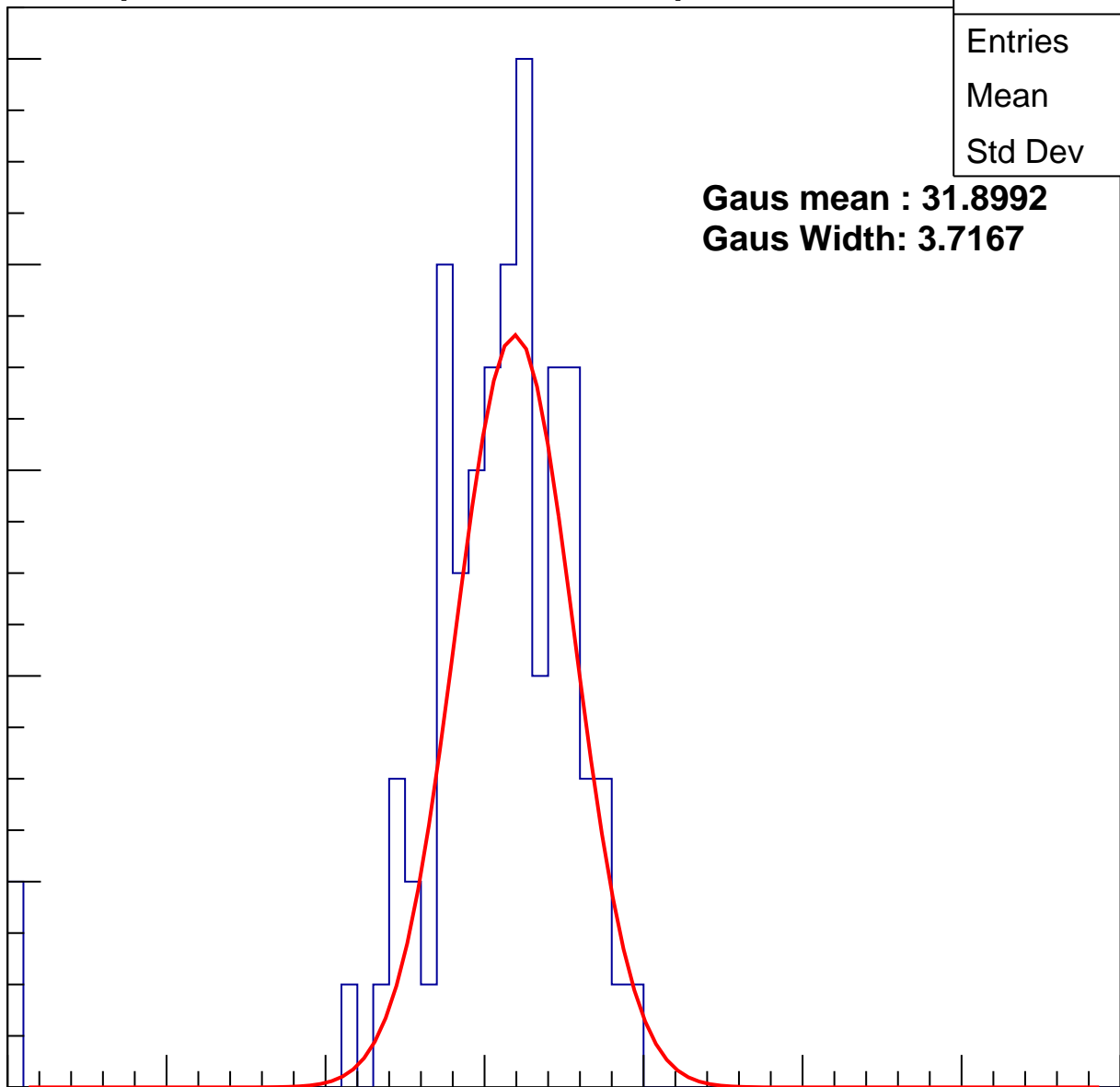
Gaus Width: 3.7167

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch58, adc1

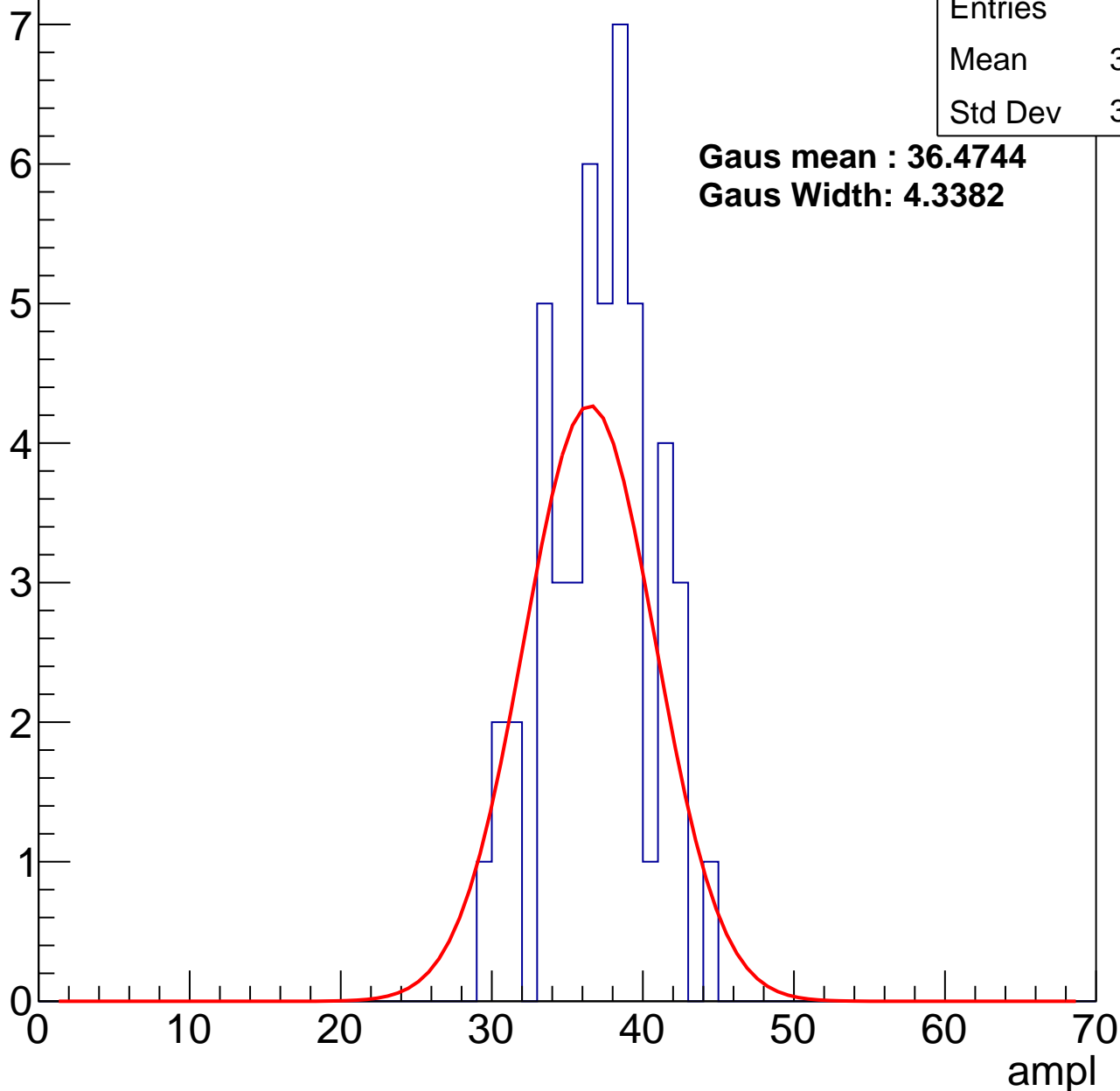
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	36.65
Std Dev	3.467

Gaus mean : 36.4744

Gaus Width: 4.3382



B1L101S, U15-ch58, adc2

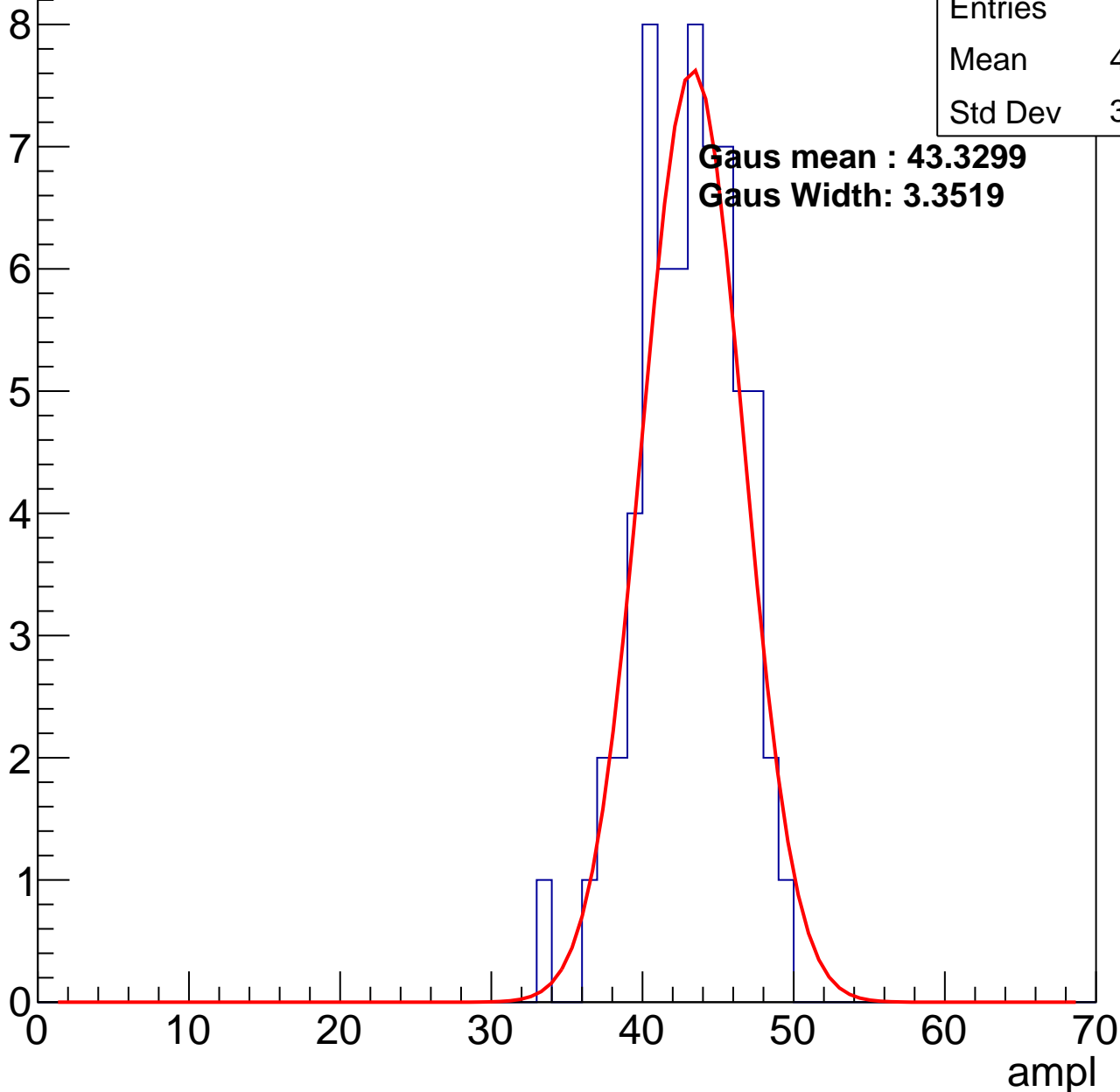
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.62
Std Dev	3.219

Gaus mean : 43.3299

Gaus Width: 3.3519



B1L101S, U15-ch58, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	50.01
Std Dev	3.773

Entry

10

8

6

4

2

0

0

10

20

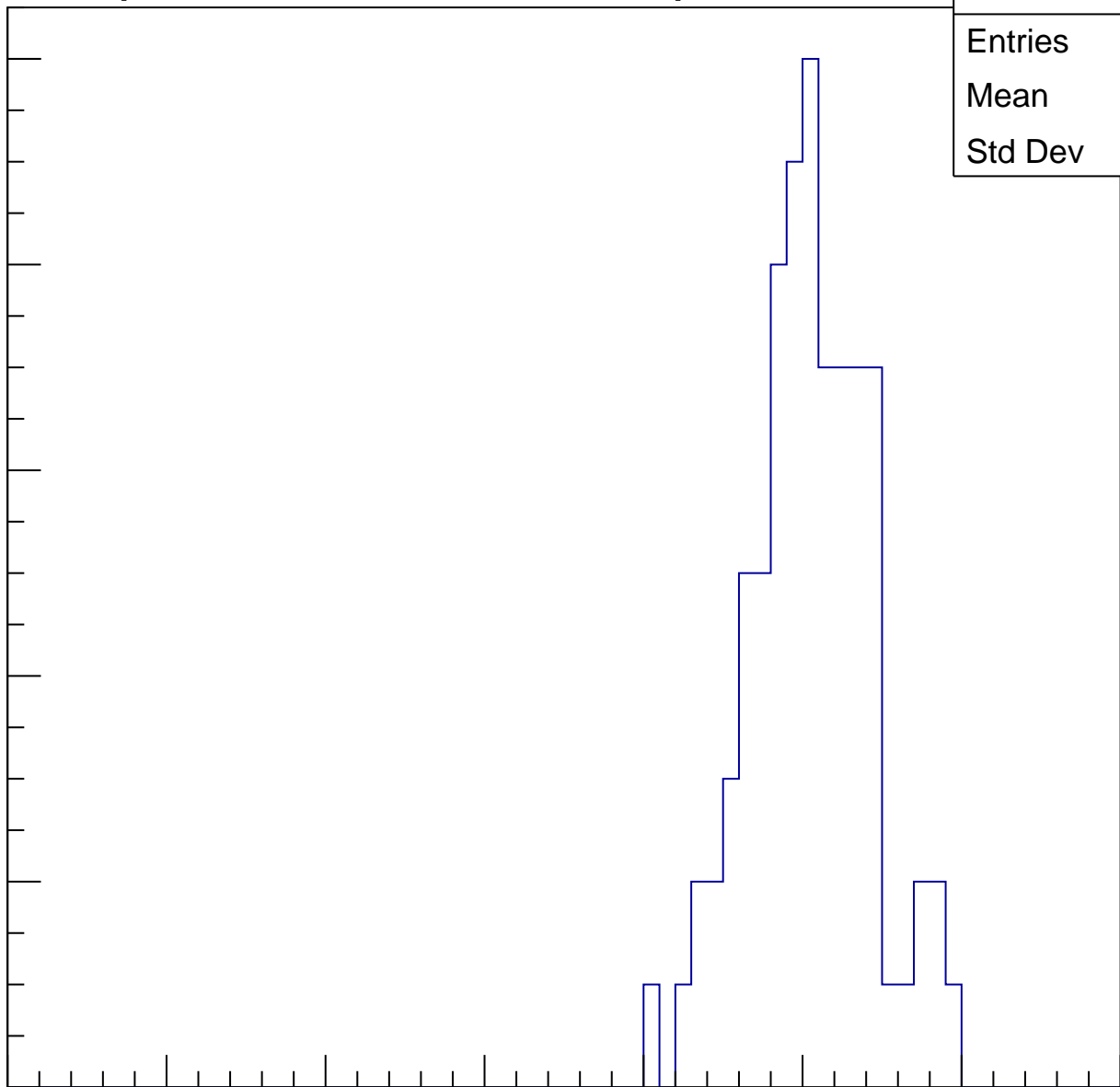
30

40

50

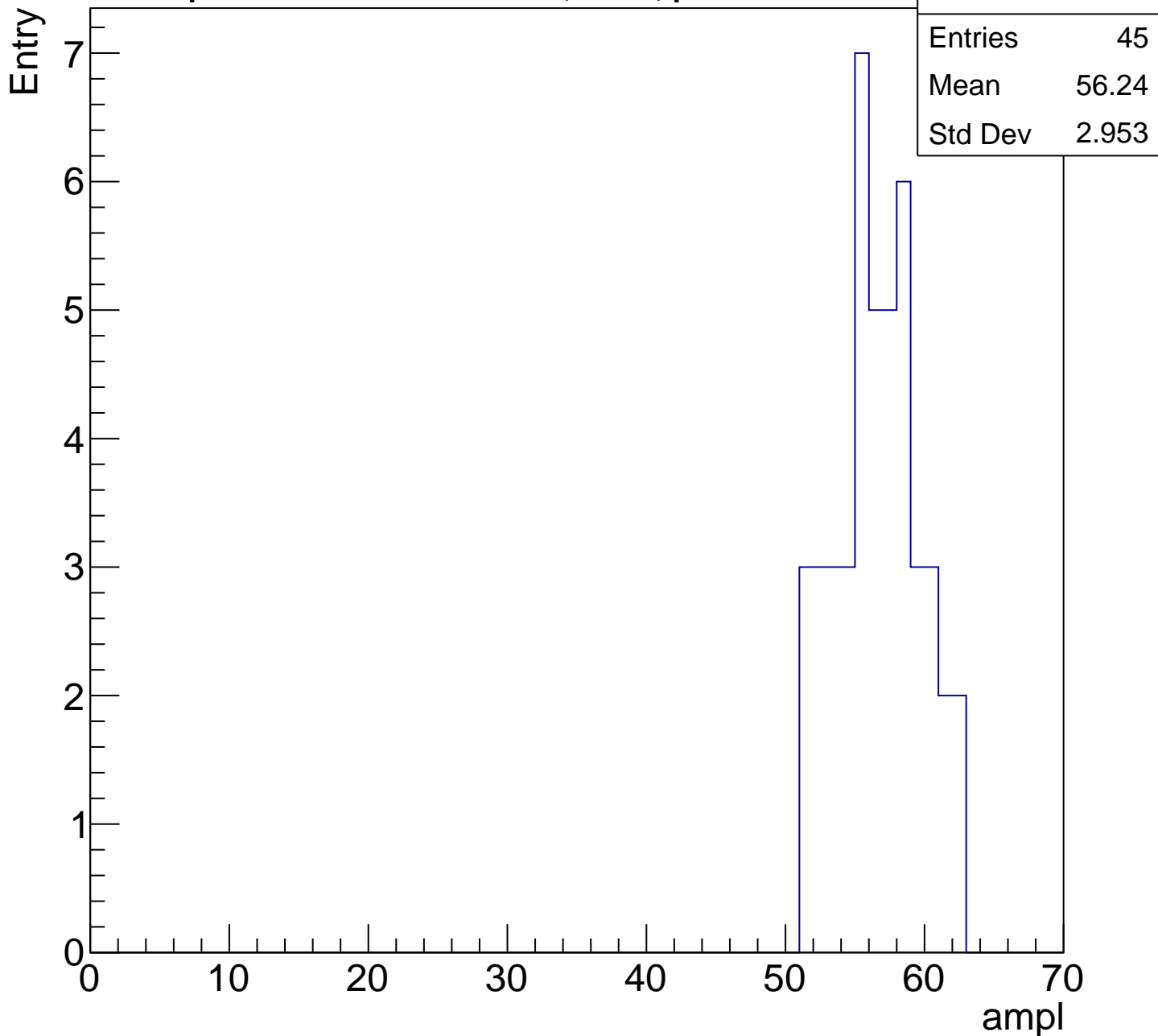
60

ampl



B1L101S, U15-ch58, adc4

calib_packv5_042523_0143.root, FC#0, port D2

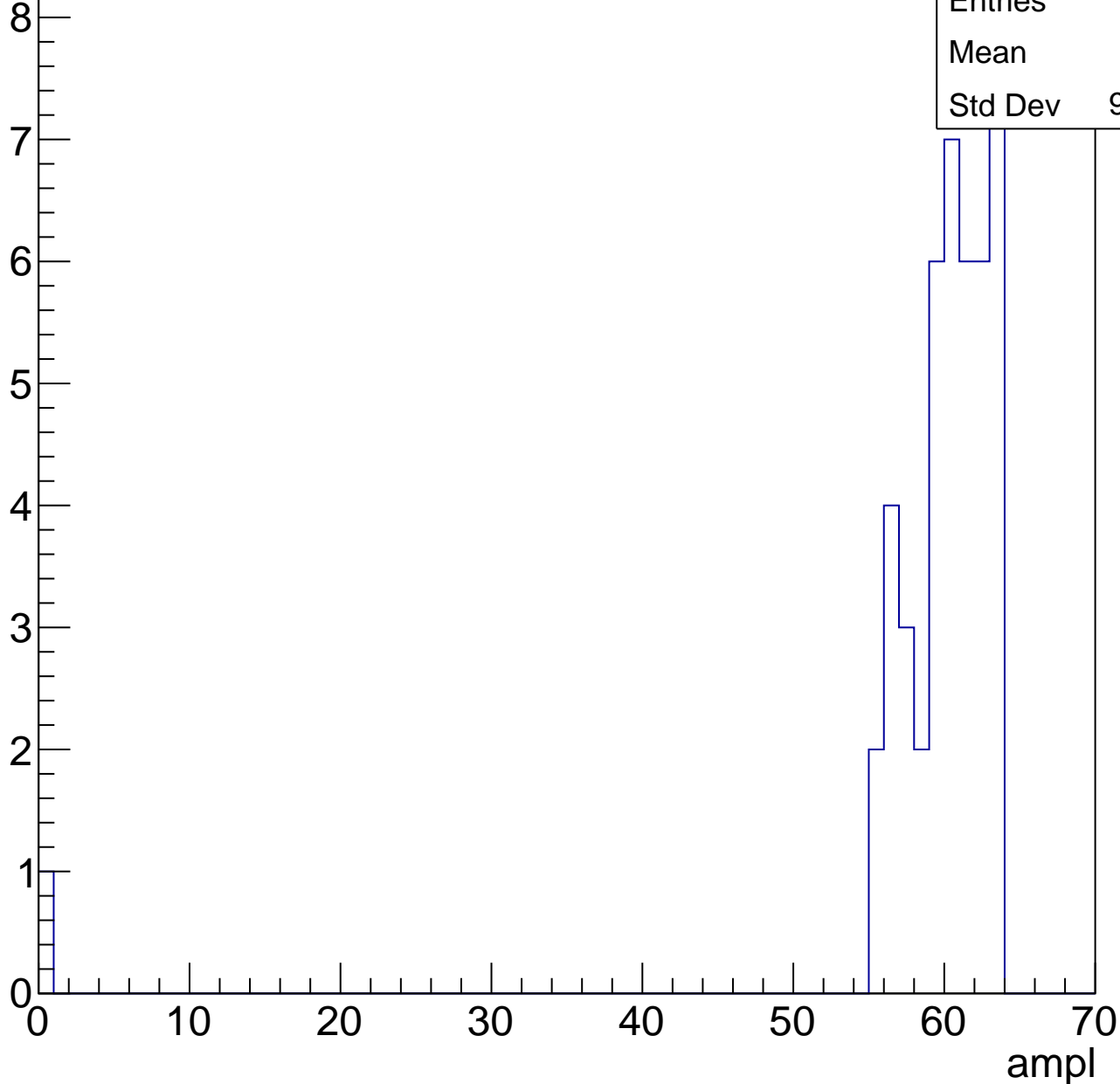


B1L101S, U15-ch58, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	58.6
Std Dev	9.152



B1L101S, U15-ch58, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0 10 20 30 40 50 60 70

ampl

Entries	6
Mean	62
Std Dev	0.8165

B1L101S, U15-ch58, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch59, adc0

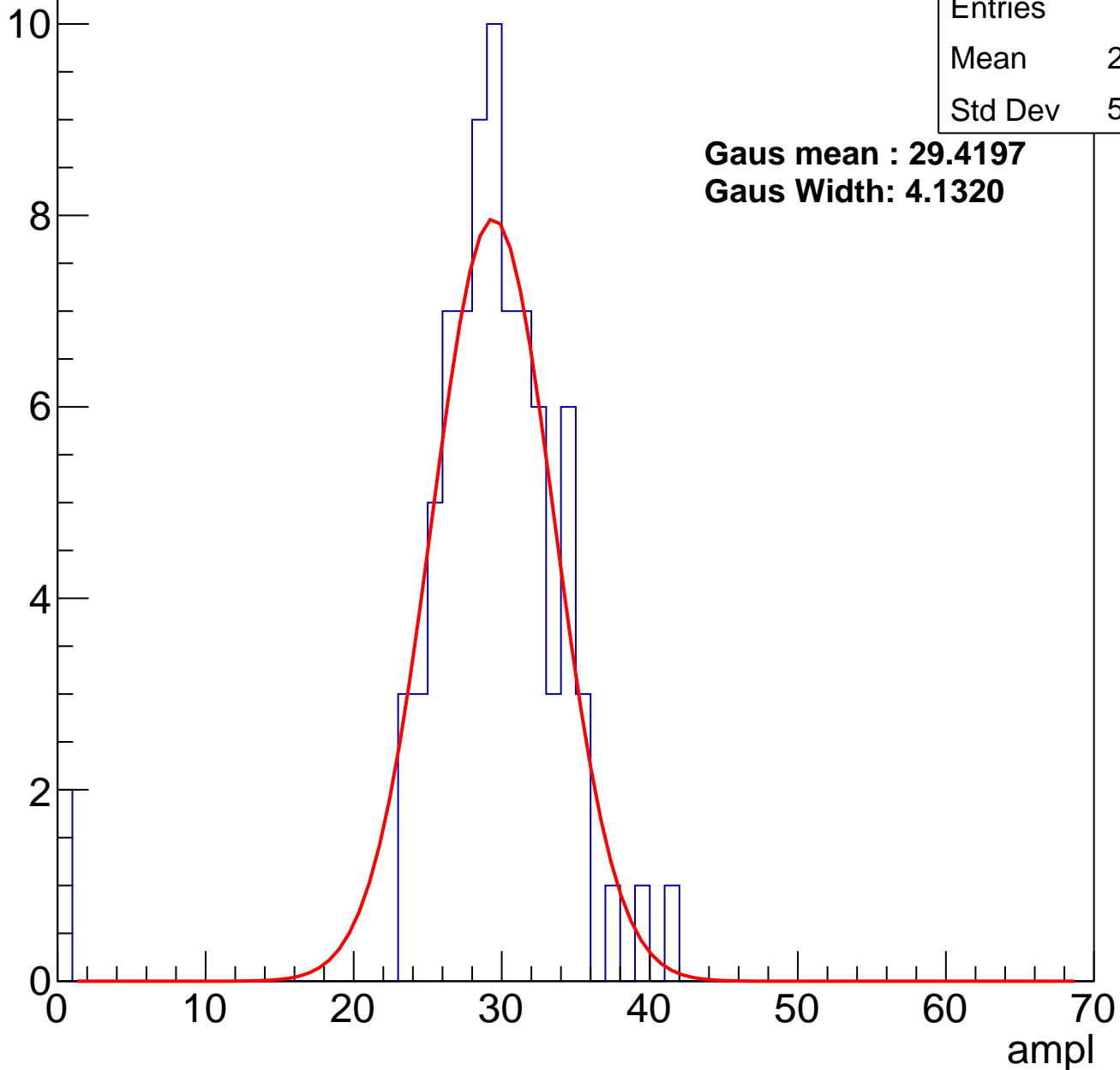
calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	28.68
Std Dev	5.816

Gaus mean : 29.4197

Gaus Width: 4.1320

Entry



B1L101S, U15-ch59, adc1

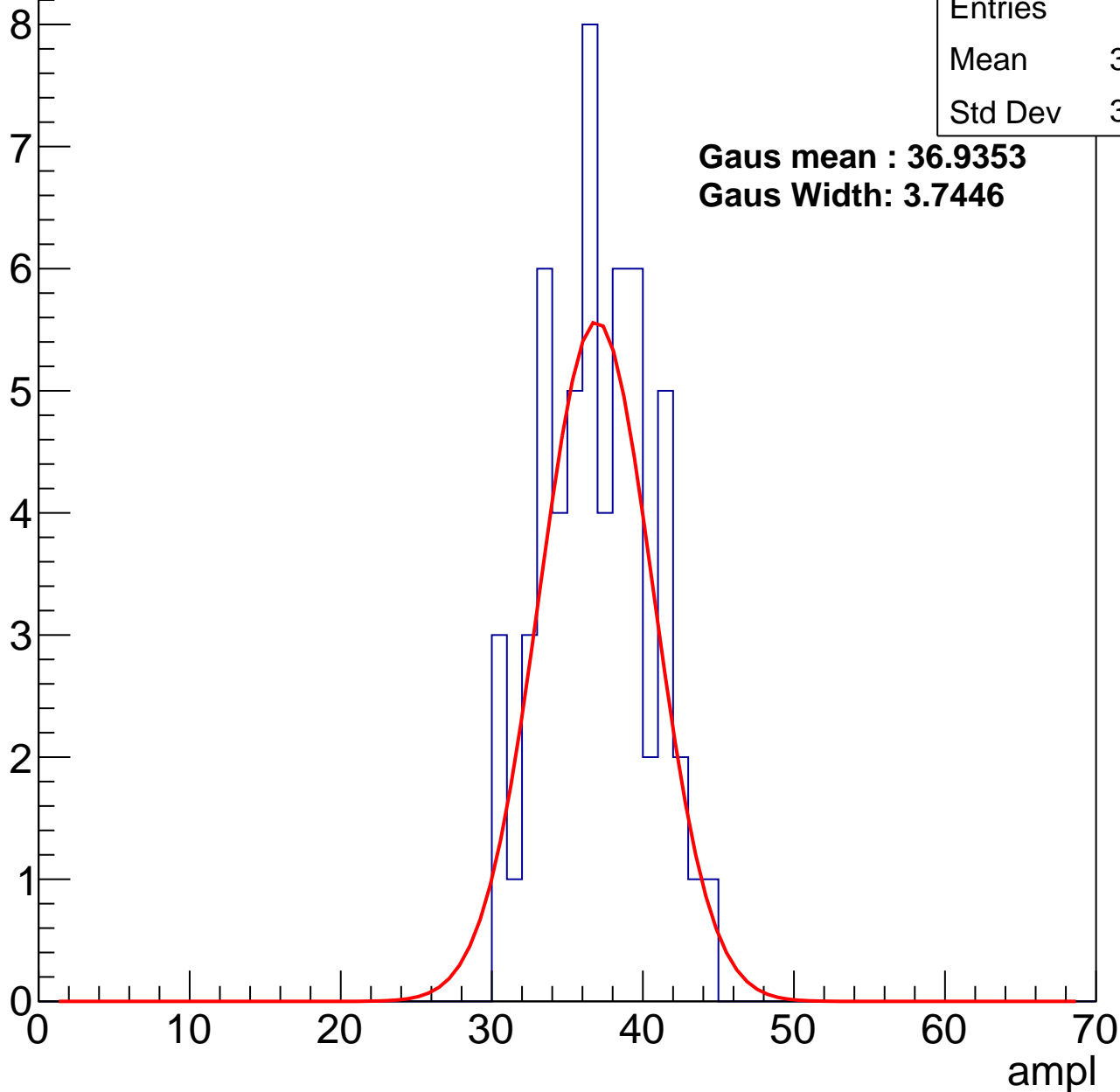
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.49
Std Dev	3.419

Gaus mean : 36.9353

Gaus Width: 3.7446



B1L101S, U15-ch59, adc2

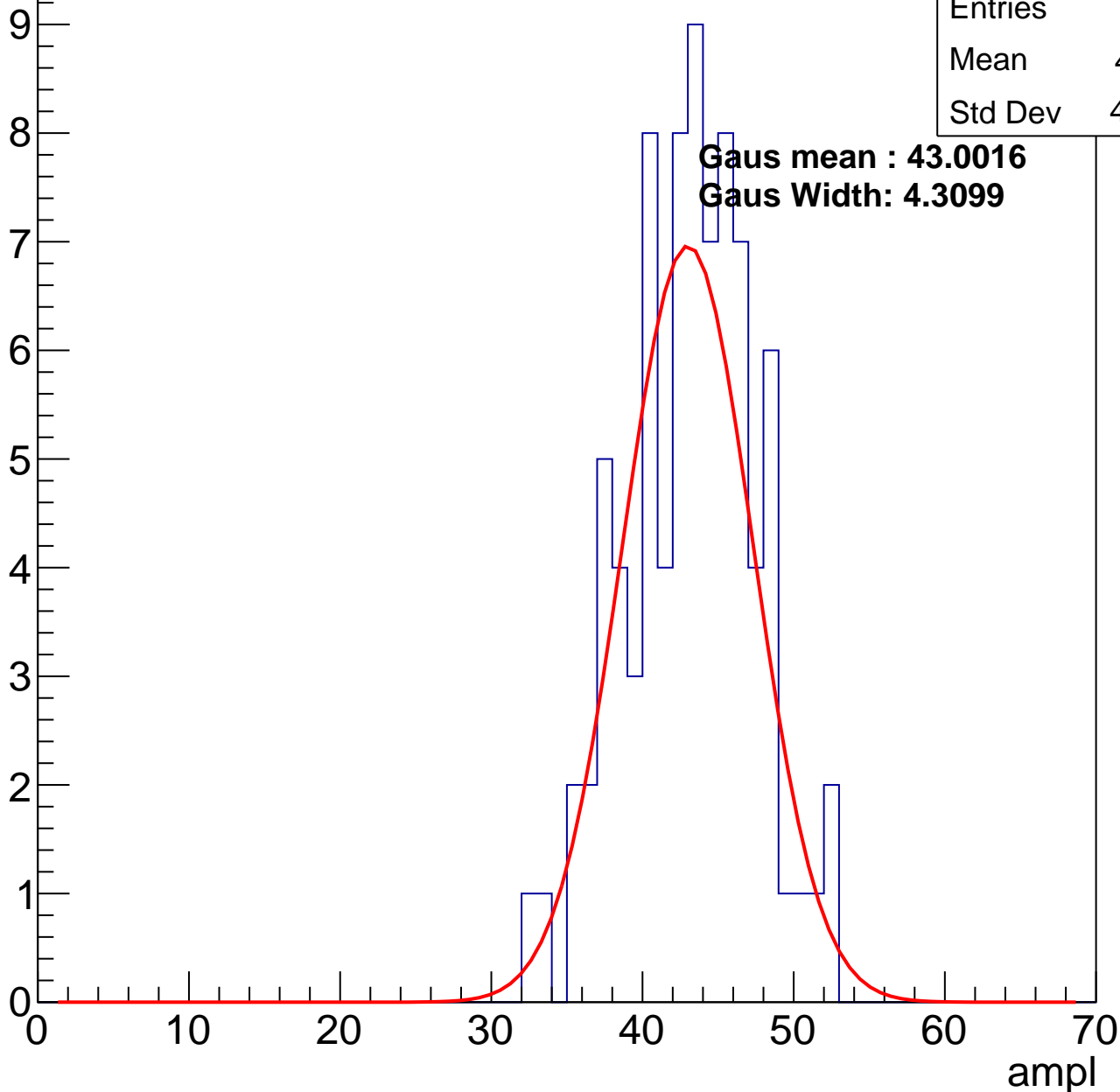
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	42.71
Std Dev	4.213

Gaus mean : 43.0016

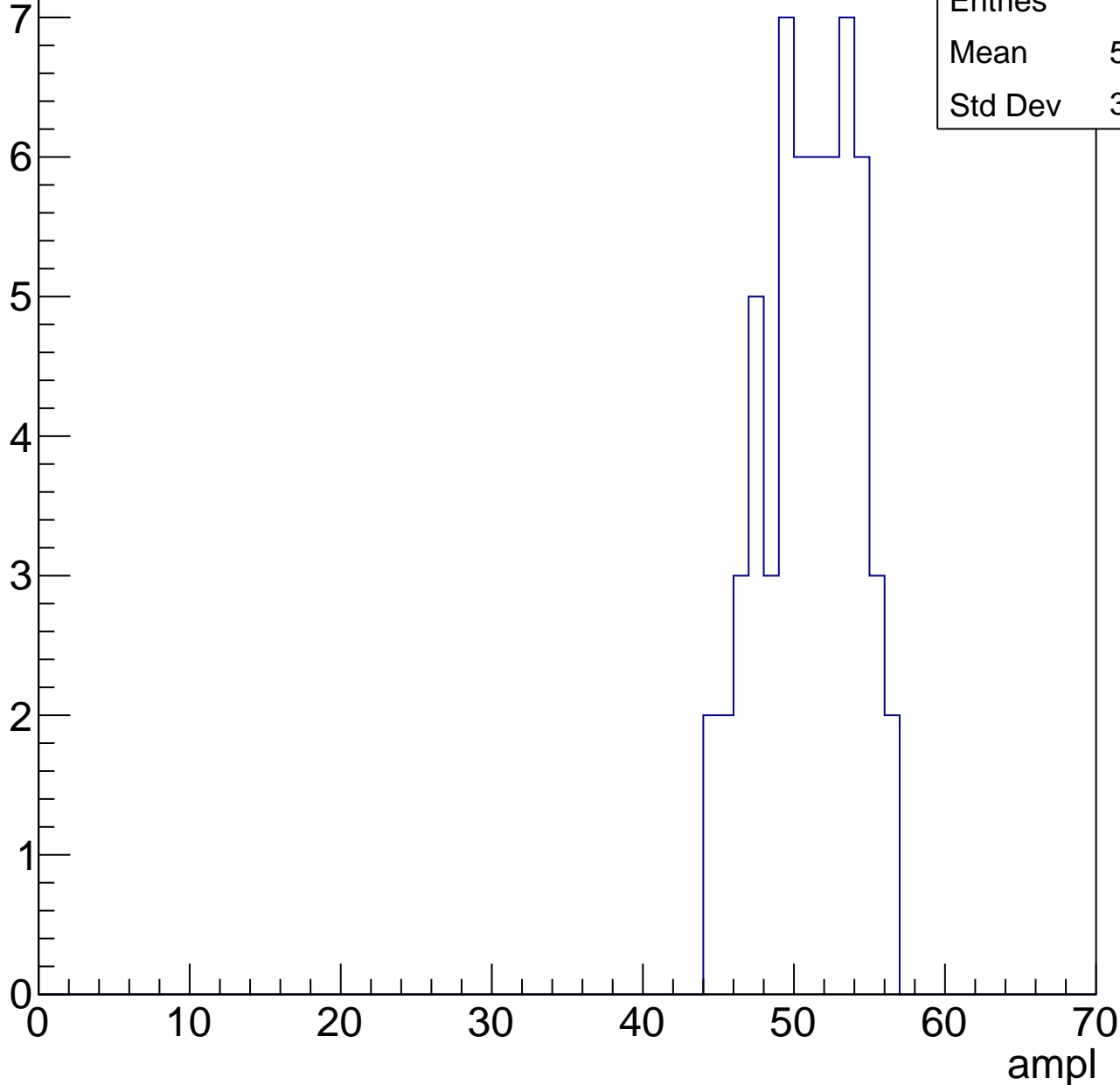
Gaus Width: 4.3099



B1L101S, U15-ch59, adc3

calib_packv5_042523_0143.root, FC#0, port D2

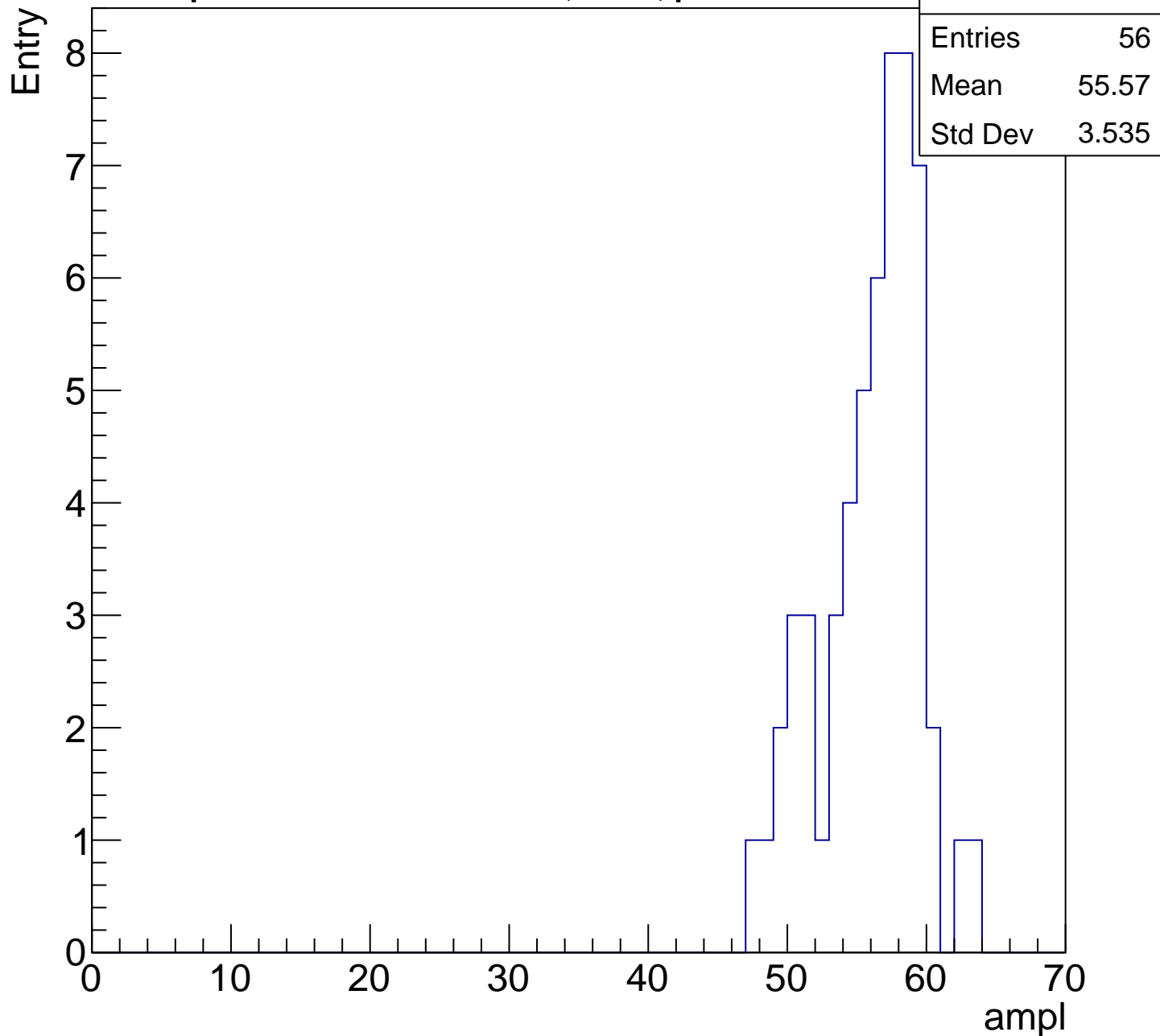
Entry



Entries	58
Mean	50.48
Std Dev	3.098

B1L101S, U15-ch59, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch59, adc5

calib_packv5_042523_0143.root, FC#0, port D2

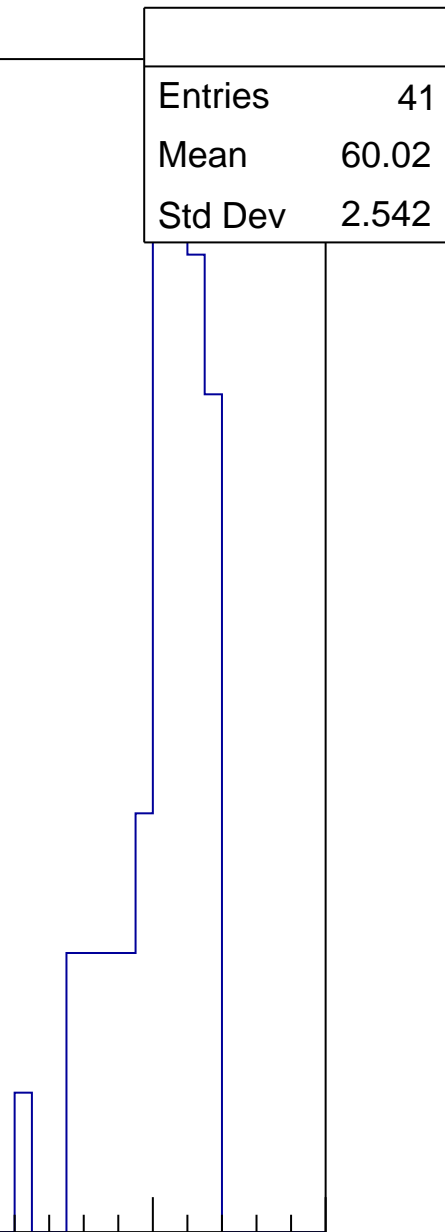
Entry

8
7
6
5
4
3
2
1
0

Entries	41
Mean	60.02
Std Dev	2.542

ampl

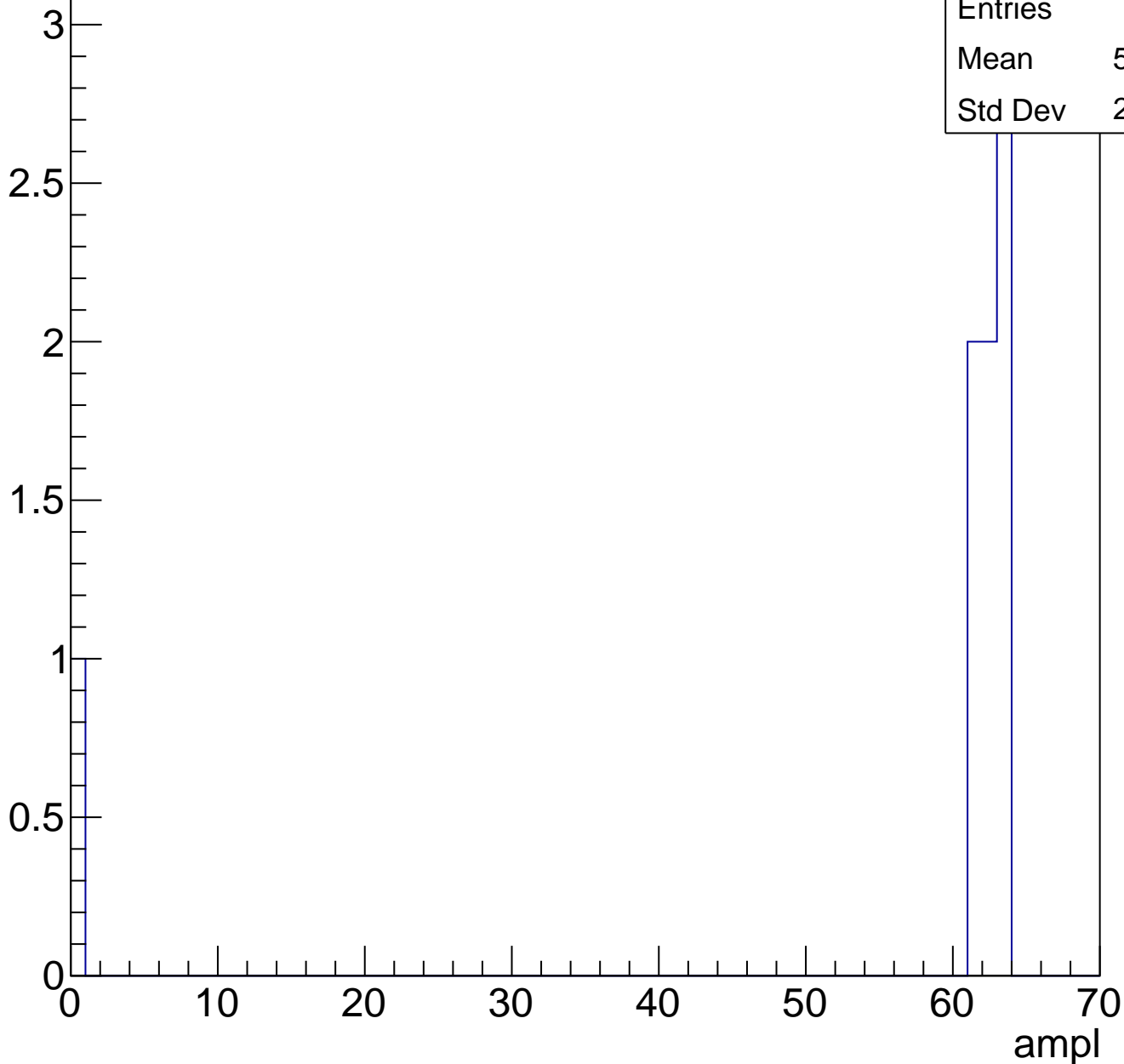
0 10 20 30 40 50 60 70



B1L101S, U15-ch59, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch59, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch60, adc0

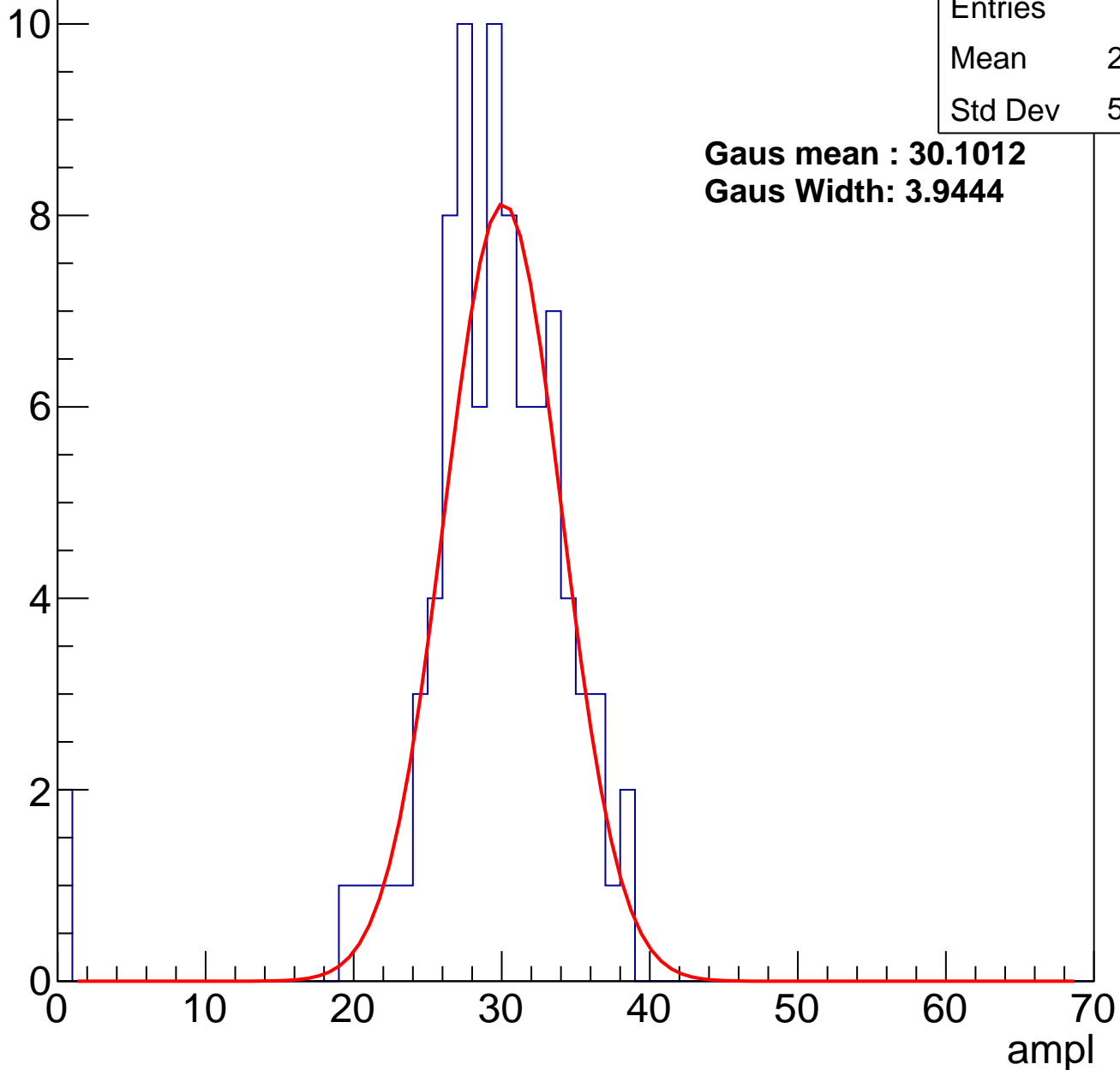
calib_packv5_042523_0143.root, FC#0, port D2

Entries	88
Mean	28.68
Std Dev	5.877

Gaus mean : 30.1012

Gaus Width: 3.9444

Entry



B1L101S, U15-ch60, adc1

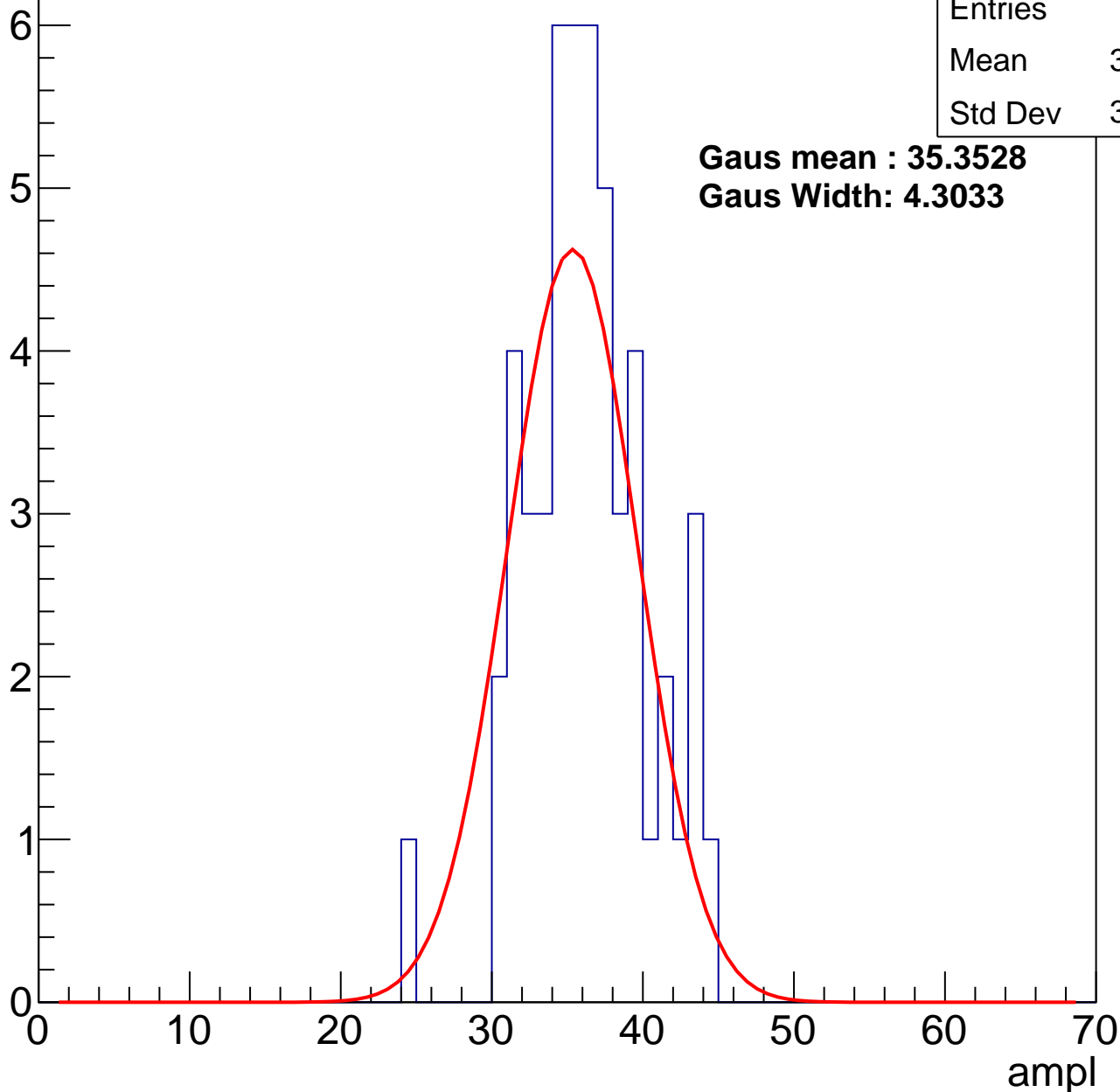
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	35.78
Std Dev	3.922

Gaus mean : 35.3528

Gaus Width: 4.3033



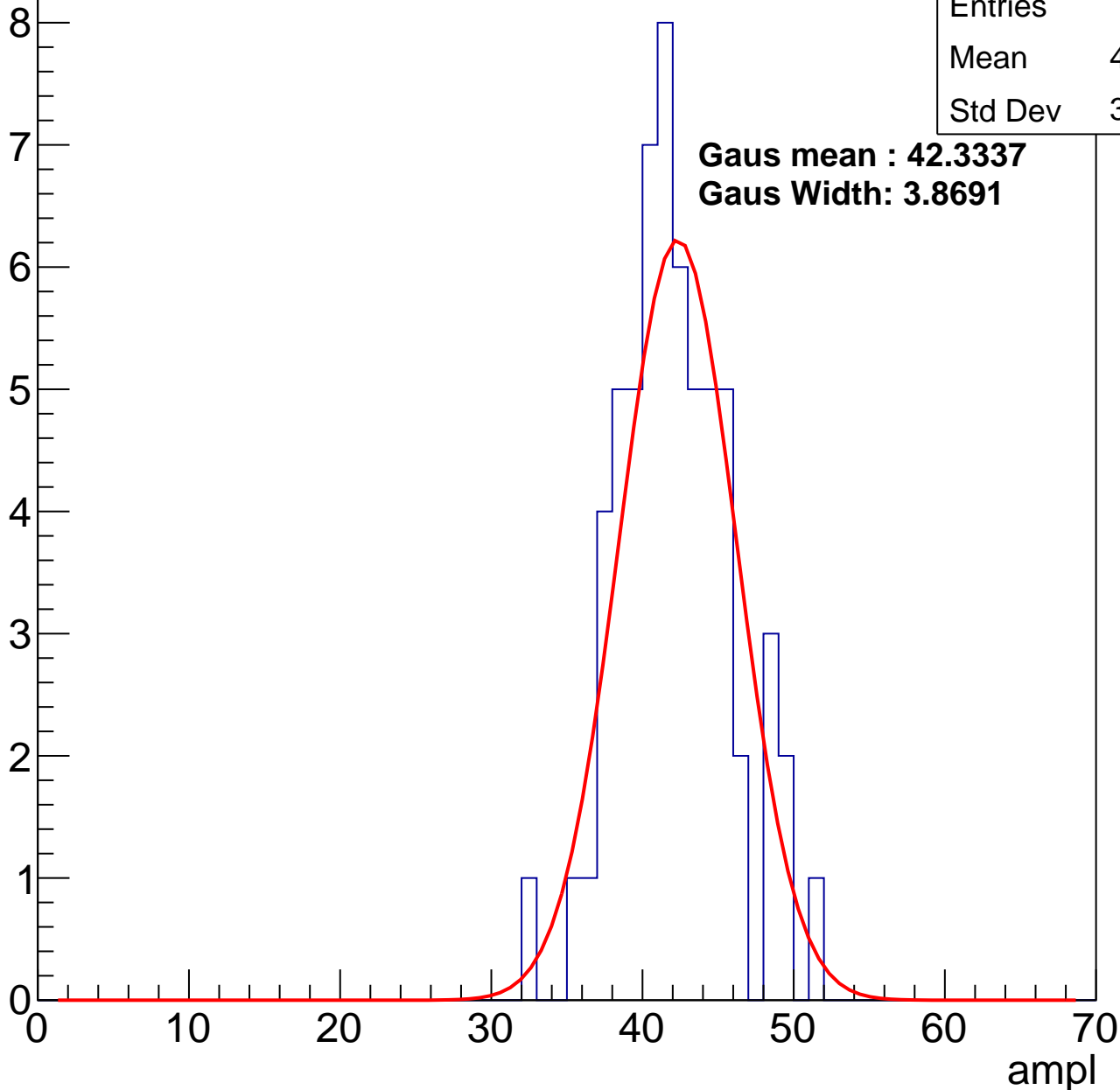
B1L101S, U15-ch60, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.66
Std Dev	3.684

Gaus mean : 42.3337
Gaus Width: 3.8691

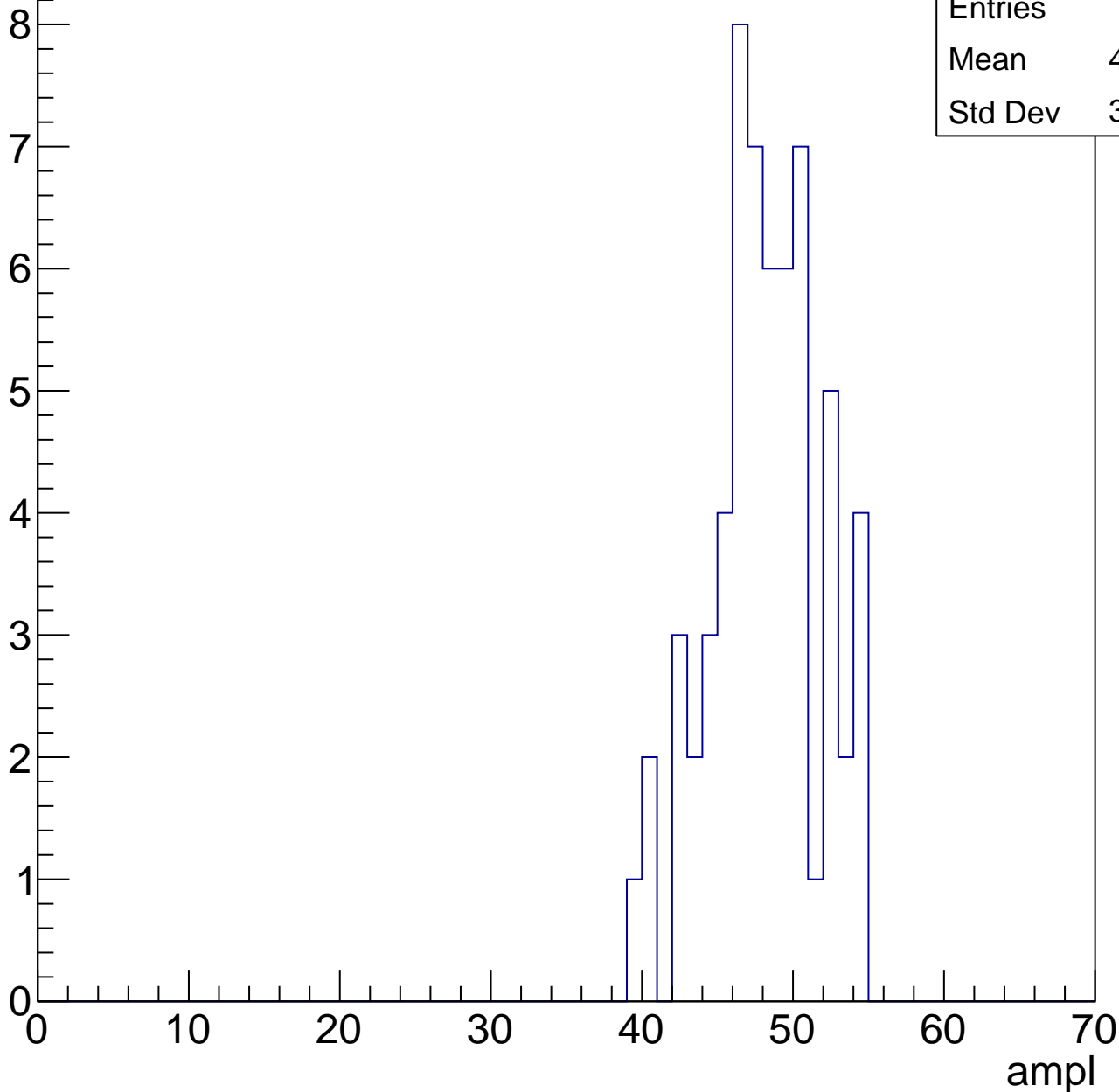


B1L101S, U15-ch60, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

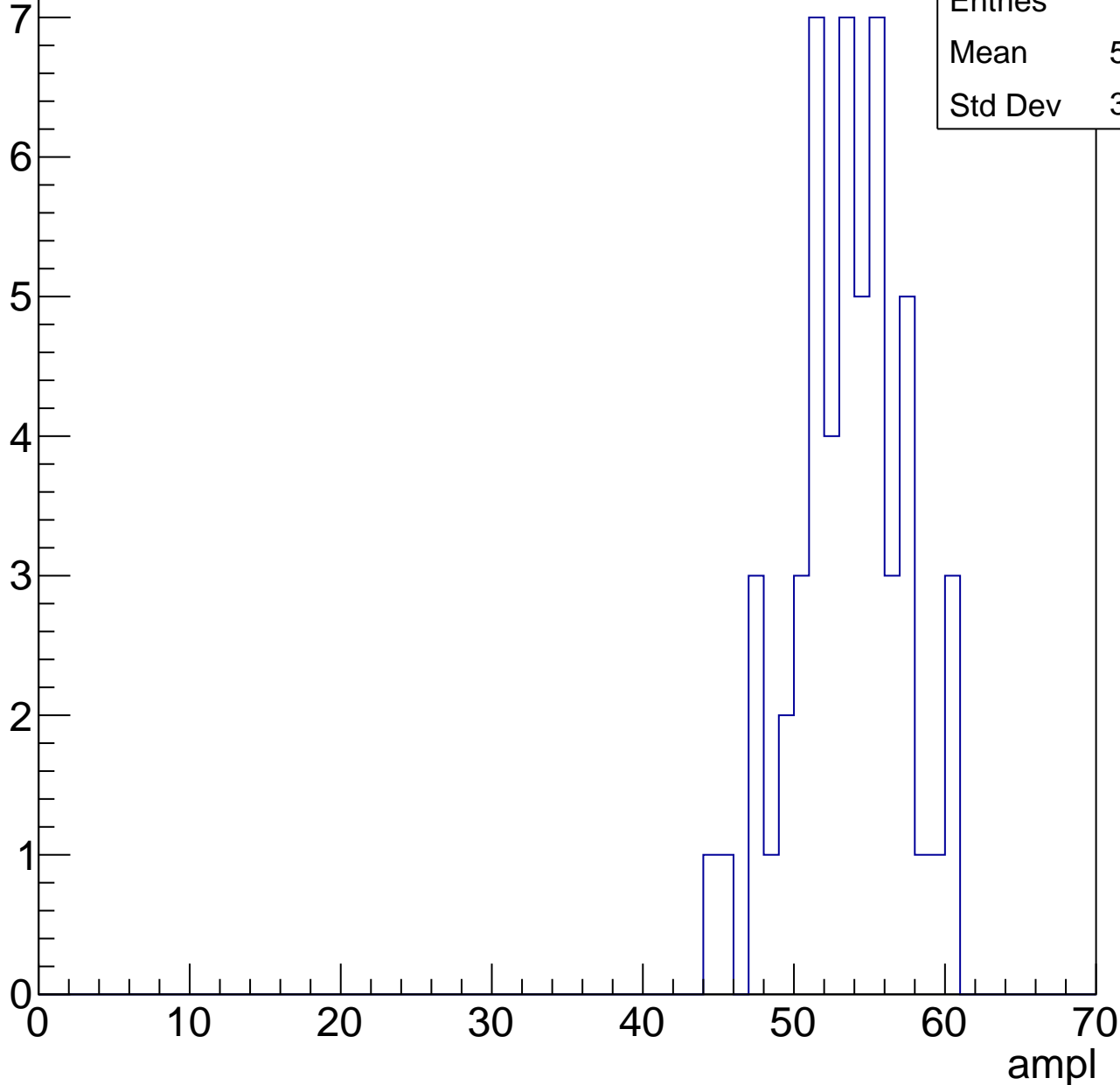
Entries	61
Mean	47.62
Std Dev	3.618



B1L101S, U15-ch60, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

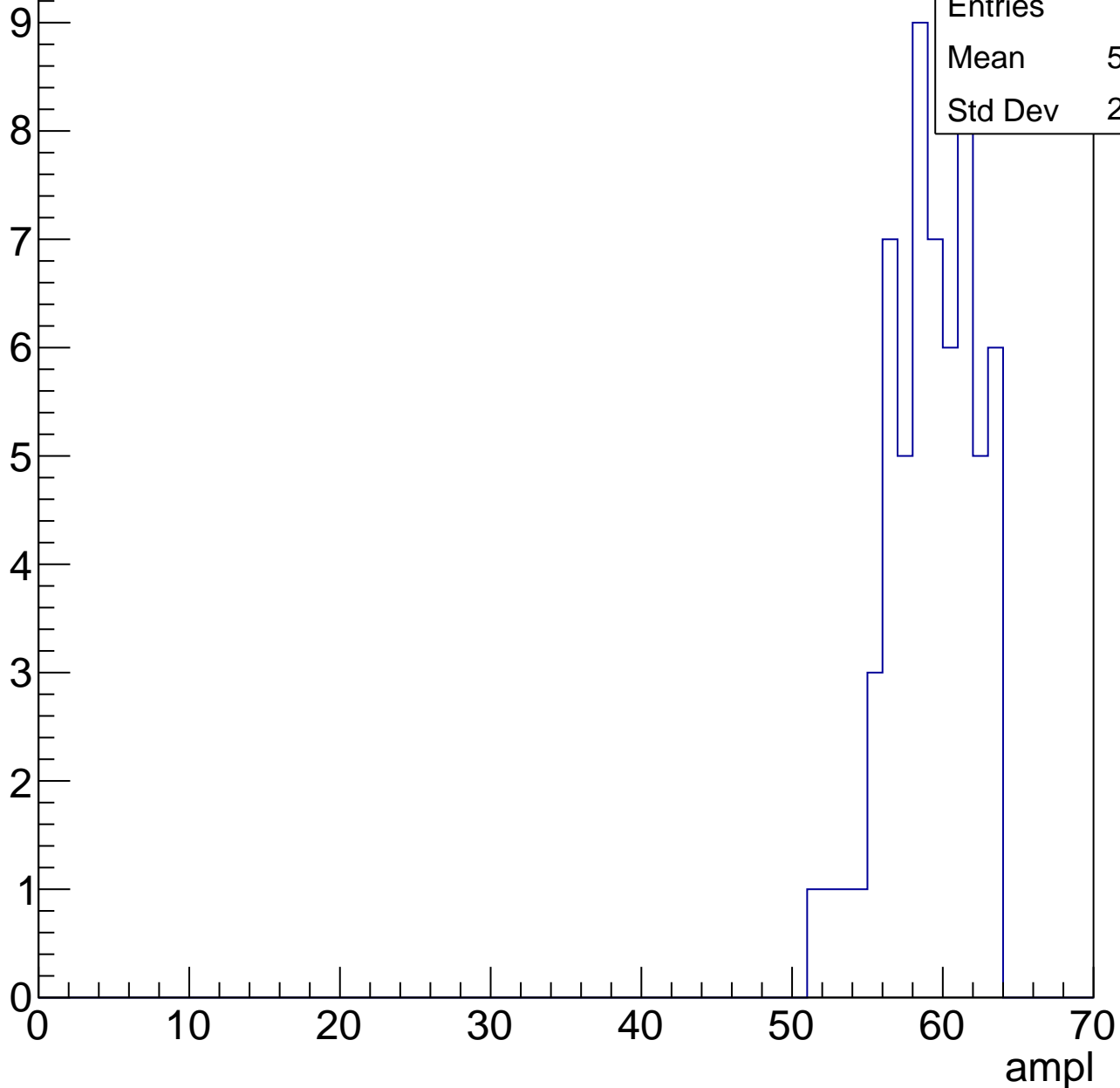


Entries	54
Mean	53.09
Std Dev	3.643

B1L101S, U15-ch60, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

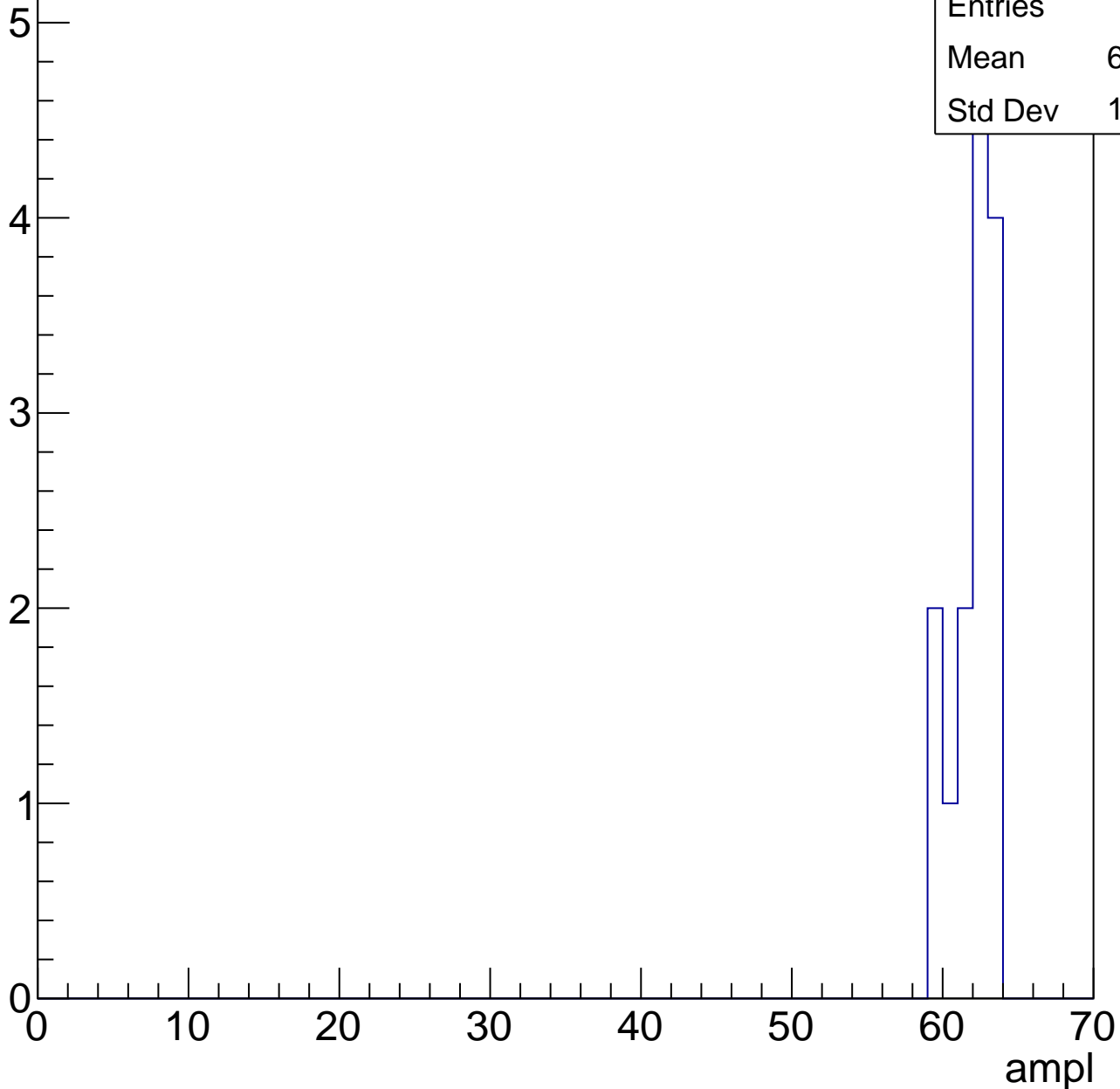


B1L101S, U15-ch60, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	61.57
Std Dev	1.348



B1L101S, U15-ch60, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch61, adc0

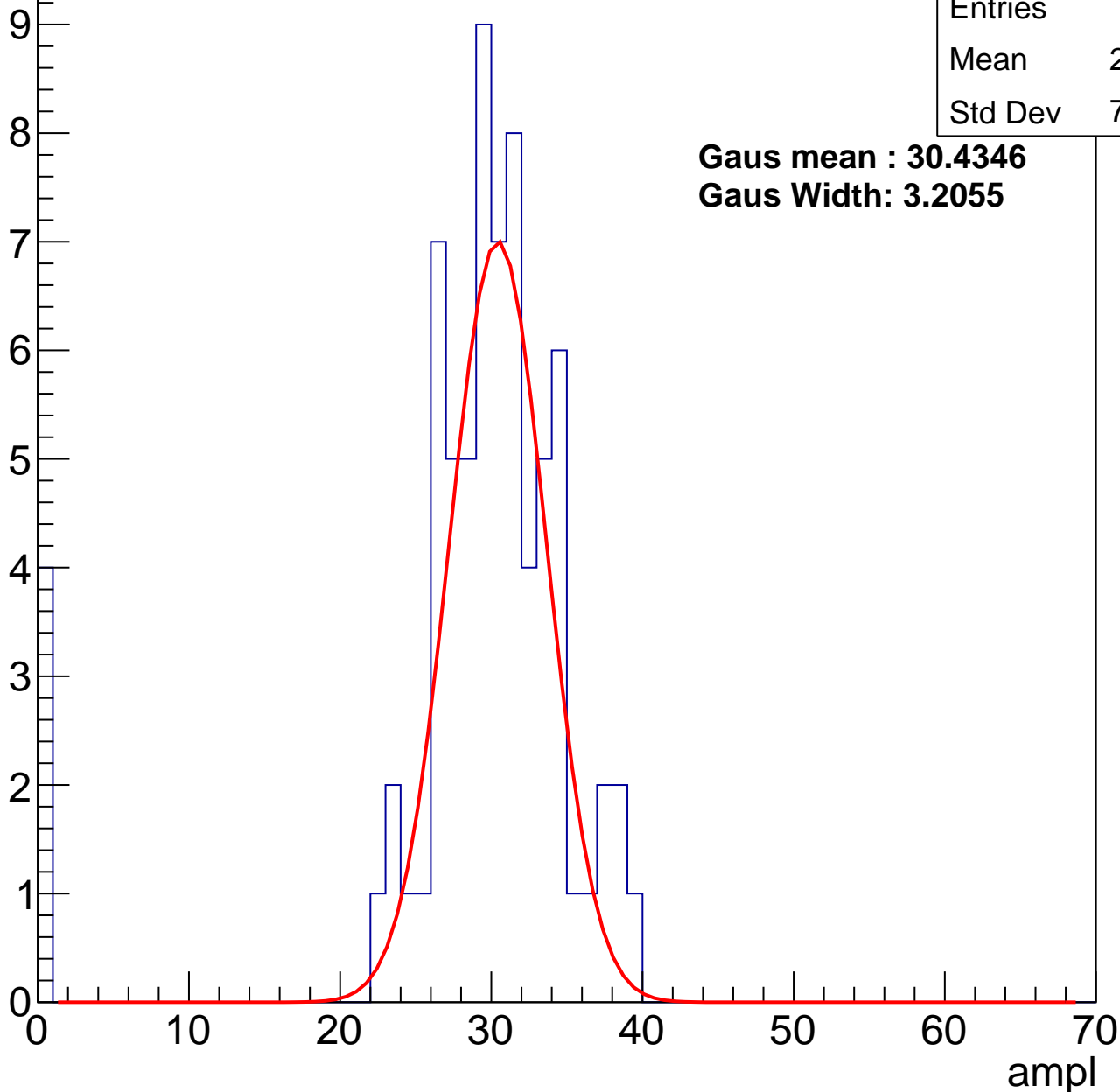
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	28.47
Std Dev	7.794

Gaus mean : 30.4346

Gaus Width: 3.2055



B1L101S, U15-ch61, adc1

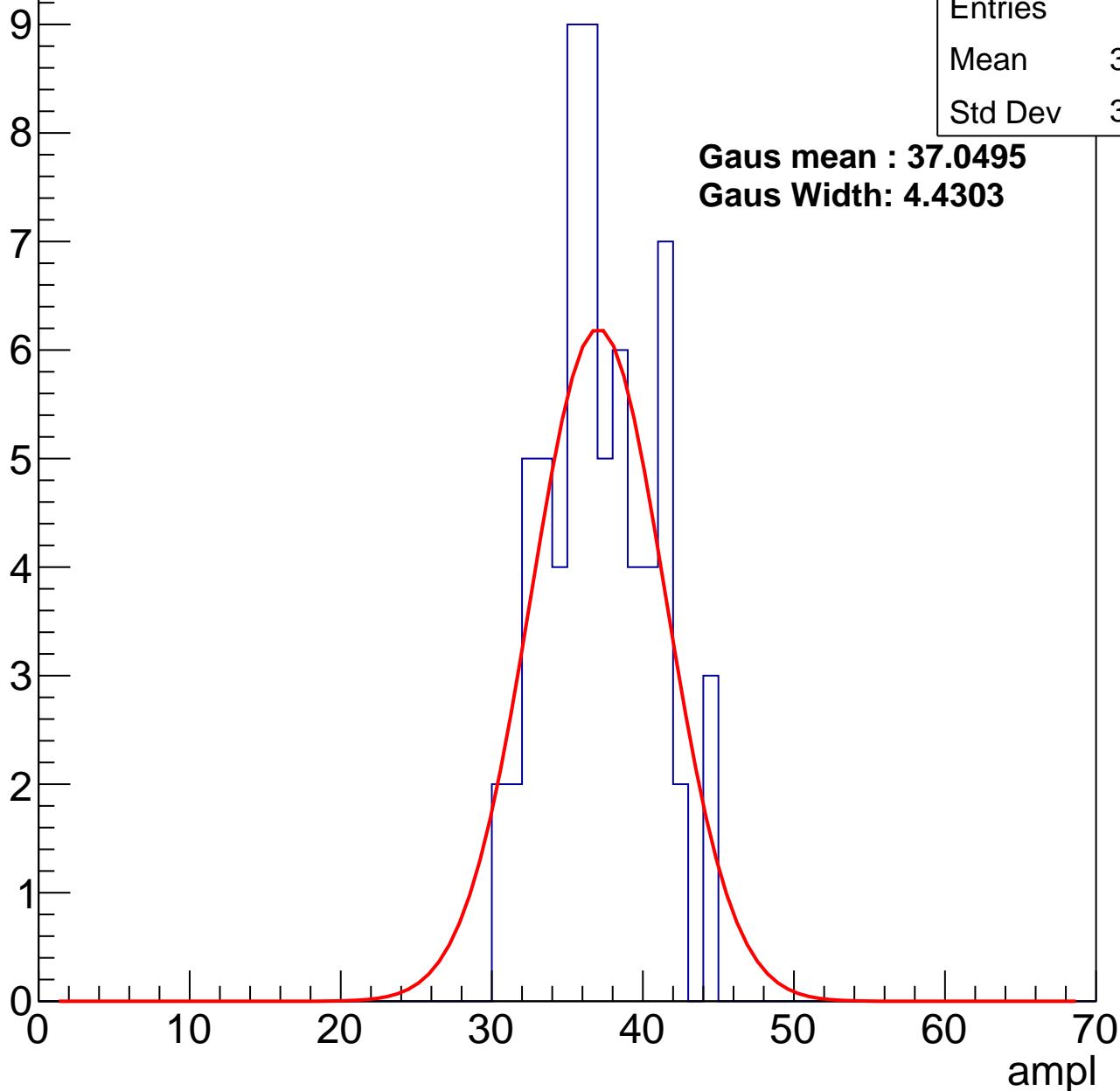
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.63
Std Dev	3.485

Gaus mean : 37.0495

Gaus Width: 4.4303



B1L101S, U15-ch61, adc2

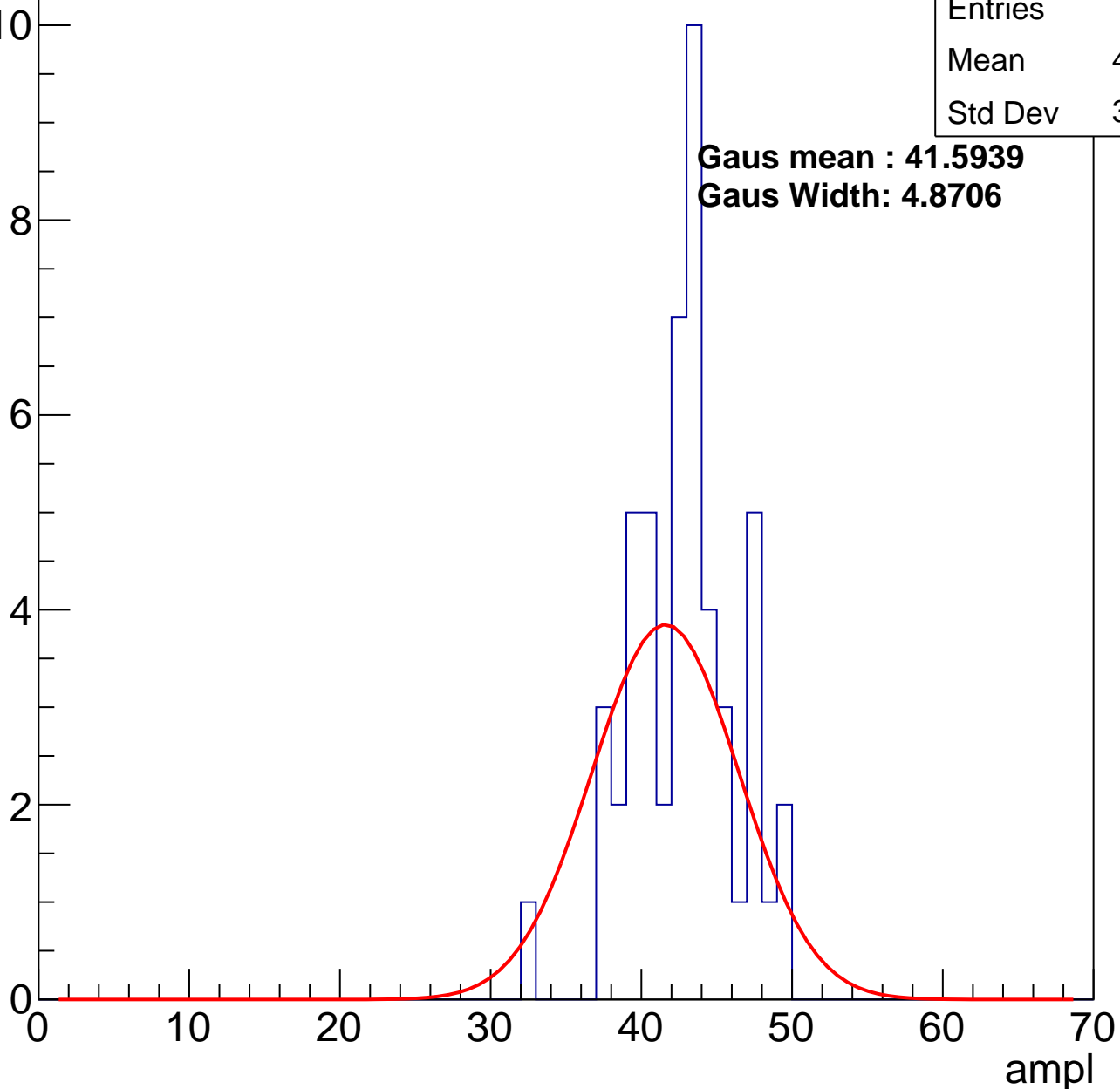
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	42.31
Std Dev	3.421

Gaus mean : 41.5939

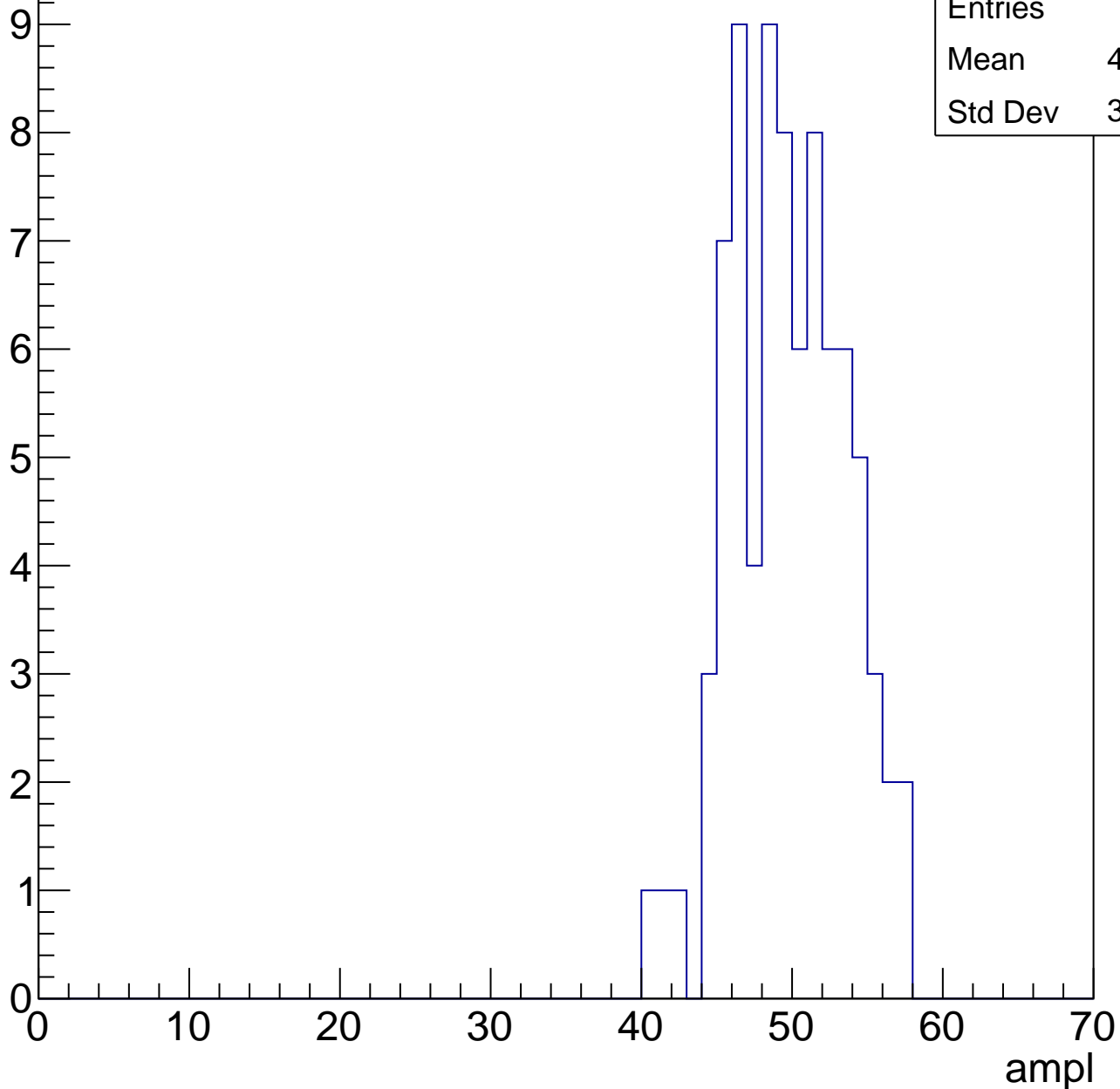
Gaus Width: 4.8706



B1L101S, U15-ch61, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

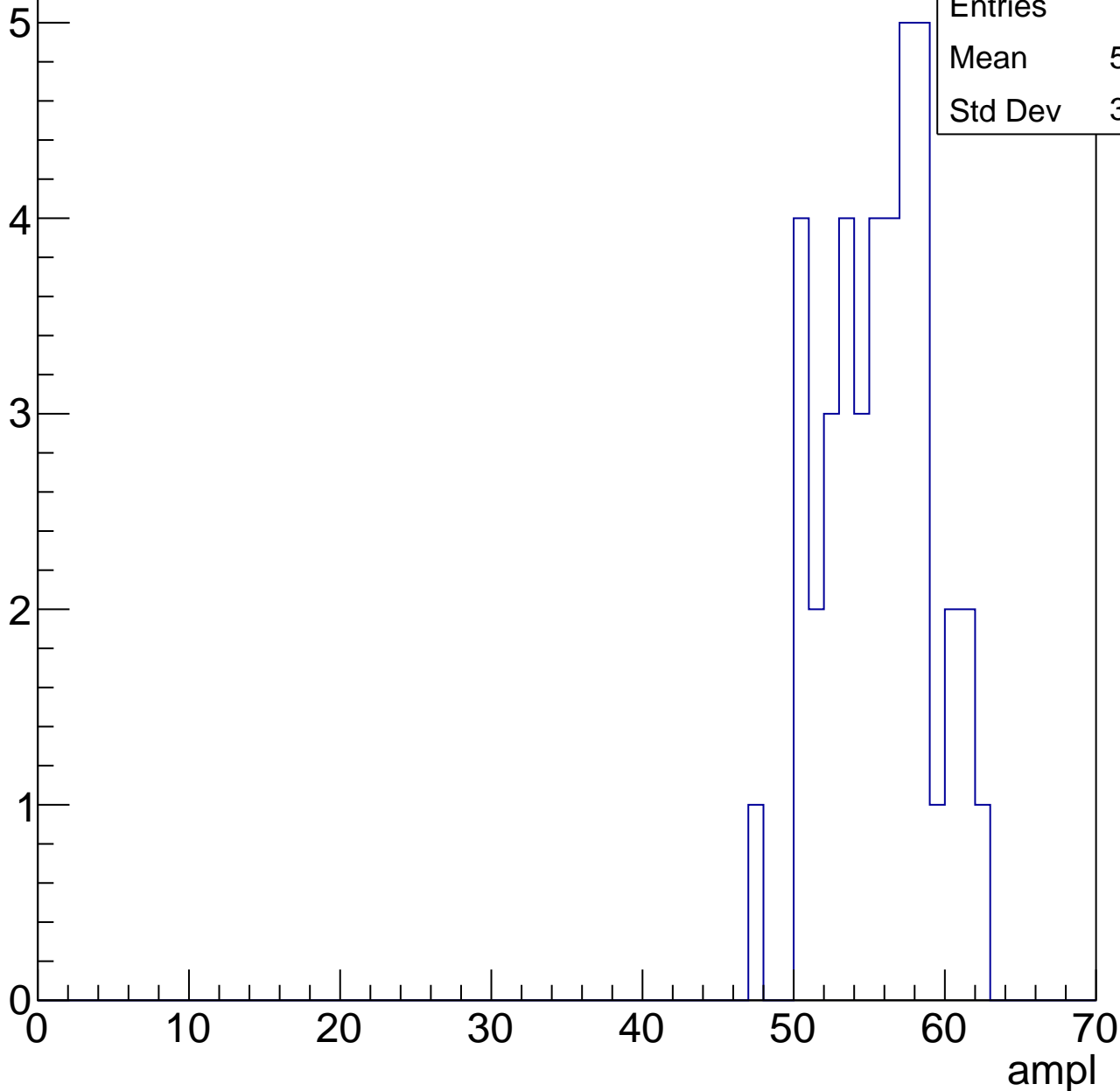


B1L101S, U15-ch61, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	55.15
Std Dev	3.482

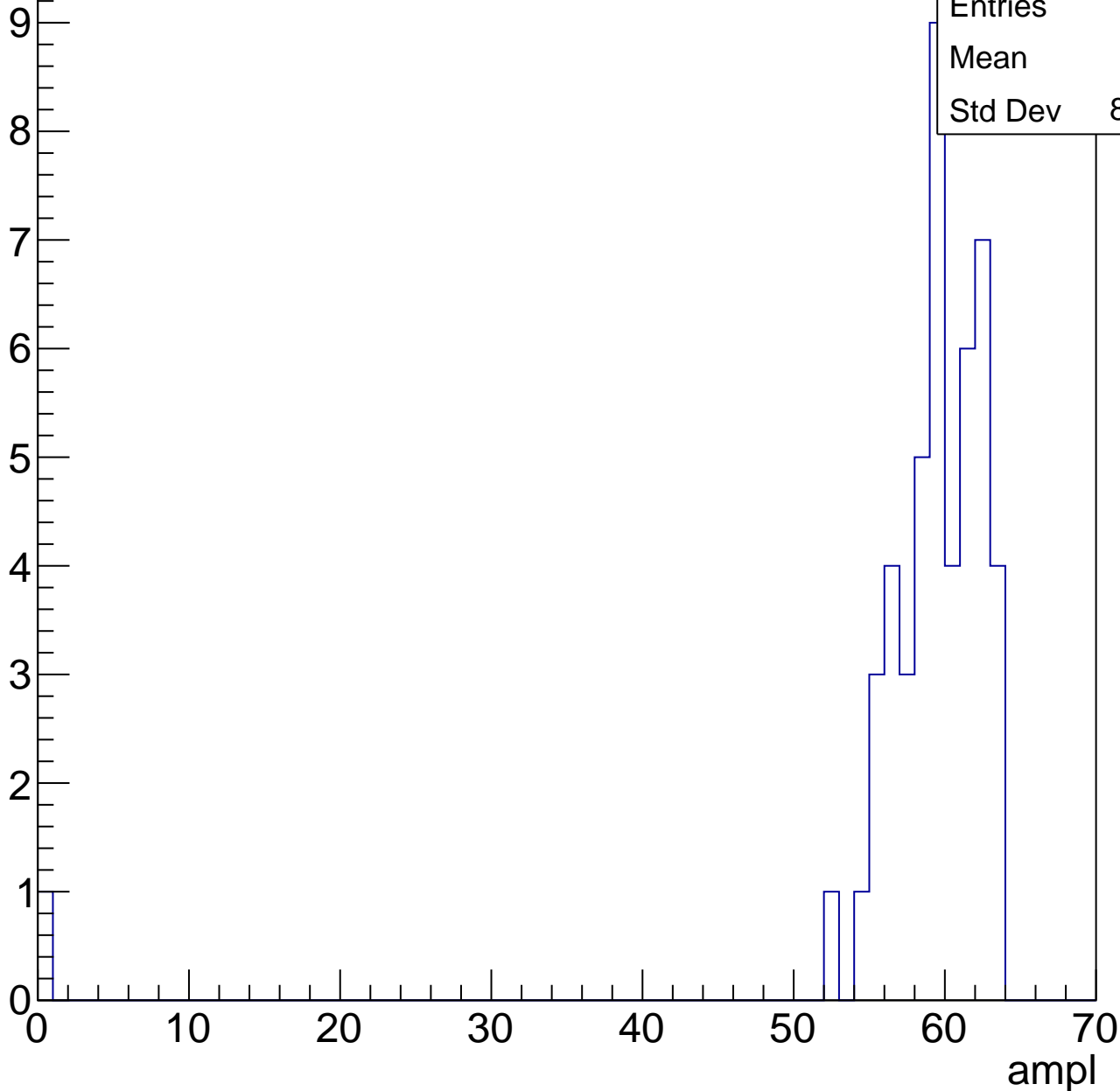


B1L101S, U15-ch61, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	57.9
Std Dev	8.842

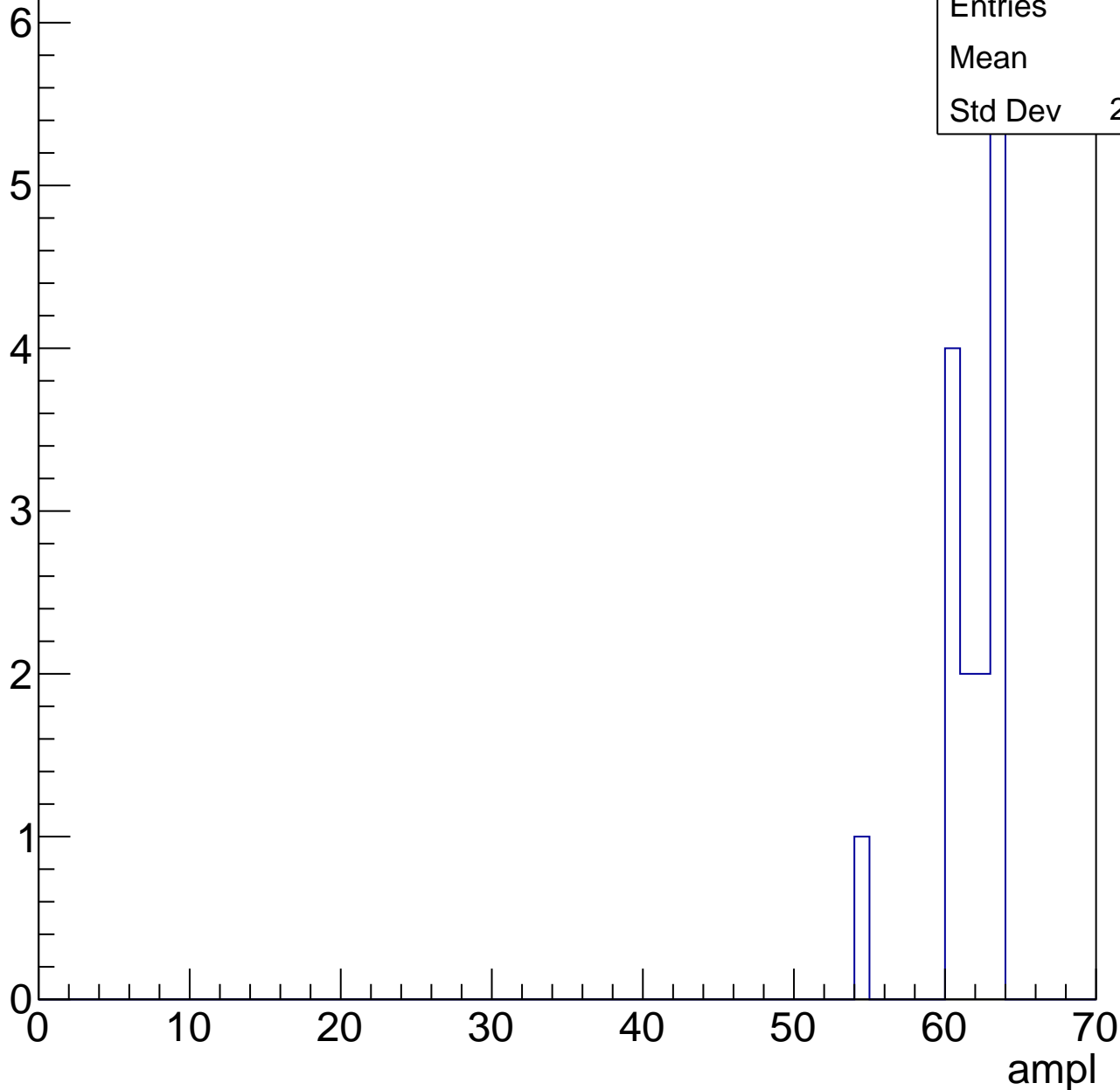


B1L101S, U15-ch61, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	15
Mean	61.2
Std Dev	2.286



B1L101S, U15-ch61, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch62, adc0

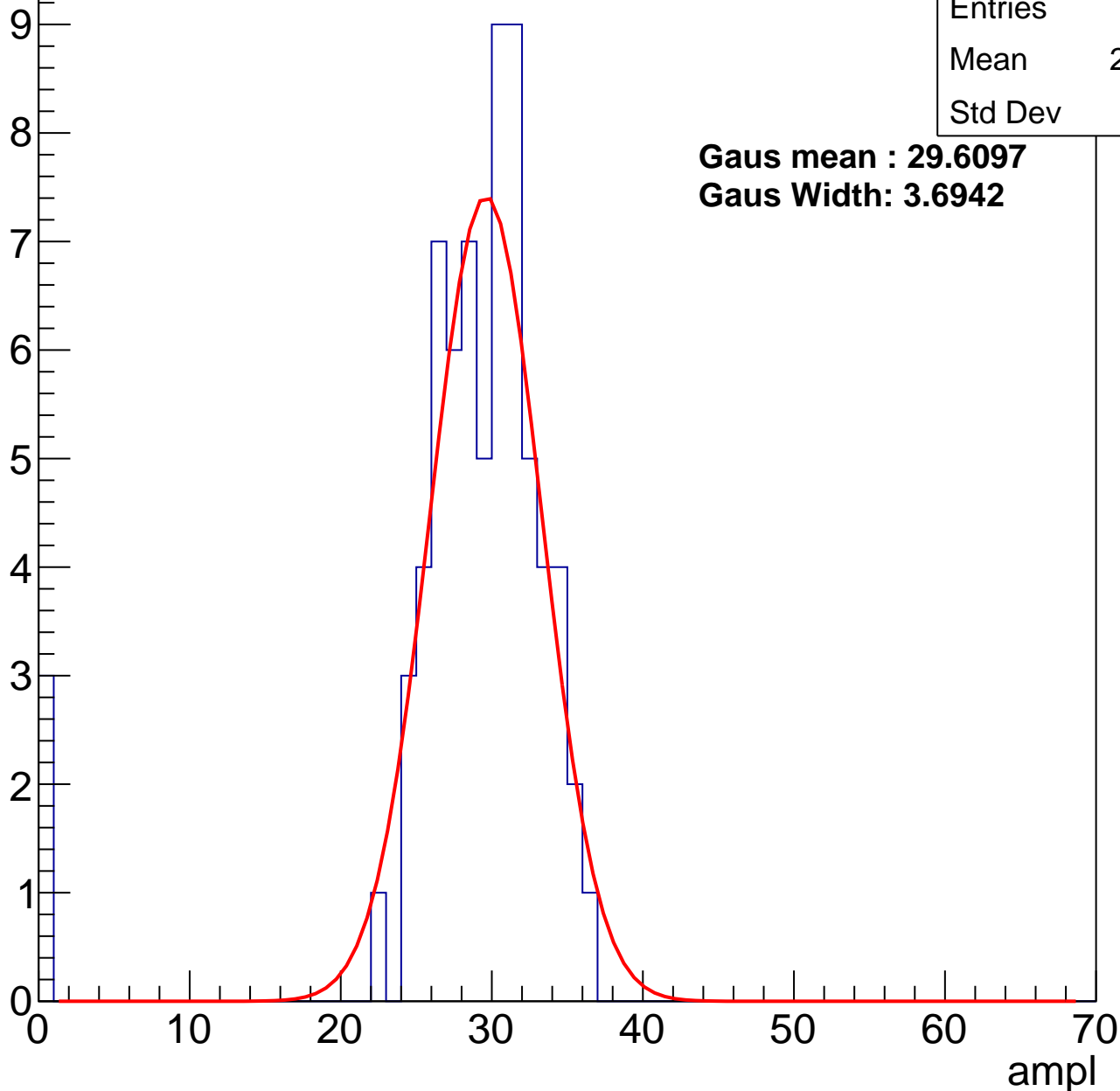
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.03
Std Dev	6.67

Gaus mean : 29.6097

Gaus Width: 3.6942



B1L101S, U15-ch62, adc1

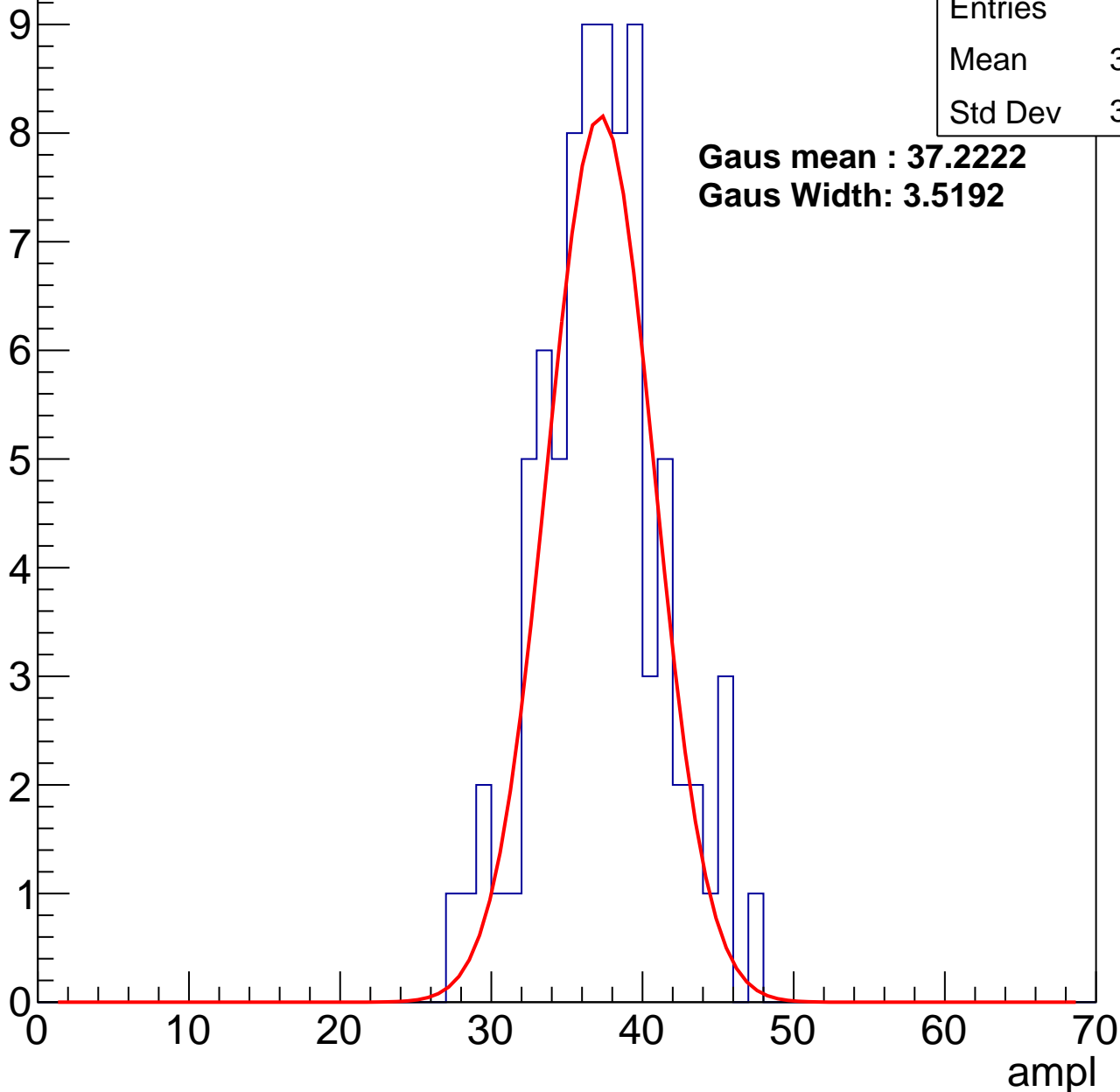
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	82
Mean	36.77
Std Dev	3.998

Gaus mean : 37.2222

Gaus Width: 3.5192

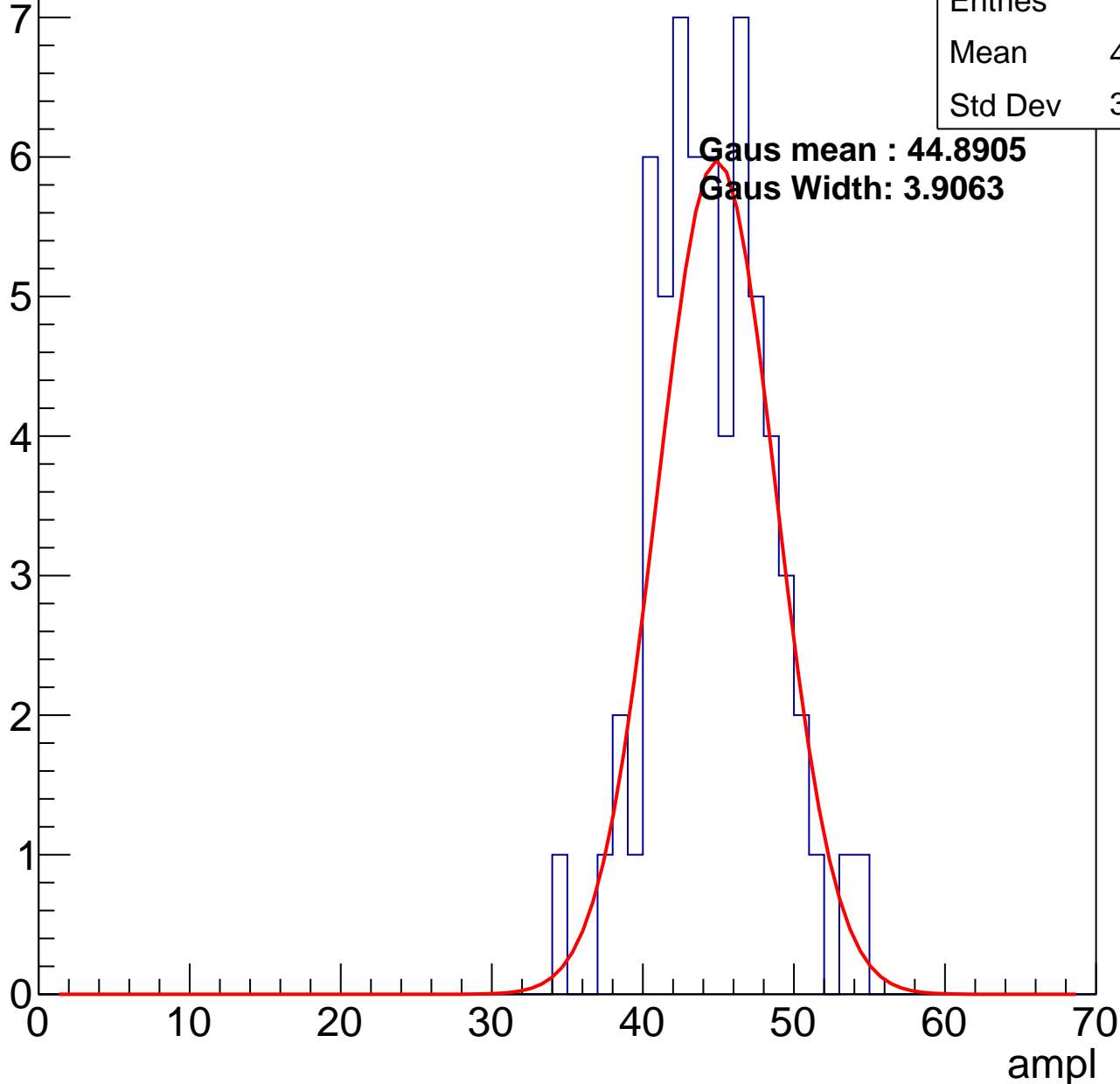


B1L101S, U15-ch62, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.14
Std Dev	3.858

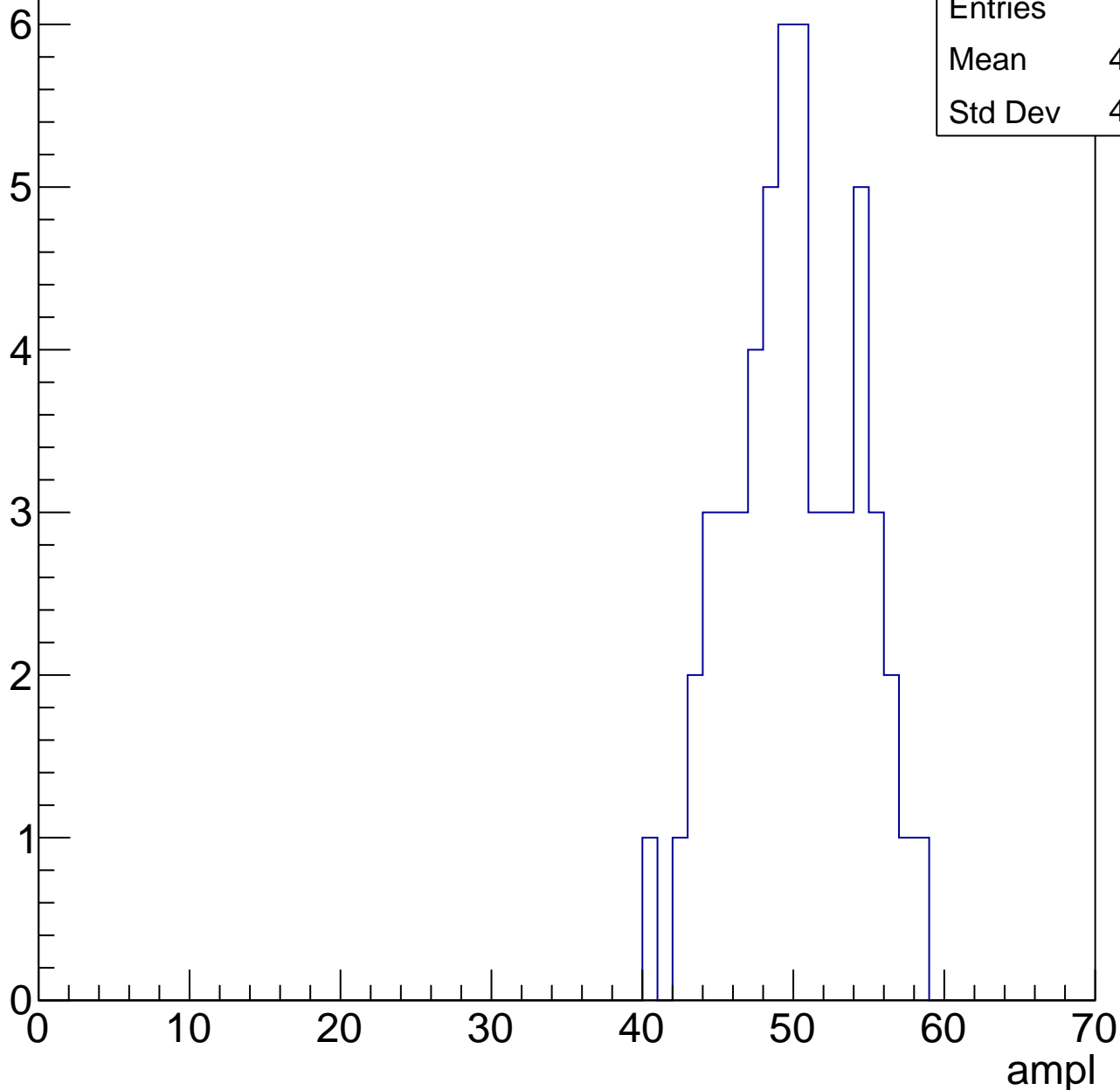


B1L101S, U15-ch62, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	49.55
Std Dev	4.102



B1L101S, U15-ch62, adc4

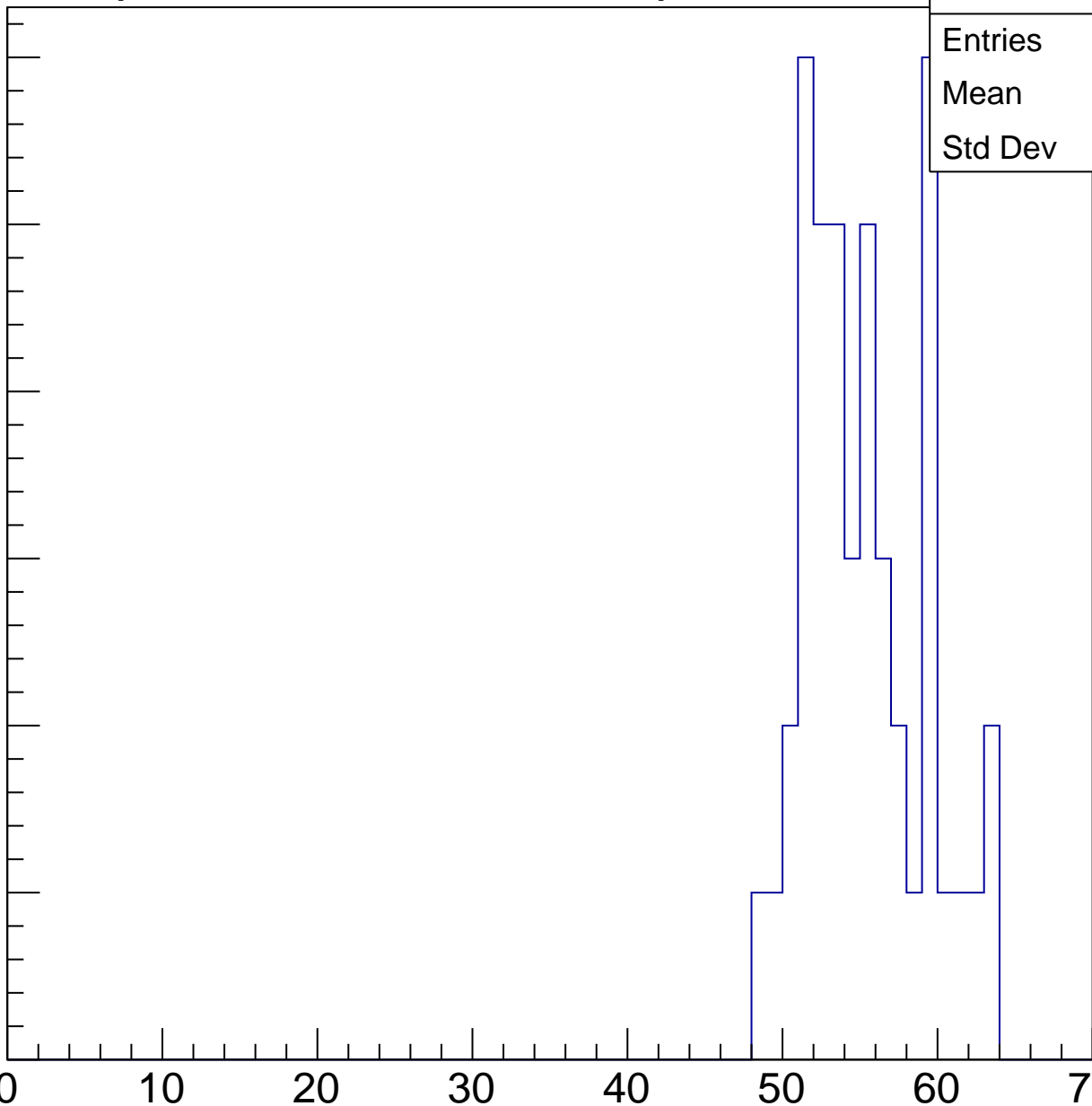
calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	45
Mean	54.84
Std Dev	3.806

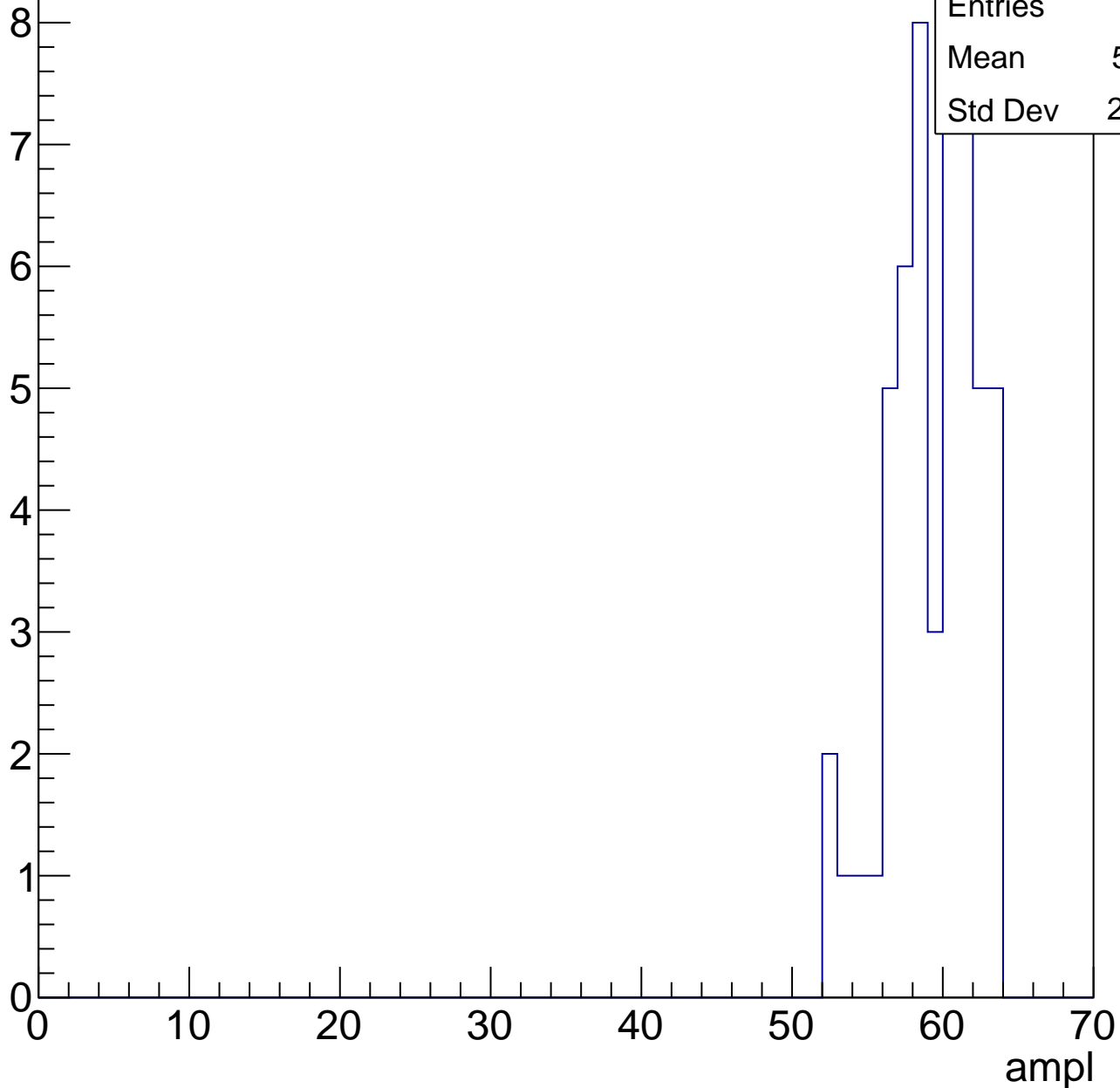
ampl



B1L101S, U15-ch62, adc5

calib_packv5_042523_0143.root, FC#0, port D2

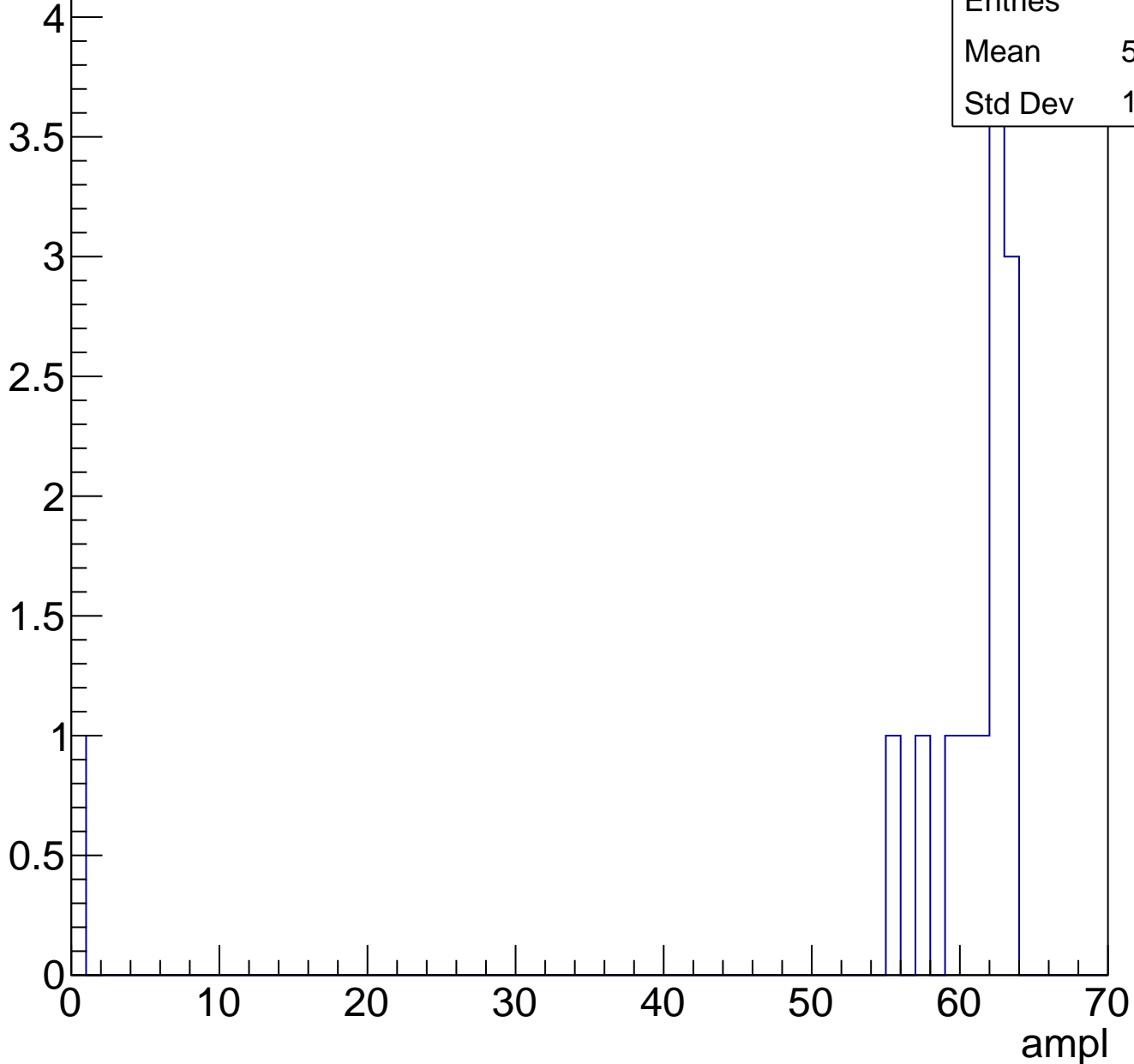
Entry



B1L101S, U15-ch62, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch62, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch63, adc0

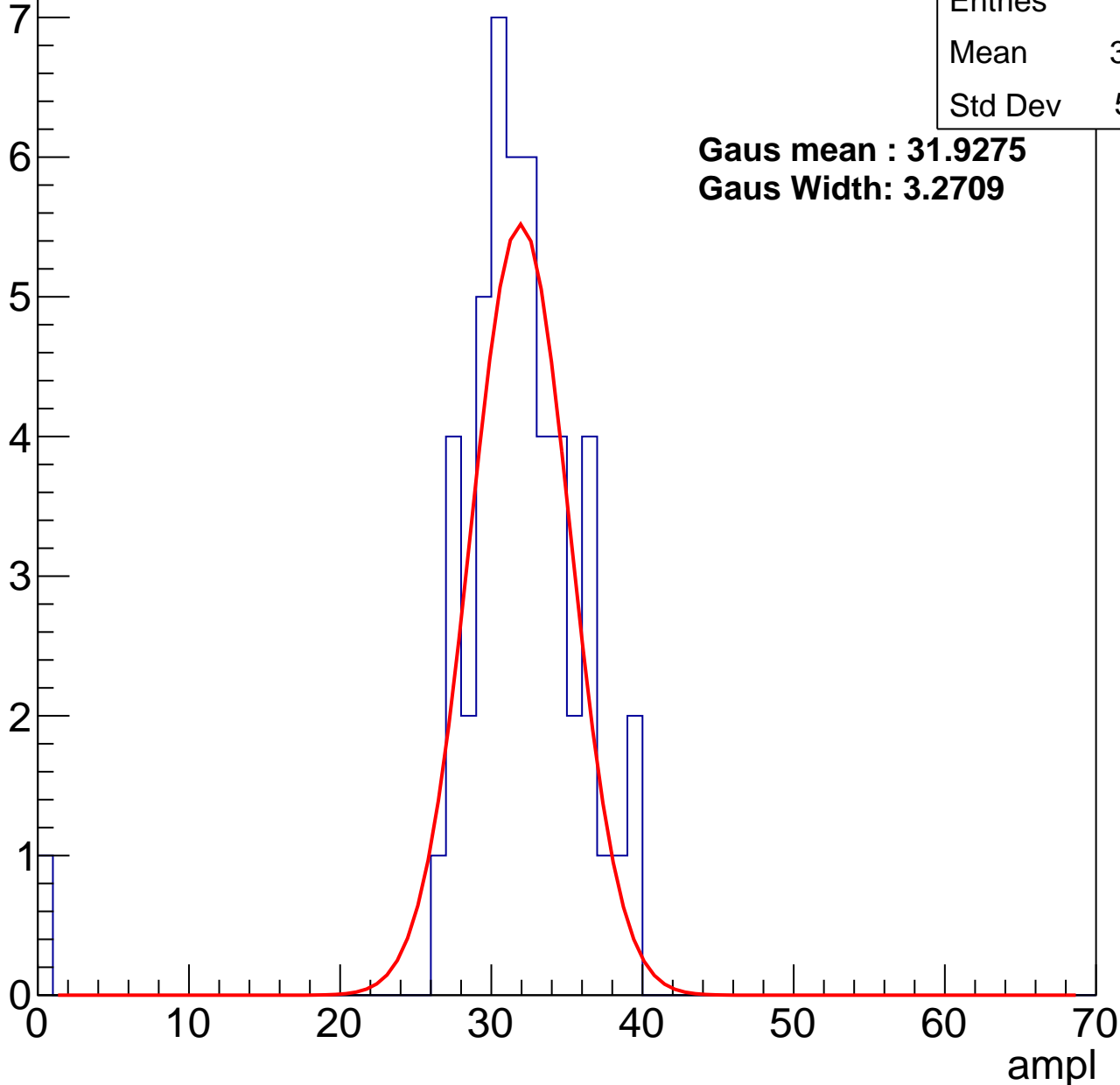
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	31.16
Std Dev	5.471

Gaus mean : 31.9275

Gaus Width: 3.2709



B1L101S, U15-ch63, adc1

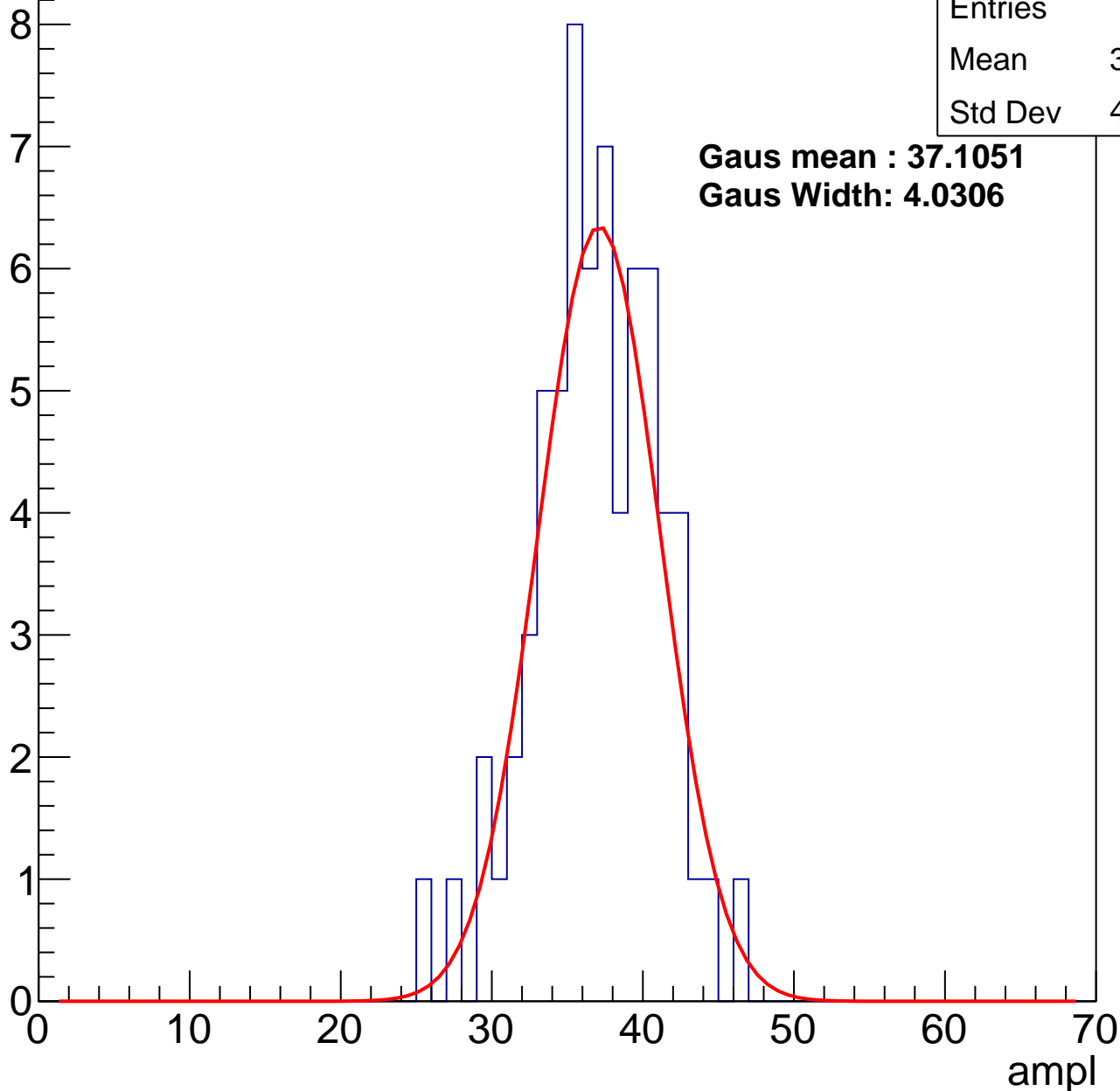
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	36.46
Std Dev	4.064

Gaus mean : 37.1051

Gaus Width: 4.0306



B1L101S, U15-ch63, adc2

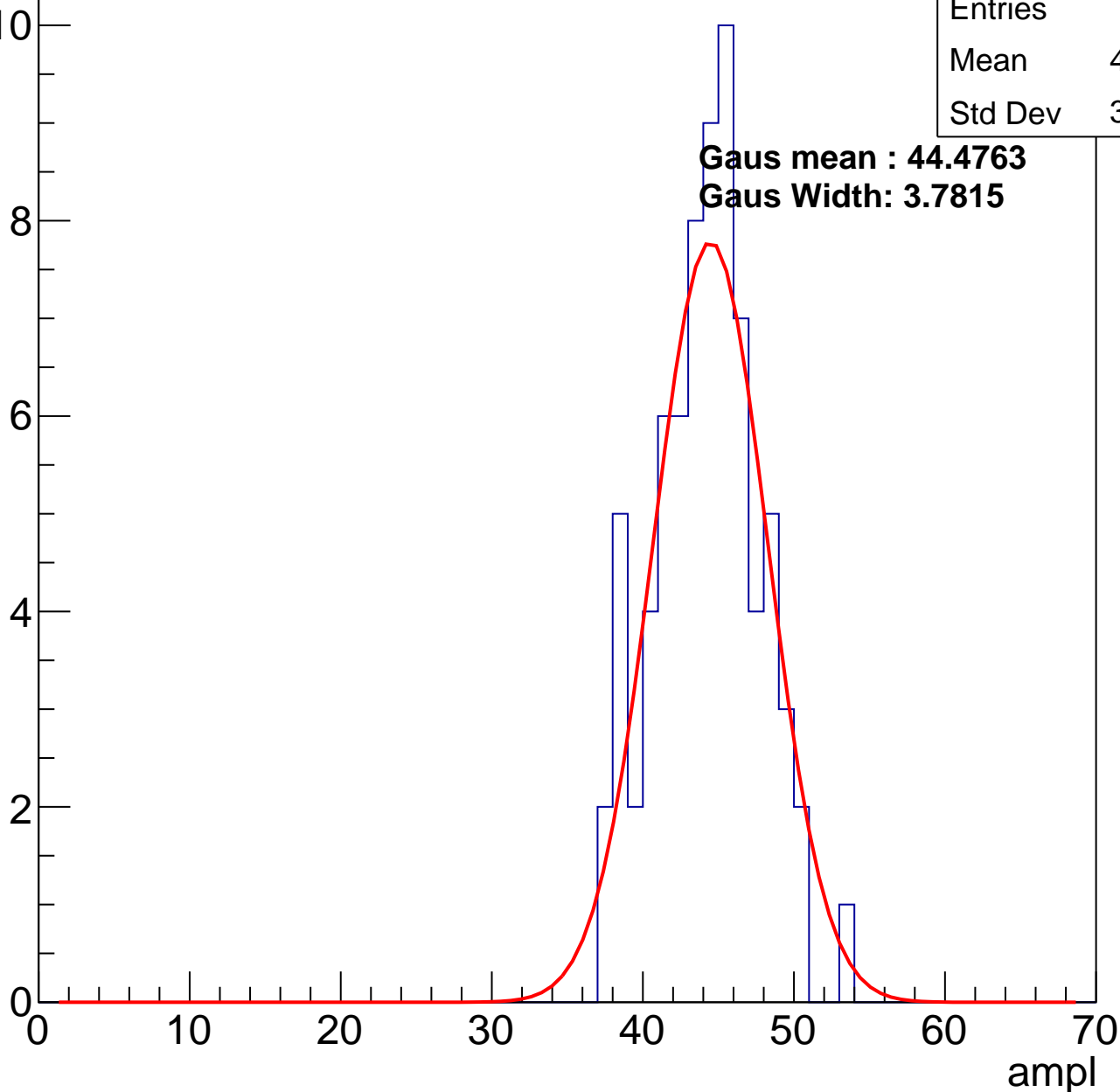
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	43.78
Std Dev	3.402

Gaus mean : 44.4763

Gaus Width: 3.7815

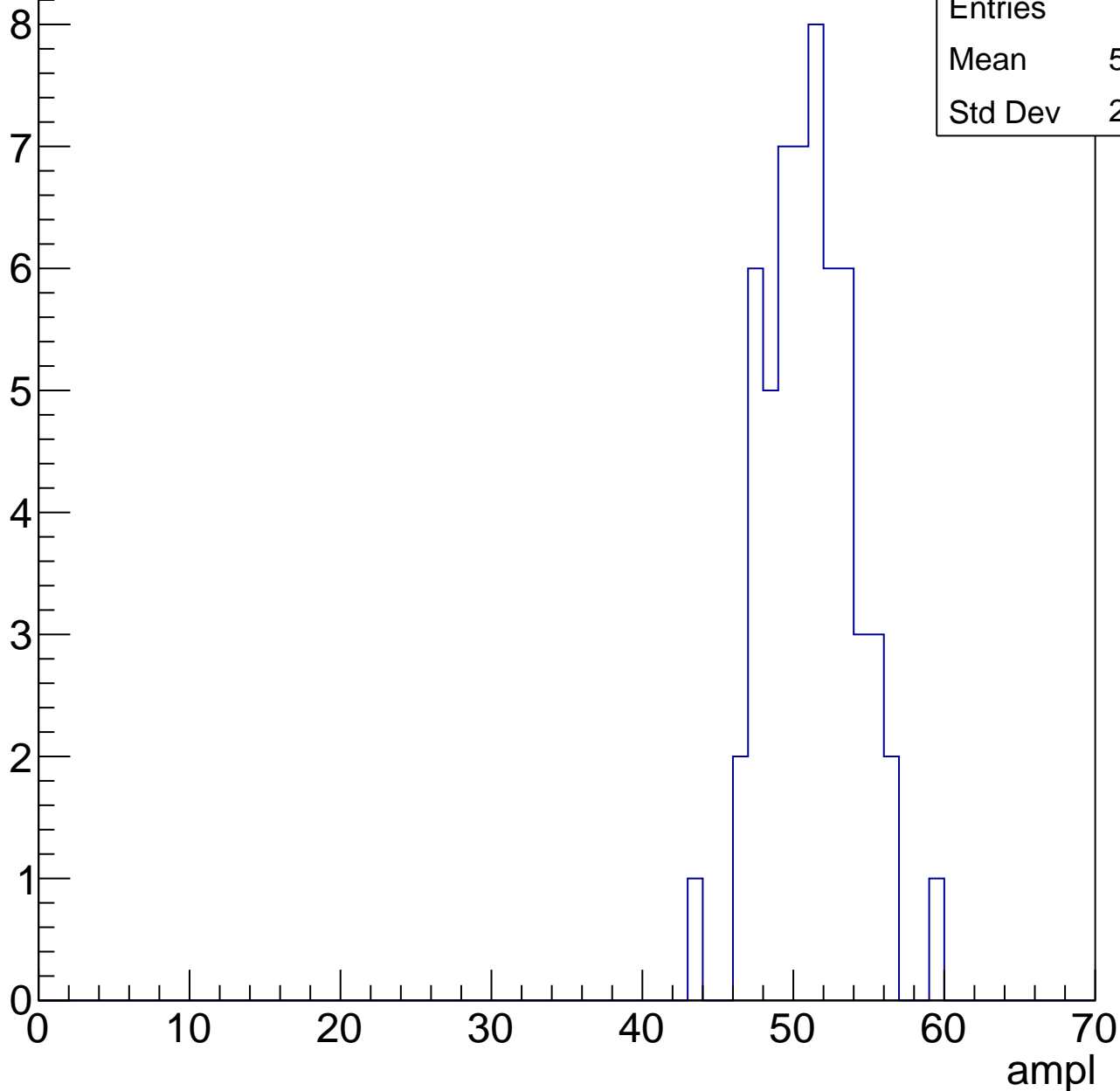


B1L101S, U15-ch63, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	50.63
Std Dev	2.965

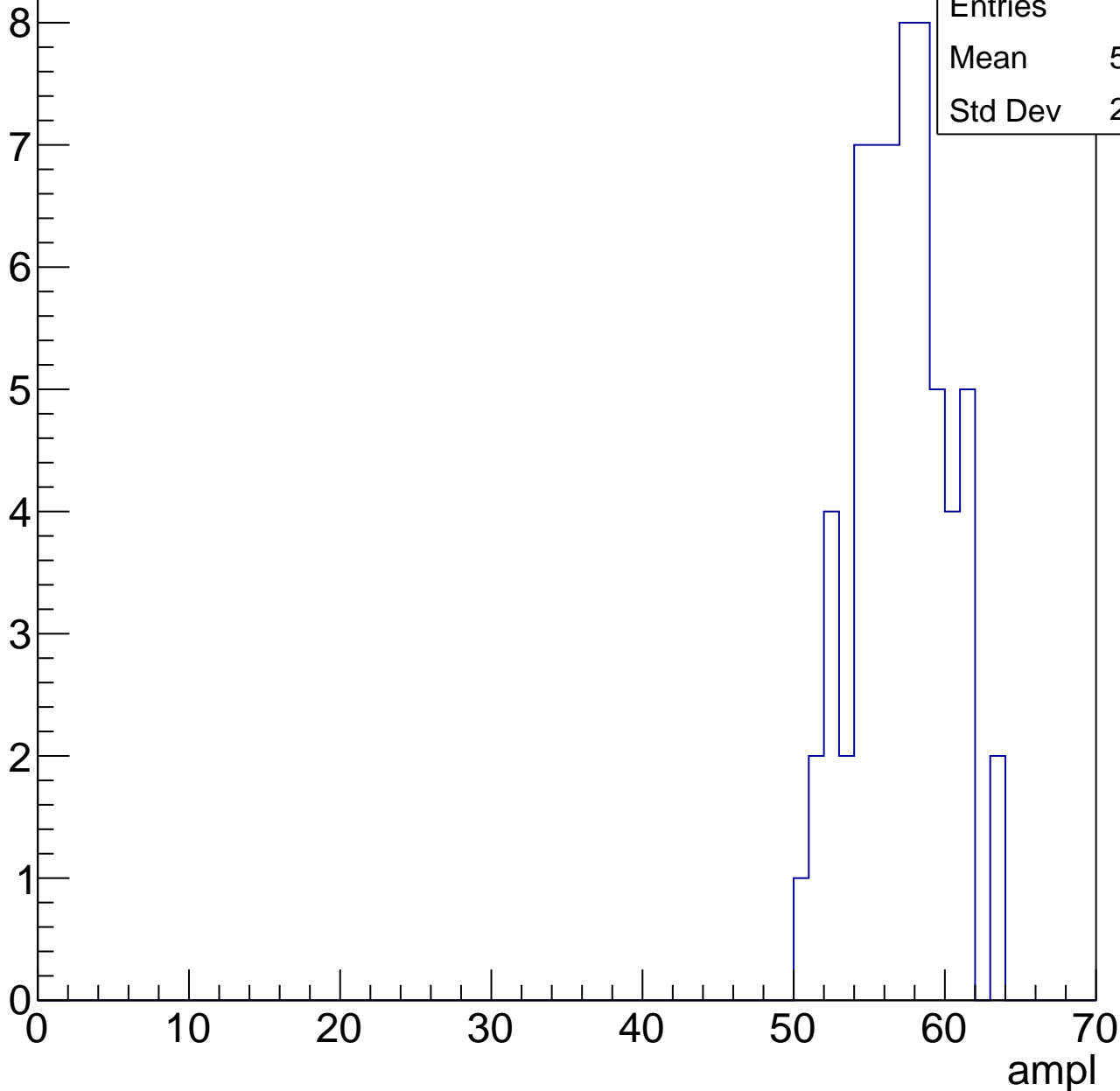


B1L101S, U15-ch63, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	56.56
Std Dev	2.993

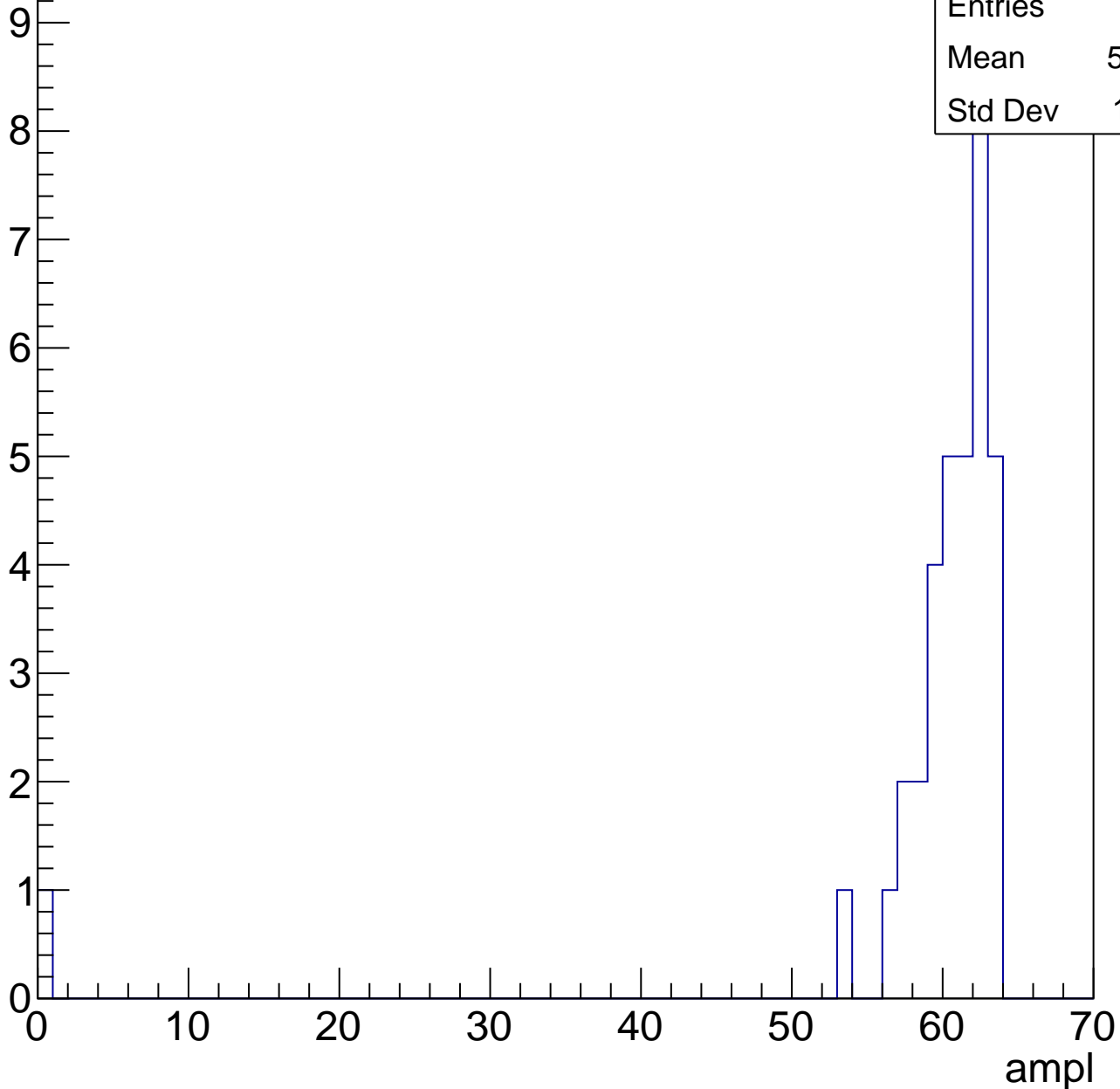


B1L101S, U15-ch63, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	35
Mean	58.66
Std Dev	10.31



B1L101S, U15-ch63, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch63, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch64, adc0

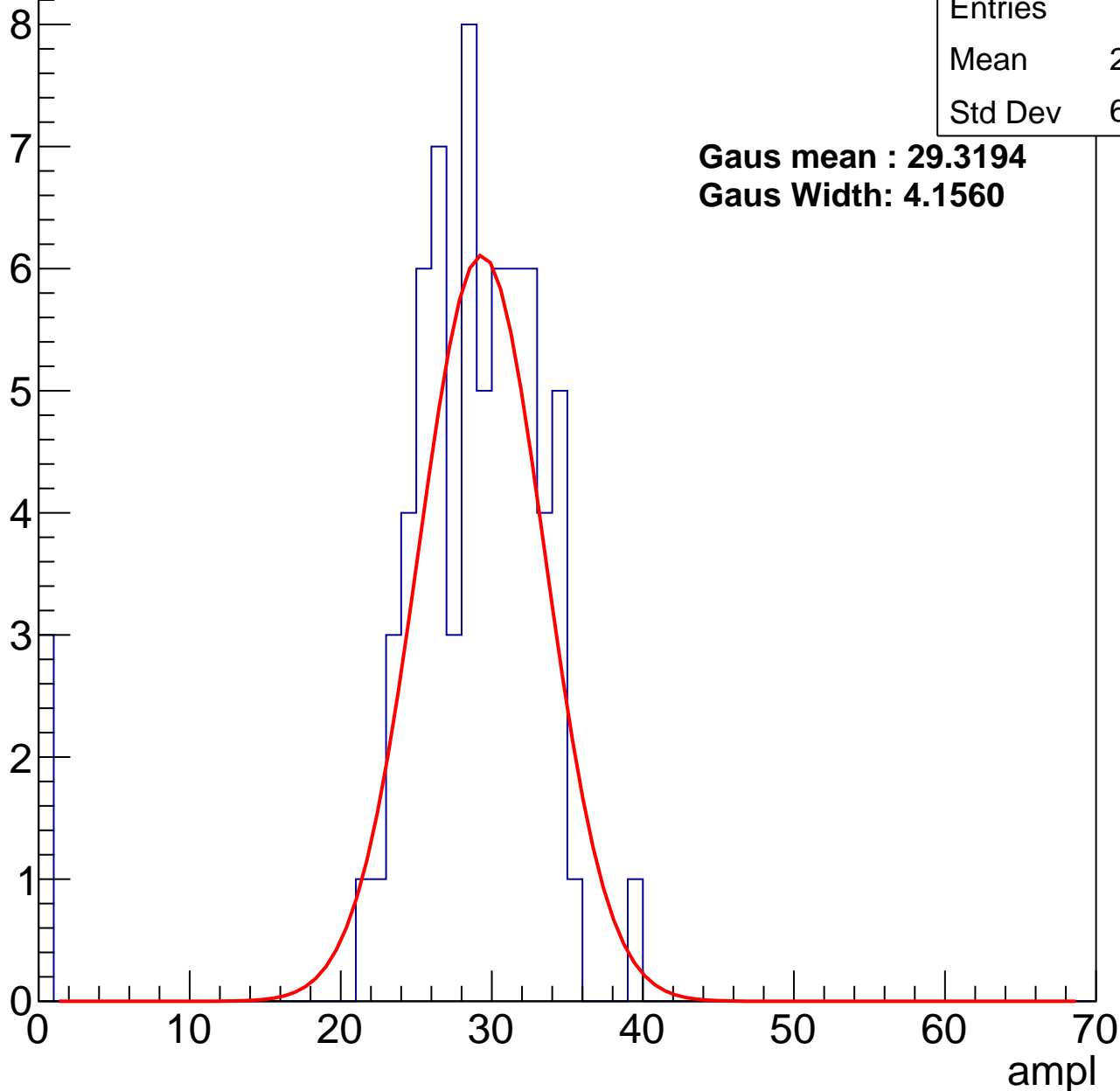
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	27.49
Std Dev	6.836

Gaus mean : 29.3194

Gaus Width: 4.1560



B1L101S, U15-ch64, adc1

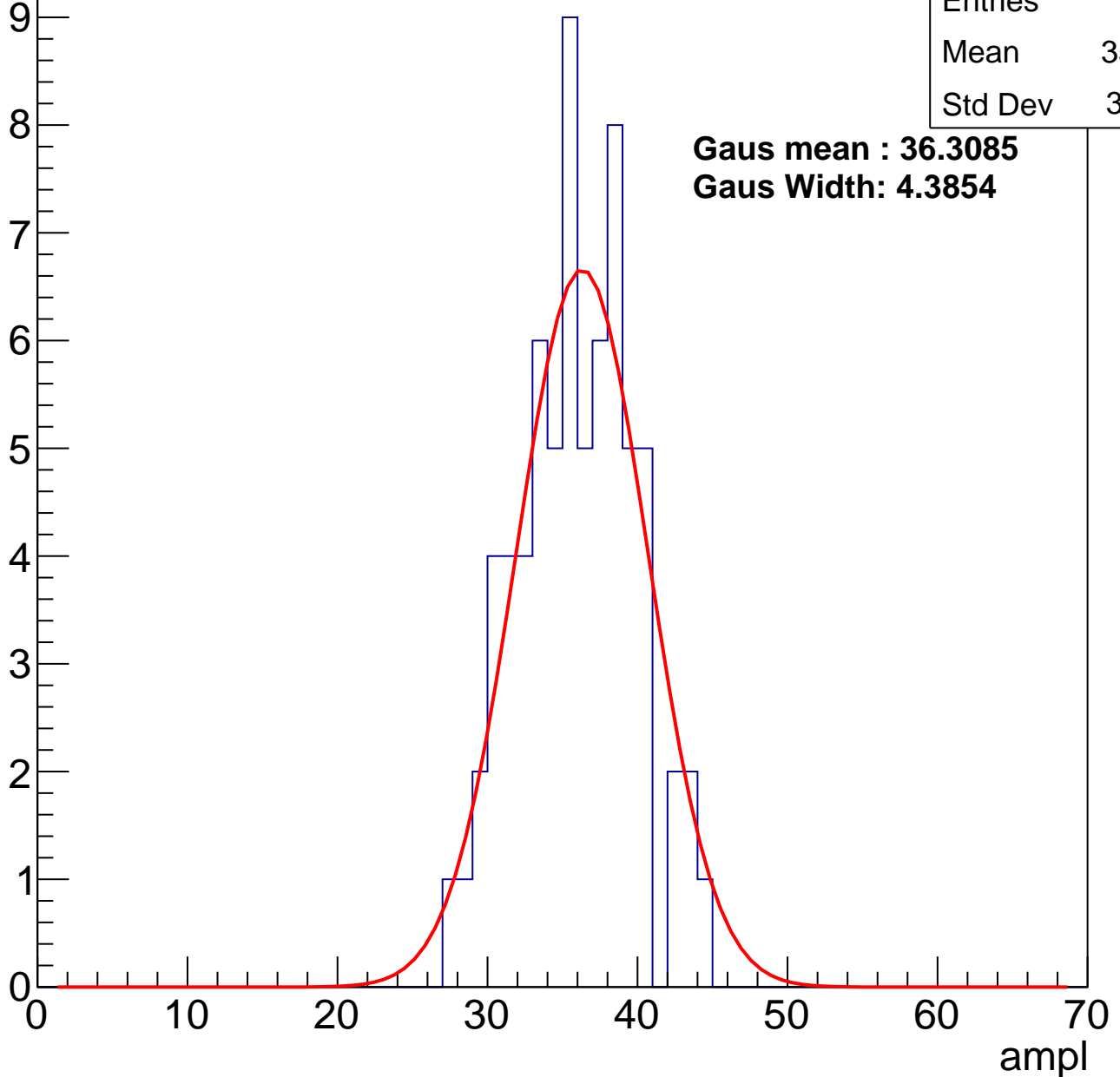
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.47
Std Dev	3.801

Gaus mean : 36.3085

Gaus Width: 4.3854



B1L101S, U15-ch64, adc2

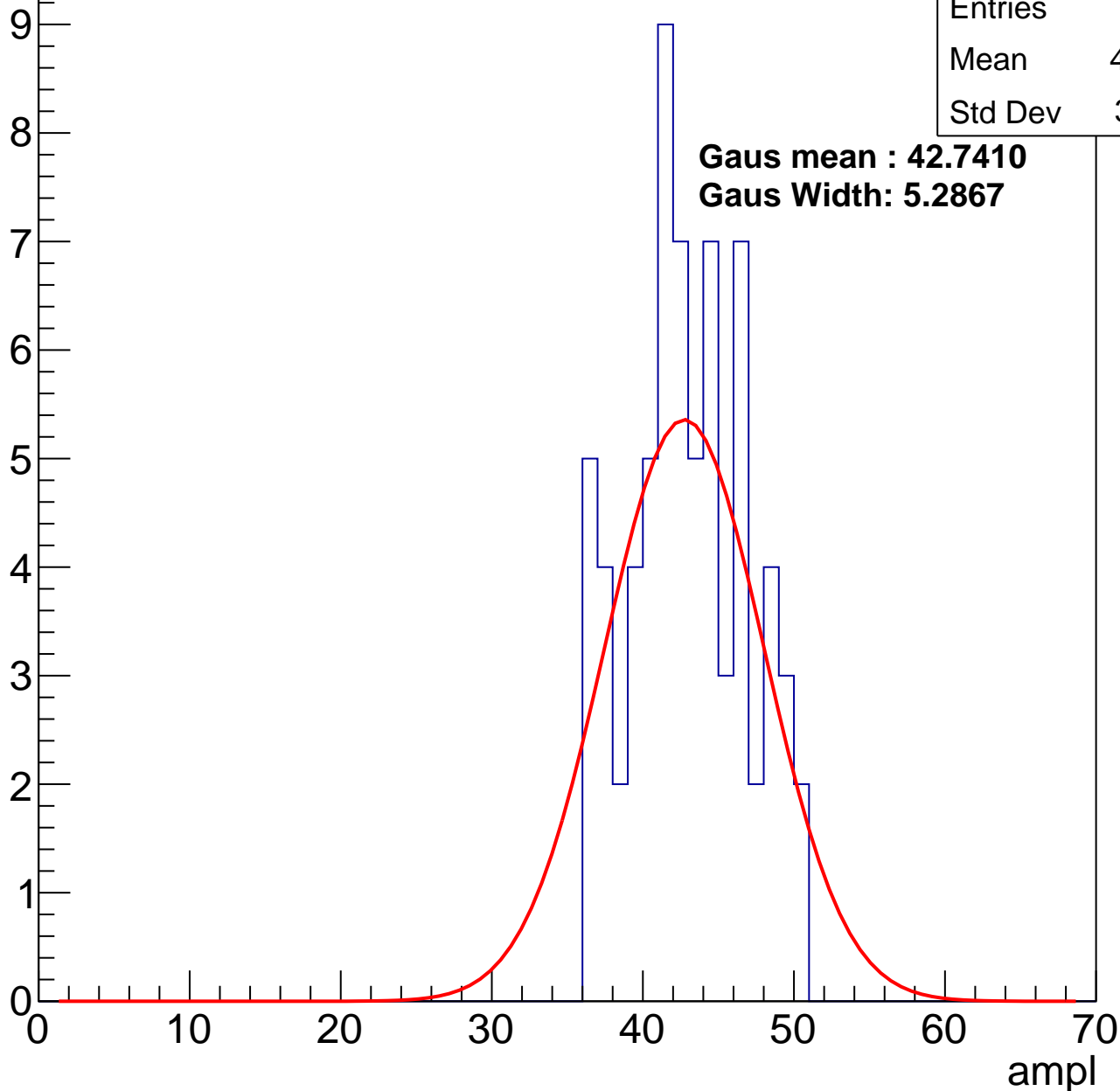
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.55
Std Dev	3.801

Gaus mean : 42.7410

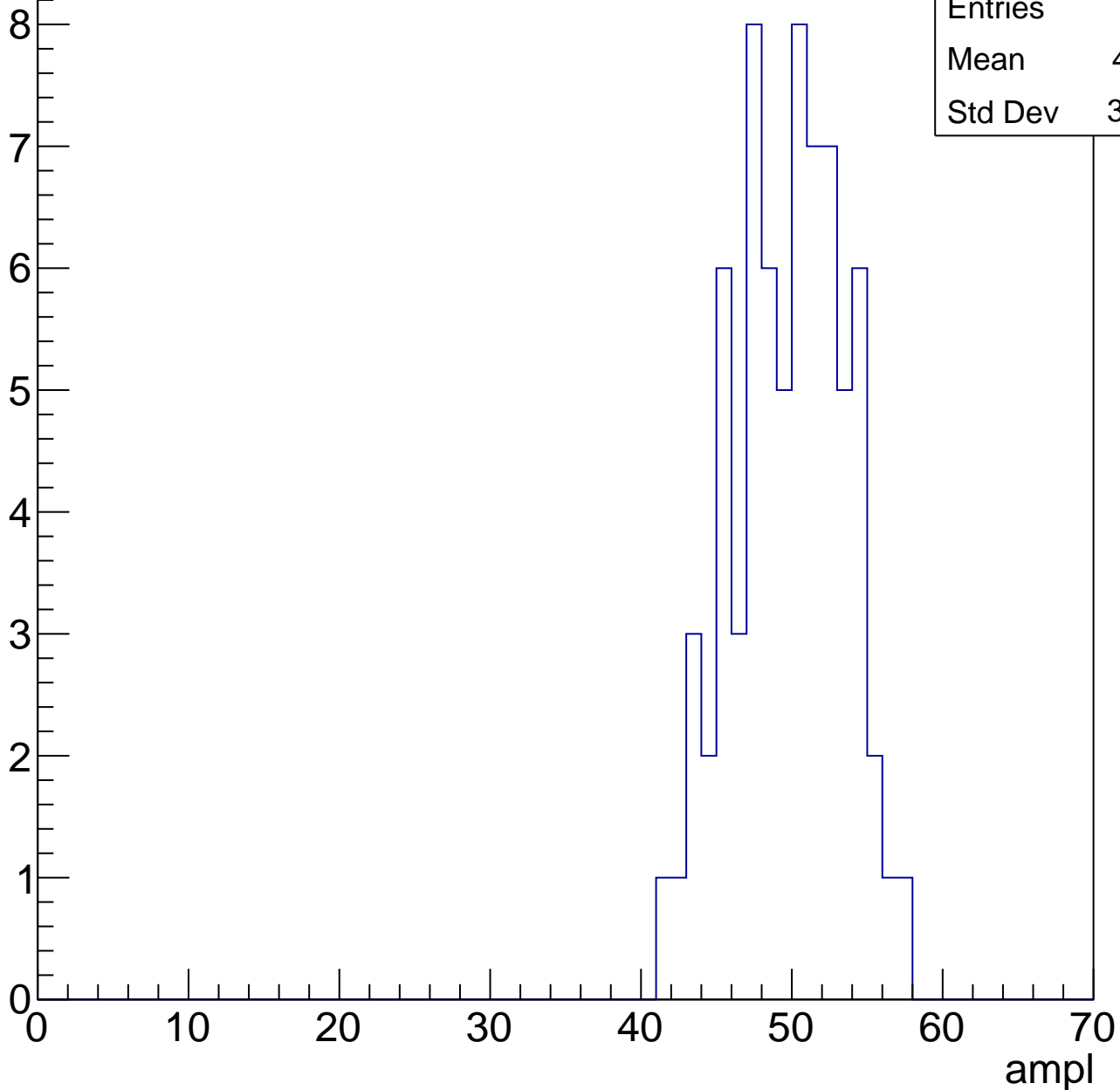
Gaus Width: 5.2867



B1L101S, U15-ch64, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

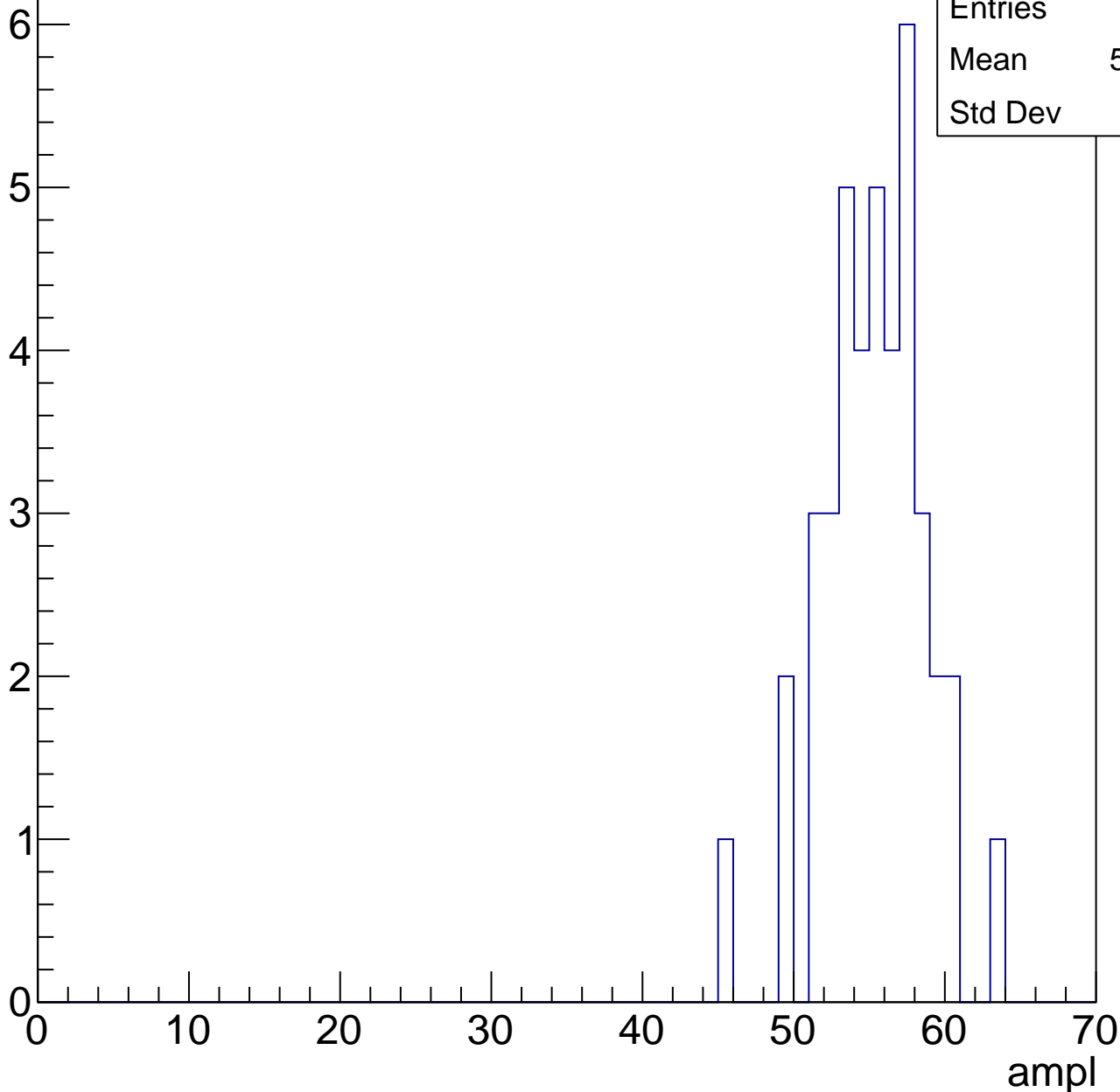


B1L101S, U15-ch64, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

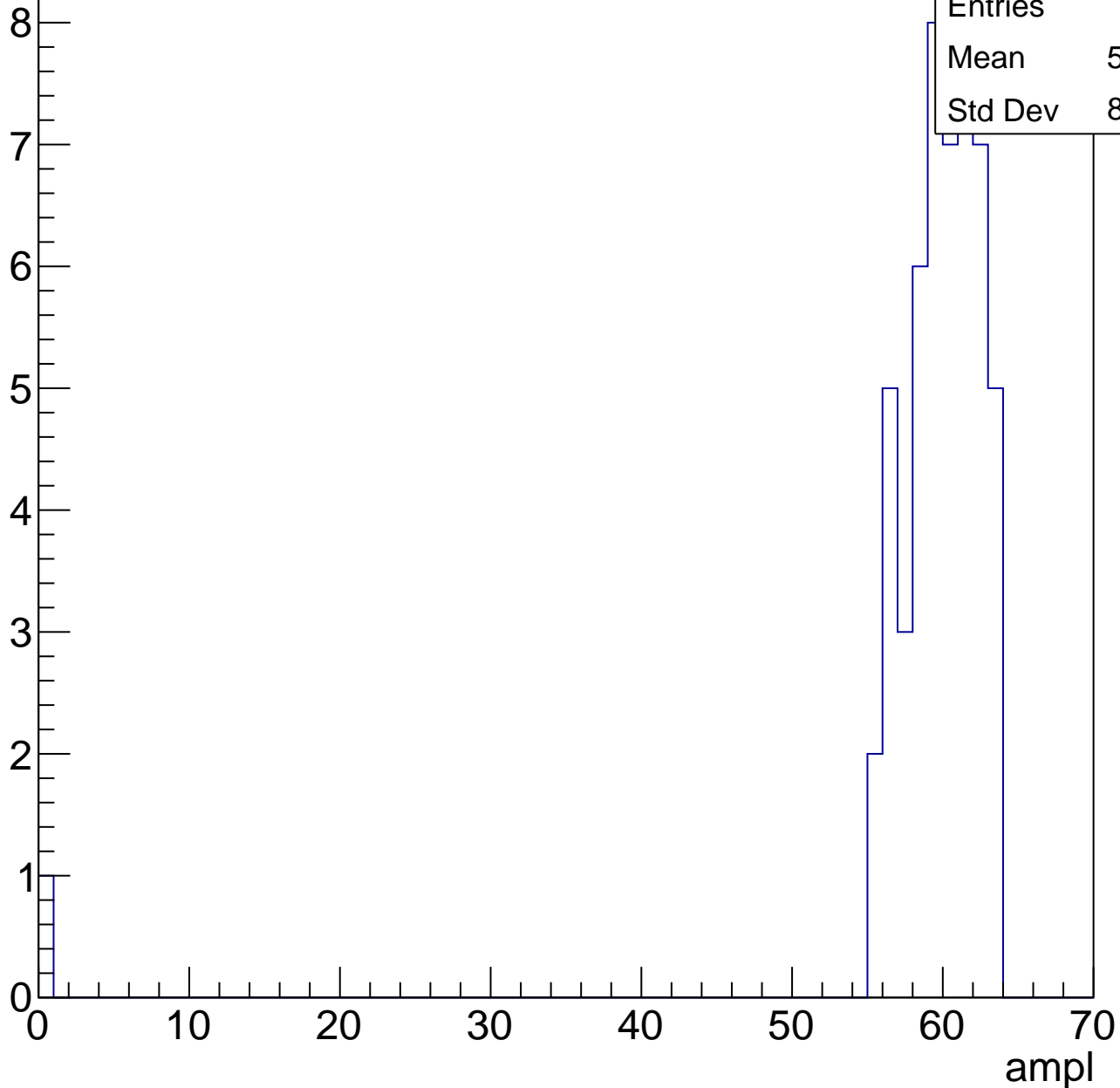
Entries	41
Mean	54.85
Std Dev	3.39



B1L101S, U15-ch64, adc5

calib_packv5_042523_0143.root, FC#0, port D2

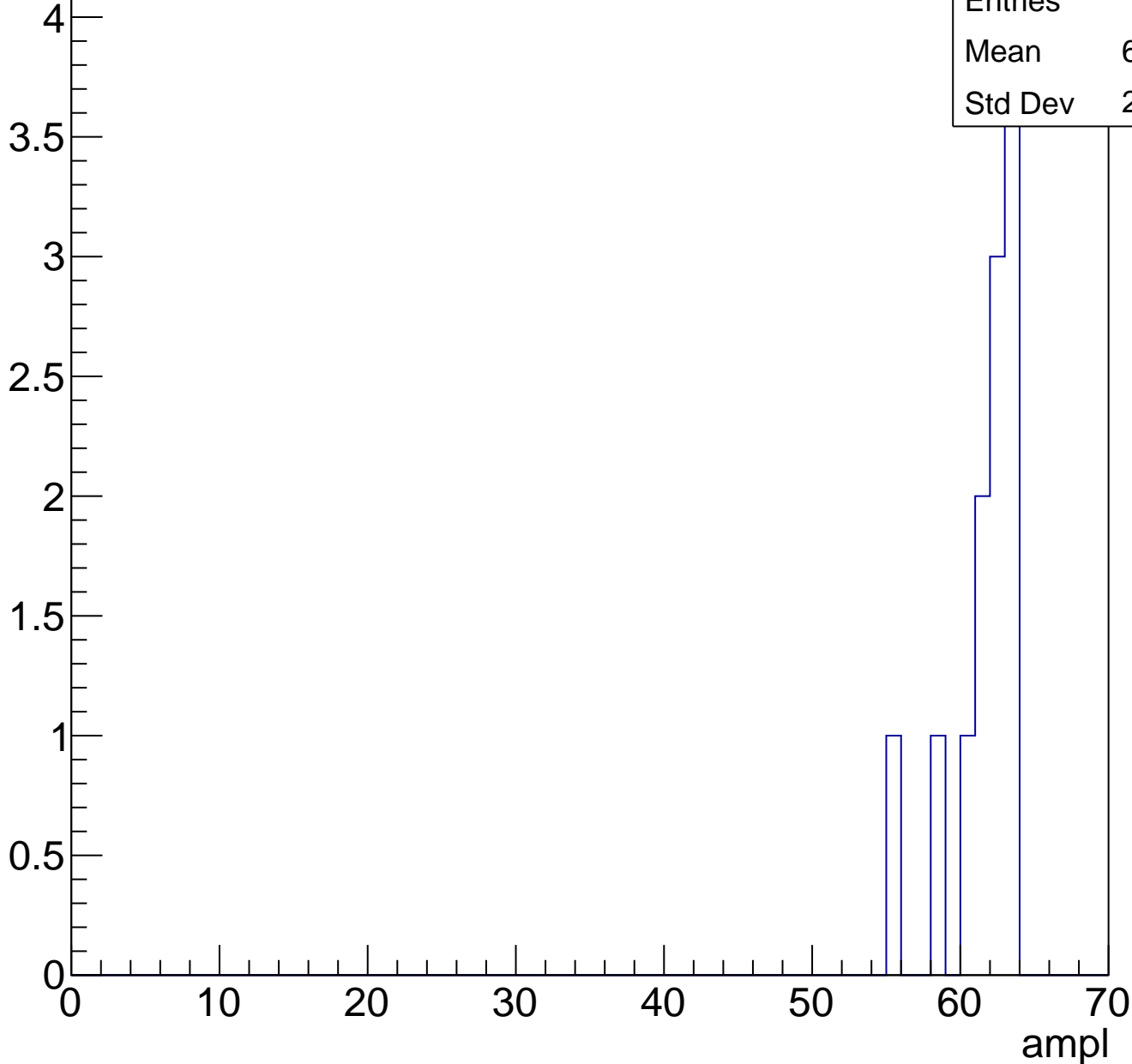
Entry



B1L101S, U15-ch64, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch64, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch65, adc0

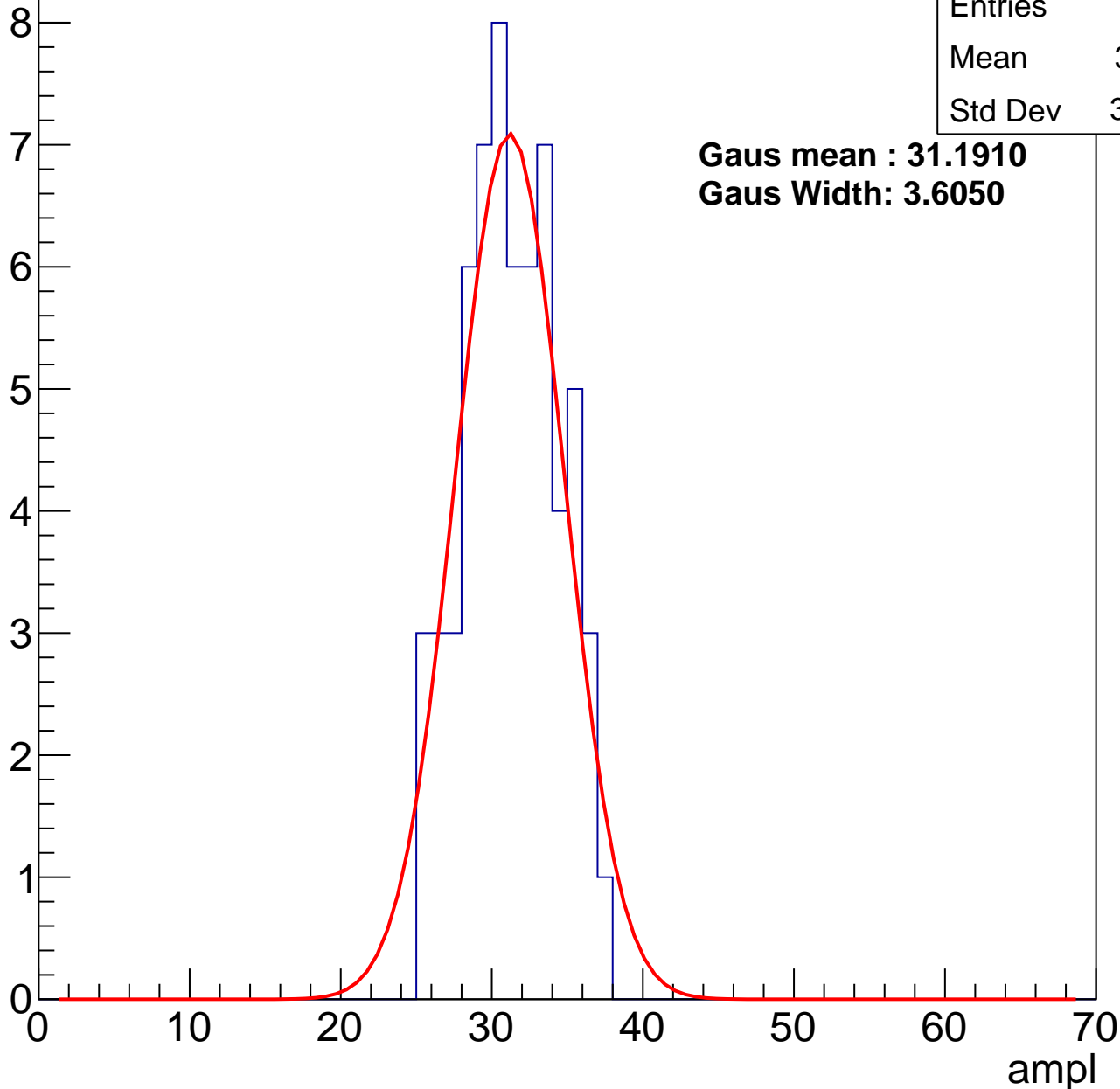
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	30.81
Std Dev	3.058

Gaus mean : 31.1910

Gaus Width: 3.6050



B1L101S, U15-ch65, adc1

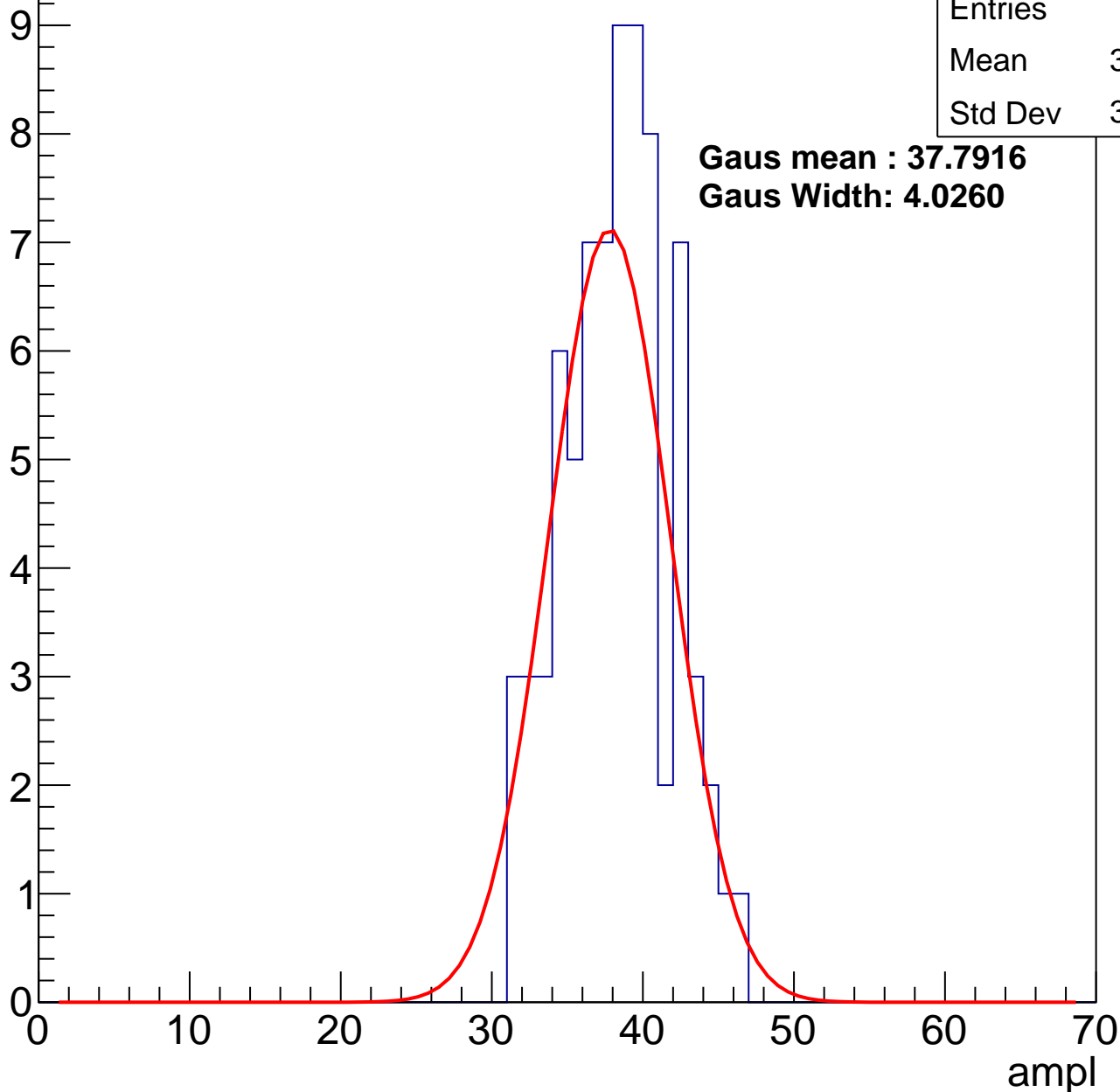
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.83
Std Dev	3.507

Gaus mean : 37.7916

Gaus Width: 4.0260



B1L101S, U15-ch65, adc2

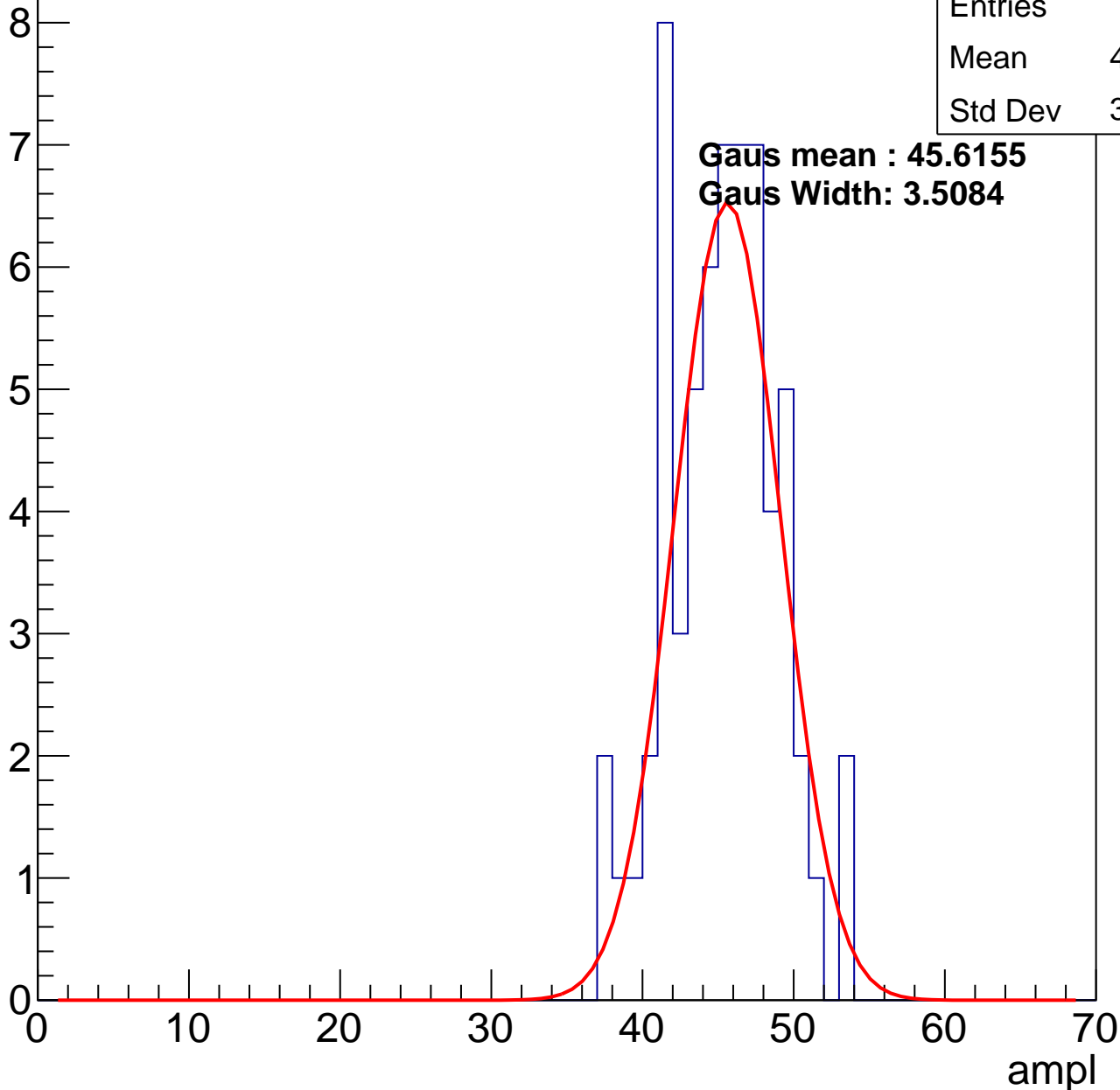
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.83
Std Dev	3.579

Gaus mean : 45.6155

Gaus Width: 3.5084

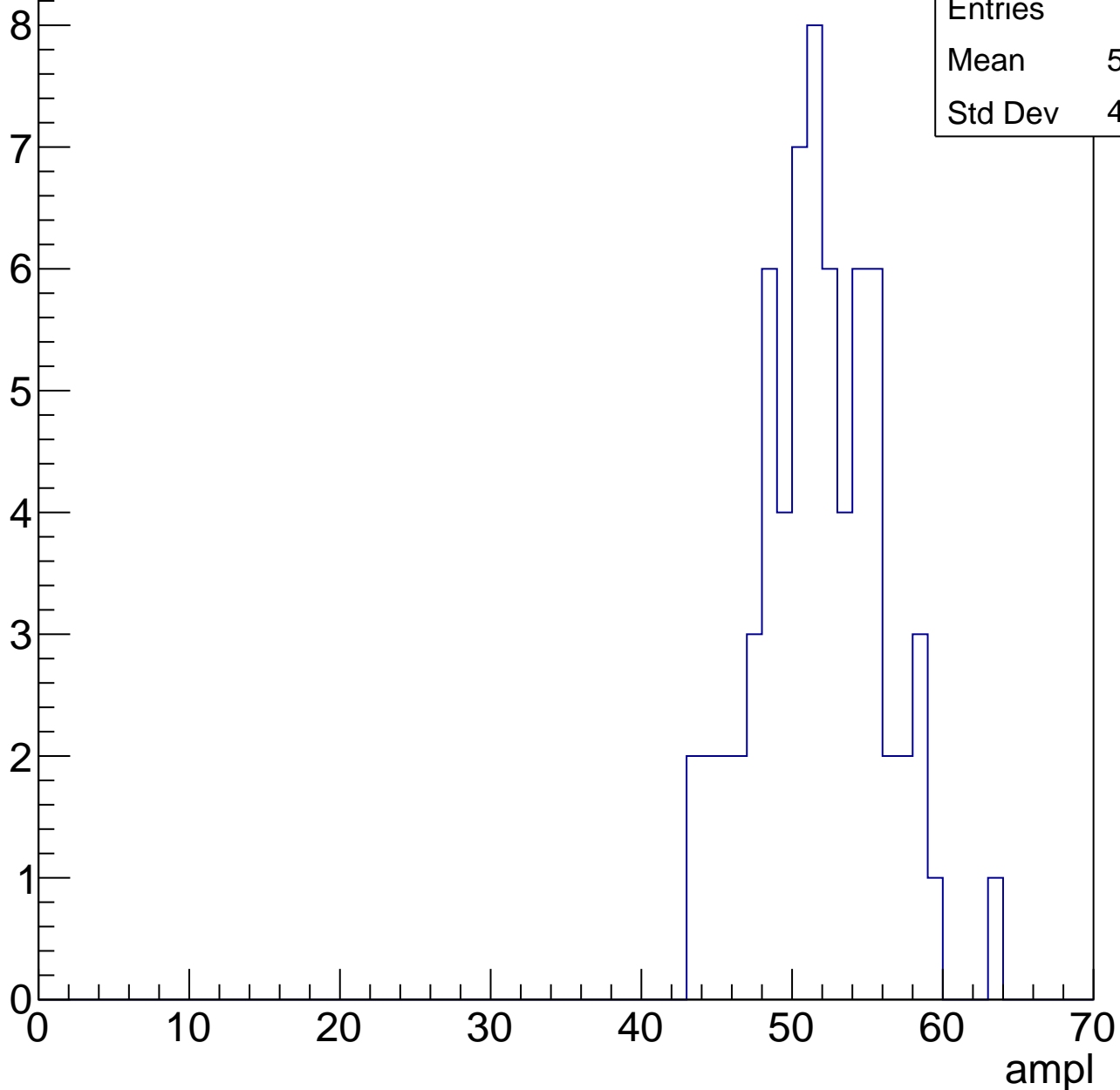


B1L101S, U15-ch65, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.33
Std Dev	4.079

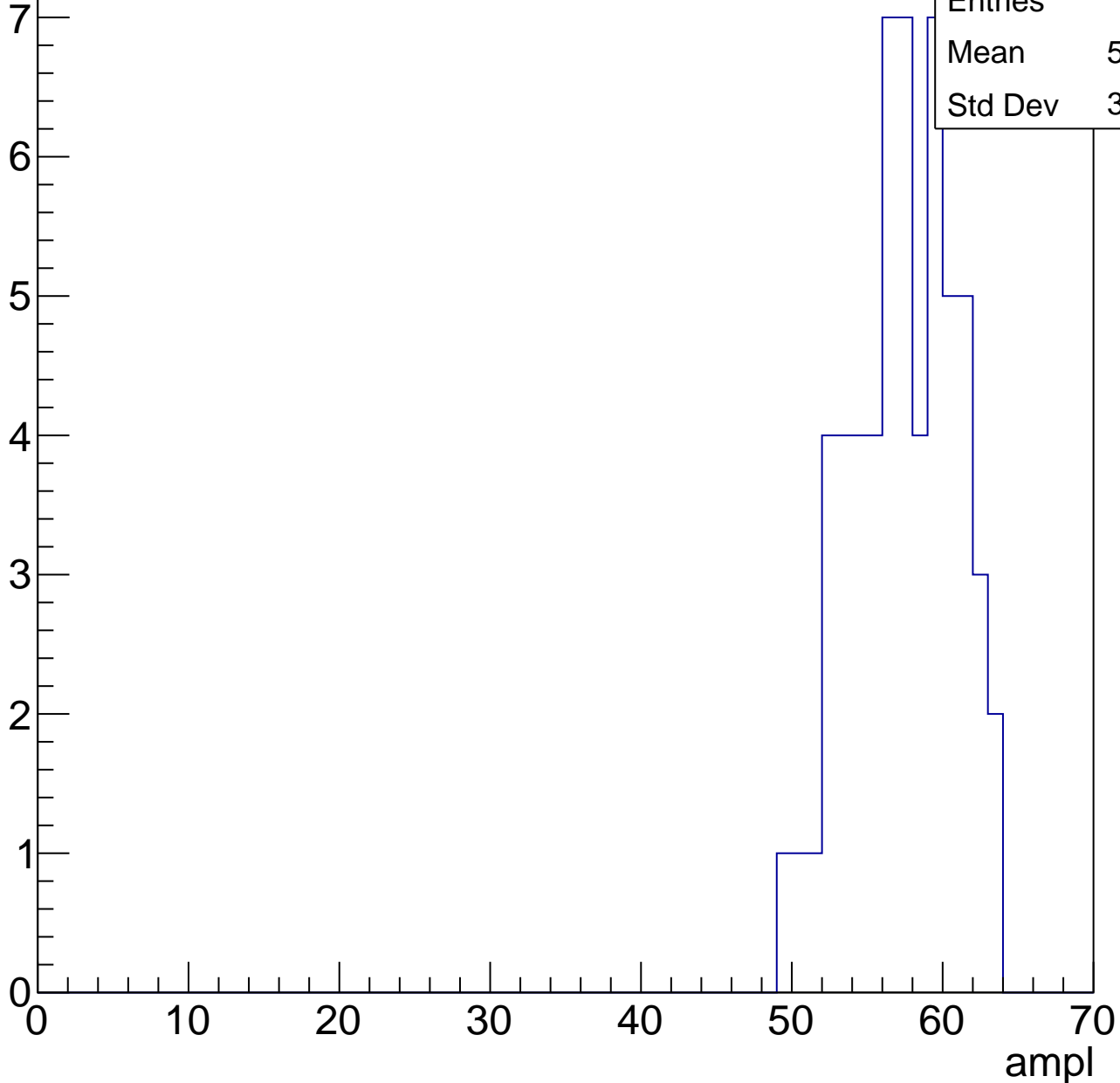


B1L101S, U15-ch65, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	56.93
Std Dev	3.384

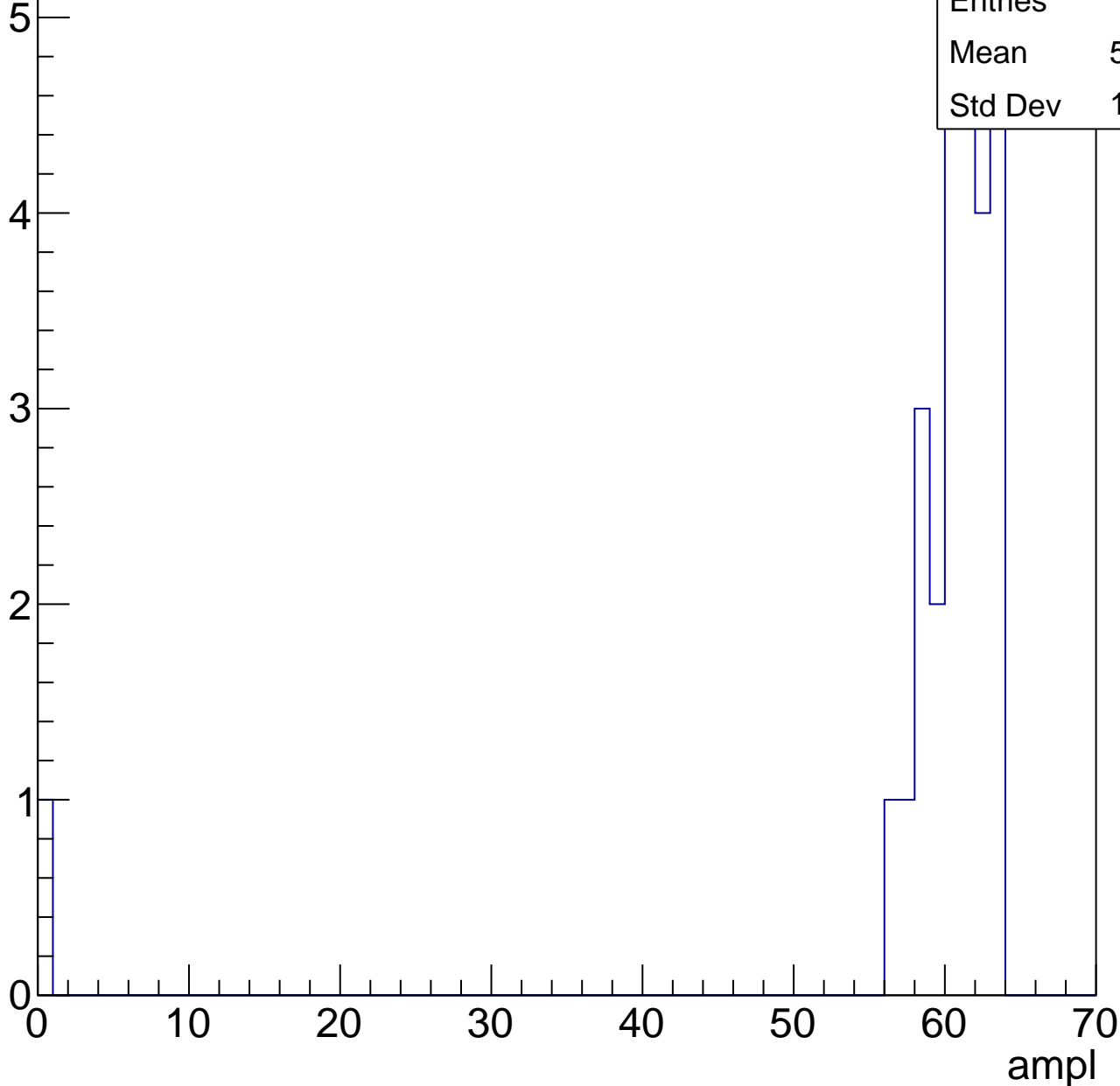


B1L101S, U15-ch65, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	27
Mean	58.26
Std Dev	11.58



B1L101S, U15-ch65, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch65, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch66, adc0

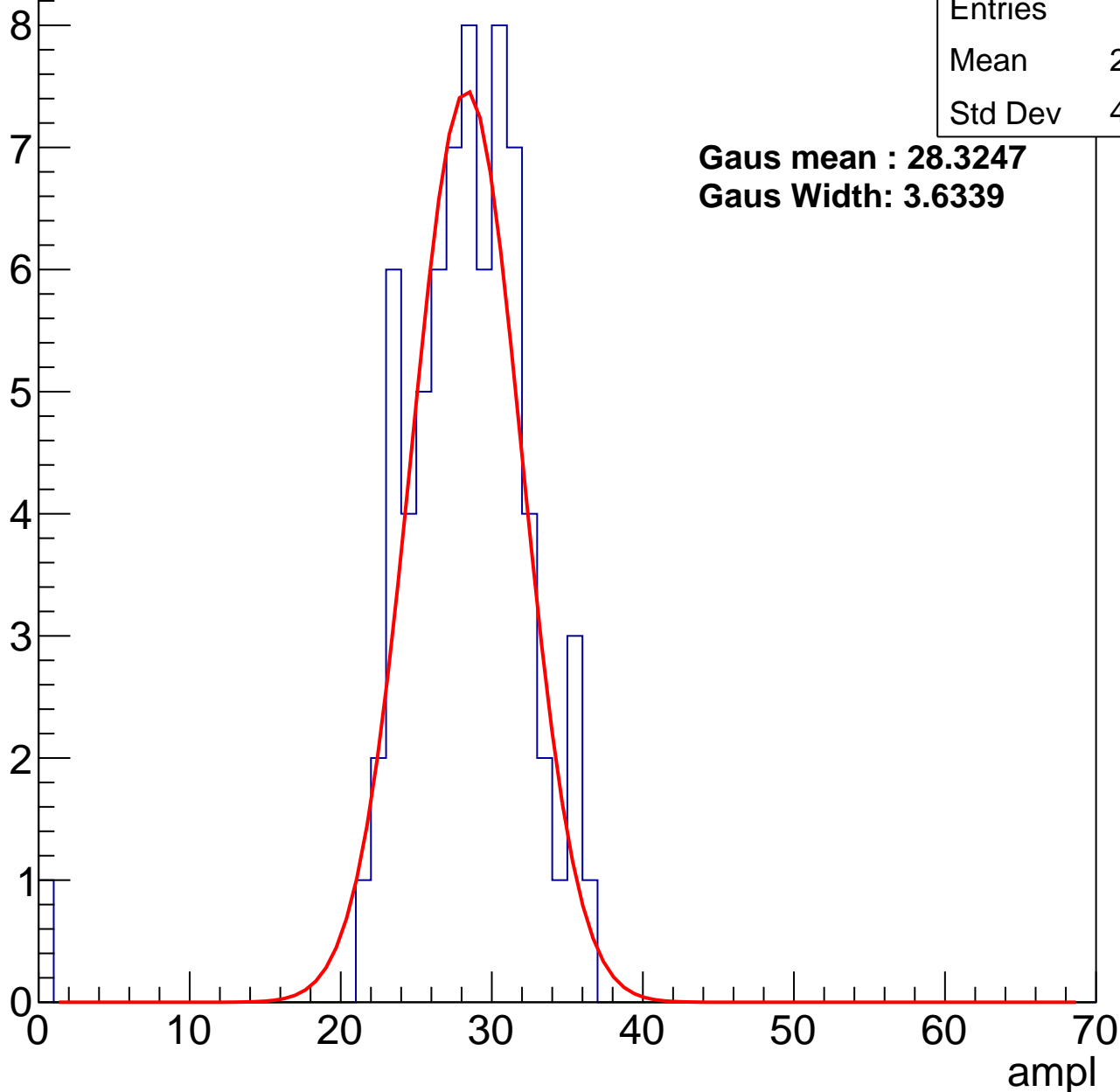
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	27.68
Std Dev	4.787

Gaus mean : 28.3247

Gaus Width: 3.6339



B1L101S, U15-ch66, adc1

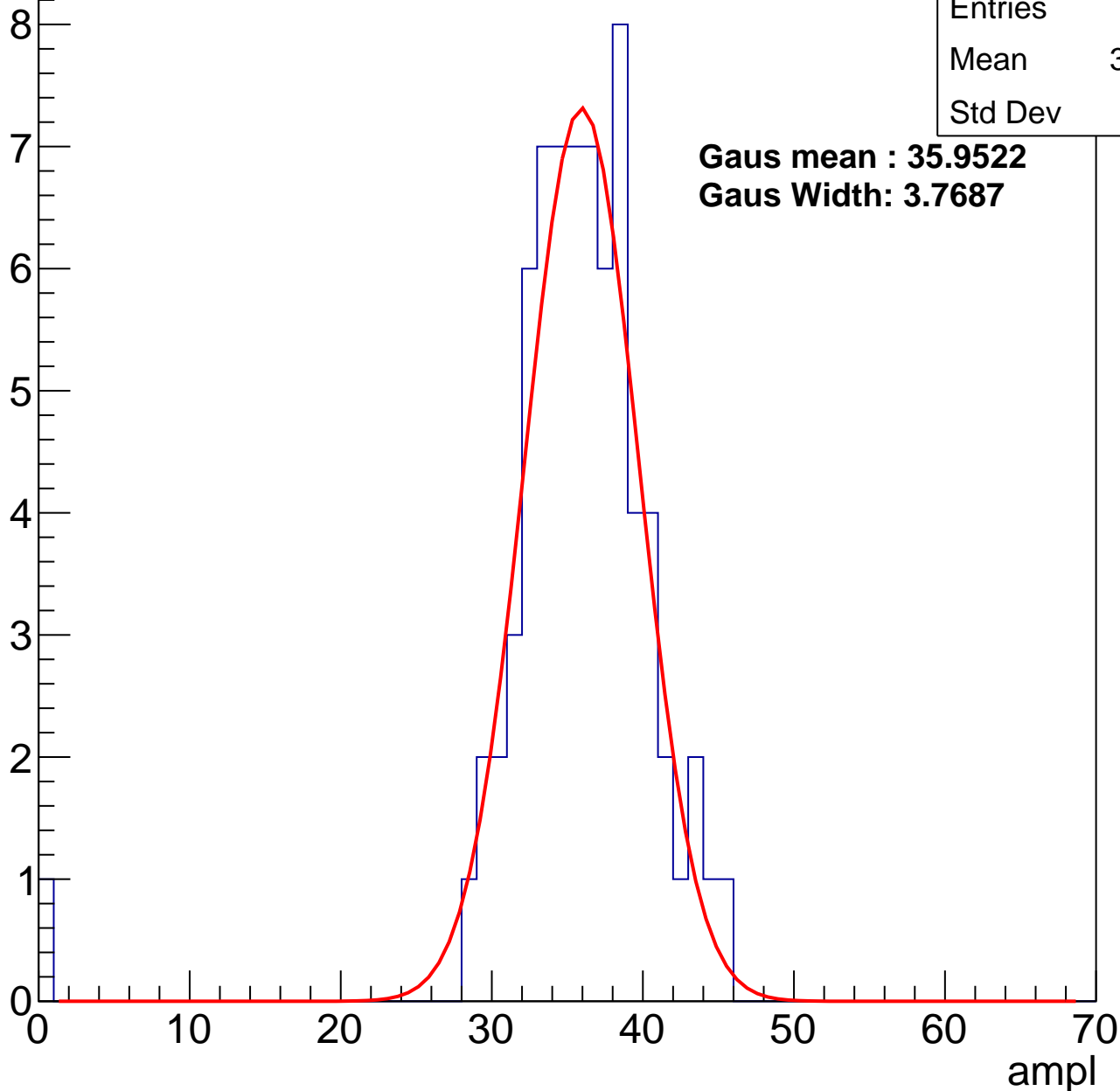
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	35.25
Std Dev	5.56

Gaus mean : 35.9522

Gaus Width: 3.7687



B1L101S, U15-ch66, adc2

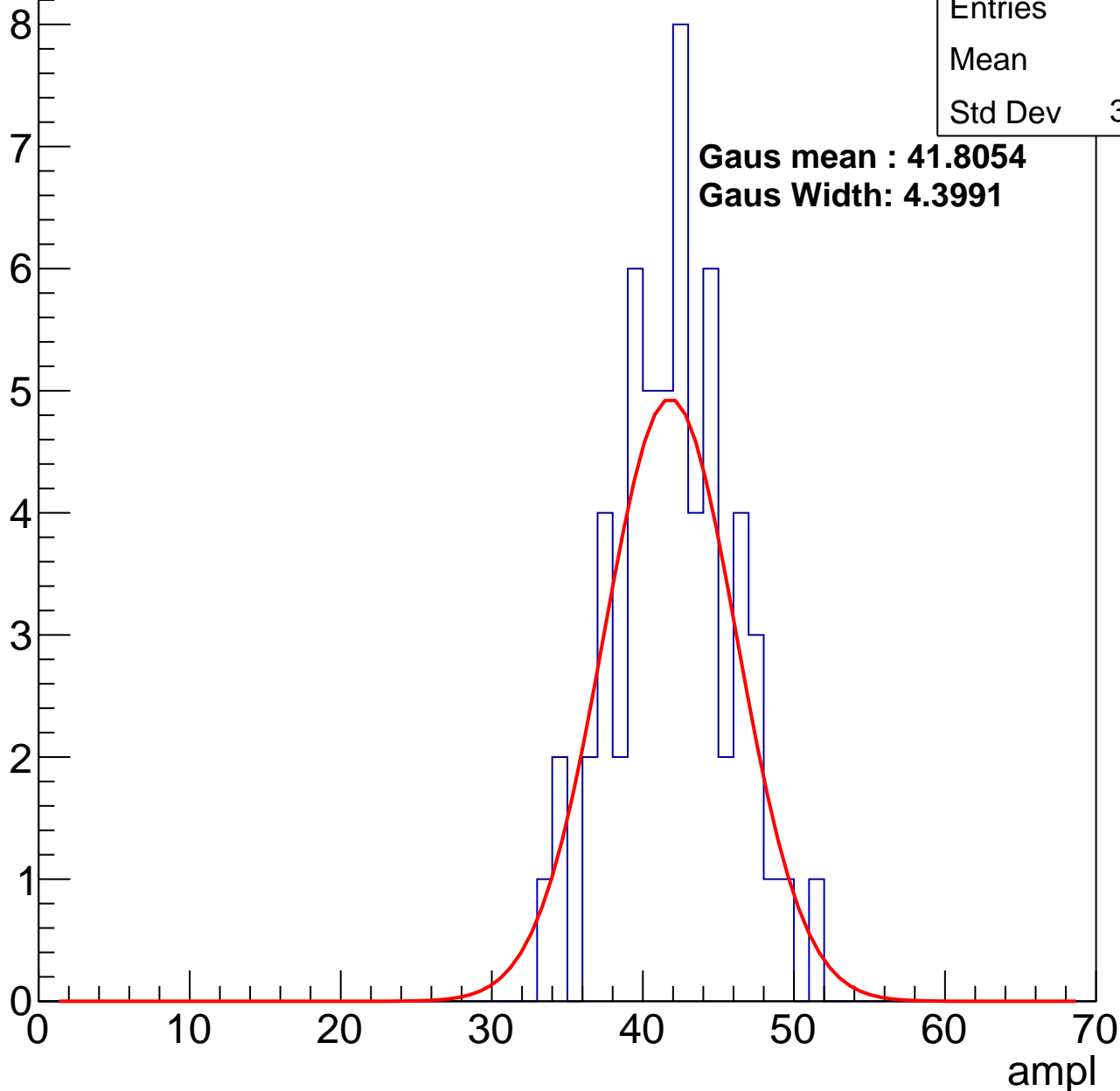
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.6
Std Dev	3.824

Gaus mean : 41.8054

Gaus Width: 4.3991

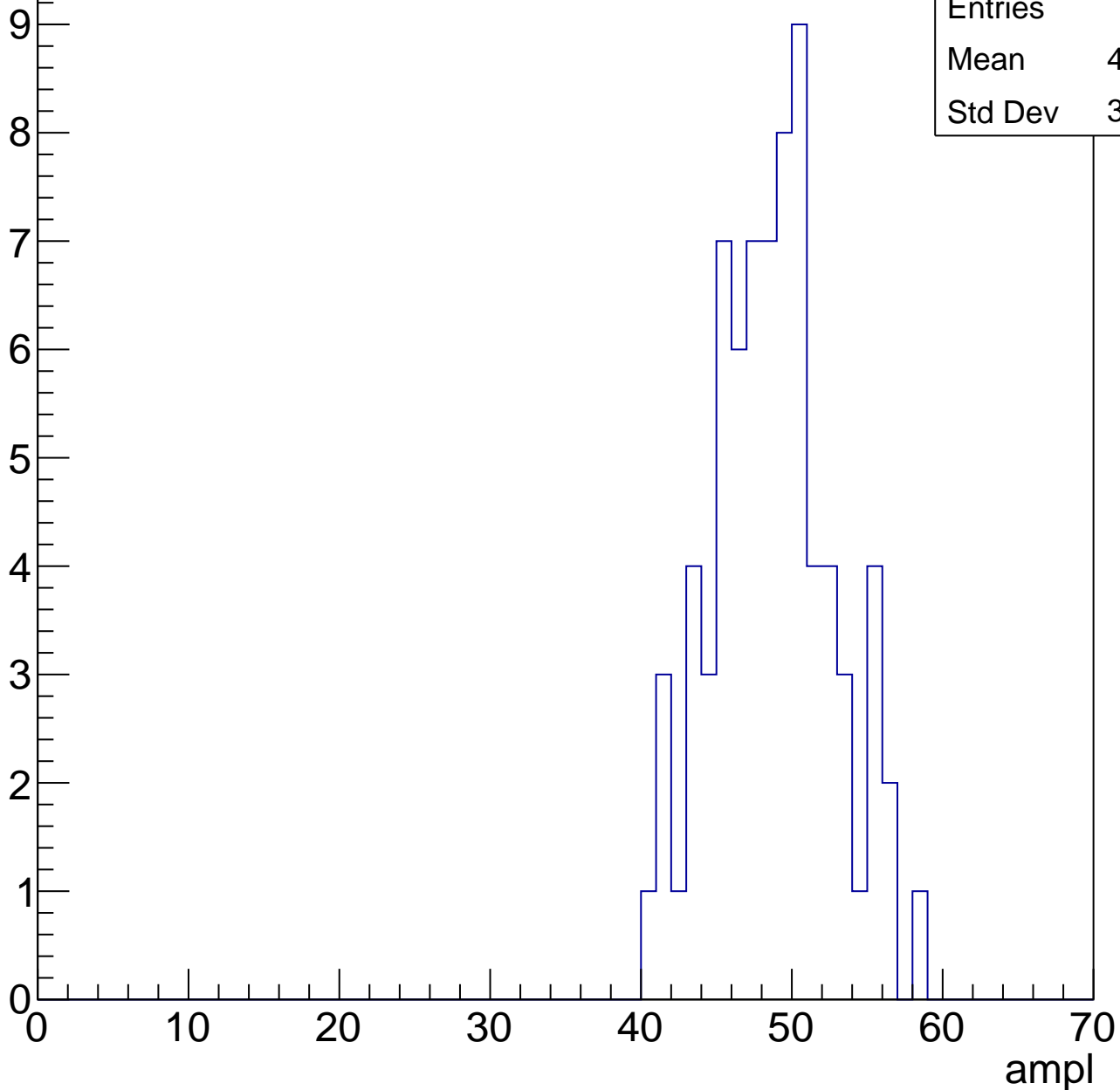


B1L101S, U15-ch66, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	48.29
Std Dev	3.935

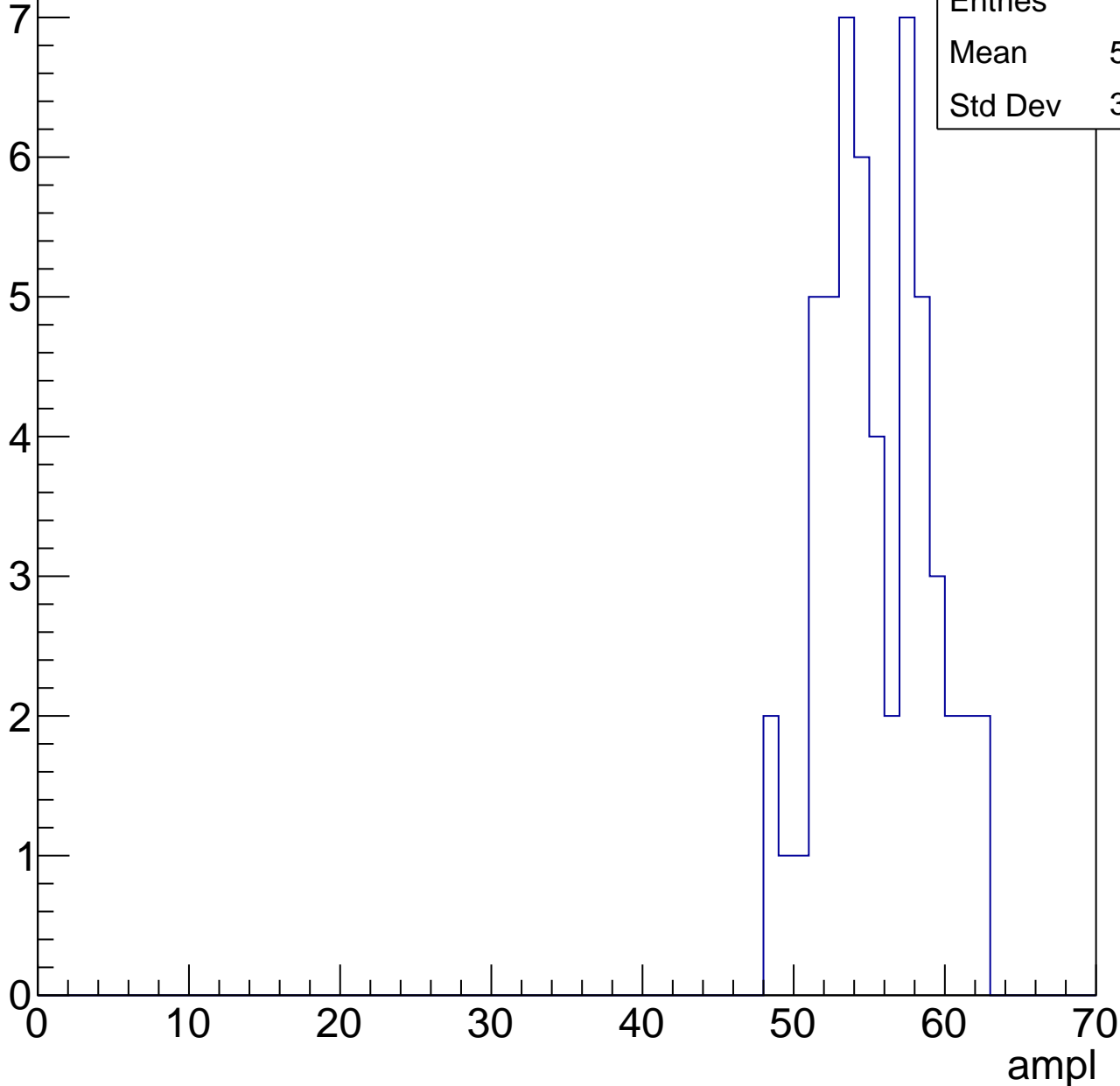


B1L101S, U15-ch66, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

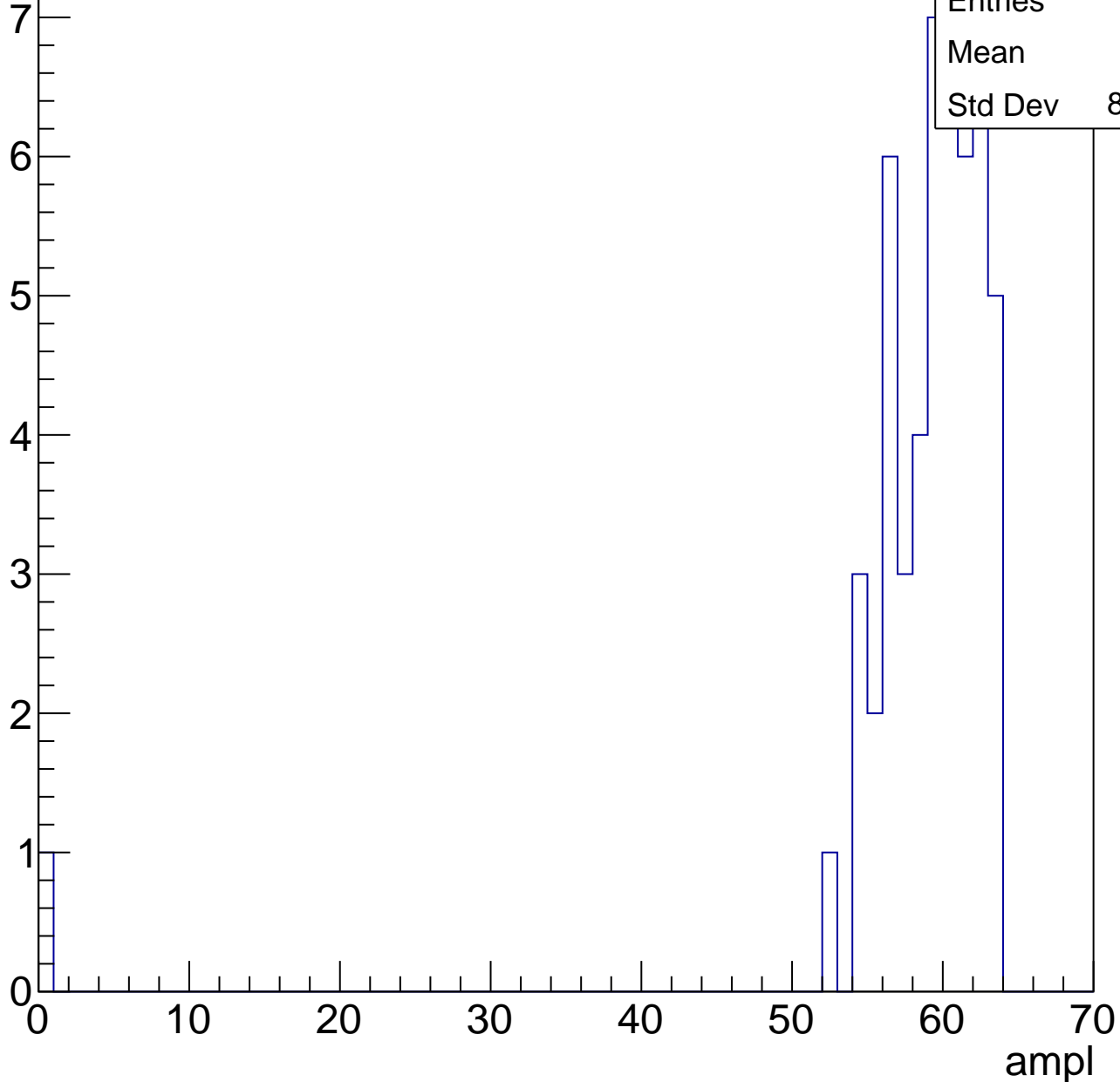
Entries	54
Mean	54.98
Std Dev	3.499



B1L101S, U15-ch66, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

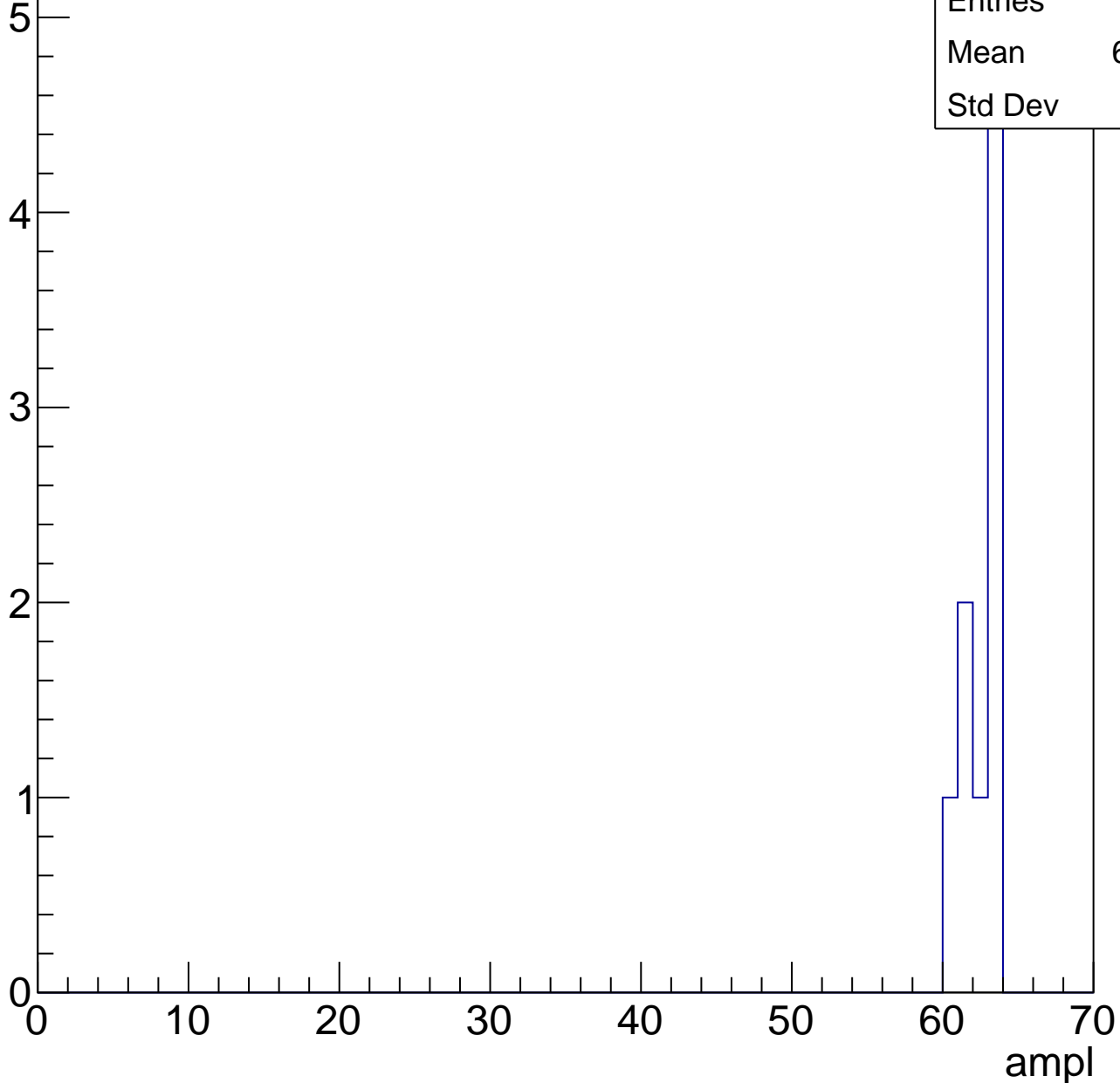


B1L101S, U15-ch66, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	62.11
Std Dev	1.1



B1L101S, U15-ch66, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch67, adc0

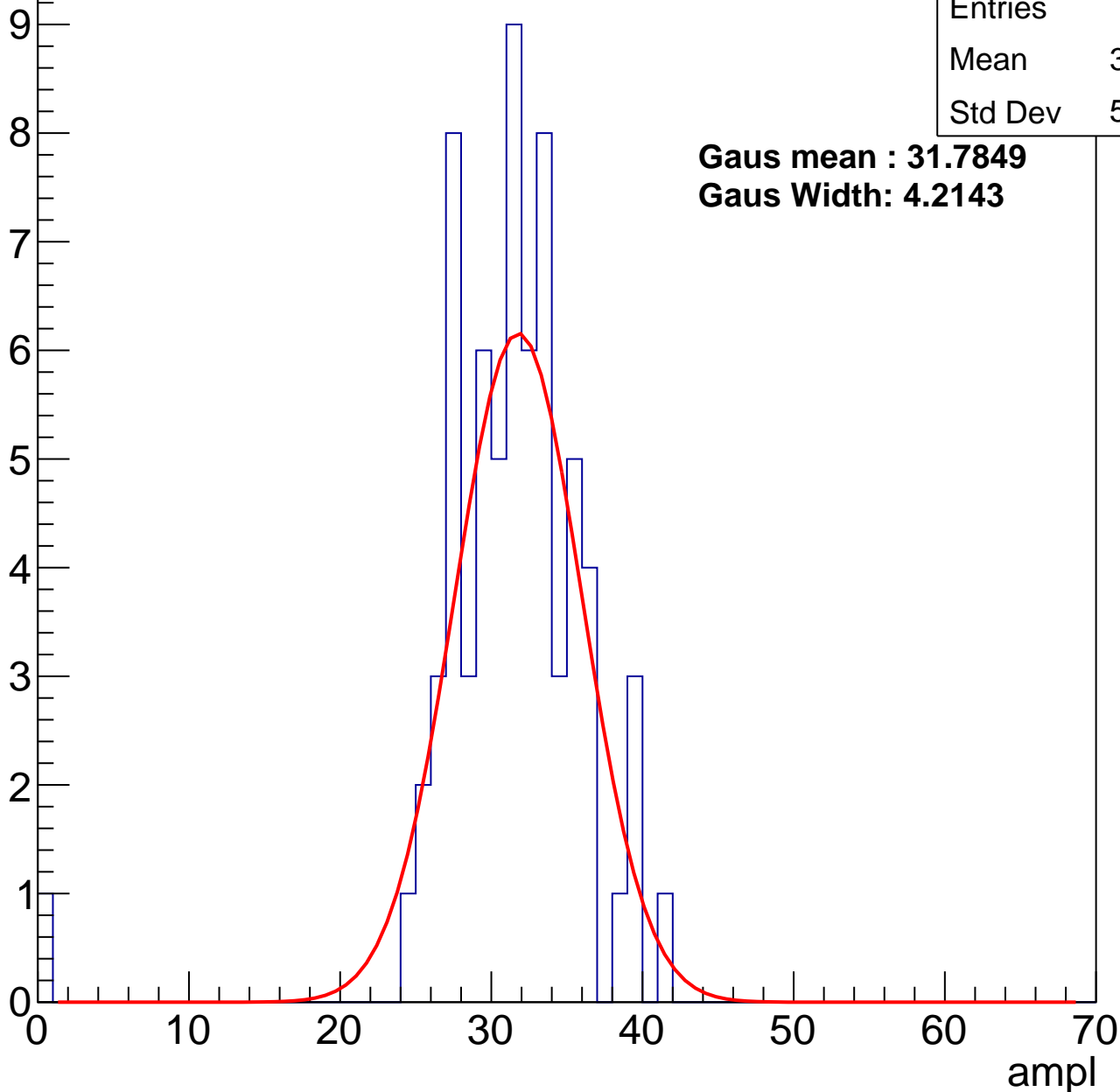
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	30.84
Std Dev	5.282

Gaus mean : 31.7849

Gaus Width: 4.2143



B1L101S, U15-ch67, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	37.92
Std Dev	3.677

Gaus mean : 38.5520

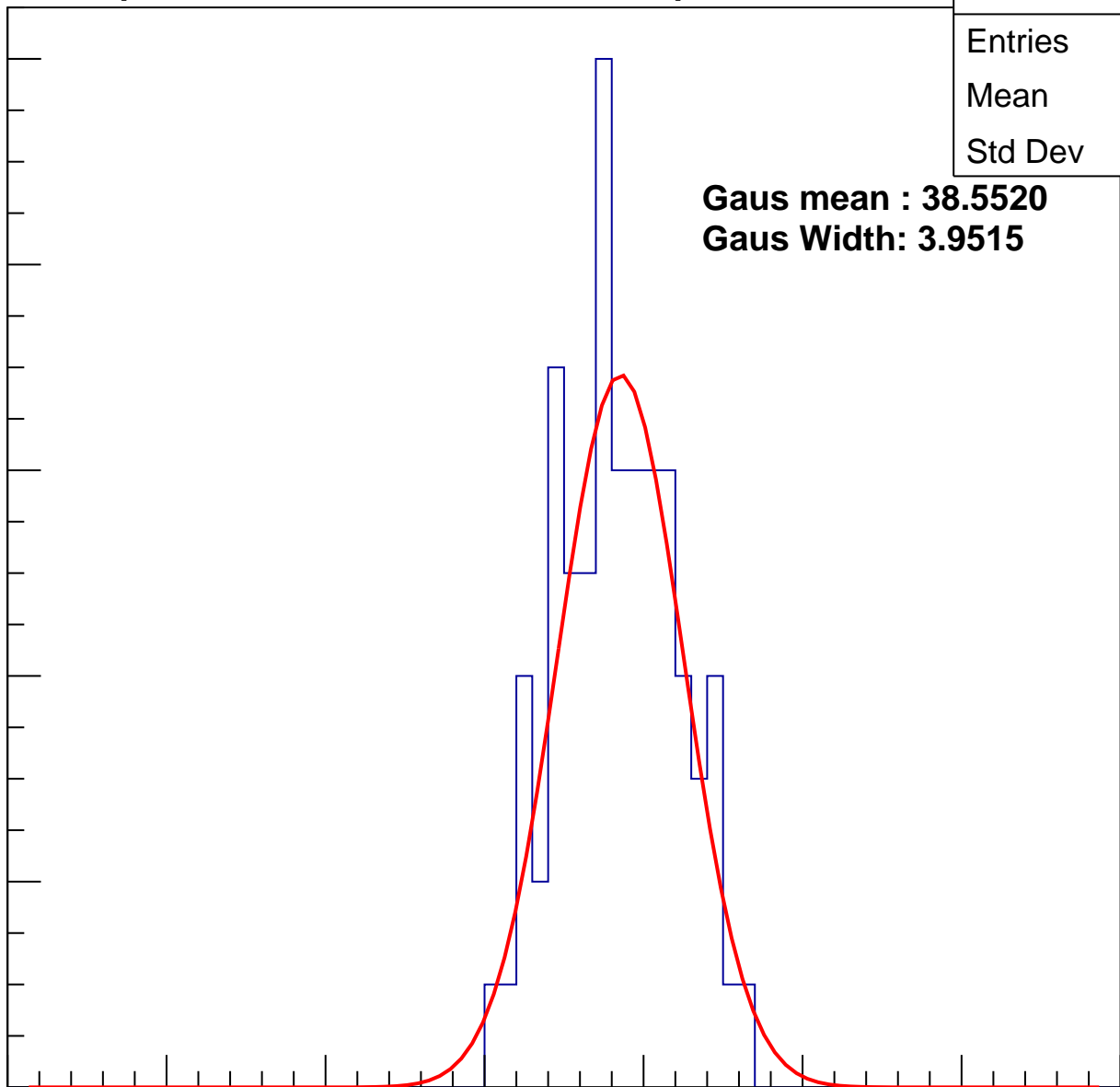
Gaus Width: 3.9515

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch67, adc2

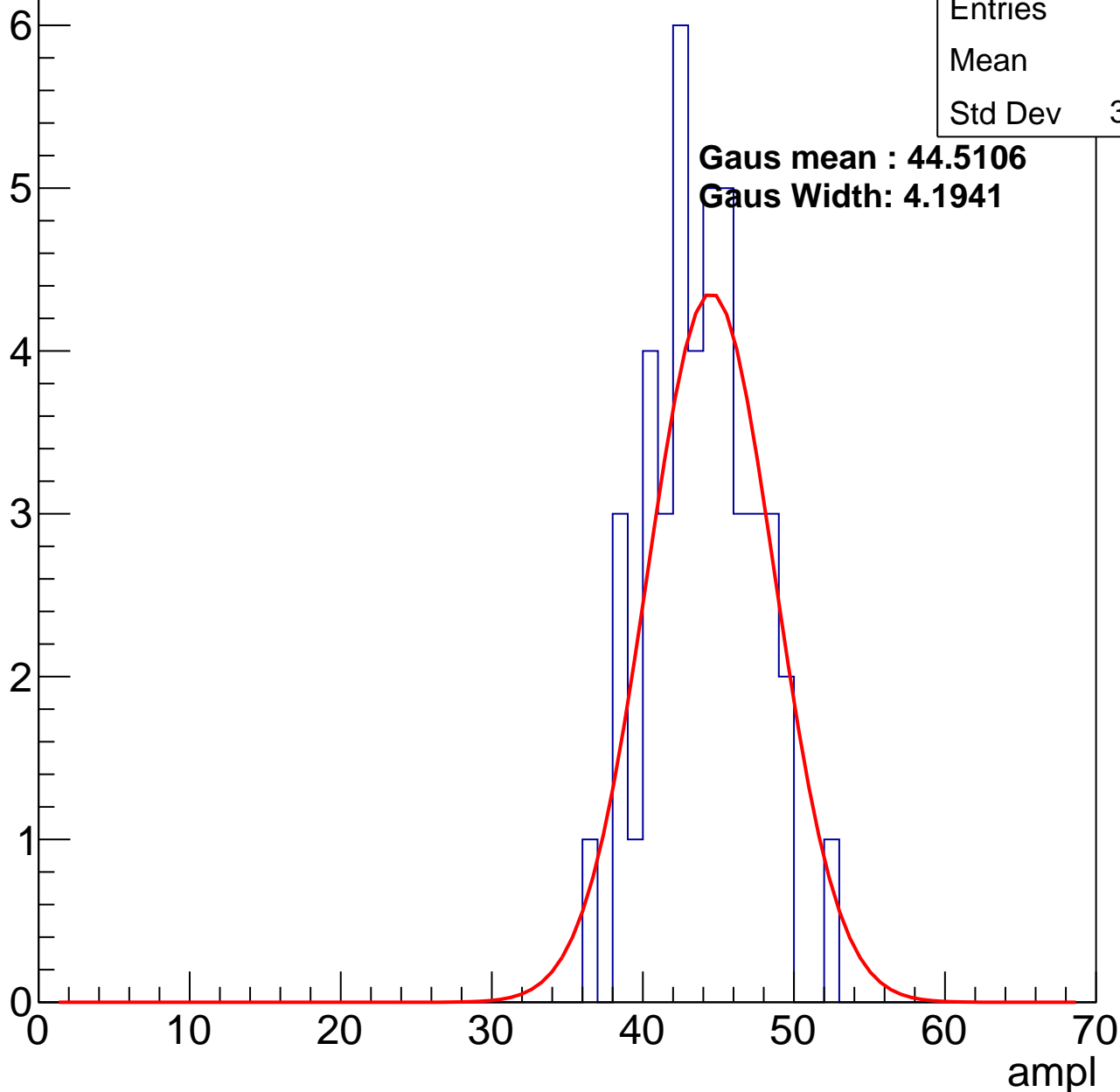
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	43.5
Std Dev	3.408

Gaus mean : 44.5106

Gaus Width: 4.1941

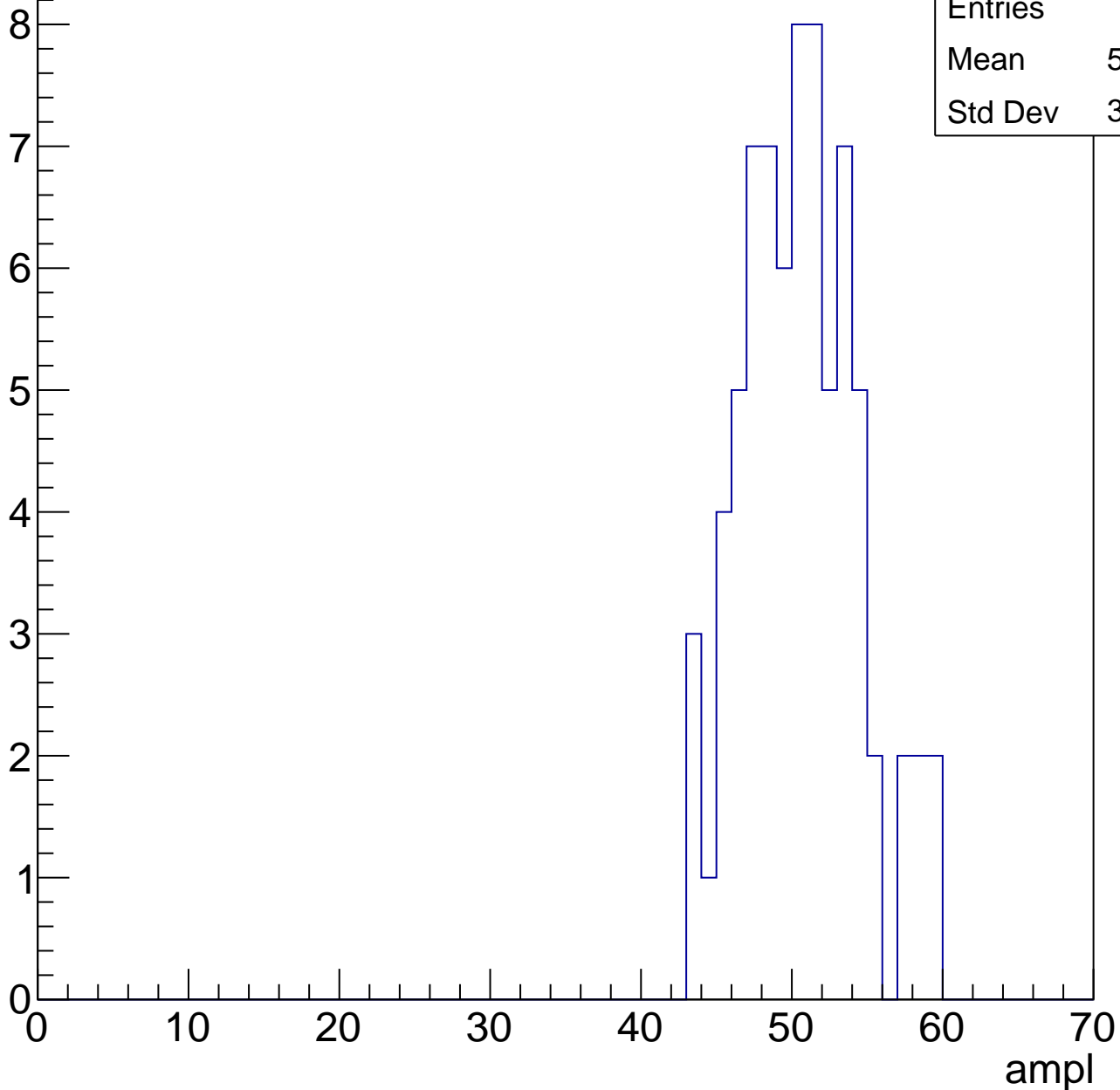


B1L101S, U15-ch67, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

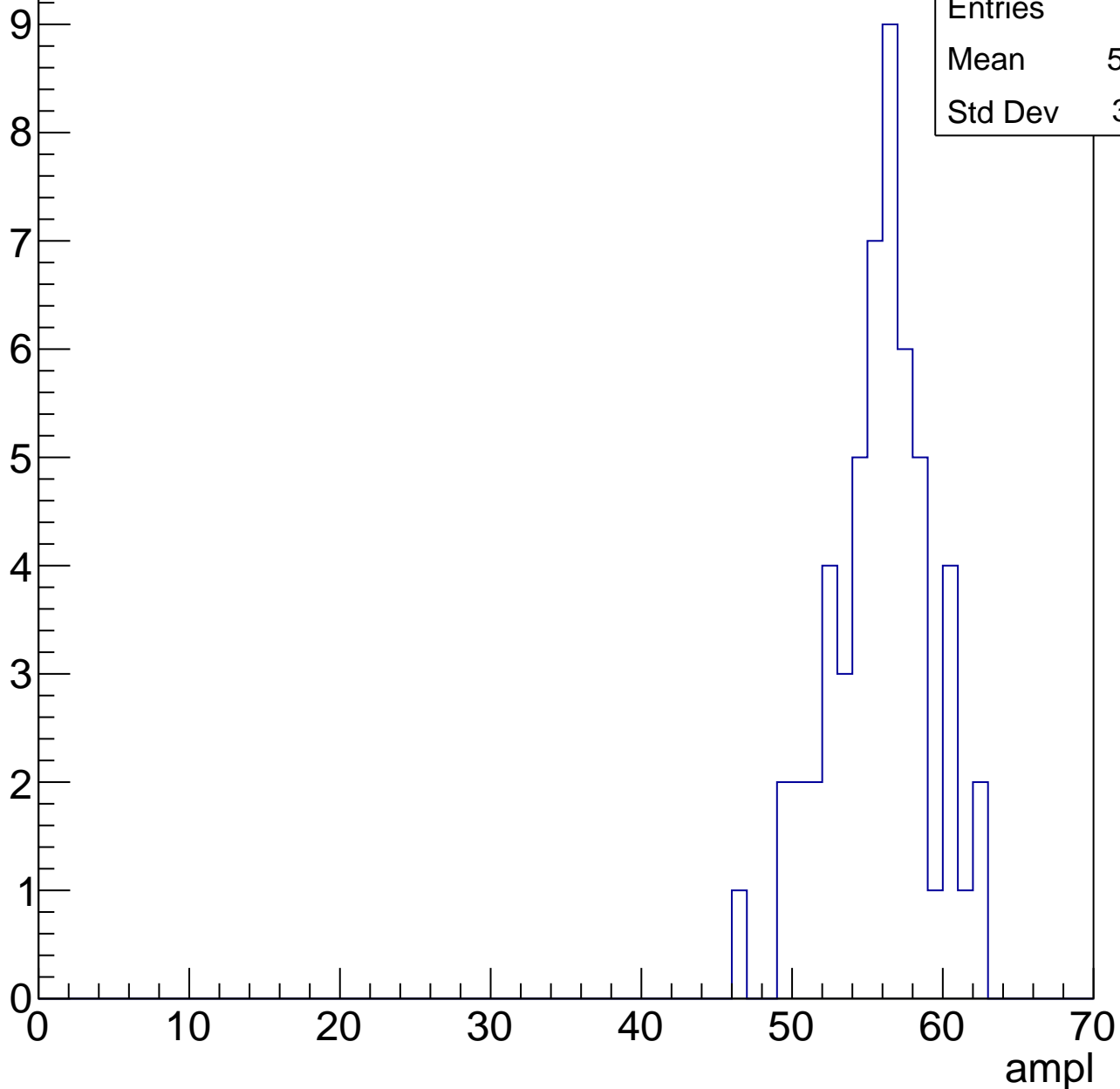
Entries	74
Mean	50.12
Std Dev	3.799



B1L101S, U15-ch67, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

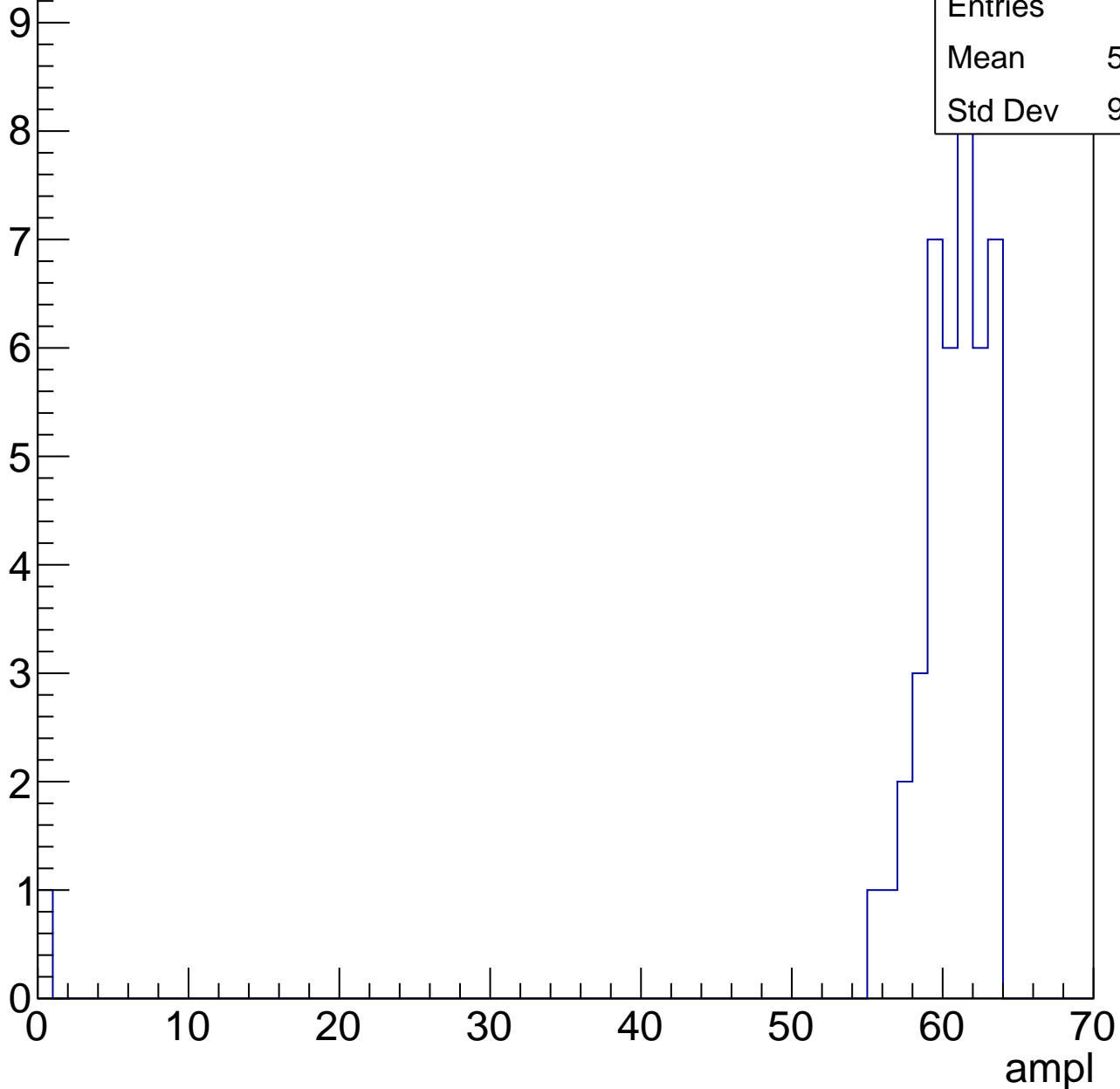


B1L101S, U15-ch67, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	58.93
Std Dev	9.307



B1L101S, U15-ch67, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch67, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L101S, U15-ch68, adc0

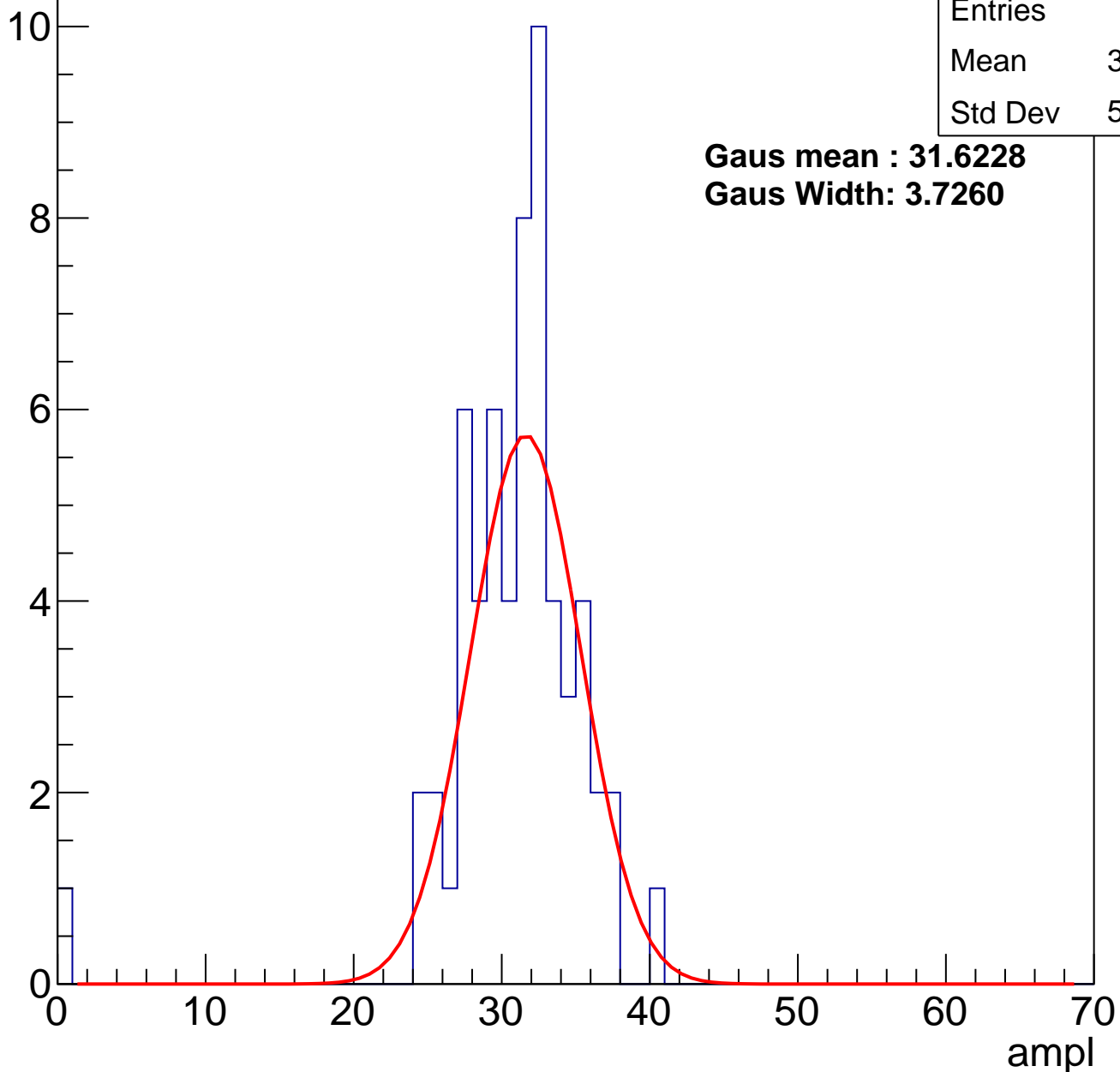
calib_packv5_042523_0143.root, FC#0, port D2

Entries	60
Mean	30.33
Std Dev	5.179

Gaus mean : 31.6228

Gaus Width: 3.7260

Entry



B1L101S, U15-ch68, adc1

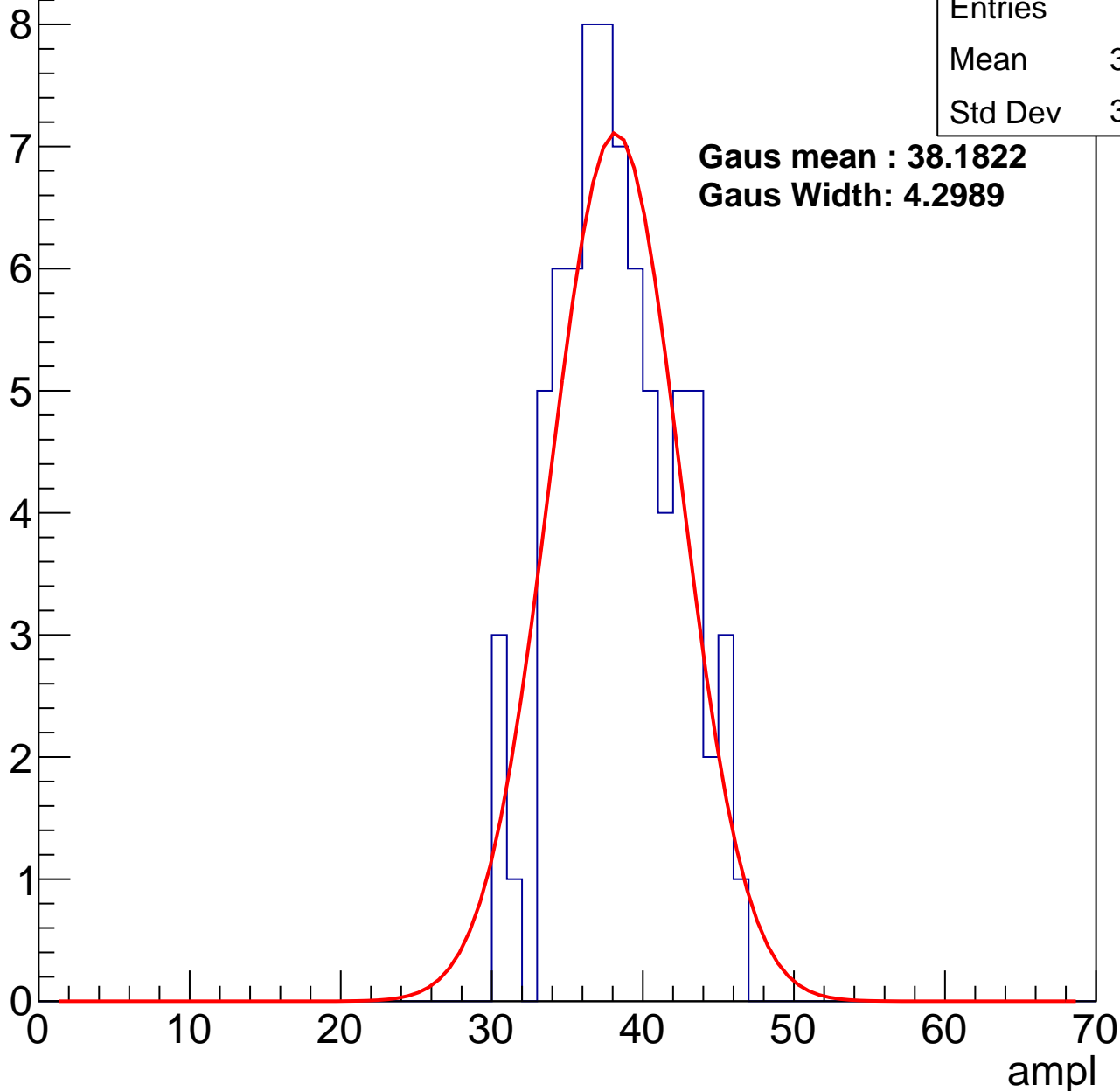
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.89
Std Dev	3.839

Gaus mean : 38.1822

Gaus Width: 4.2989



B1L101S, U15-ch68, adc2

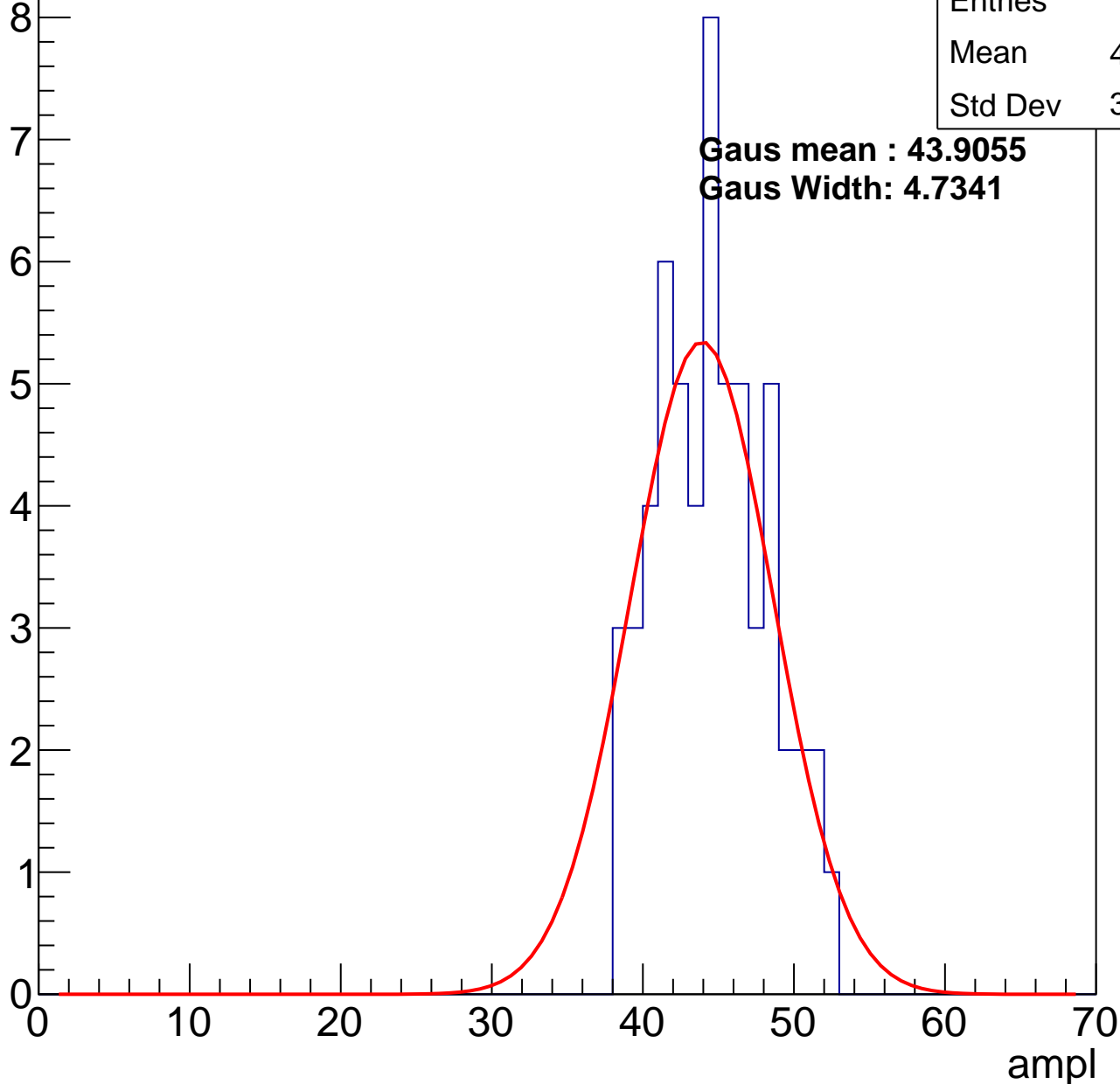
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	44.12
Std Dev	3.572

Gaus mean : 43.9055

Gaus Width: 4.7341

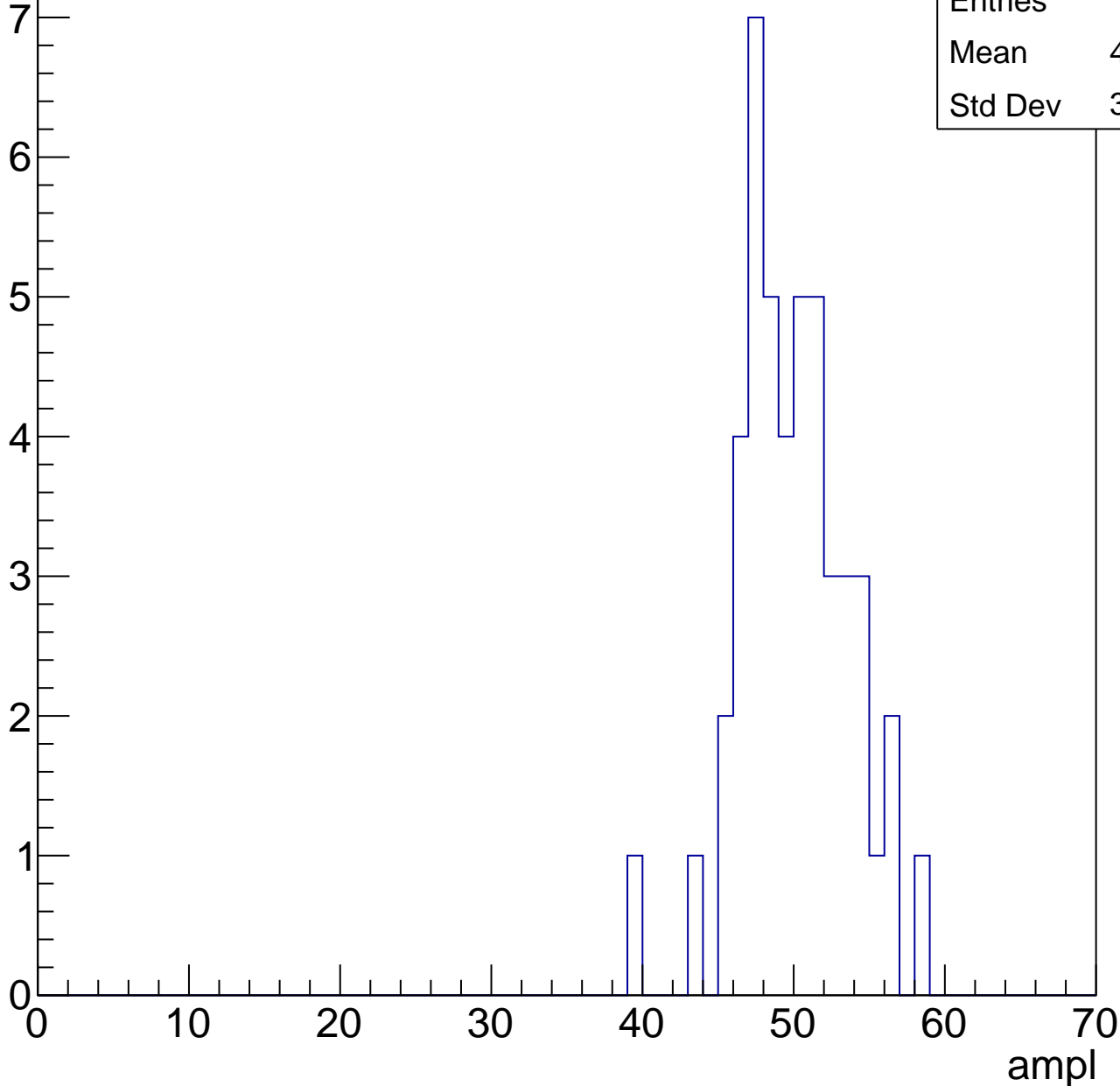


B1L101S, U15-ch68, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	49.53
Std Dev	3.614

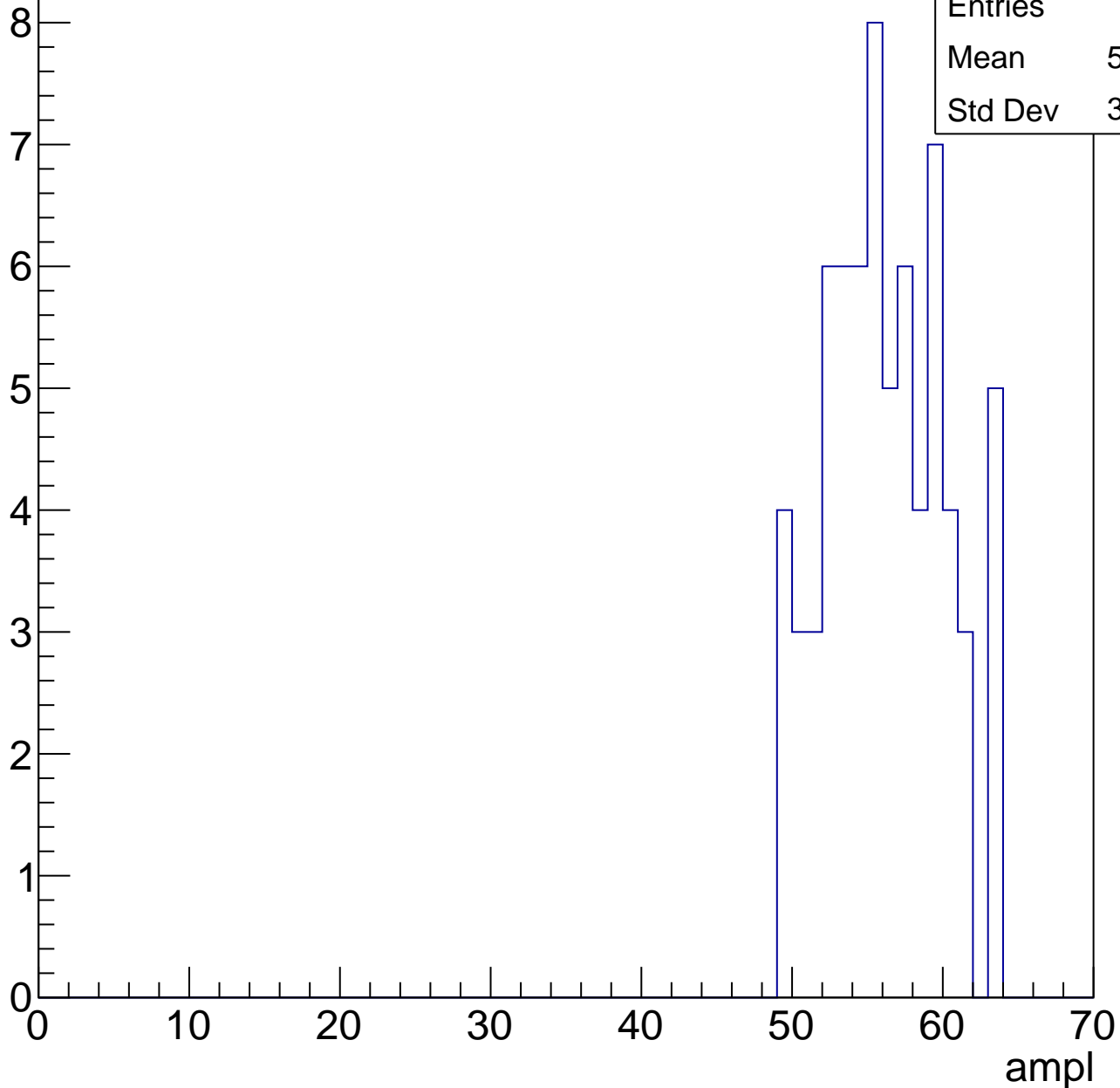


B1L101S, U15-ch68, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	55.69
Std Dev	3.823

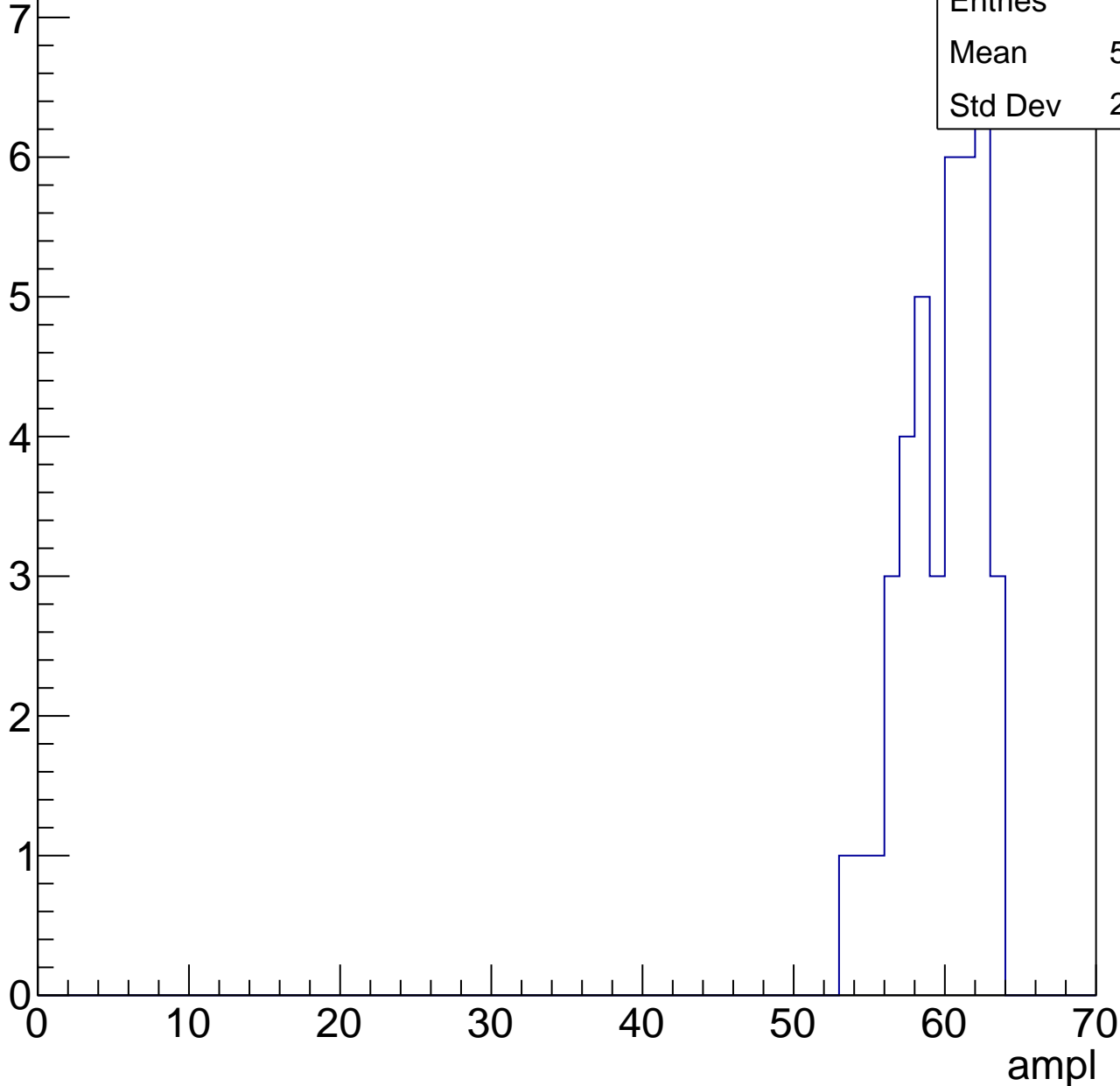


B1L101S, U15-ch68, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

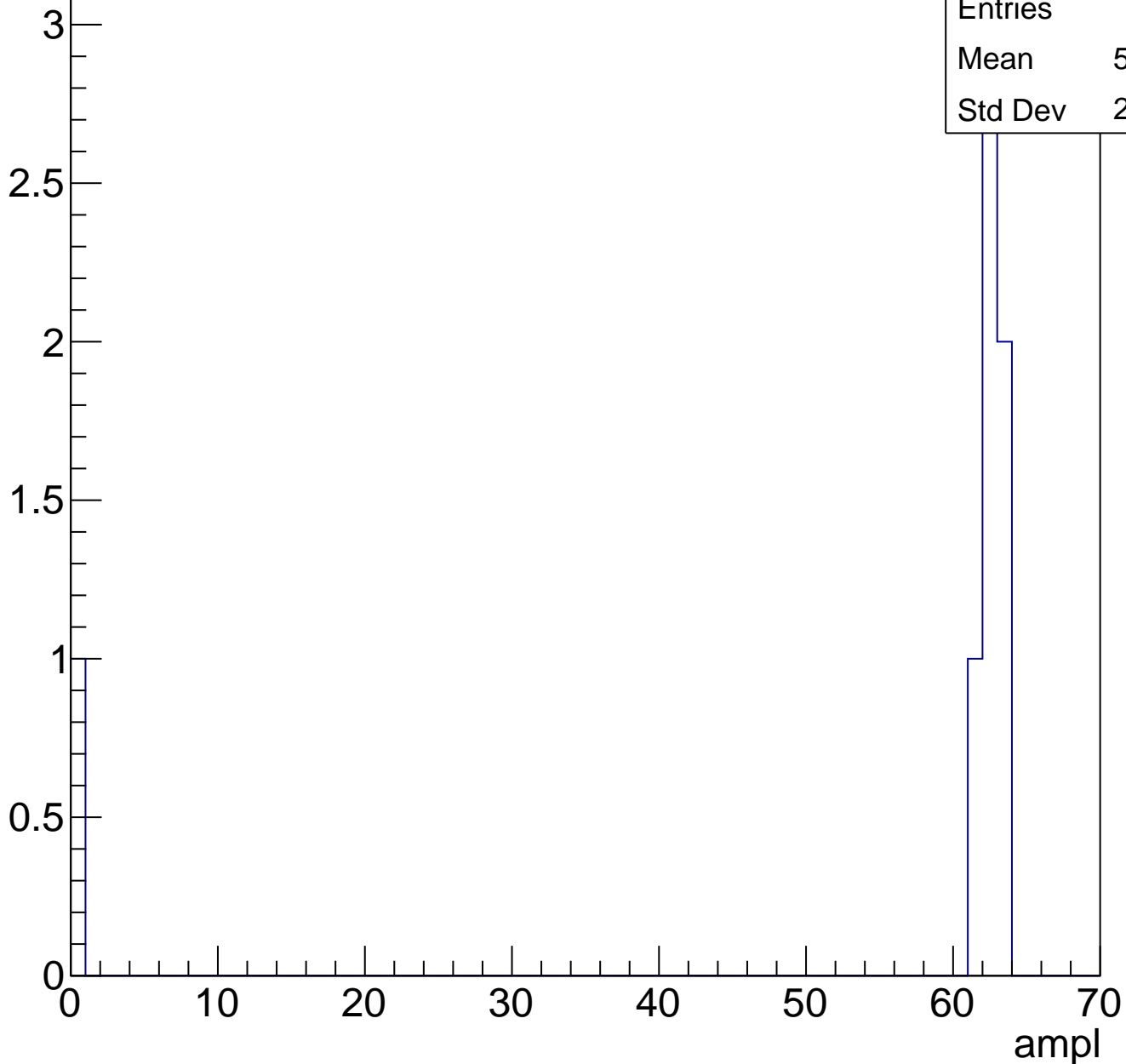
Entries	40
Mean	59.35
Std Dev	2.555



B1L101S, U15-ch68, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch68, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch69, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	72
Mean	29.44
Std Dev	5.866

Gaus mean : 31.1665

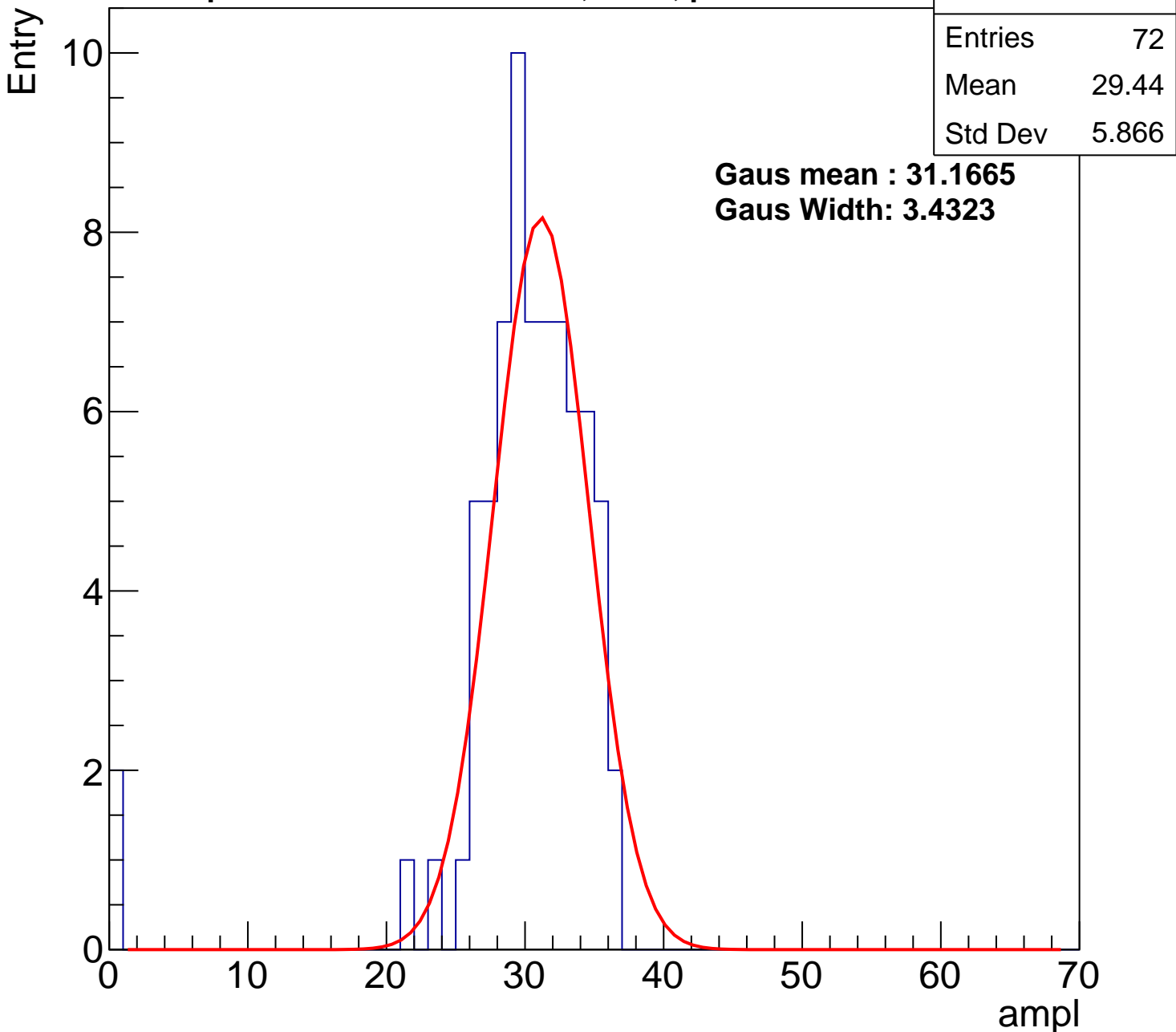
Gaus Width: 3.4323

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch69, adc1

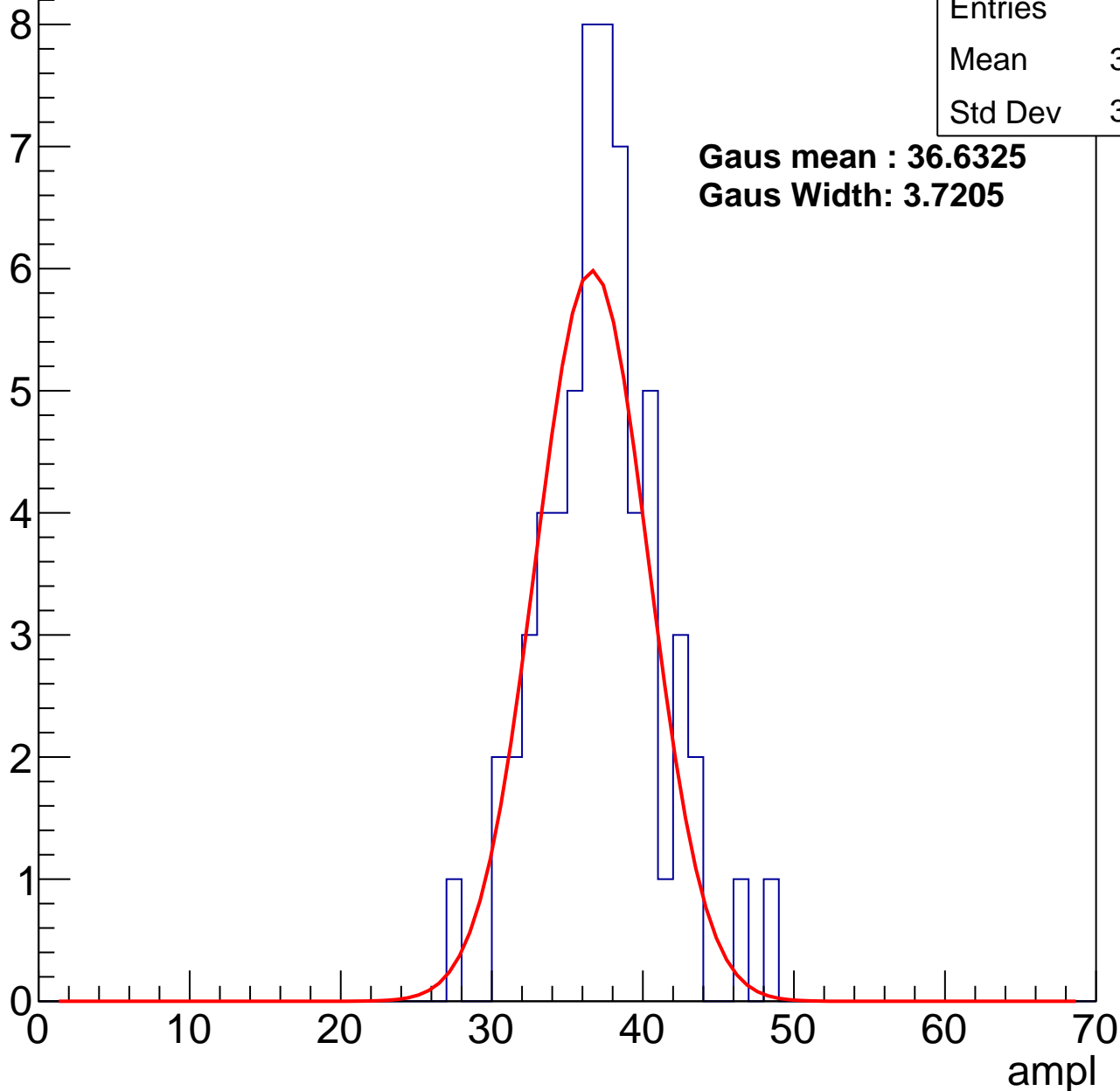
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	36.74
Std Dev	3.845

Gaus mean : 36.6325

Gaus Width: 3.7205



B1L101S, U15-ch69, adc2

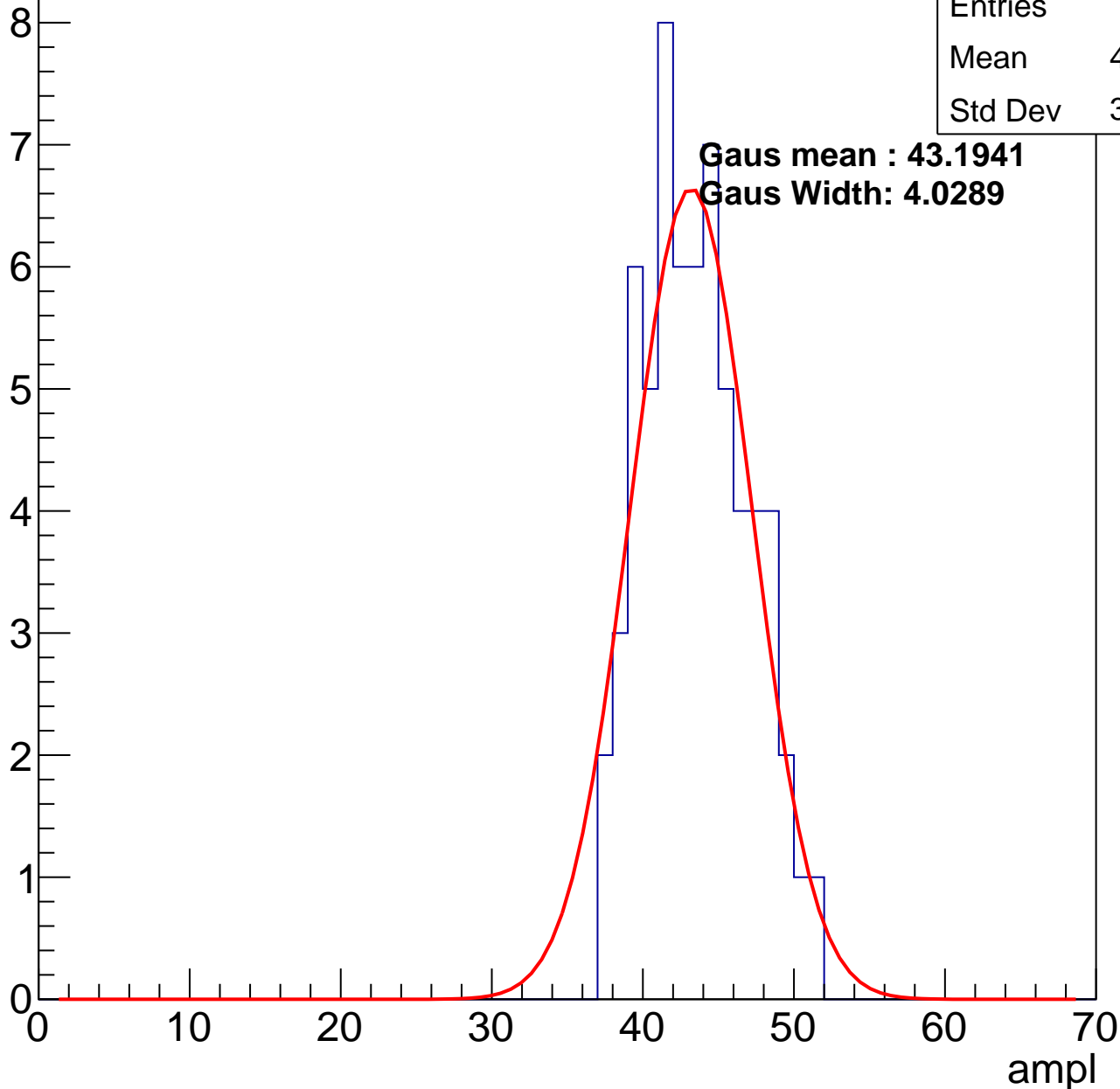
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.06
Std Dev	3.395

Gaus mean : 43.1941

Gaus Width: 4.0289

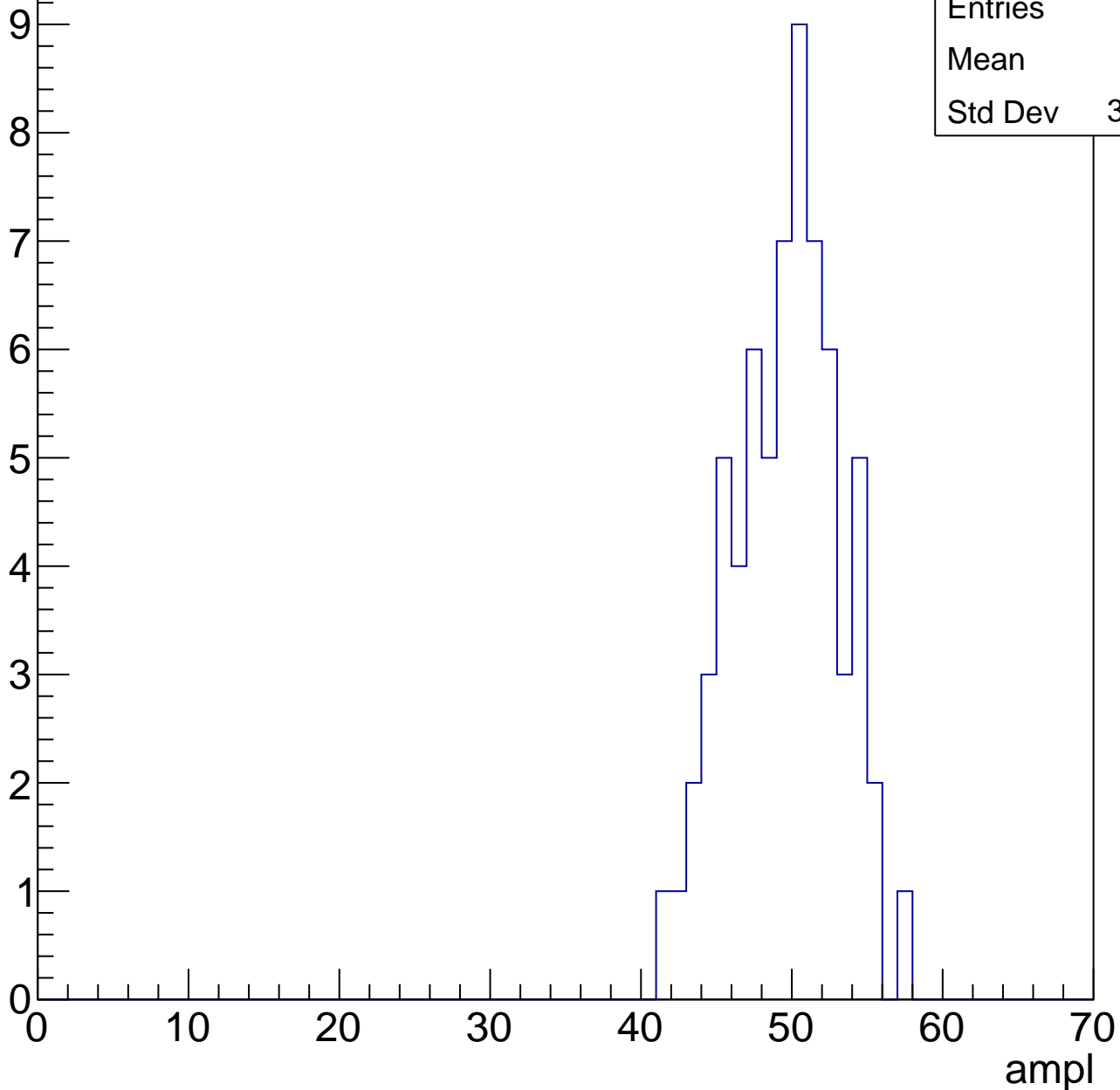


B1L101S, U15-ch69, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.1
Std Dev	3.465

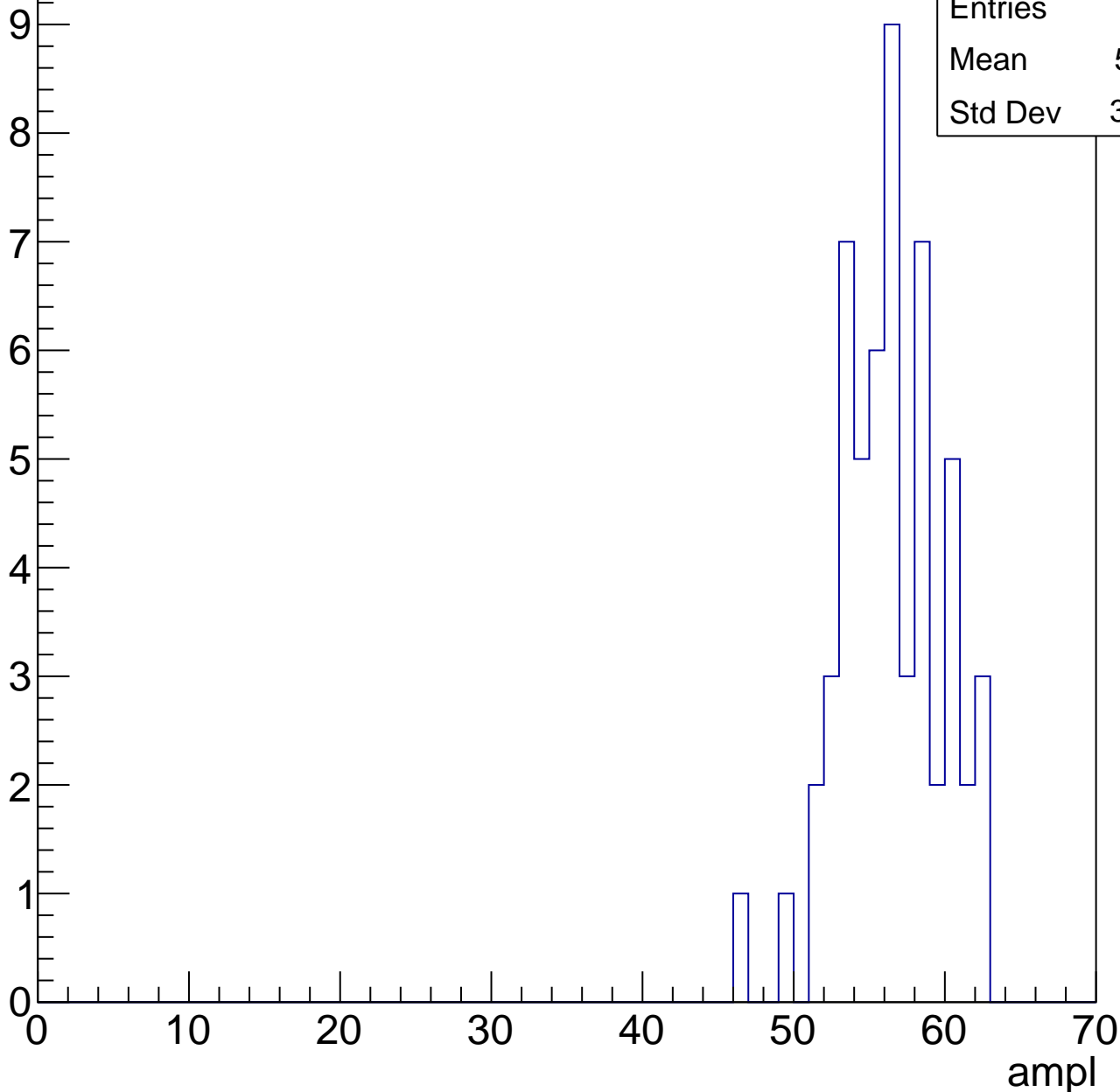


B1L101S, U15-ch69, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

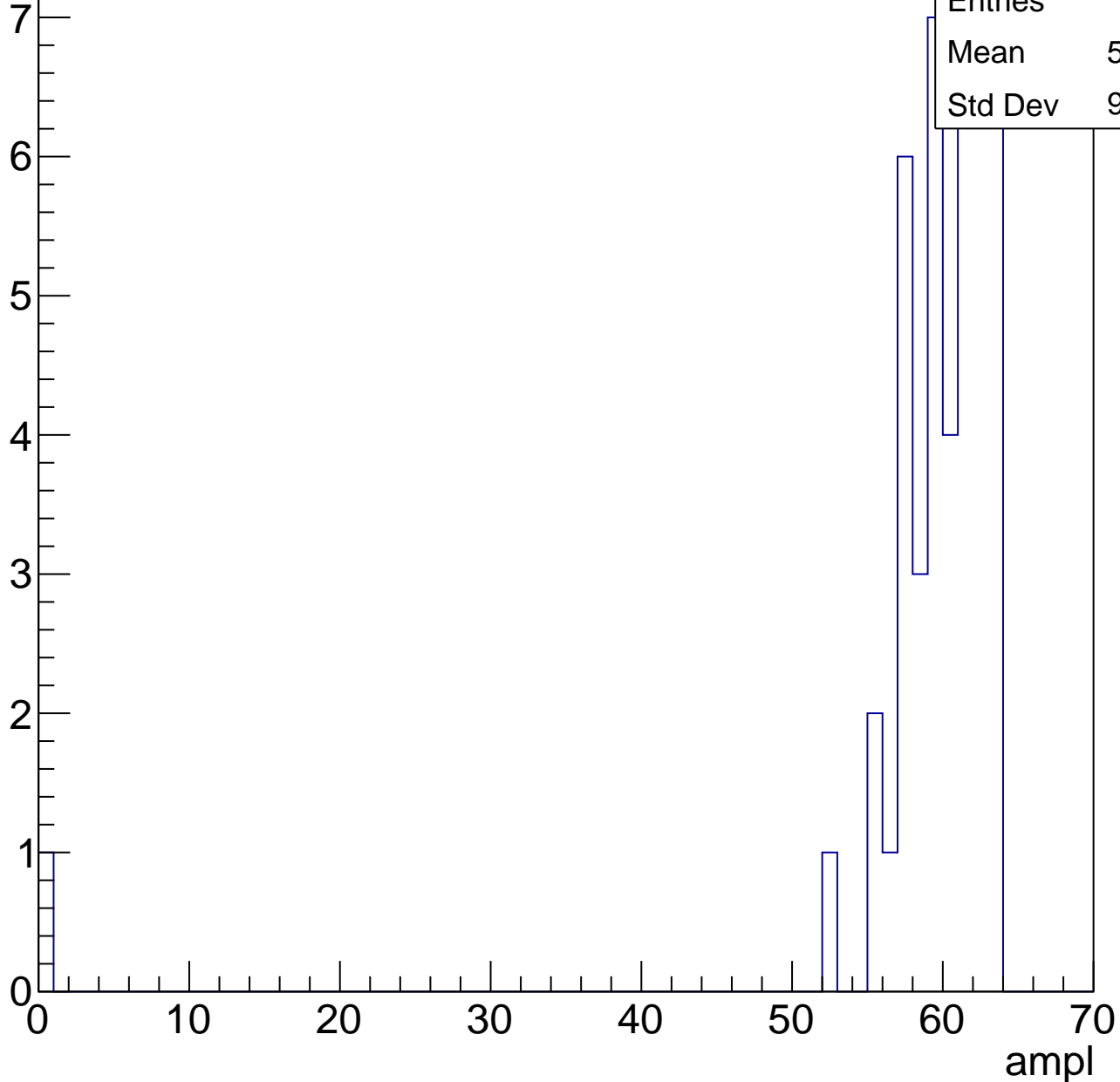
Entries	56
Mean	55.91
Std Dev	3.334



B1L101S, U15-ch69, adc5

calib_packv5_042523_0143.root, FC#0, port D2

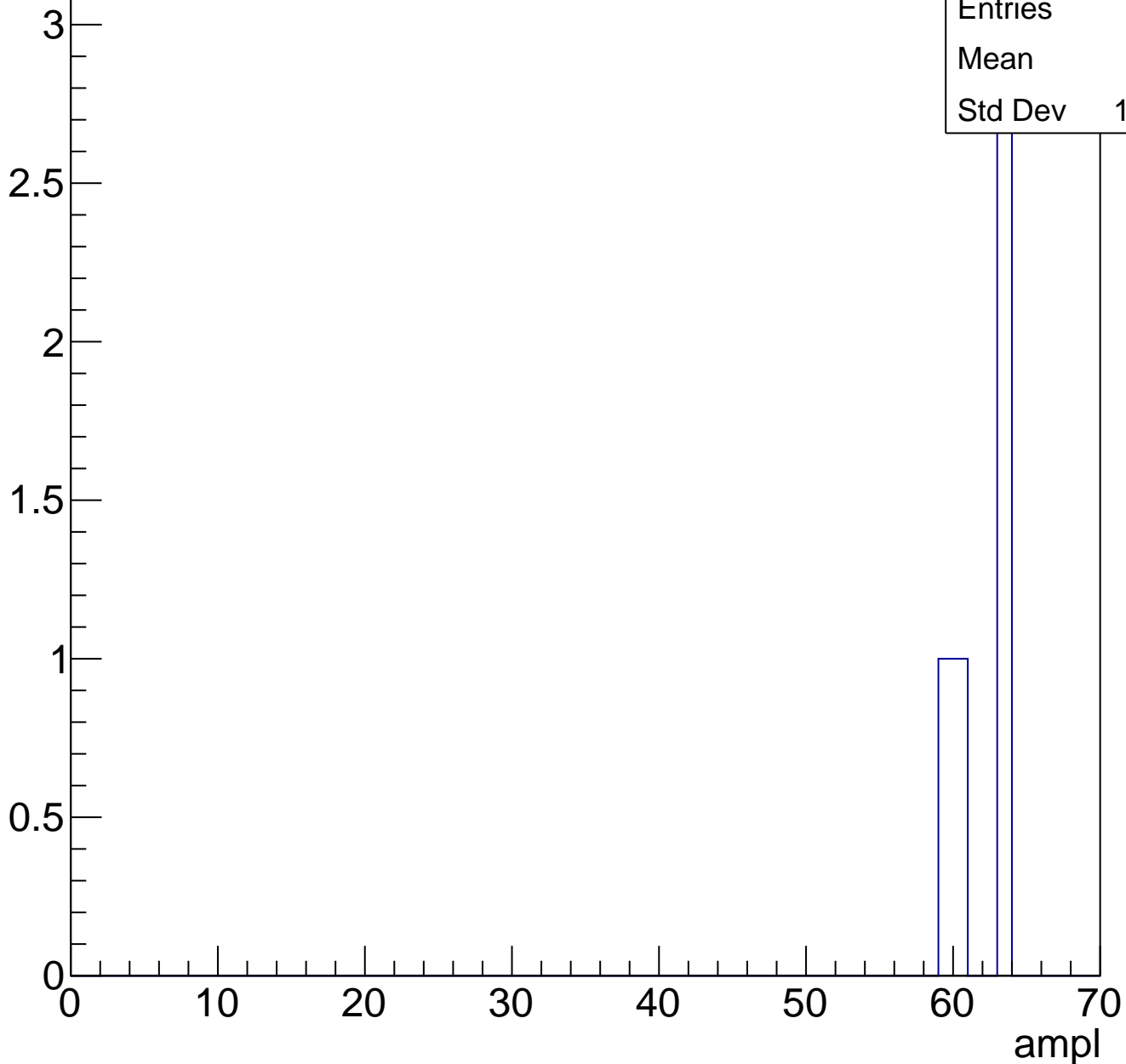
Entry



B1L101S, U15-ch69, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch69, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L101S, U15-ch70, adc0

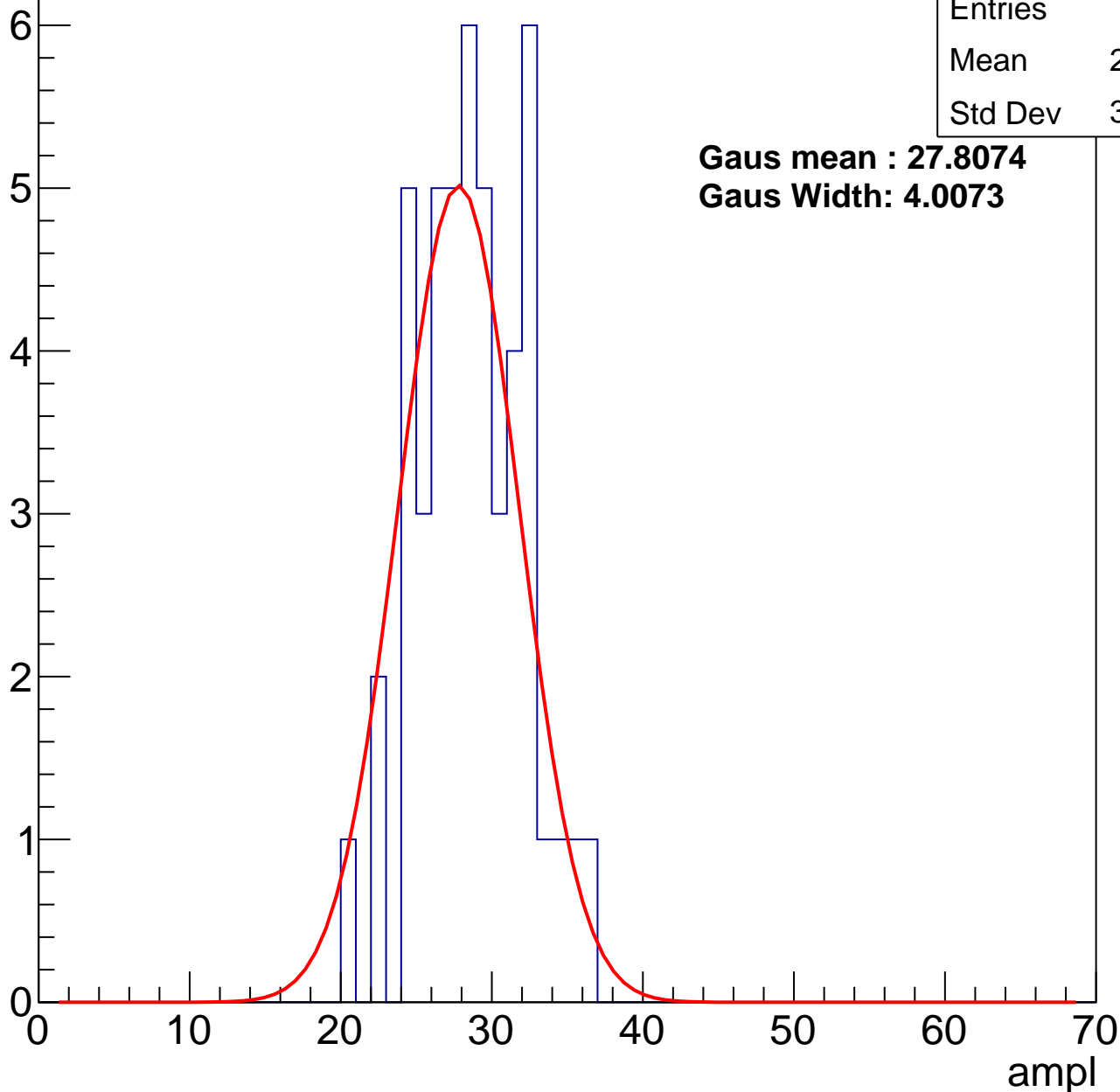
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	28.18
Std Dev	3.468

Gaus mean : 27.8074

Gaus Width: 4.0073



B1L101S, U15-ch70, adc1

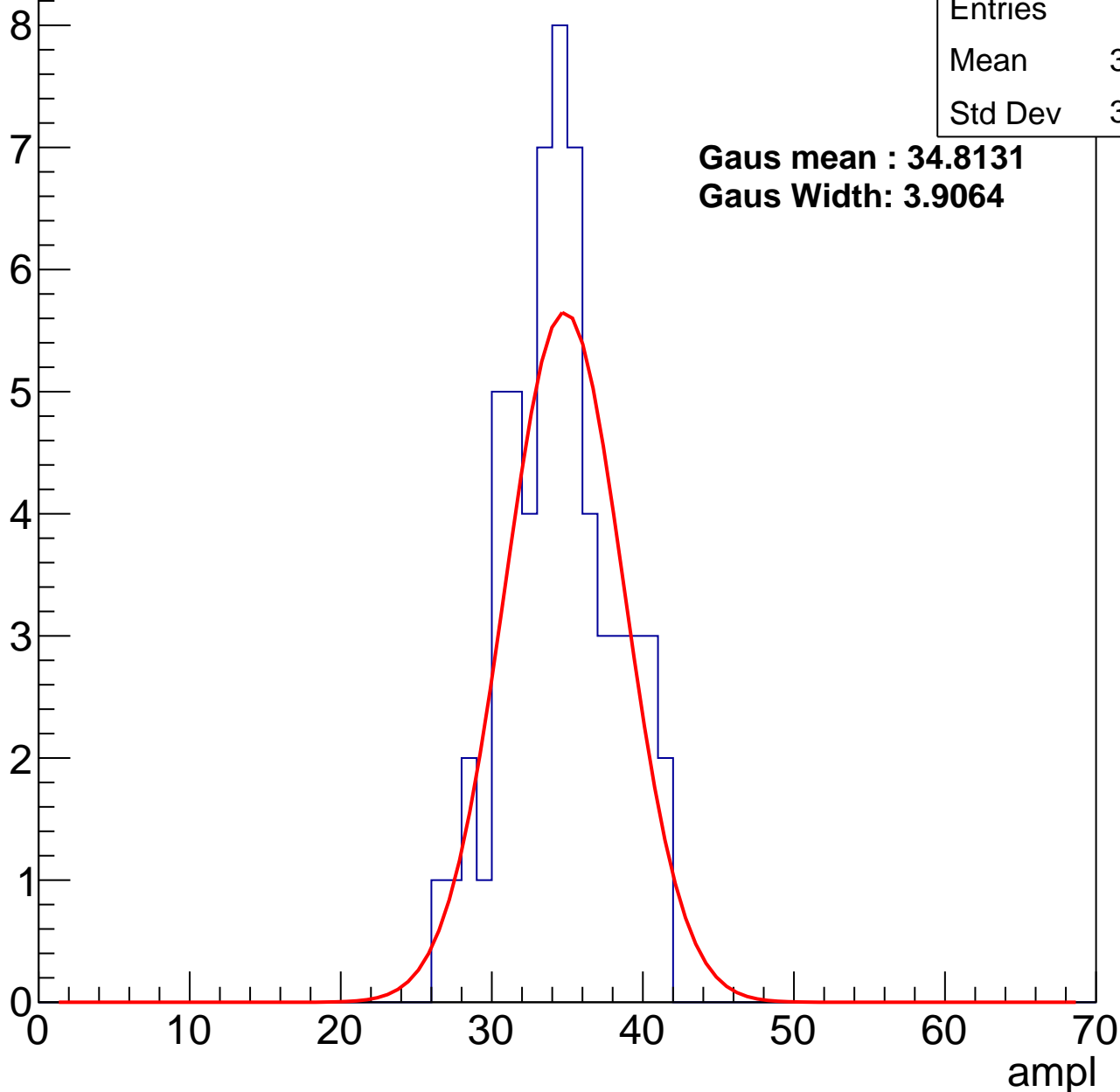
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	34.02
Std Dev	3.534

Gaus mean : 34.8131

Gaus Width: 3.9064



B1L101S, U15-ch70, adc2

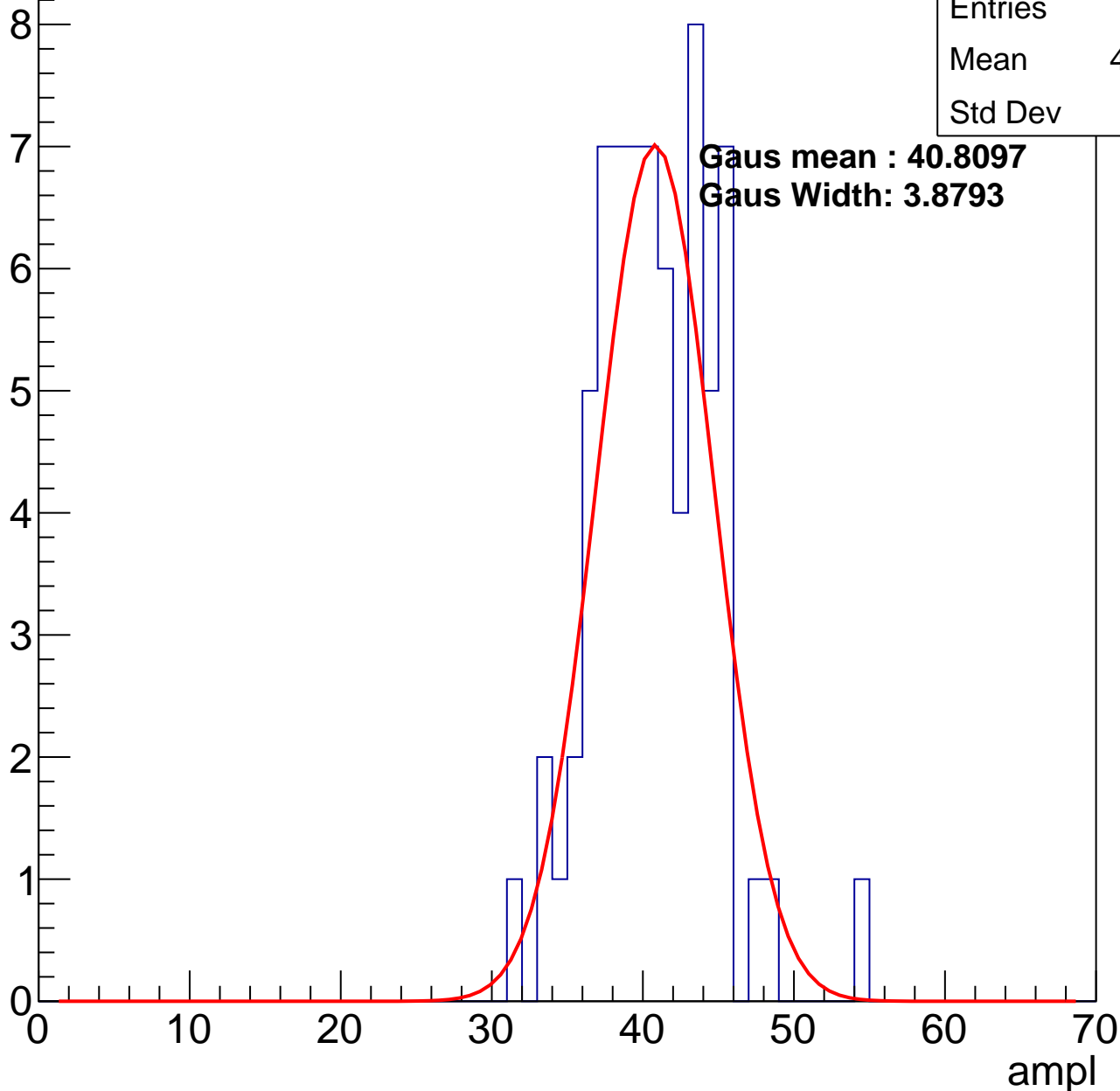
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	40.29
Std Dev	3.9

Gaus mean : 40.8097

Gaus Width: 3.8793



B1L101S, U15-ch70, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	46.92
Std Dev	2.972

Entry

10

8

6

4

2

0

0

10

20

30

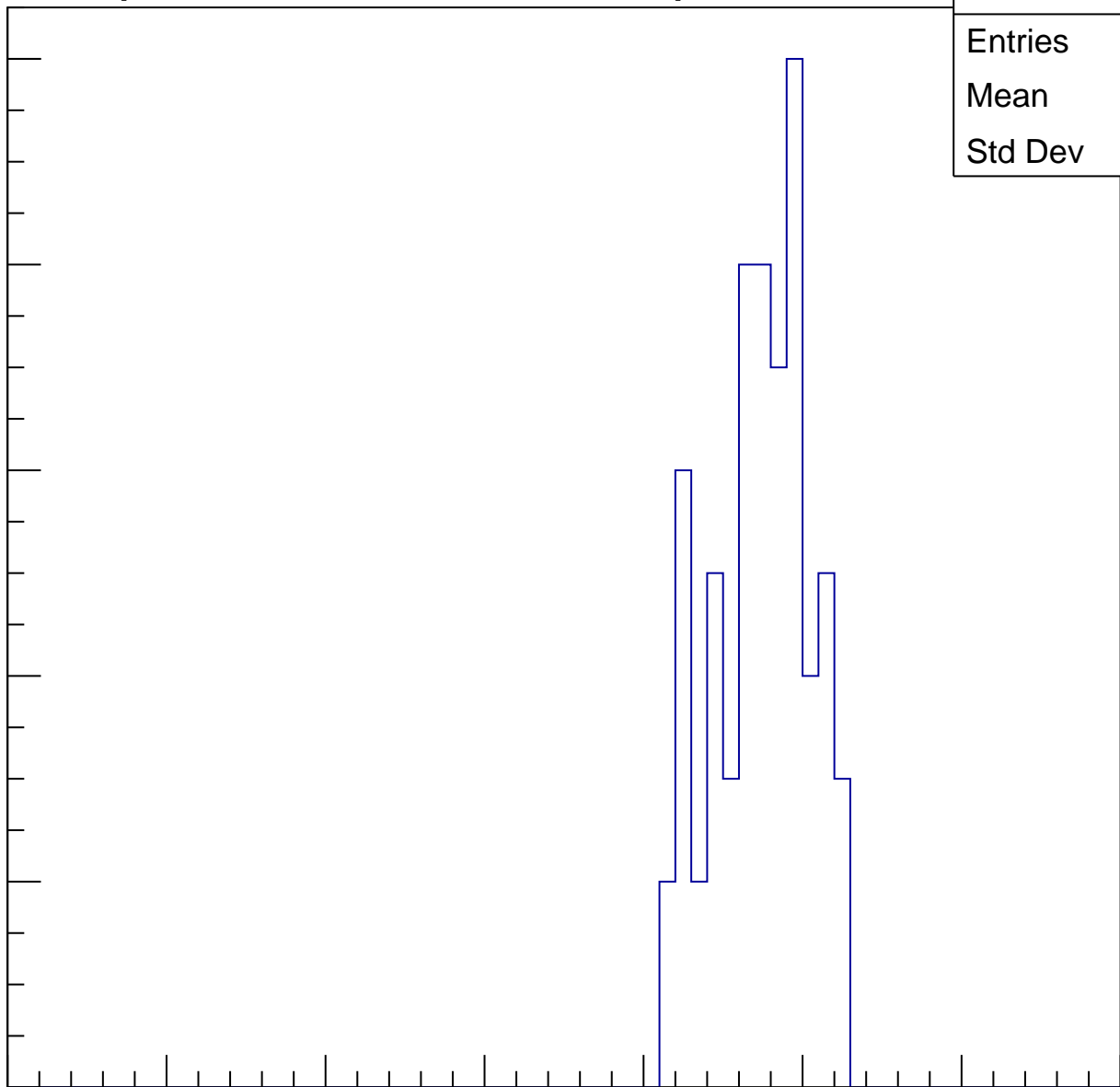
40

50

60

70

ampl

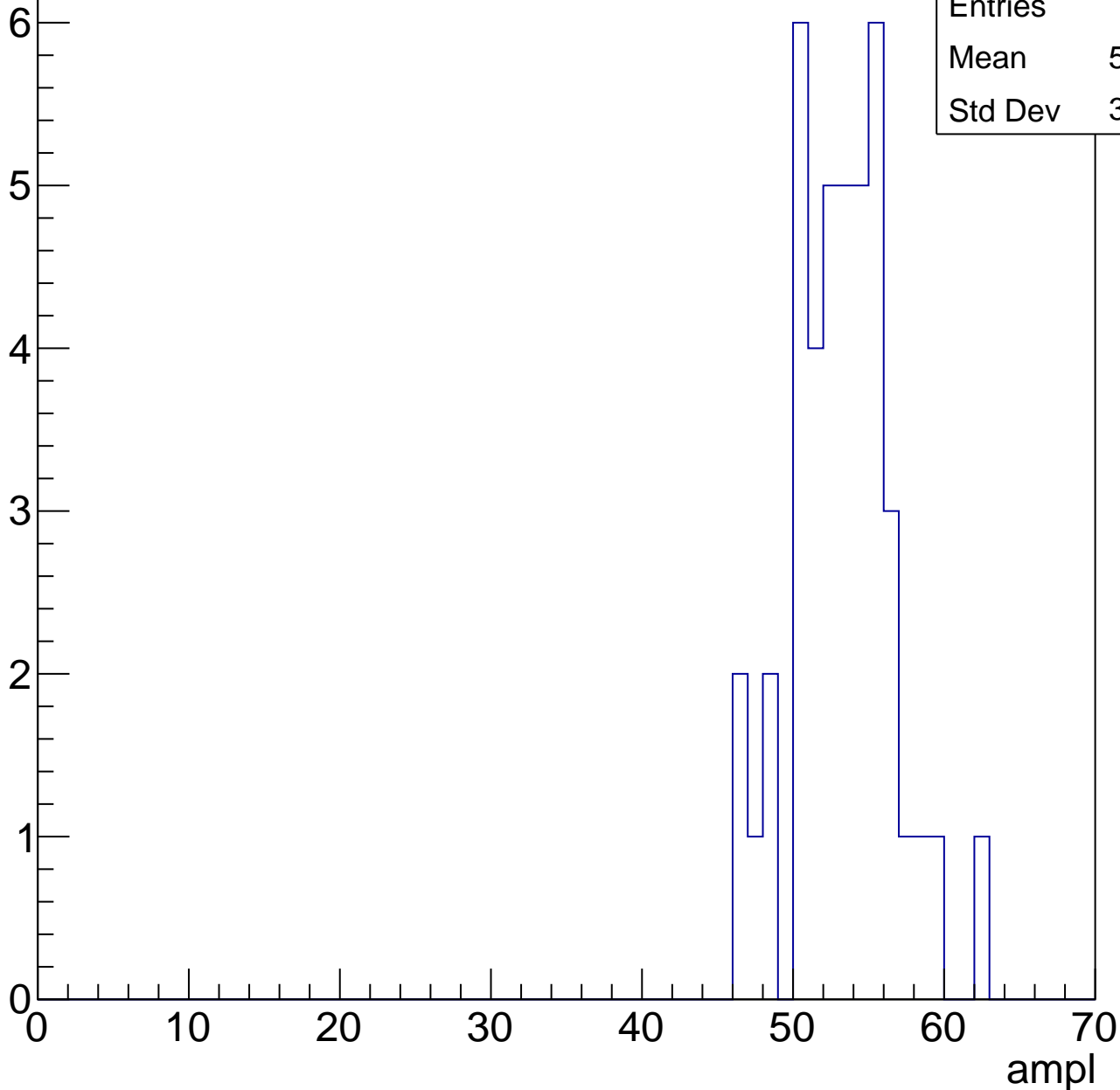


B1L101S, U15-ch70, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

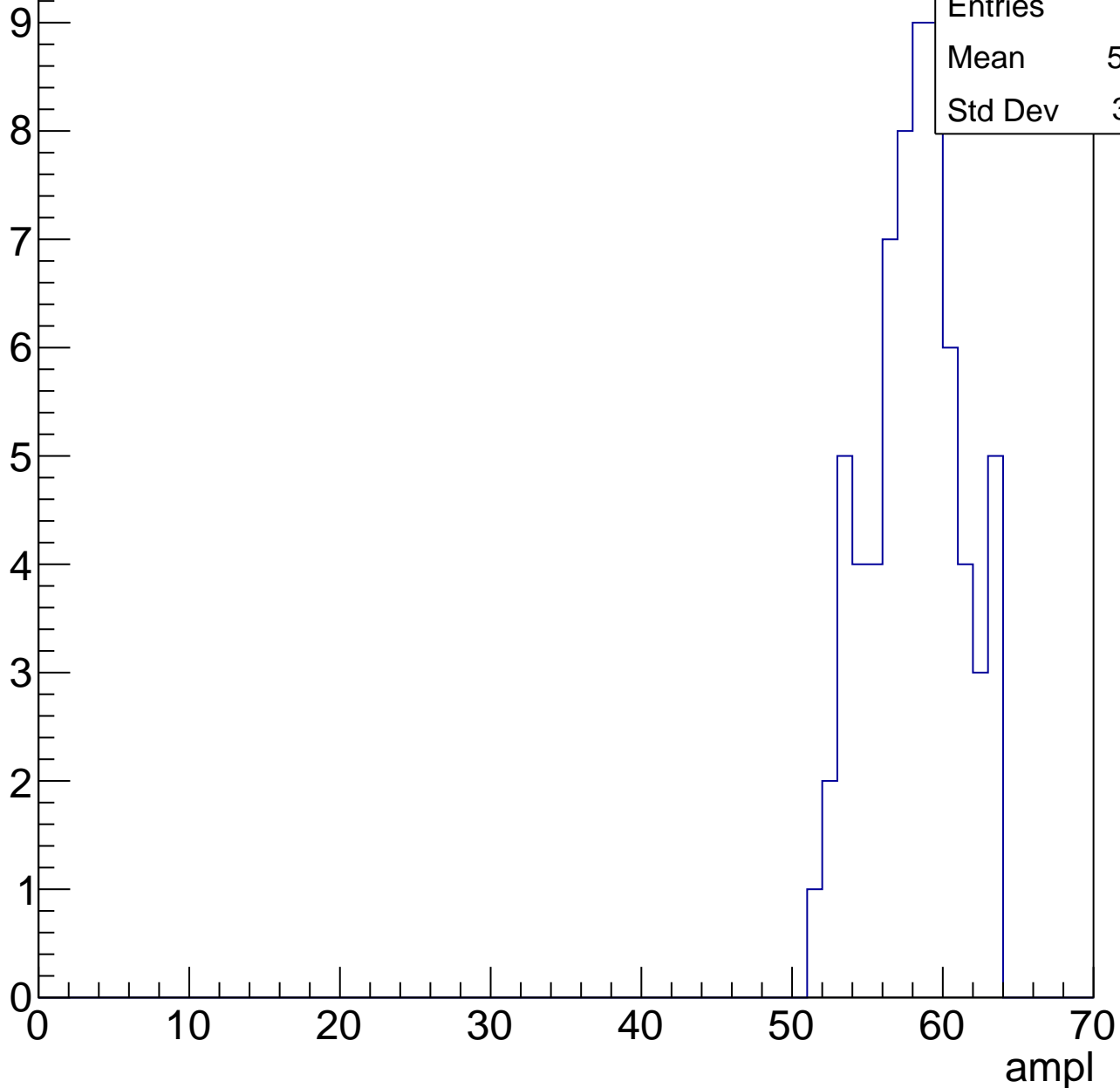
Entries	43
Mean	52.74
Std Dev	3.307



B1L101S, U15-ch70, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



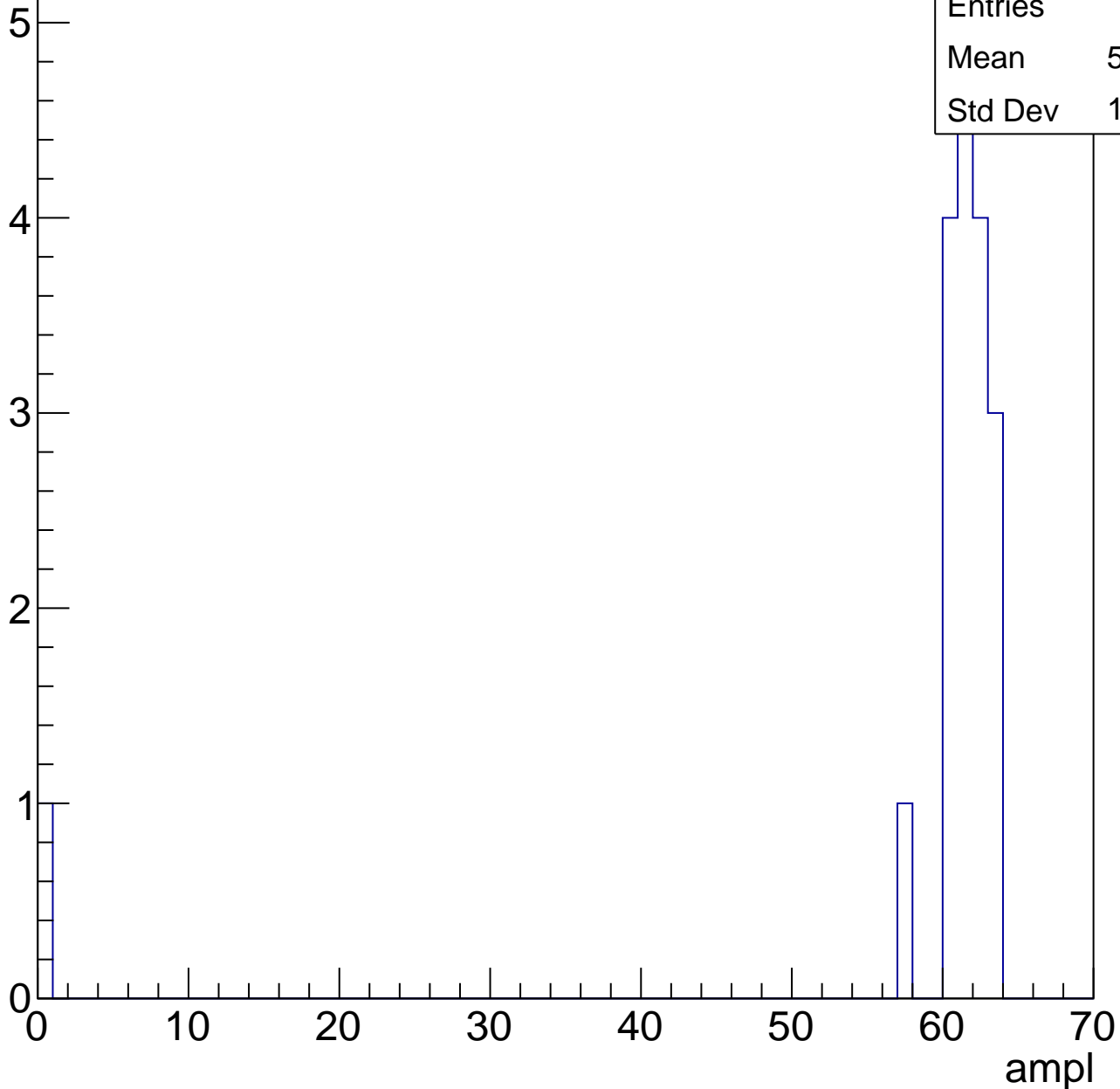
Entries	67
Mean	57.64
Std Dev	3.031

B1L101S, U15-ch70, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	18
Mean	57.72
Std Dev	14.07



B1L101S, U15-ch70, adc7

calib_packv5_042523_0143.root, FC#0, port D2

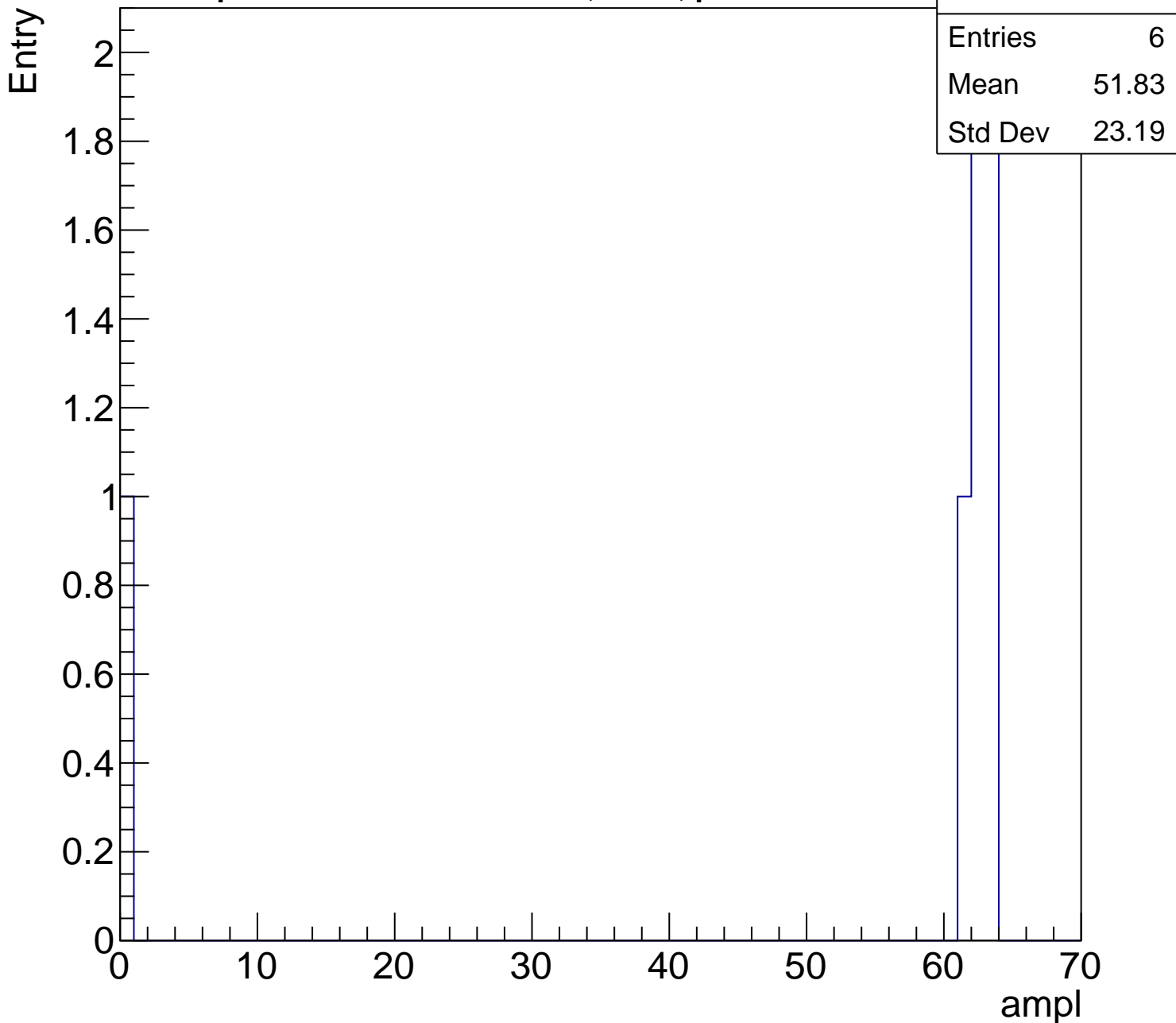
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	51.83
Std Dev	23.19

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch71, adc0

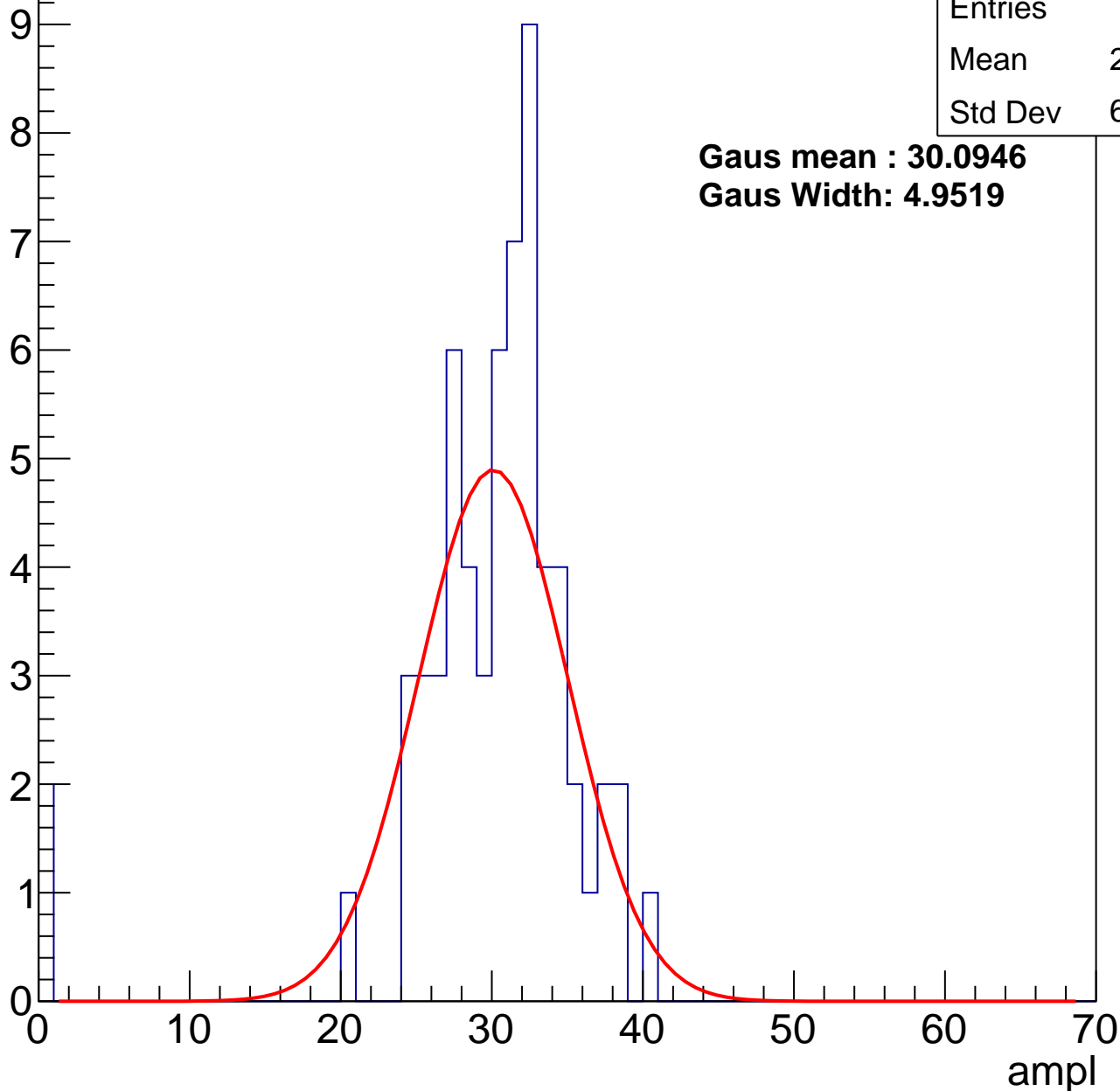
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	29.44
Std Dev	6.594

Gaus mean : 30.0946

Gaus Width: 4.9519



B1L101S, U15-ch71, adc1

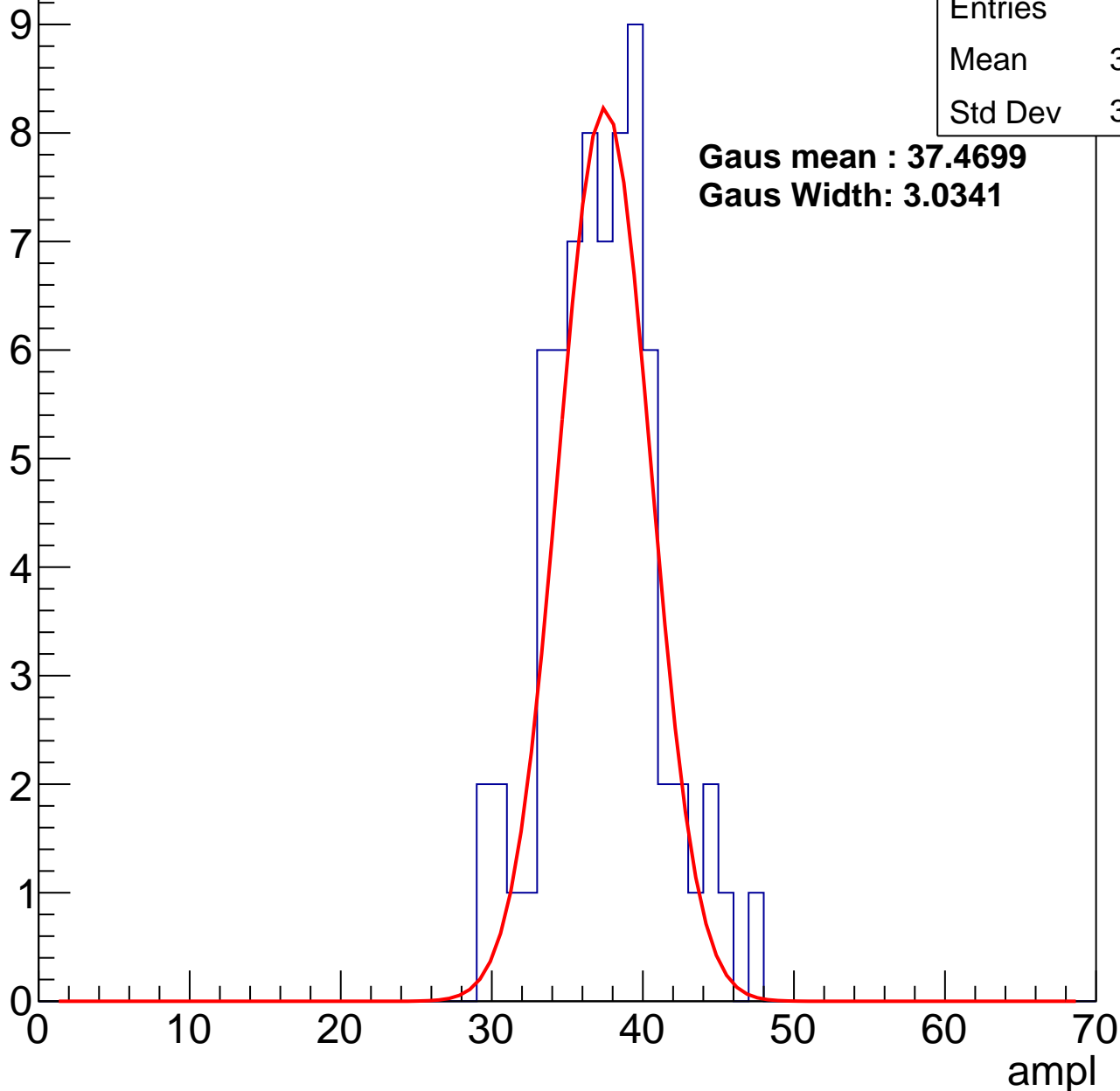
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.93
Std Dev	3.634

Gaus mean : 37.4699

Gaus Width: 3.0341



B1L101S, U15-ch71, adc2

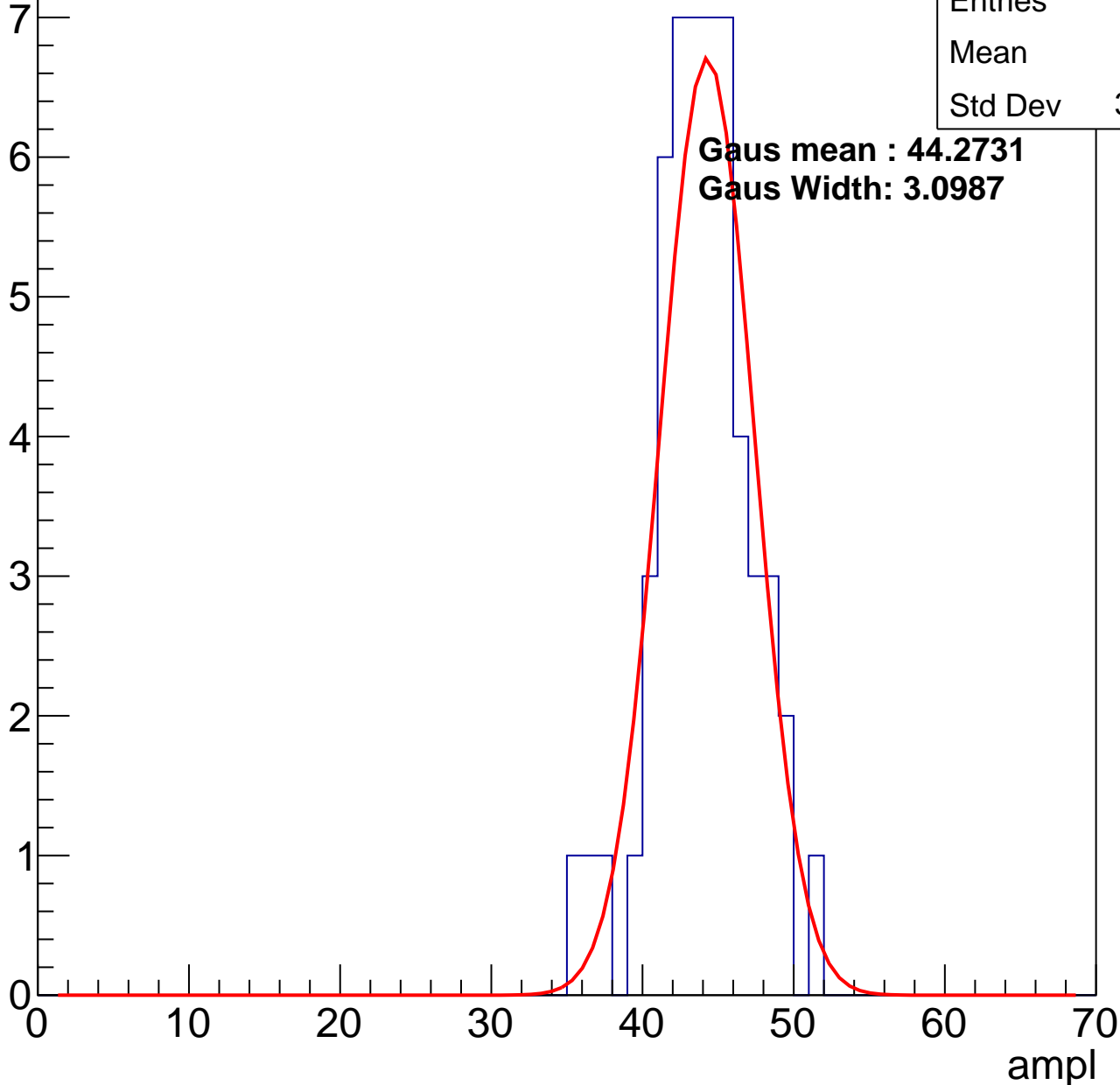
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	43.5
Std Dev	3.161

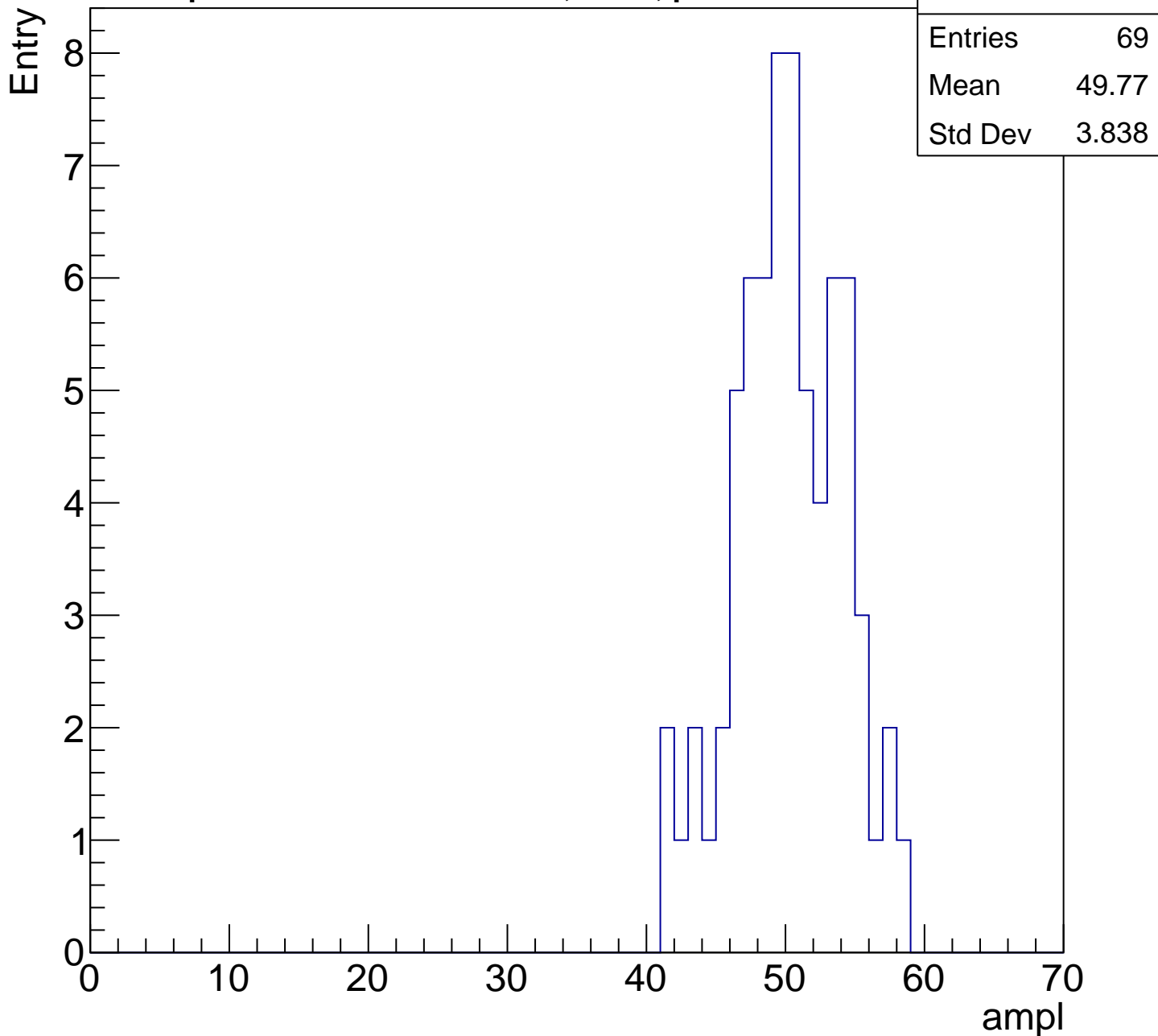
Gaus mean : 44.2731

Gaus Width: 3.0987



B1L101S, U15-ch71, adc3

calib_packv5_042523_0143.root, FC#0, port D2

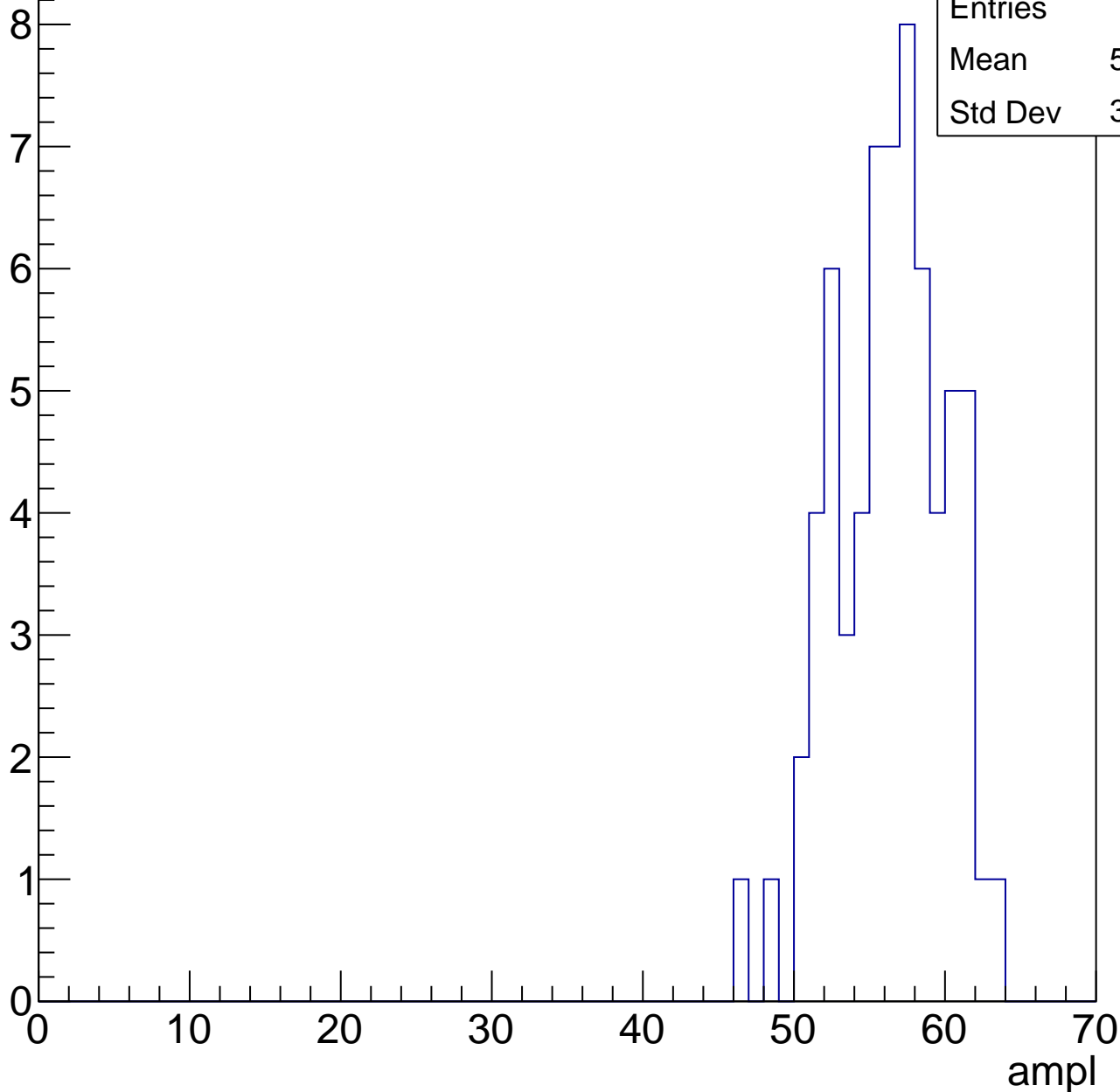


B1L101S, U15-ch71, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	55.88
Std Dev	3.593

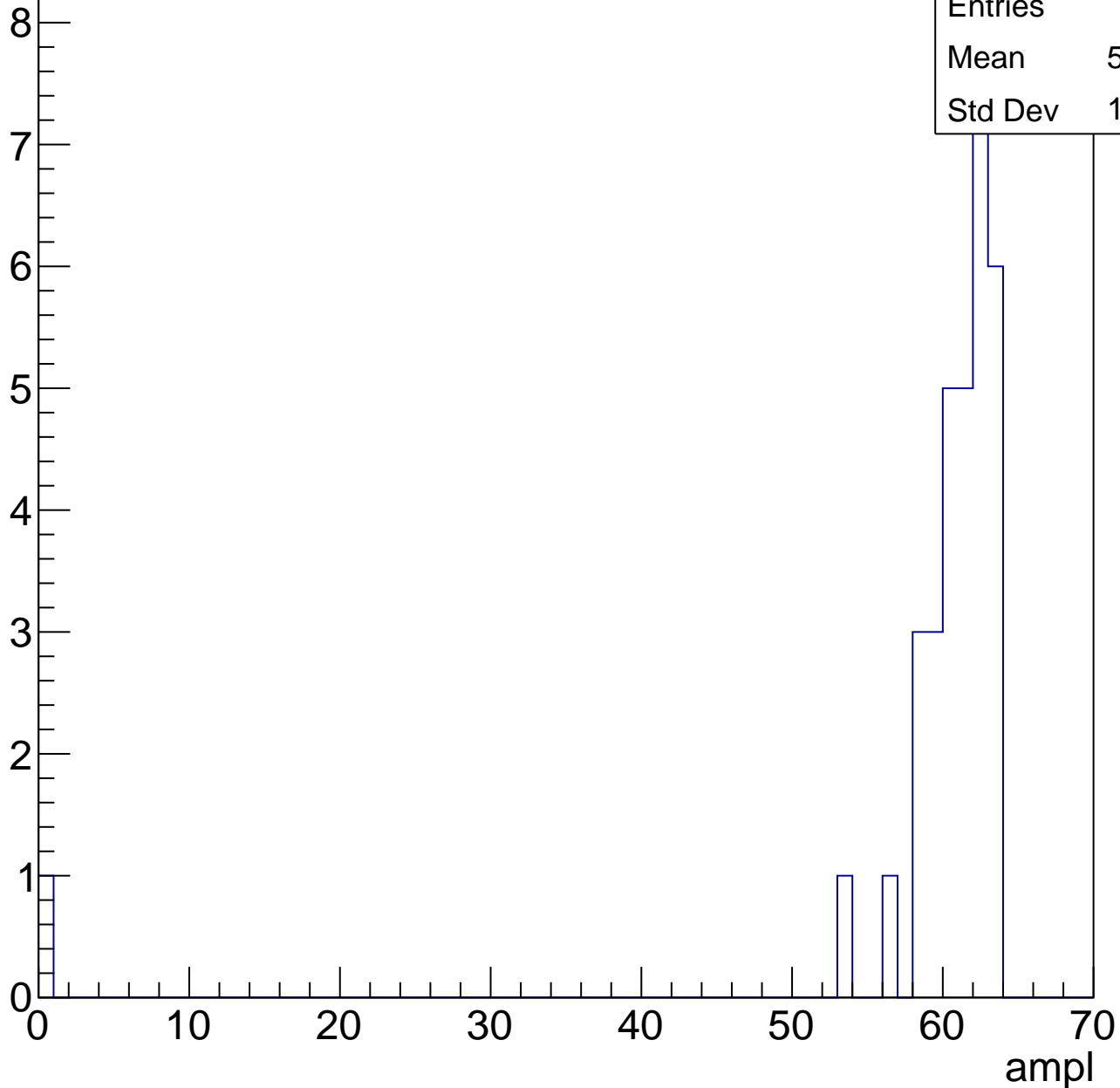


B1L101S, U15-ch71, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	33
Mean	58.76
Std Dev	10.62



B1L101S, U15-ch71, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	8
Mean	60.5
Std Dev	2.449

0

B1L101S, U15-ch71, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch72, adc0

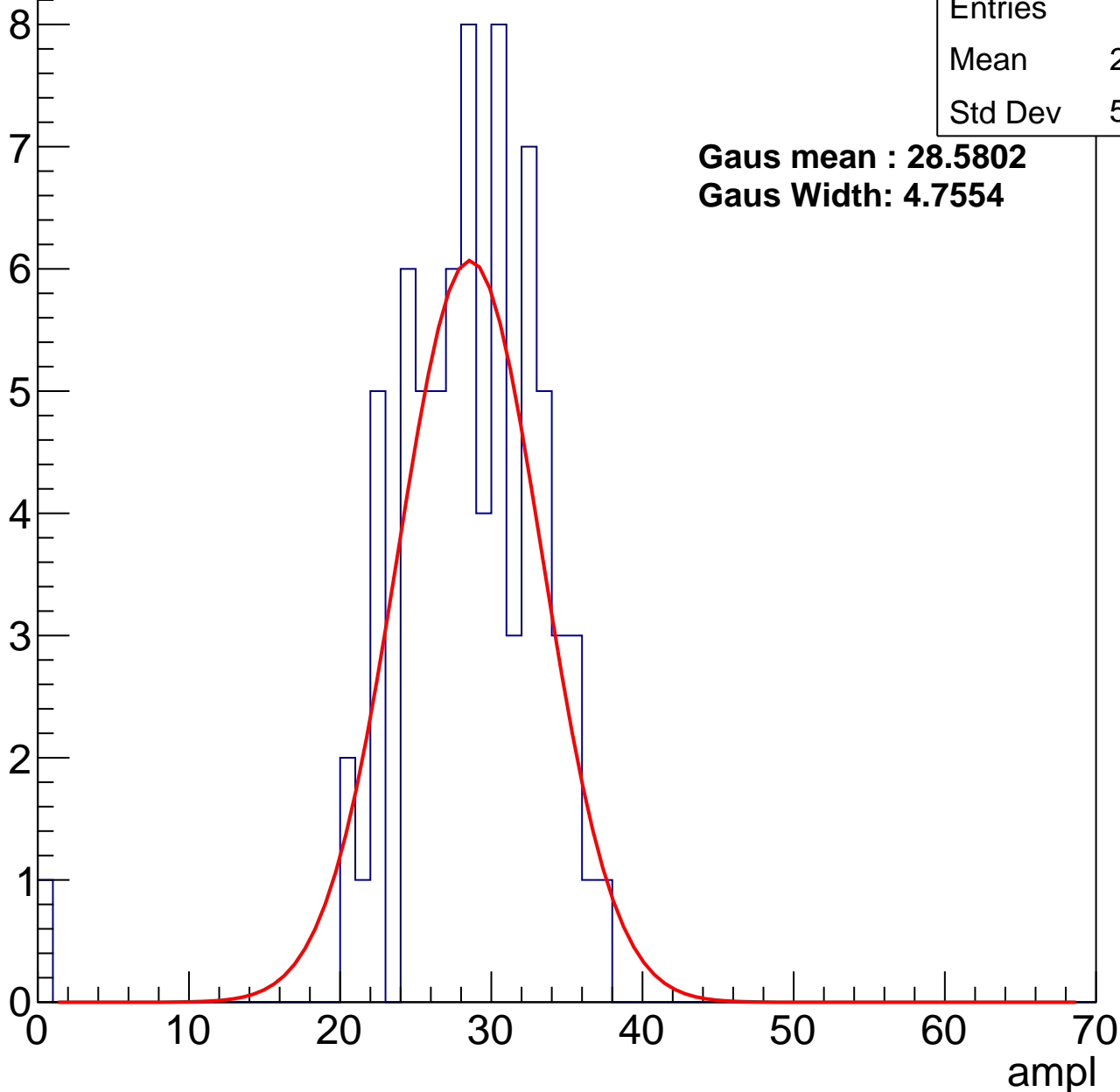
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	28.03
Std Dev	5.209

Gaus mean : 28.5802

Gaus Width: 4.7554



B1L101S, U15-ch72, adc1

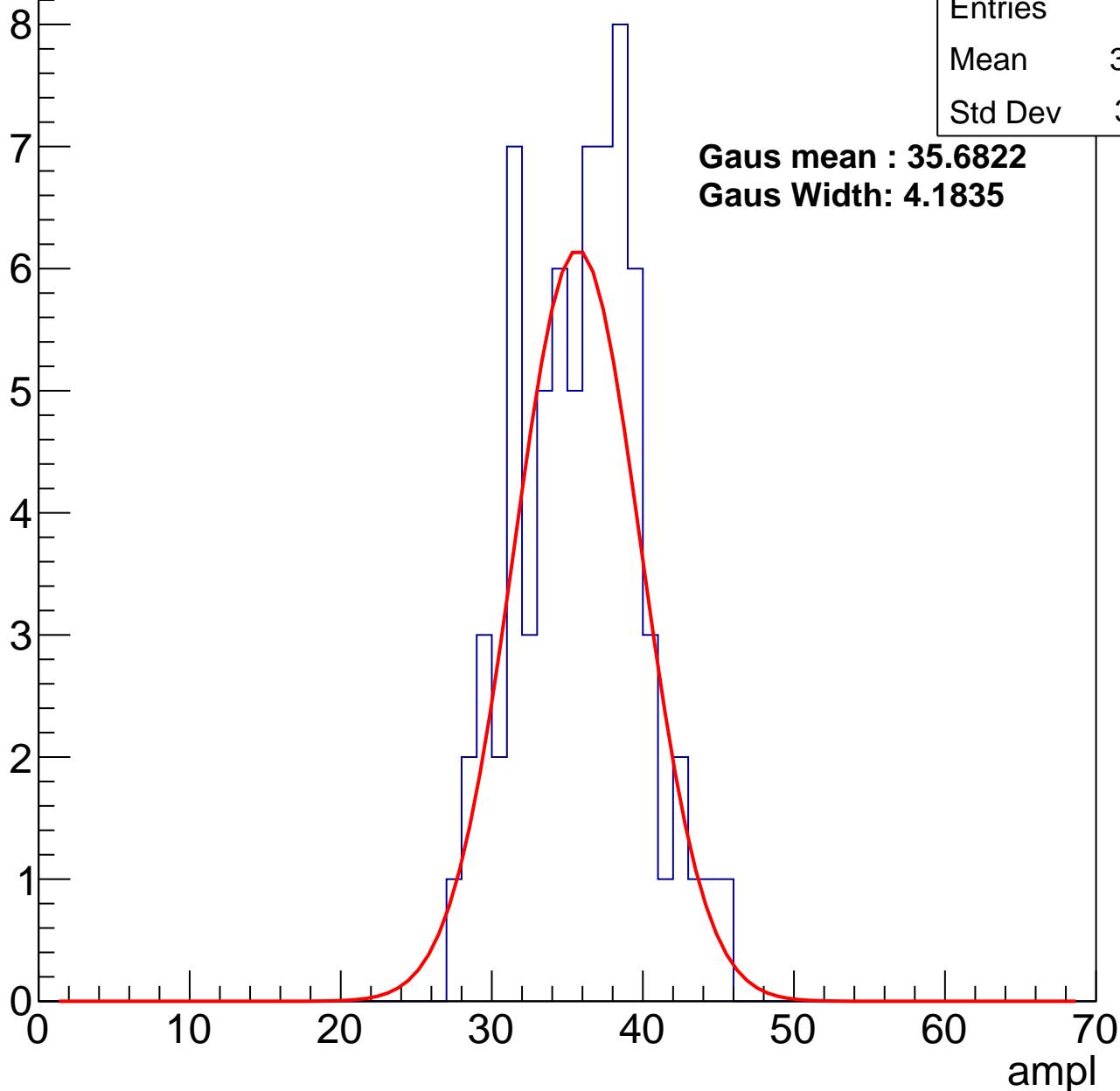
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.39
Std Dev	3.991

Gaus mean : 35.6822

Gaus Width: 4.1835



B1L101S, U15-ch72, adc2

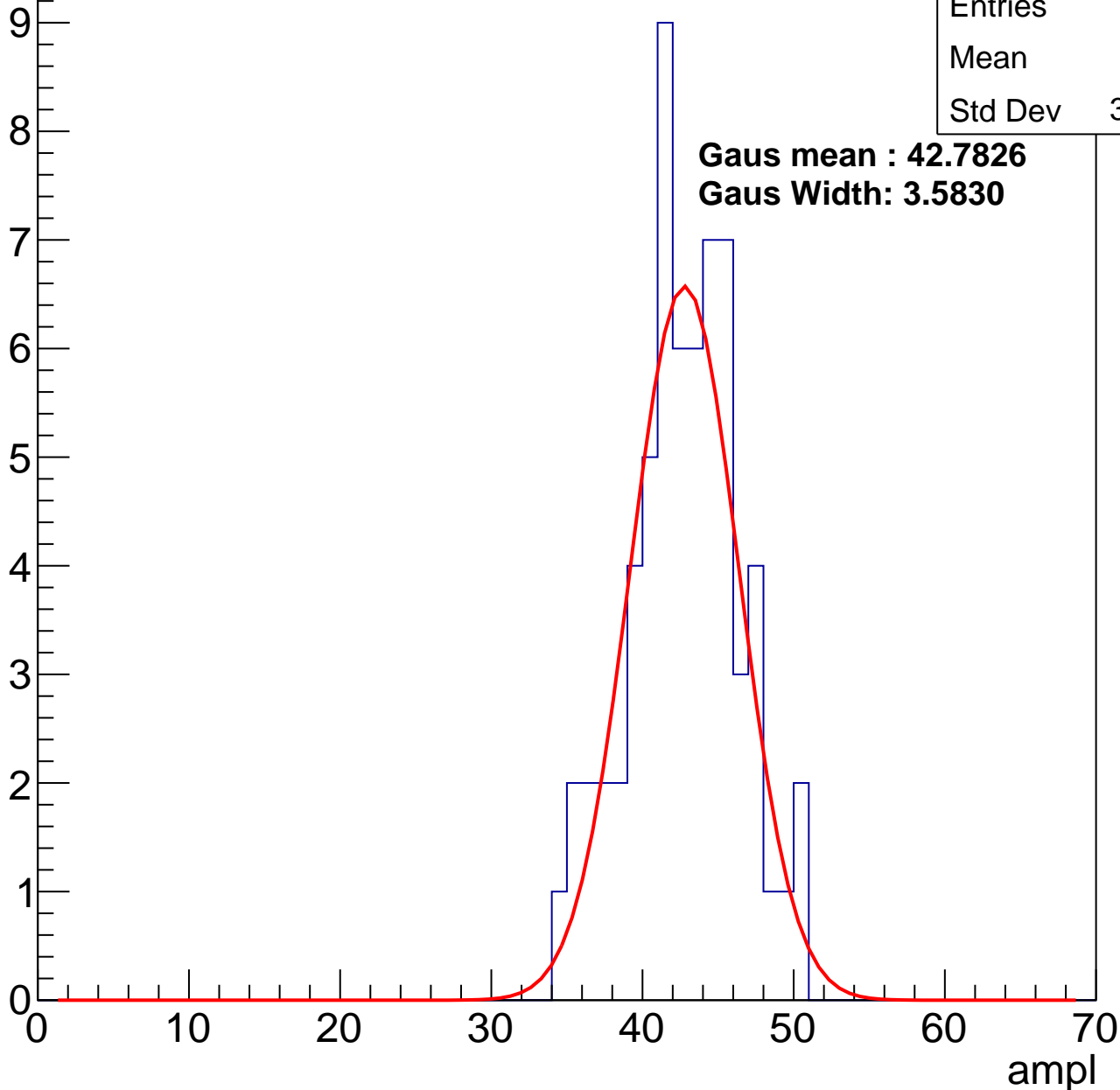
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.3
Std Dev	3.608

Gaus mean : 42.7826

Gaus Width: 3.5830

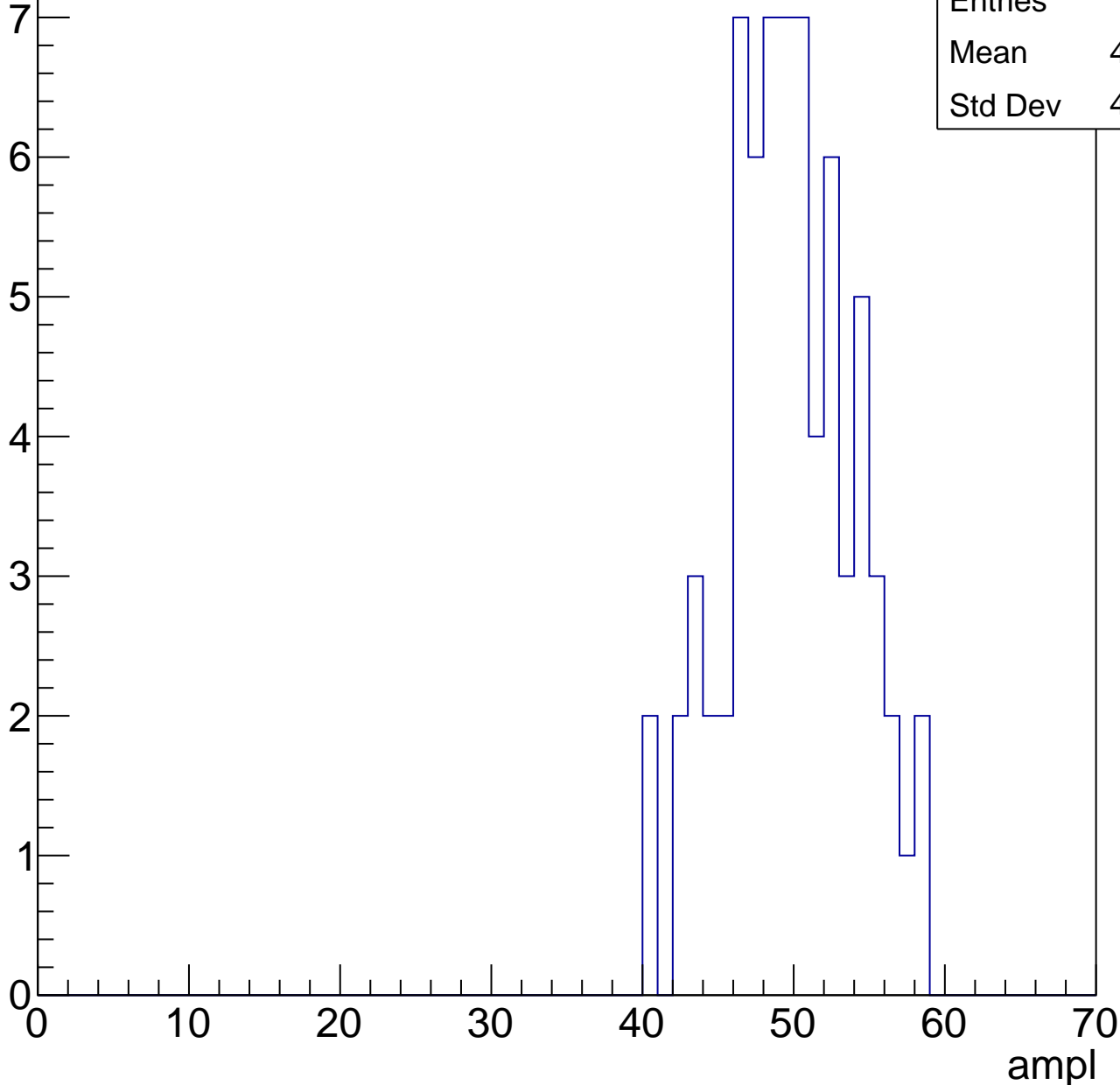


B1L101S, U15-ch72, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.28
Std Dev	4.146

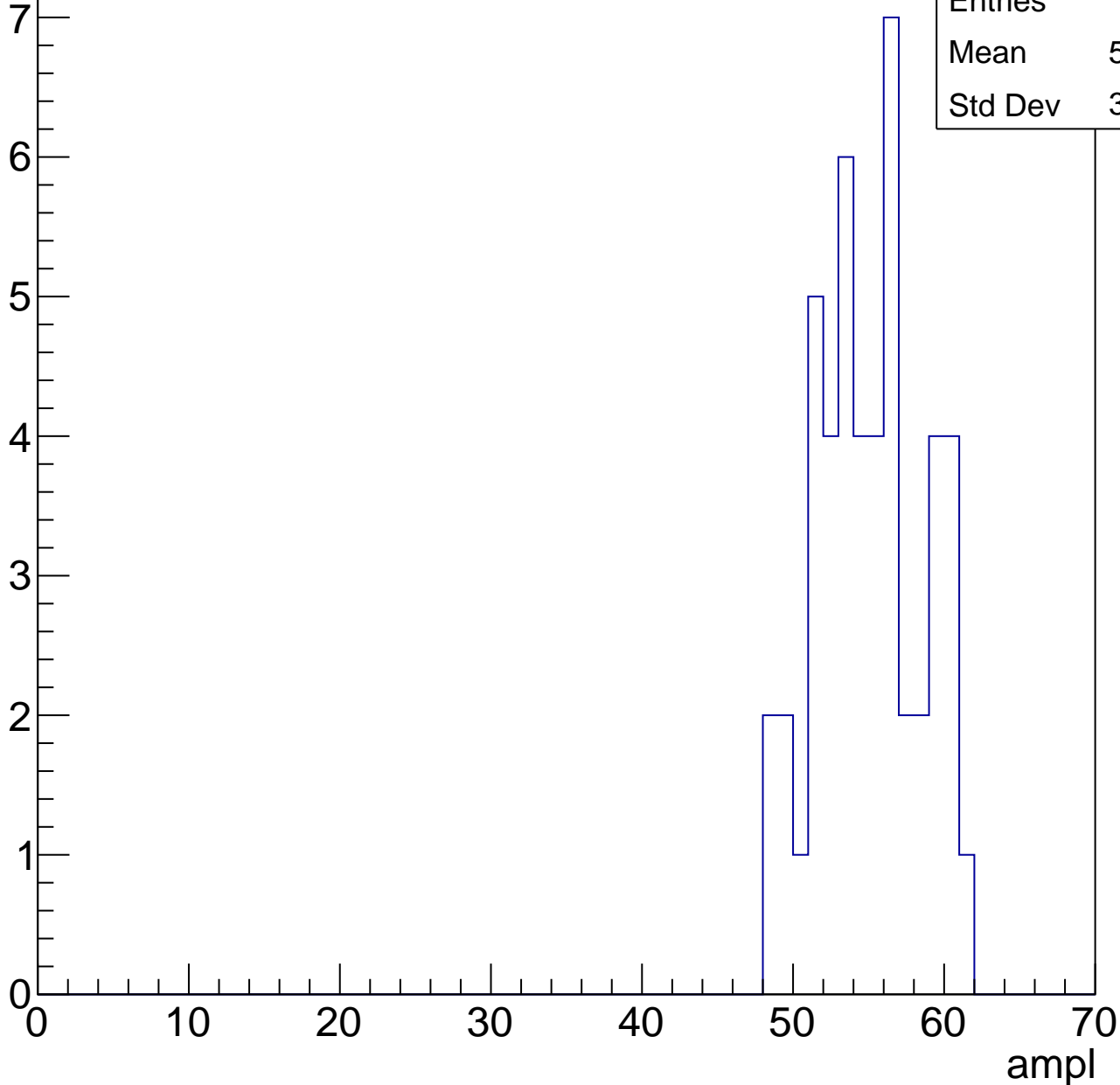


B1L101S, U15-ch72, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	54.58
Std Dev	3.427

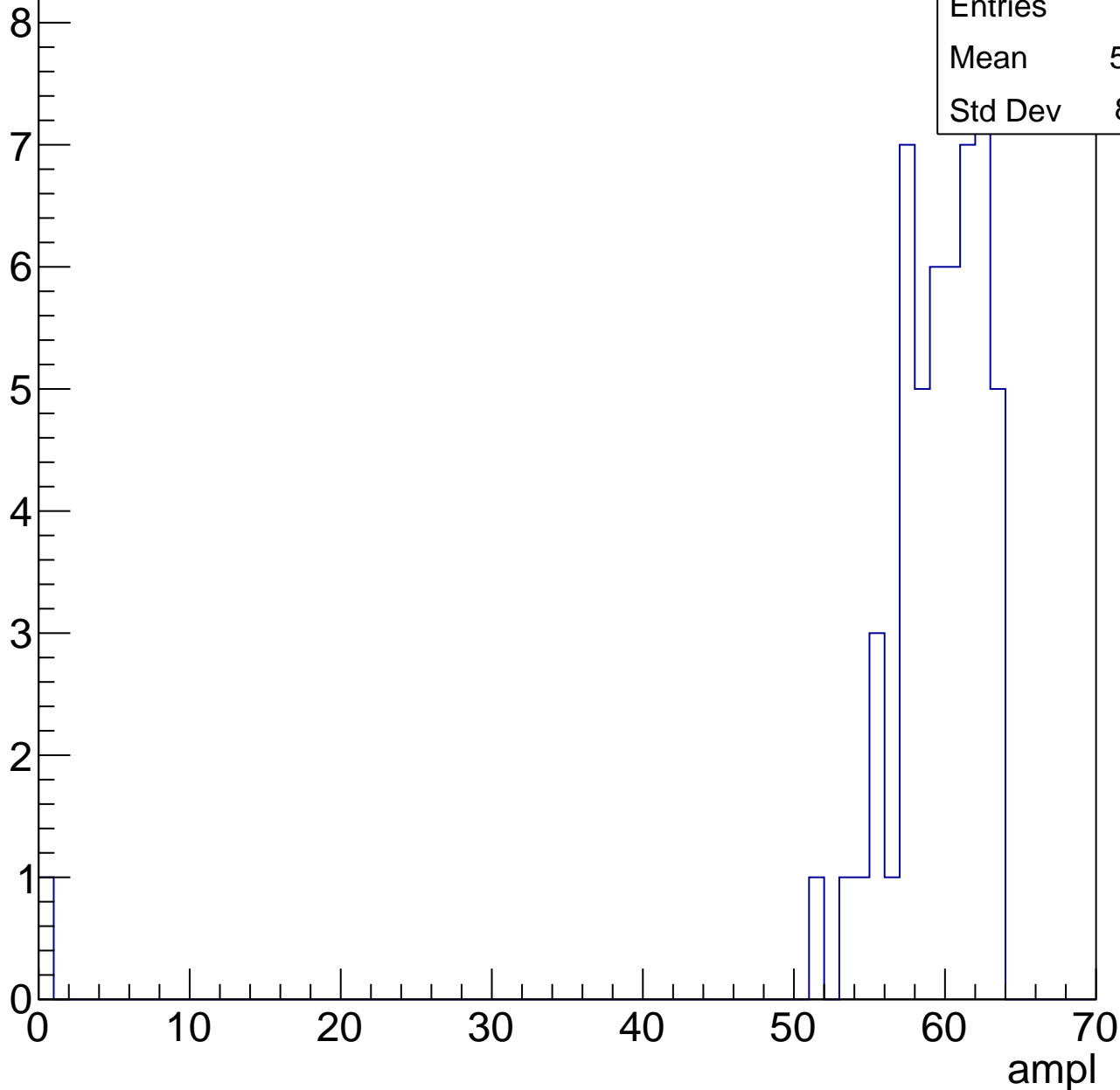


B1L101S, U15-ch72, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

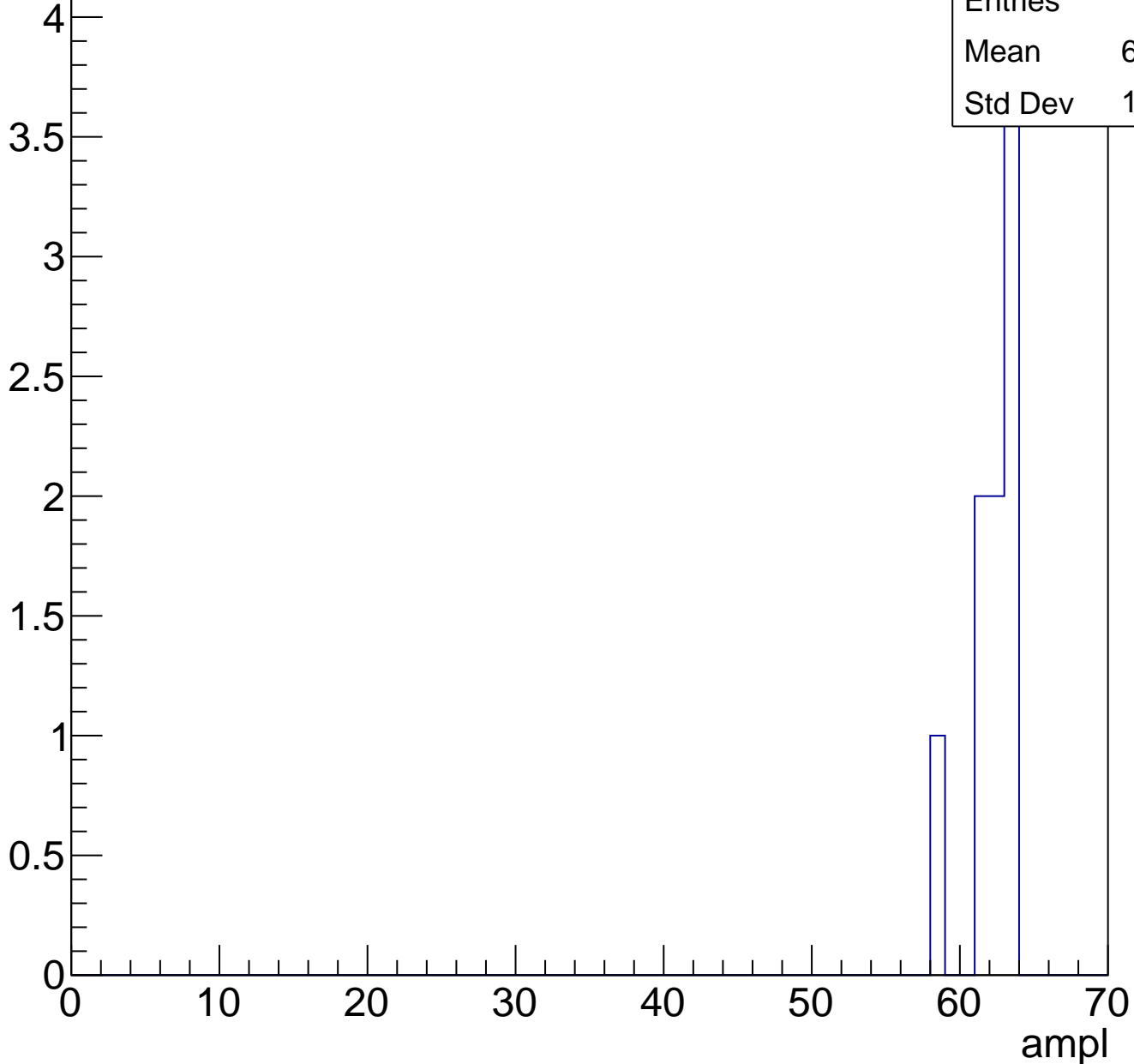
Entries	52
Mean	58.08
Std Dev	8.591



B1L101S, U15-ch72, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch72, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch73, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	84
Mean	29.9
Std Dev	5.126

Gaus mean : 30.3732

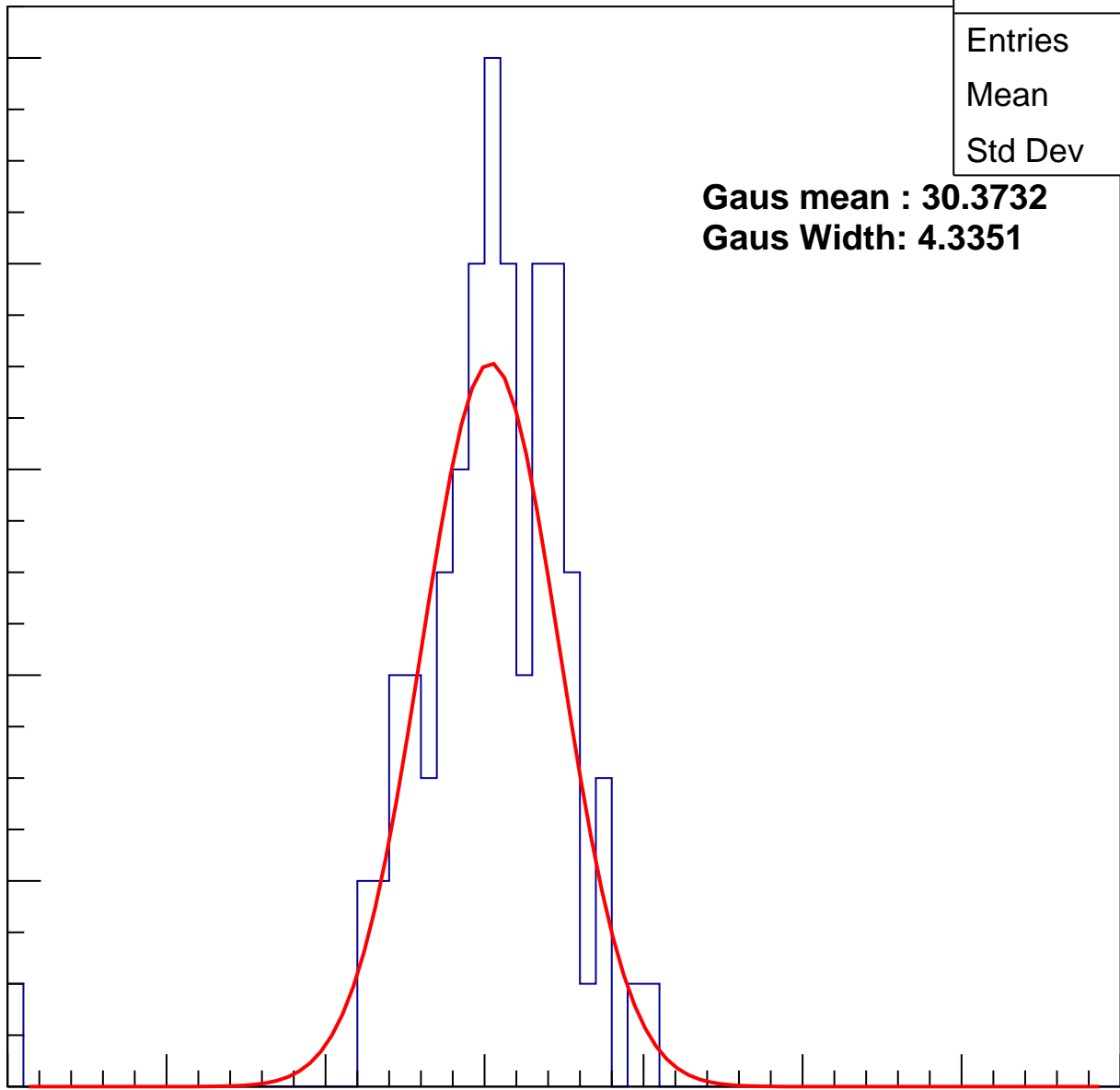
Gaus Width: 4.3351

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch73, adc1

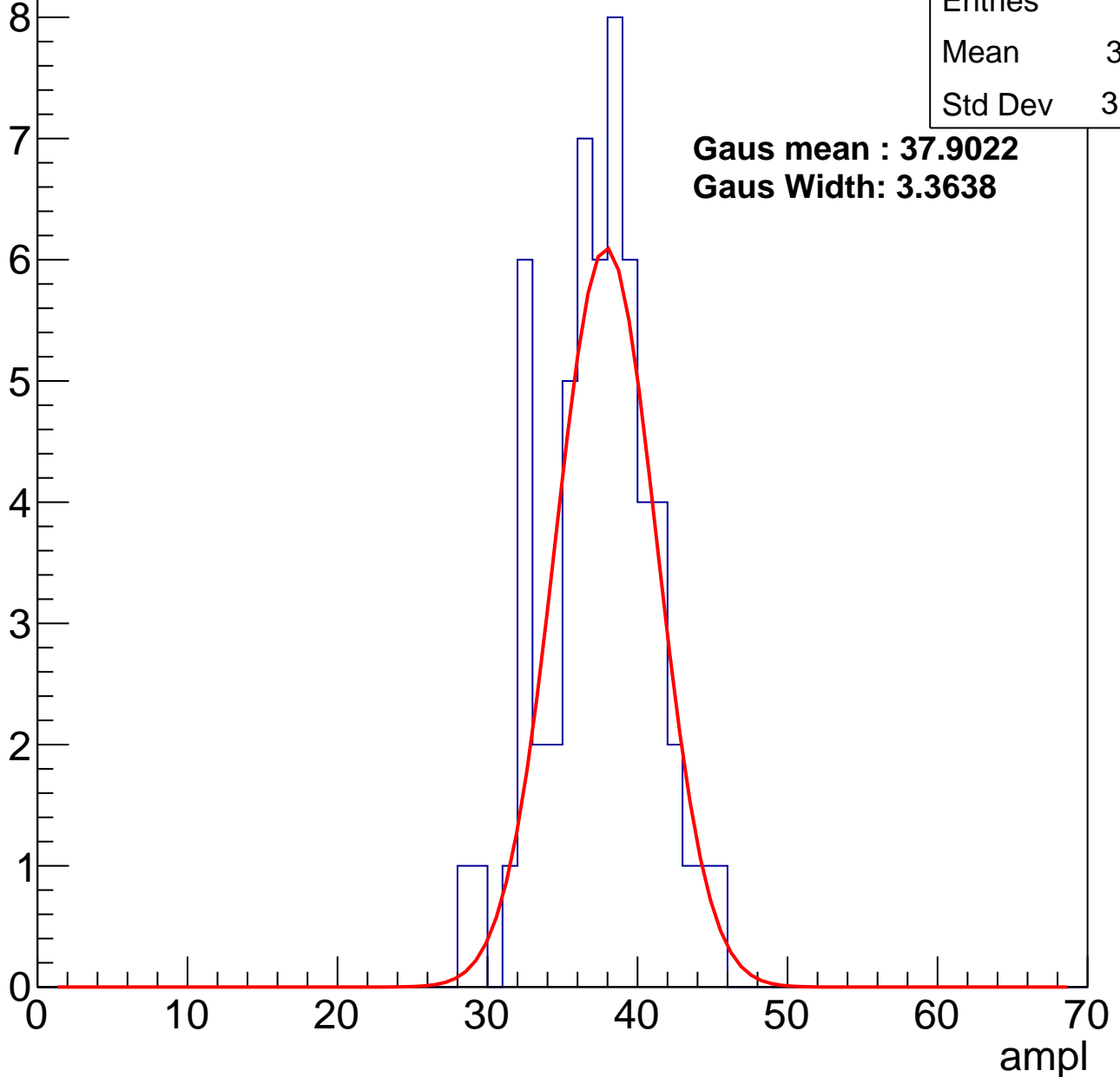
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	36.91
Std Dev	3.568

Gaus mean : 37.9022

Gaus Width: 3.3638



B1L101S, U15-ch73, adc2

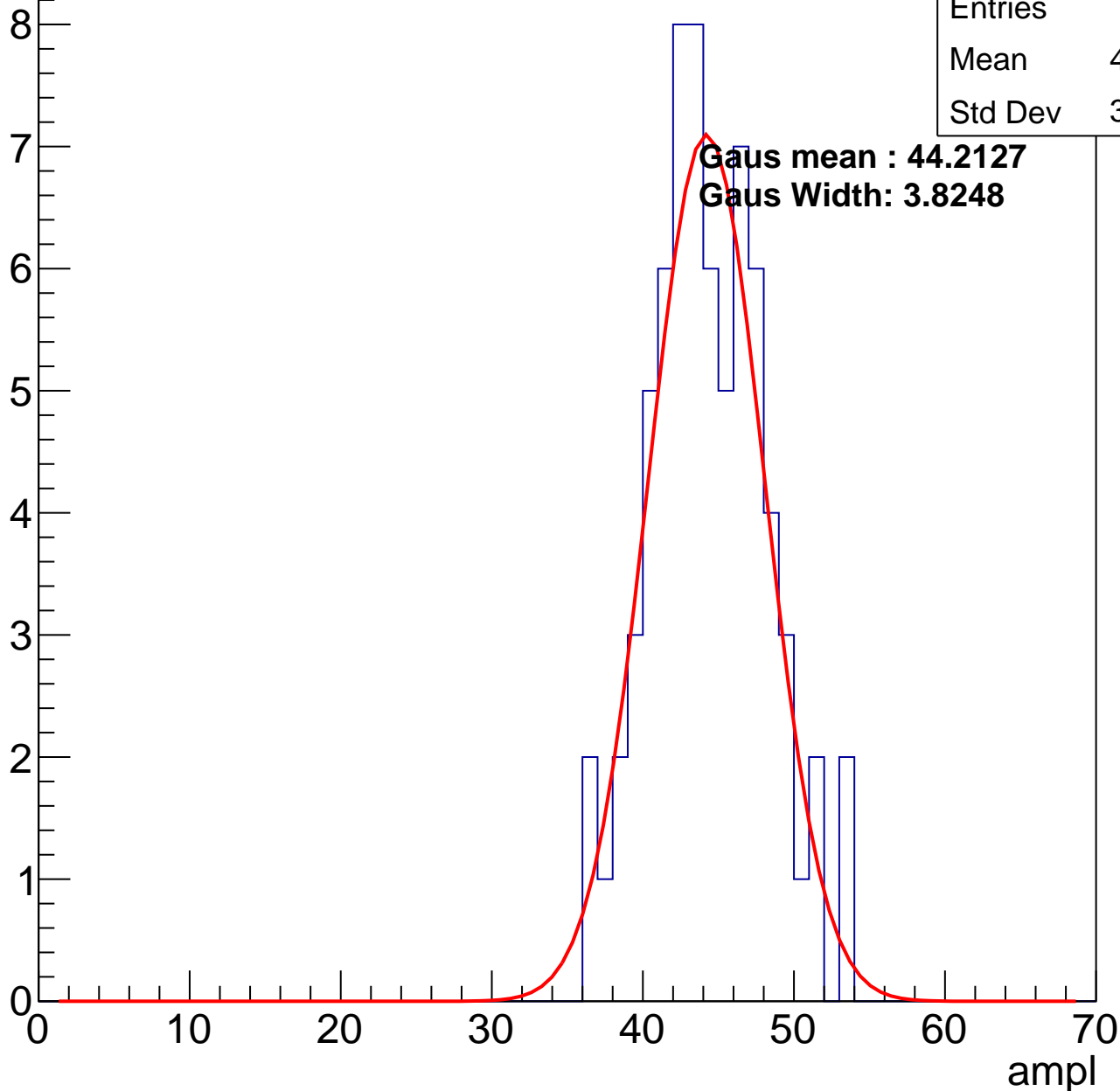
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	43.92
Std Dev	3.789

Gaus mean : 44.2127

Gaus Width: 3.8248

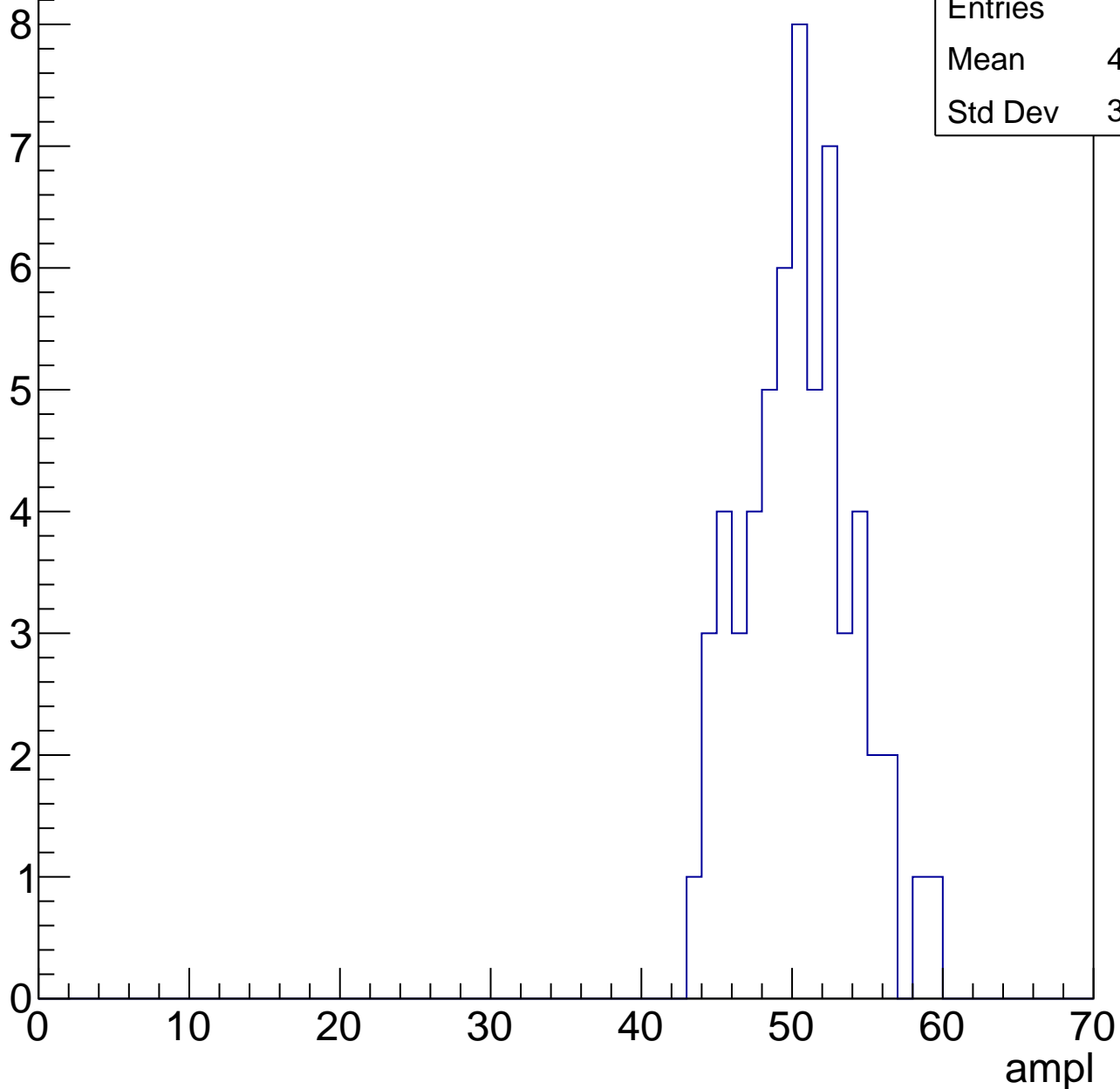


B1L101S, U15-ch73, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.97
Std Dev	3.584

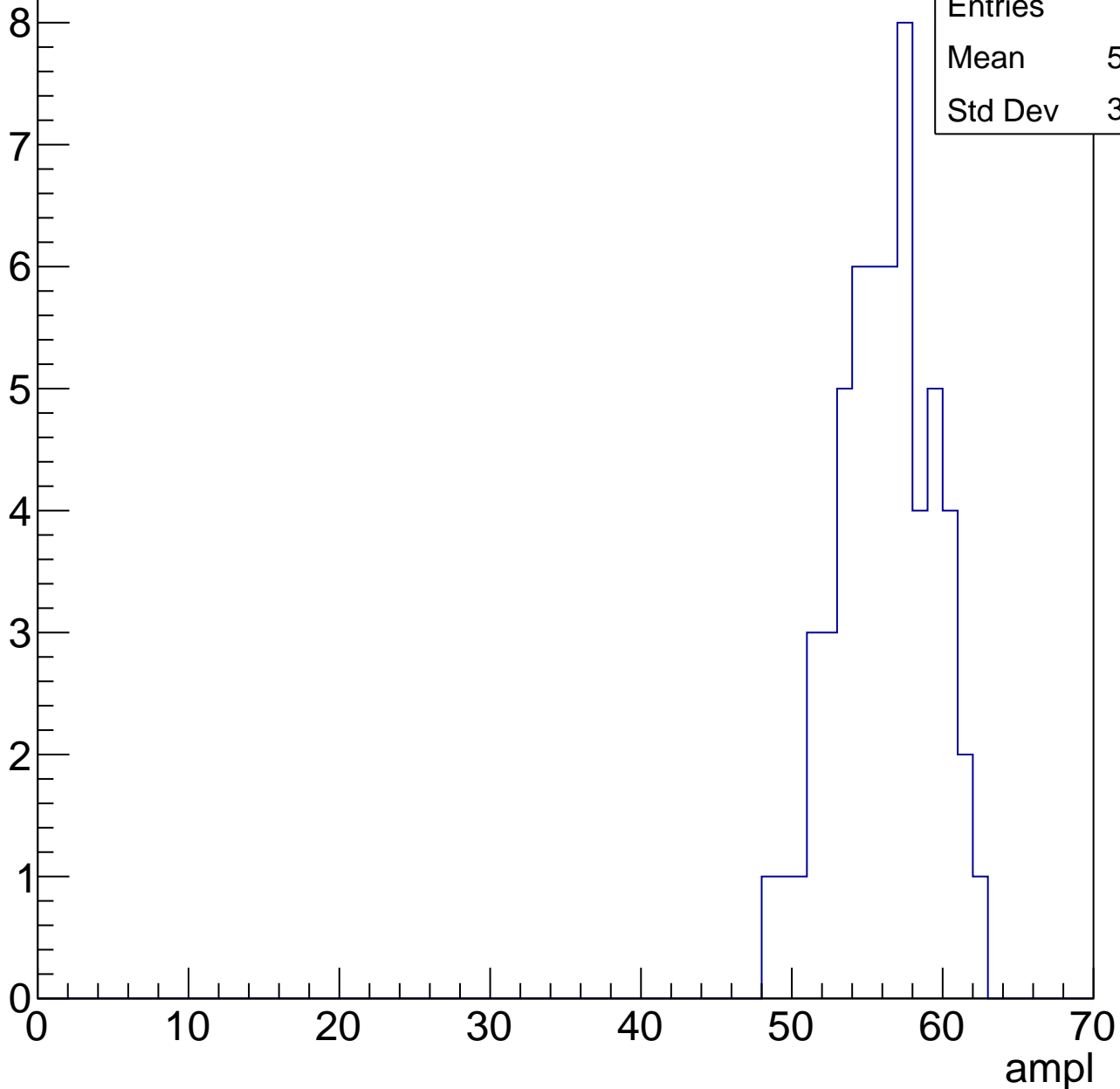


B1L101S, U15-ch73, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

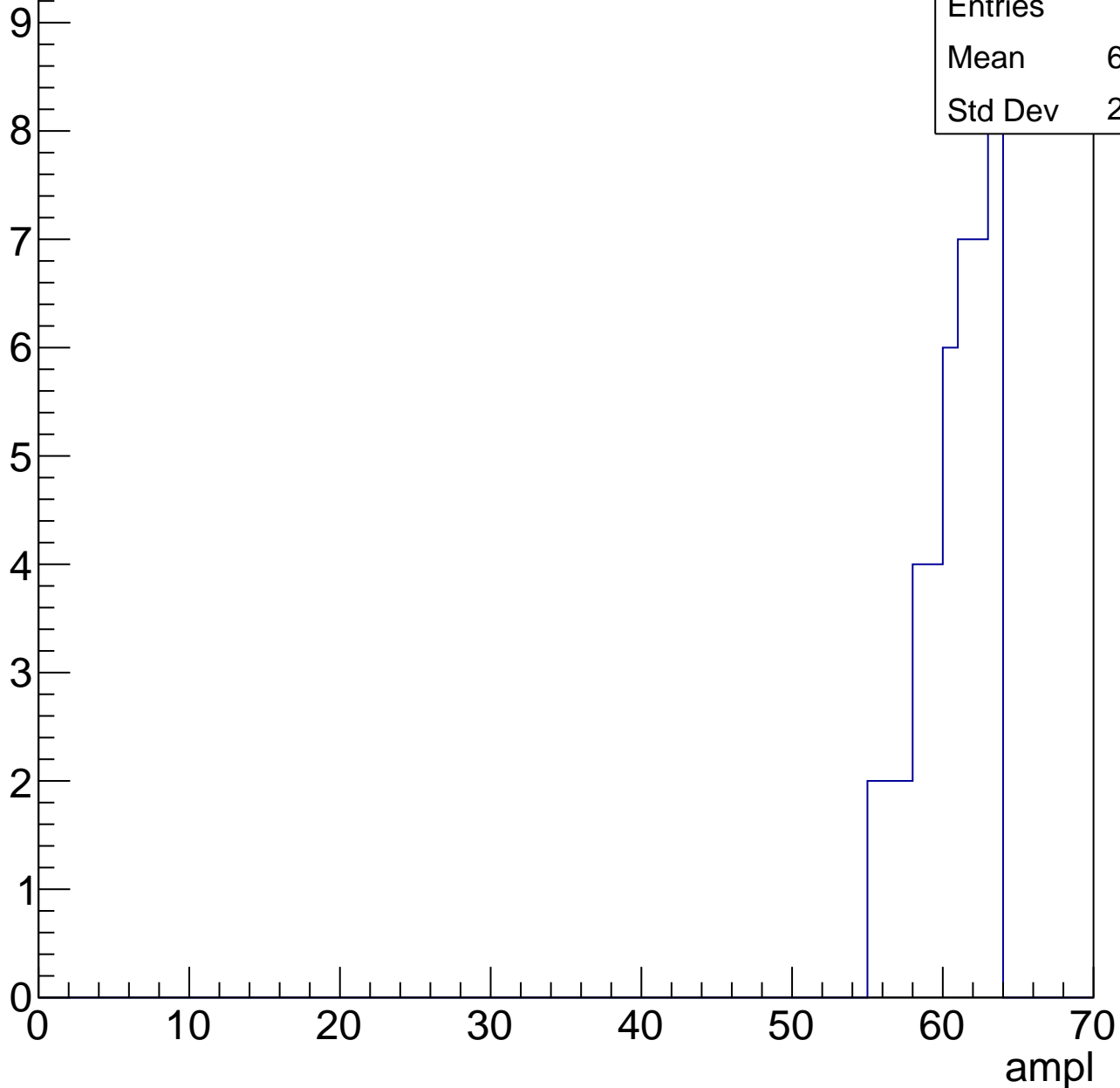
Entries	56
Mean	55.68
Std Dev	3.163



B1L101S, U15-ch73, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

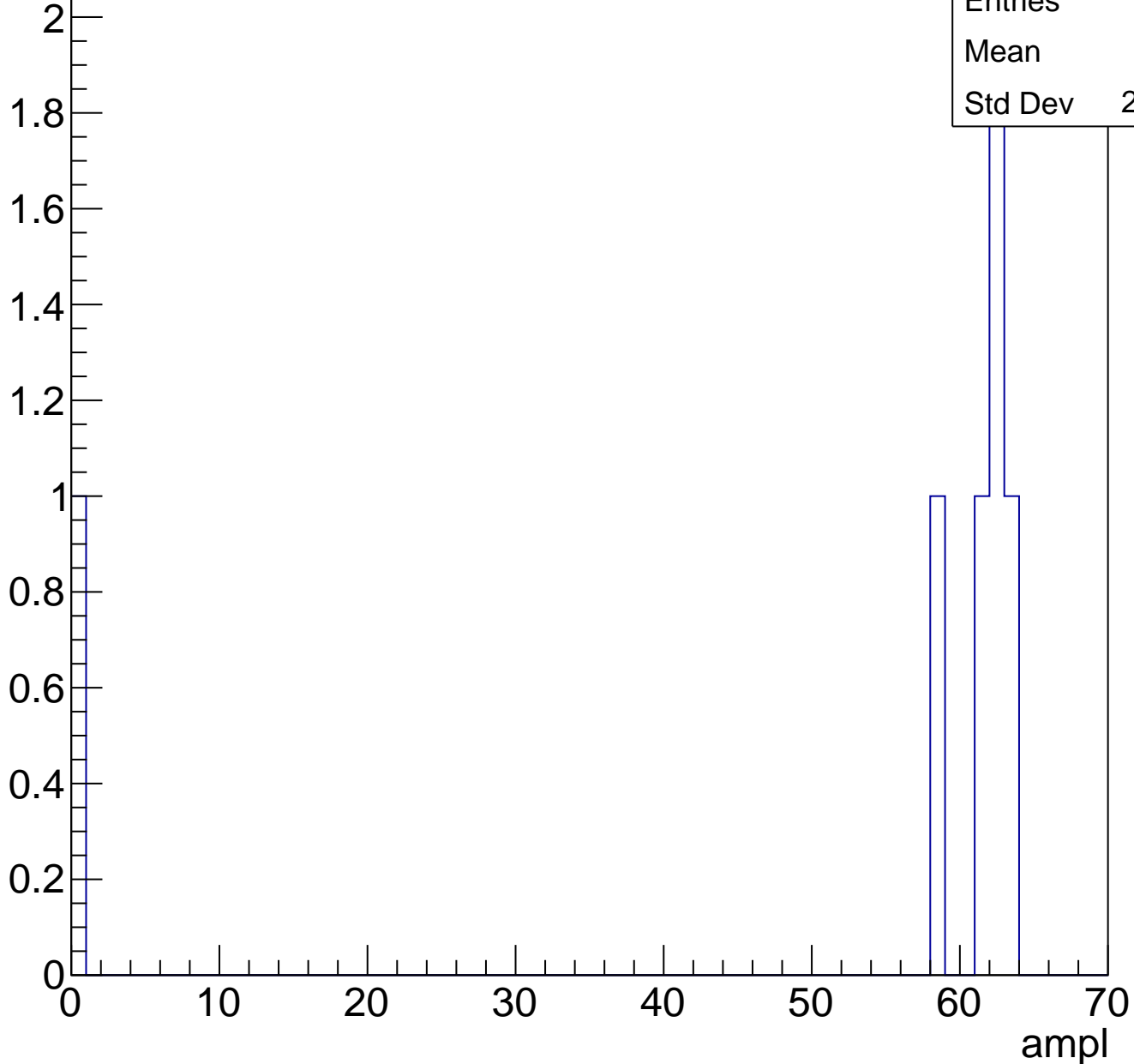


Entries	43
Mean	60.28
Std Dev	2.326

B1L101S, U15-ch73, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch73, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch74, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	63
Mean	28.62
Std Dev	3.268

Gaus mean : 28.9237

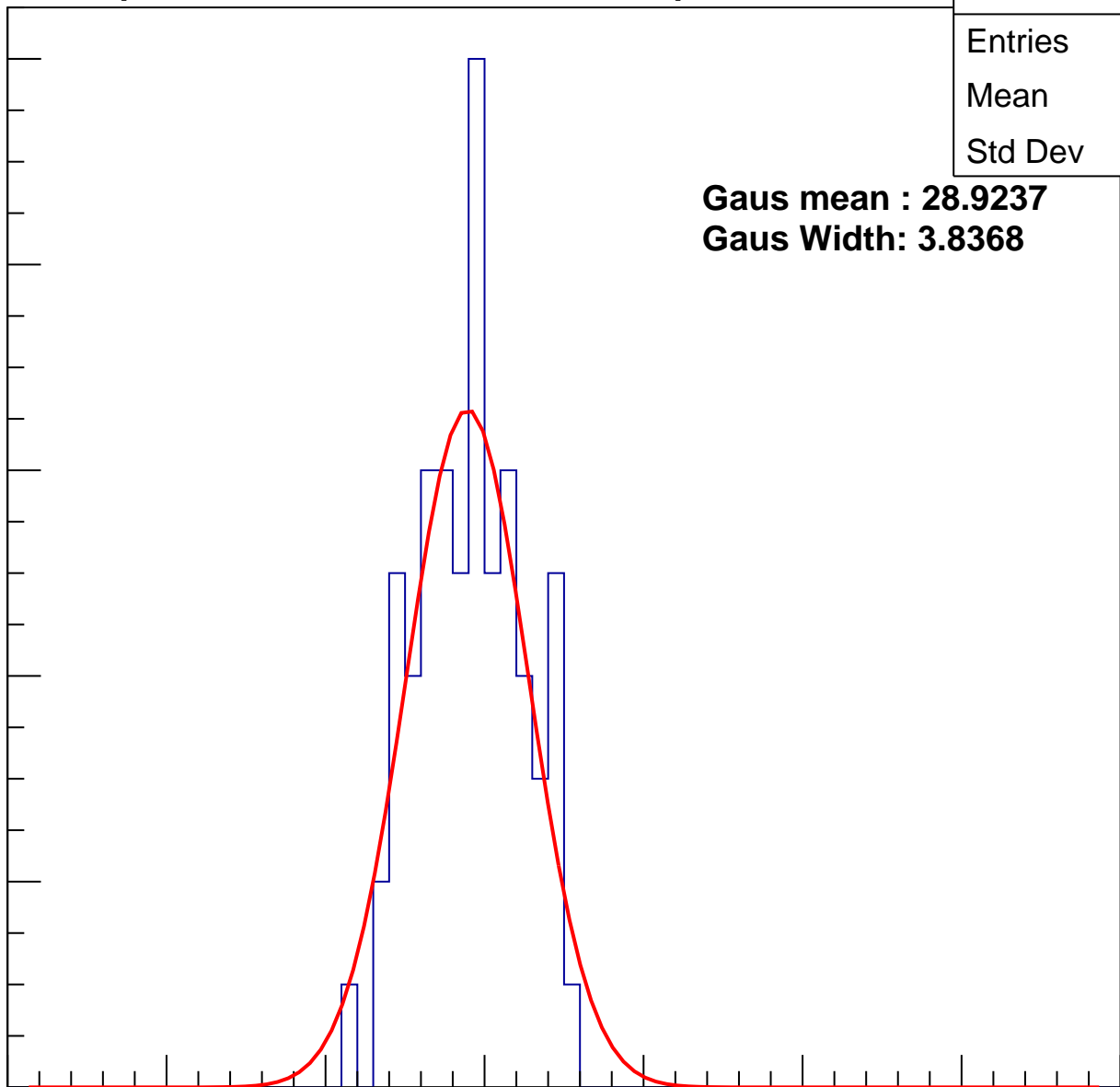
Gaus Width: 3.8368

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch74, adc1

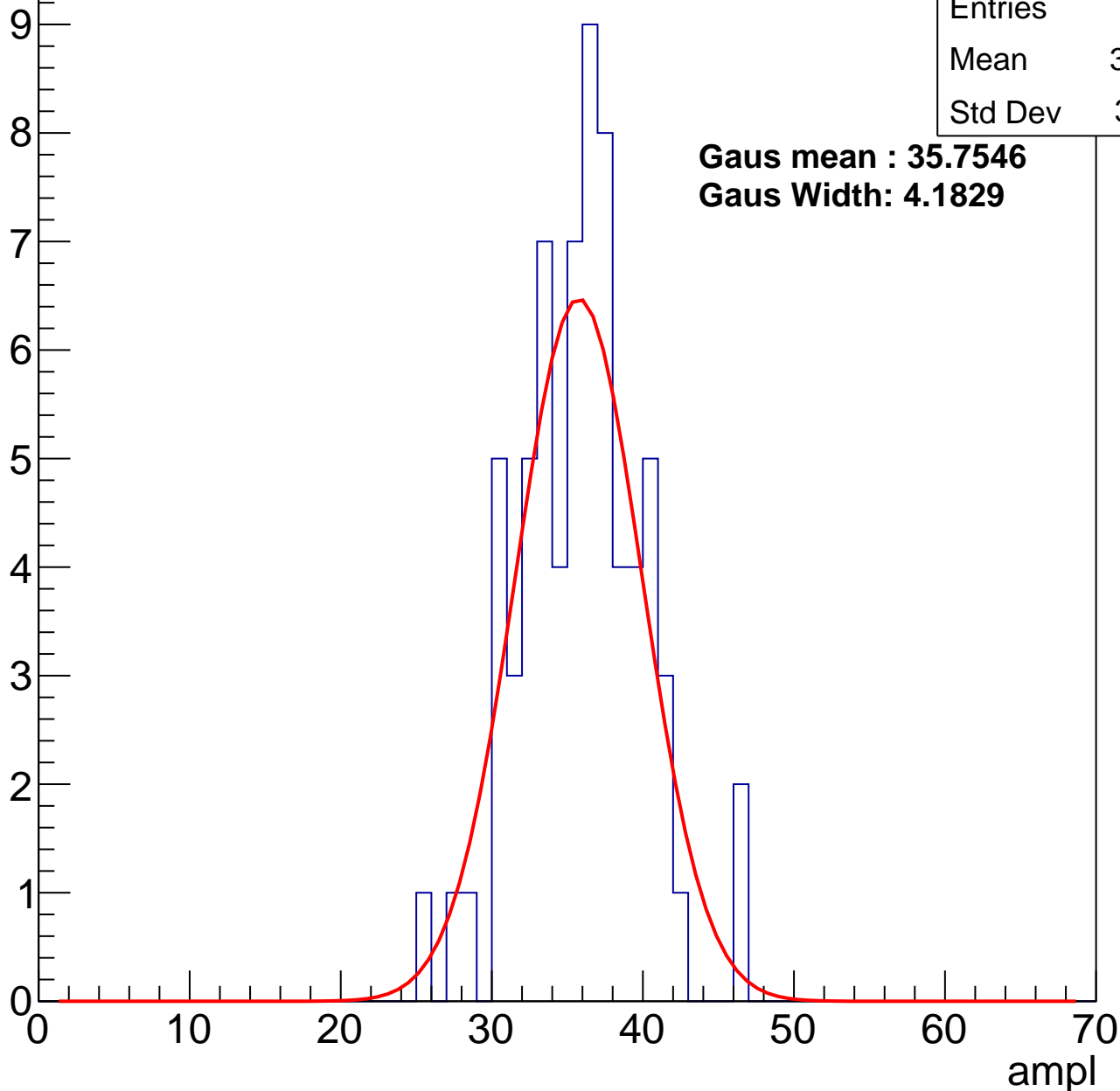
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.43
Std Dev	3.991

Gaus mean : 35.7546

Gaus Width: 4.1829



B1L101S, U15-ch74, adc2

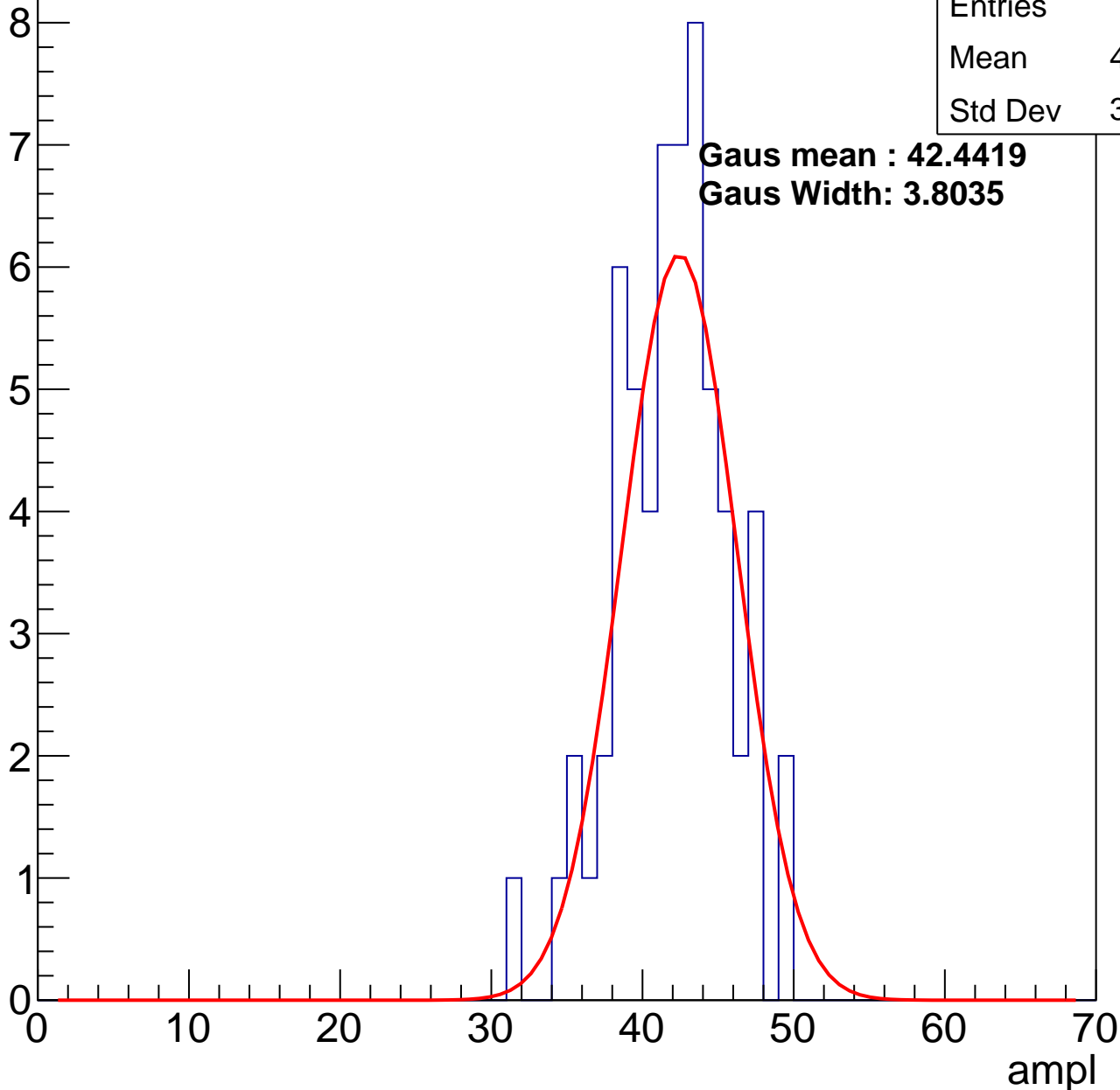
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	41.49
Std Dev	3.647

Gaus mean : 42.4419

Gaus Width: 3.8035

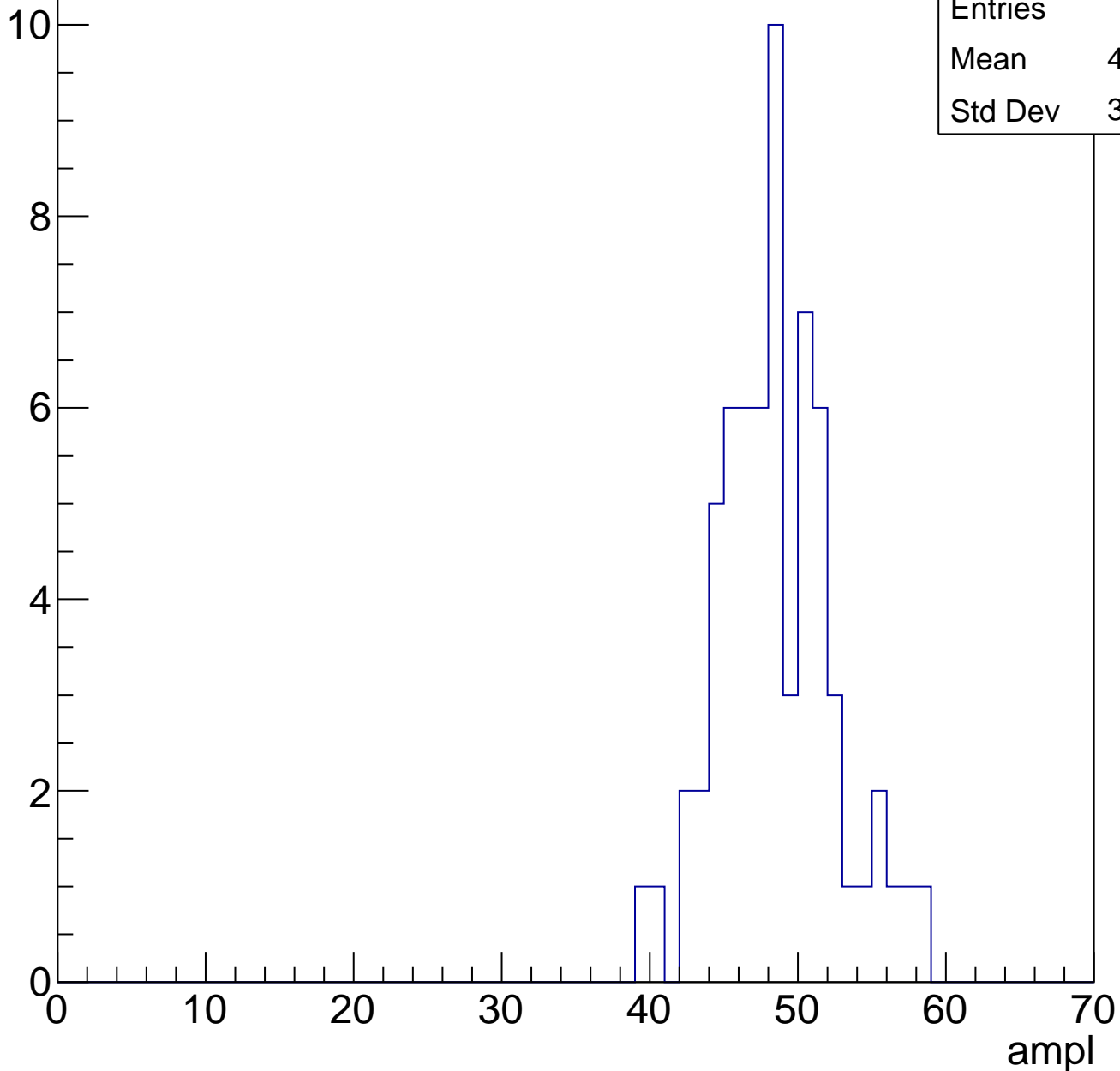


B1L101S, U15-ch74, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	65
Mean	48.06
Std Dev	3.859

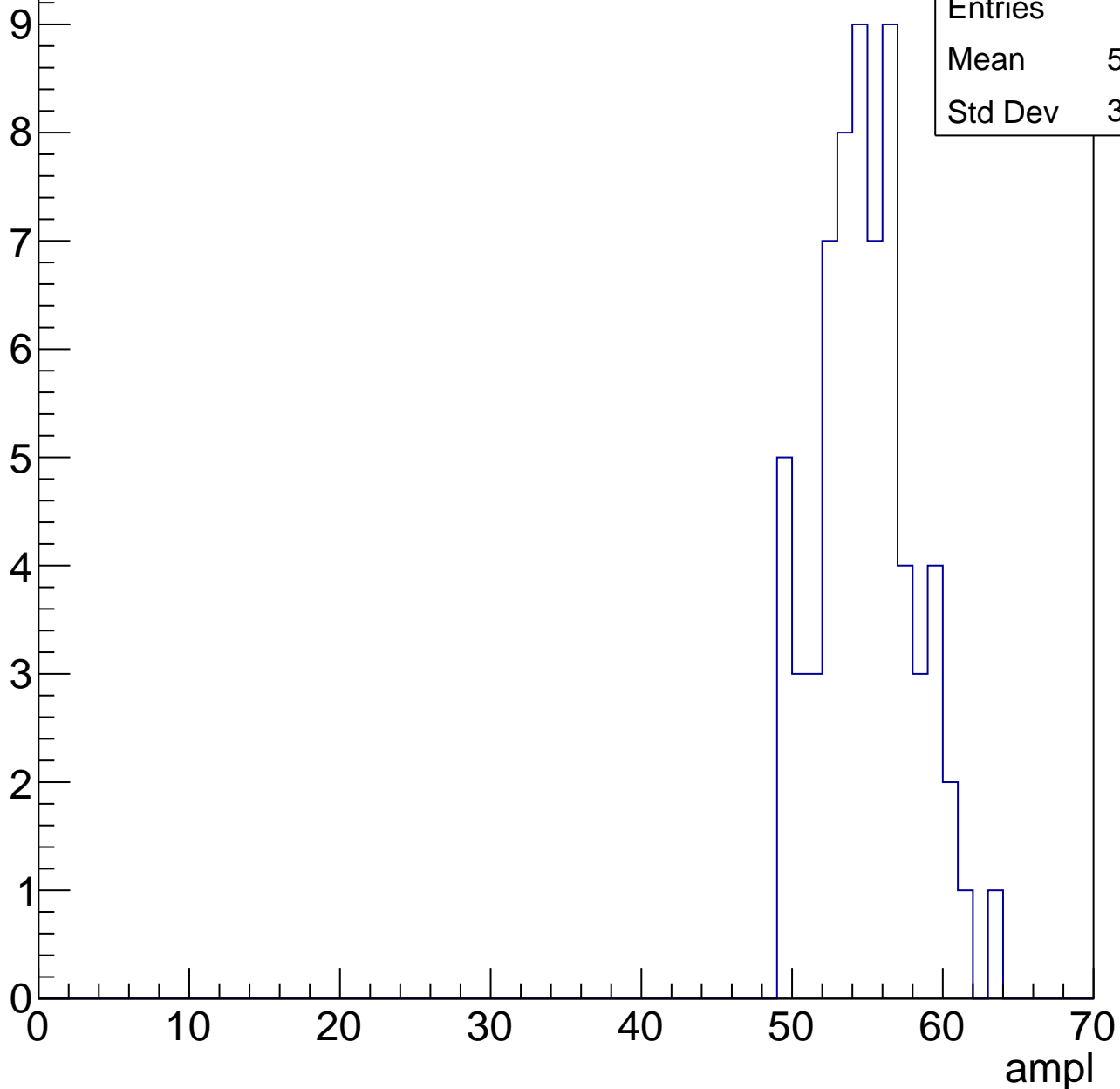
Entry



B1L101S, U15-ch74, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

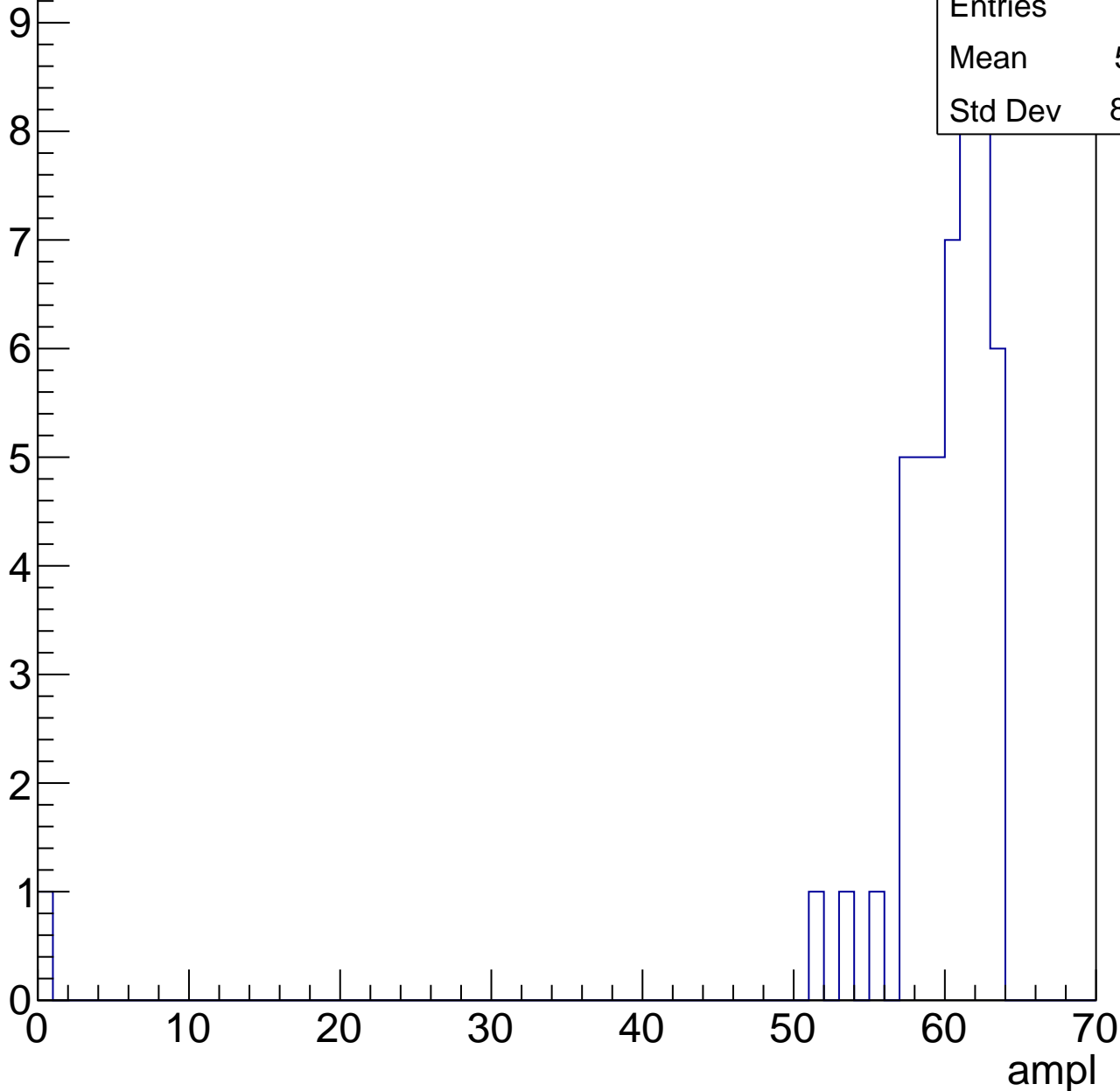


B1L101S, U15-ch74, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

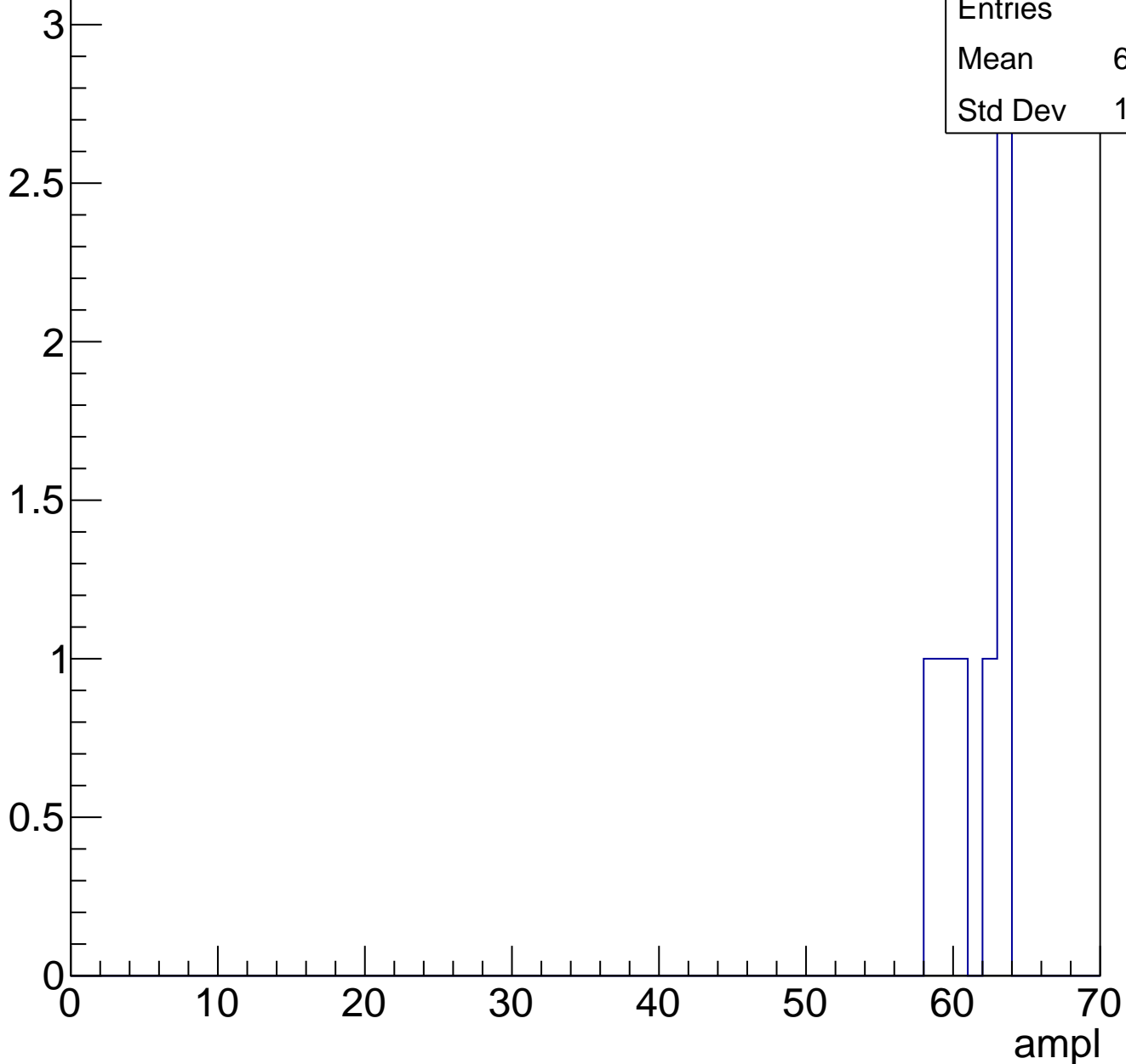
Entries	49
Mean	58.61
Std Dev	8.836



B1L101S, U15-ch74, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch74, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U15-ch75, adc0

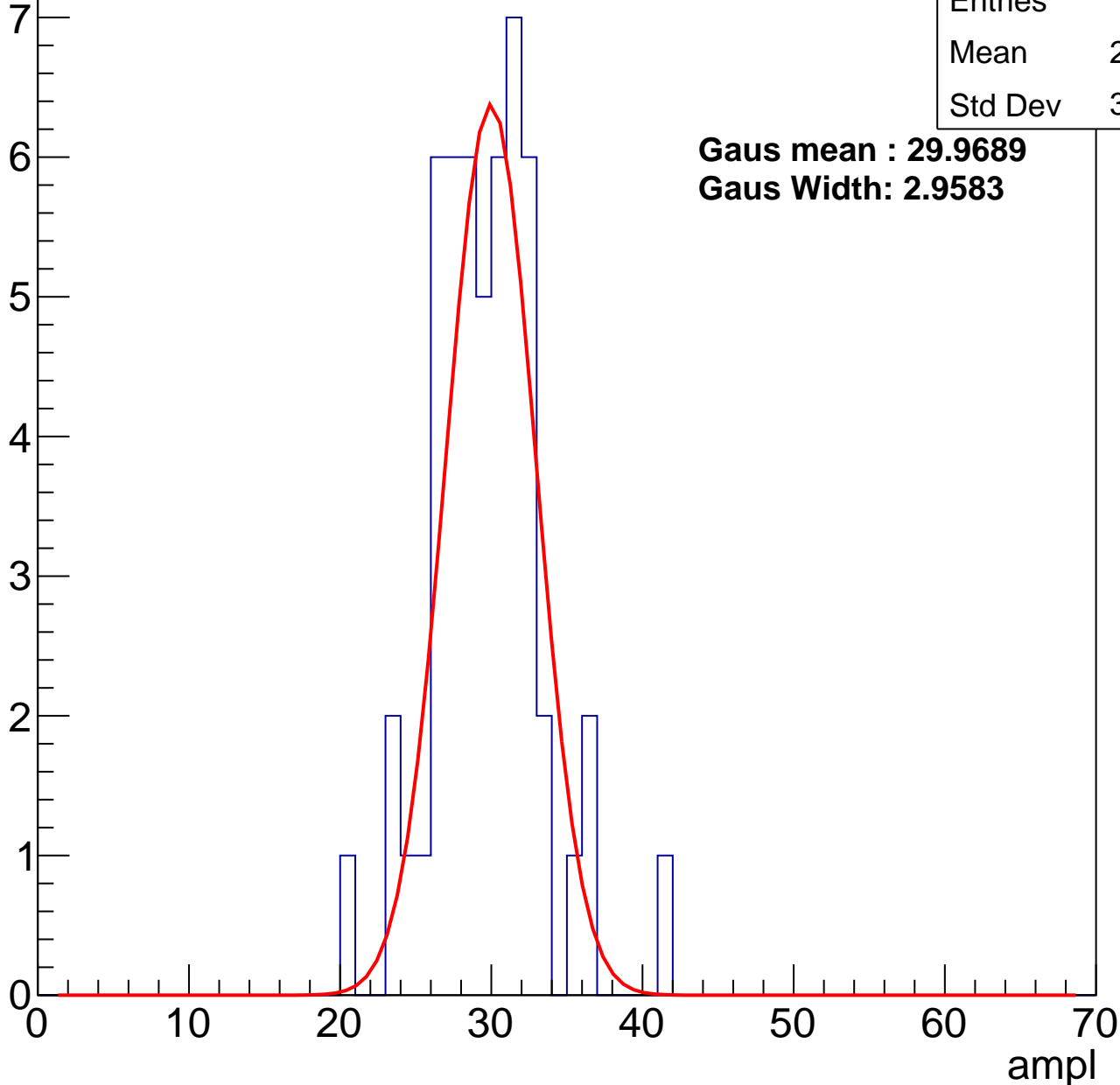
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.23
Std Dev	3.564

Gaus mean : 29.9689

Gaus Width: 2.9583



B1L101S, U15-ch75, adc1

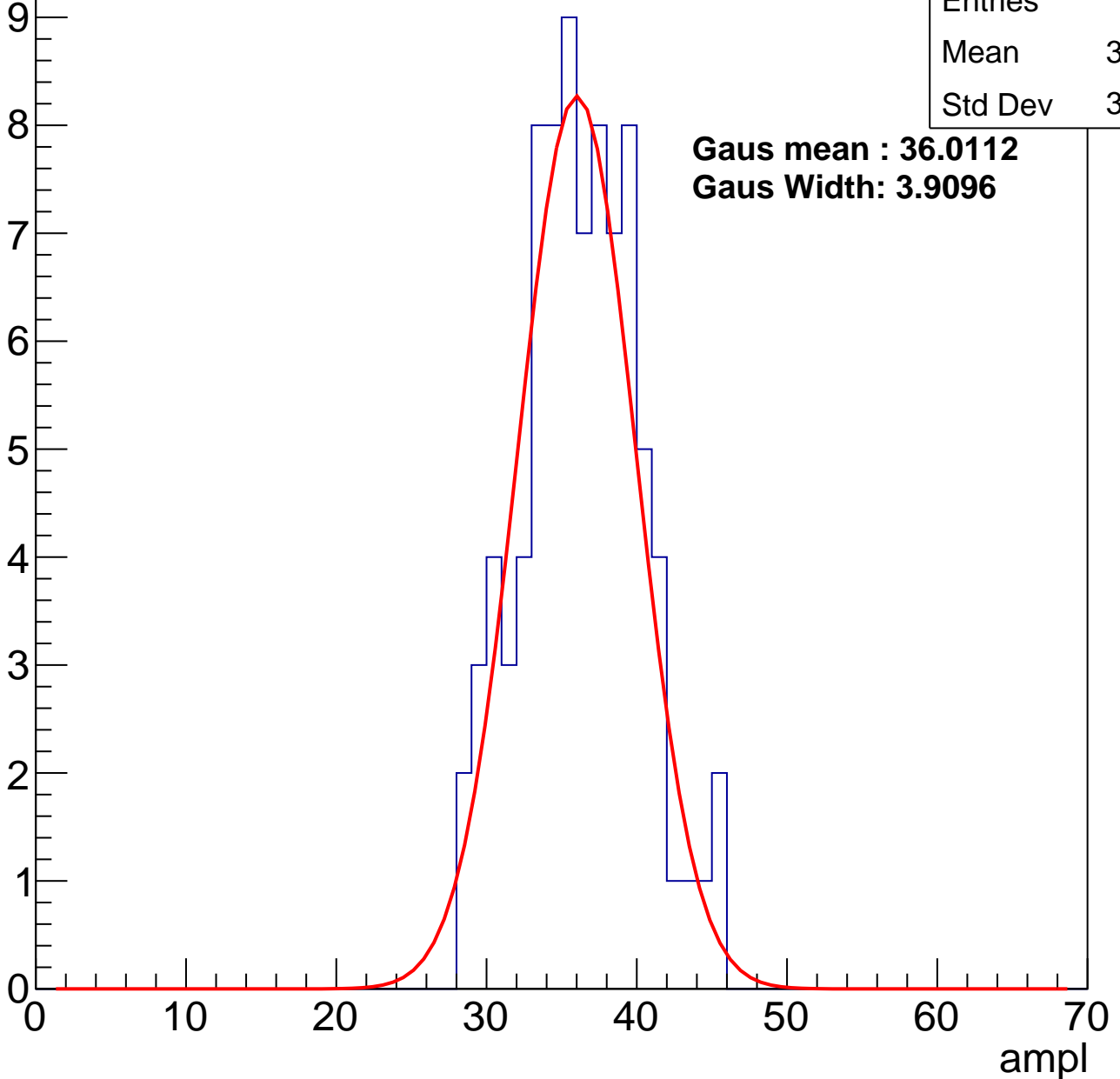
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	35.81
Std Dev	3.861

Gaus mean : 36.0112

Gaus Width: 3.9096



B1L101S, U15-ch75, adc2

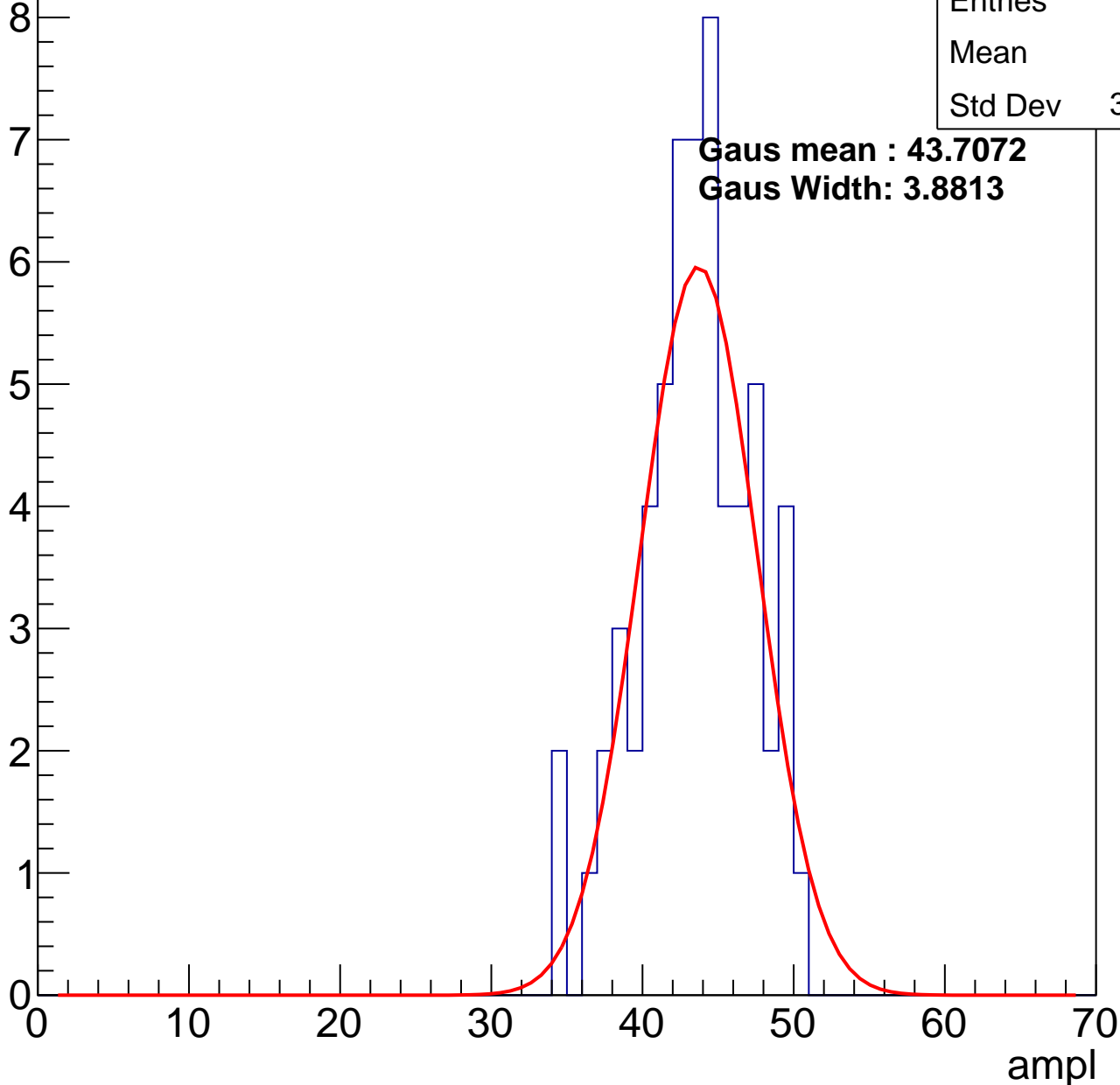
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43
Std Dev	3.702

Gaus mean : 43.7072

Gaus Width: 3.8813

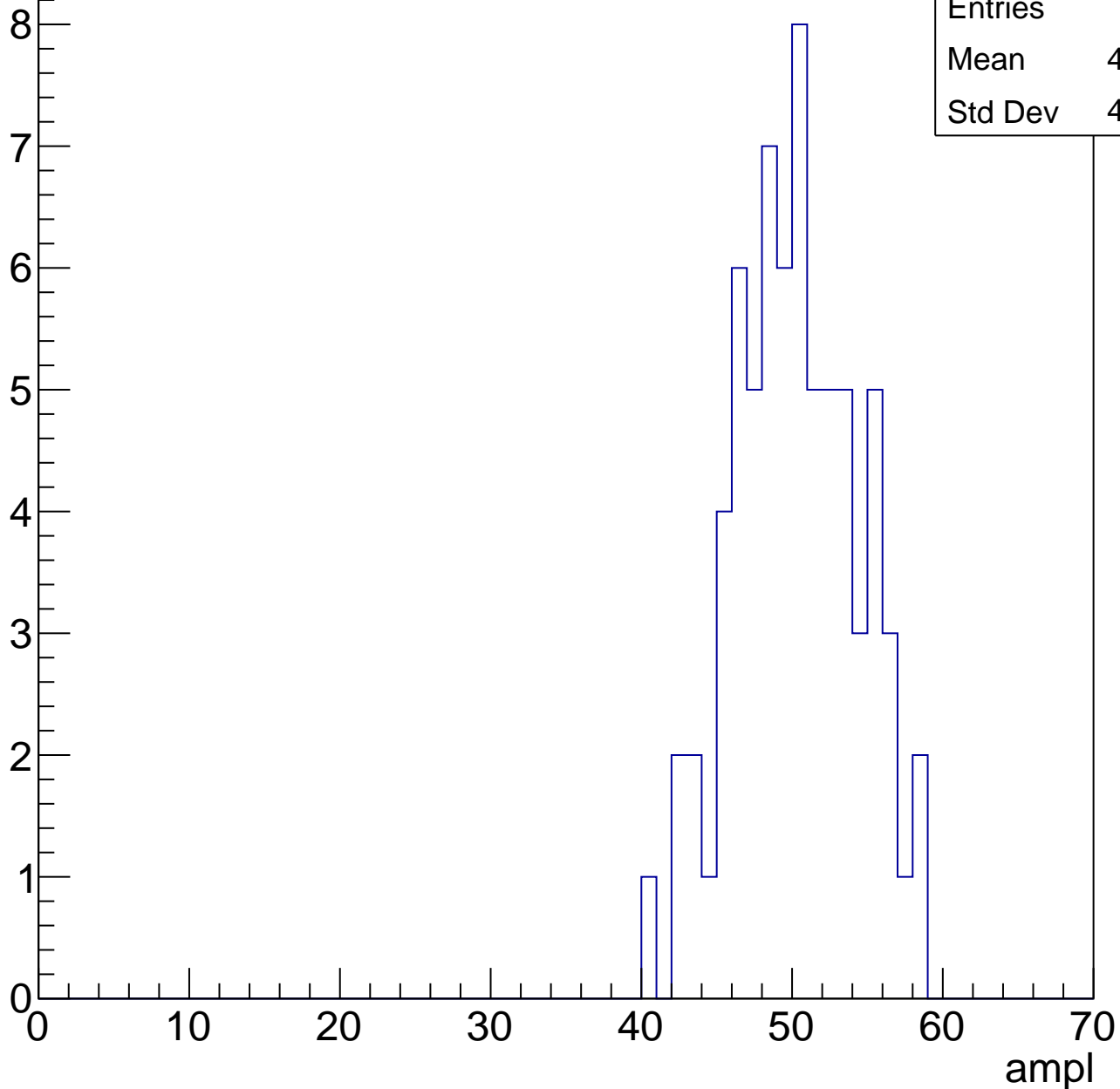


B1L101S, U15-ch75, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	49.76
Std Dev	4.044

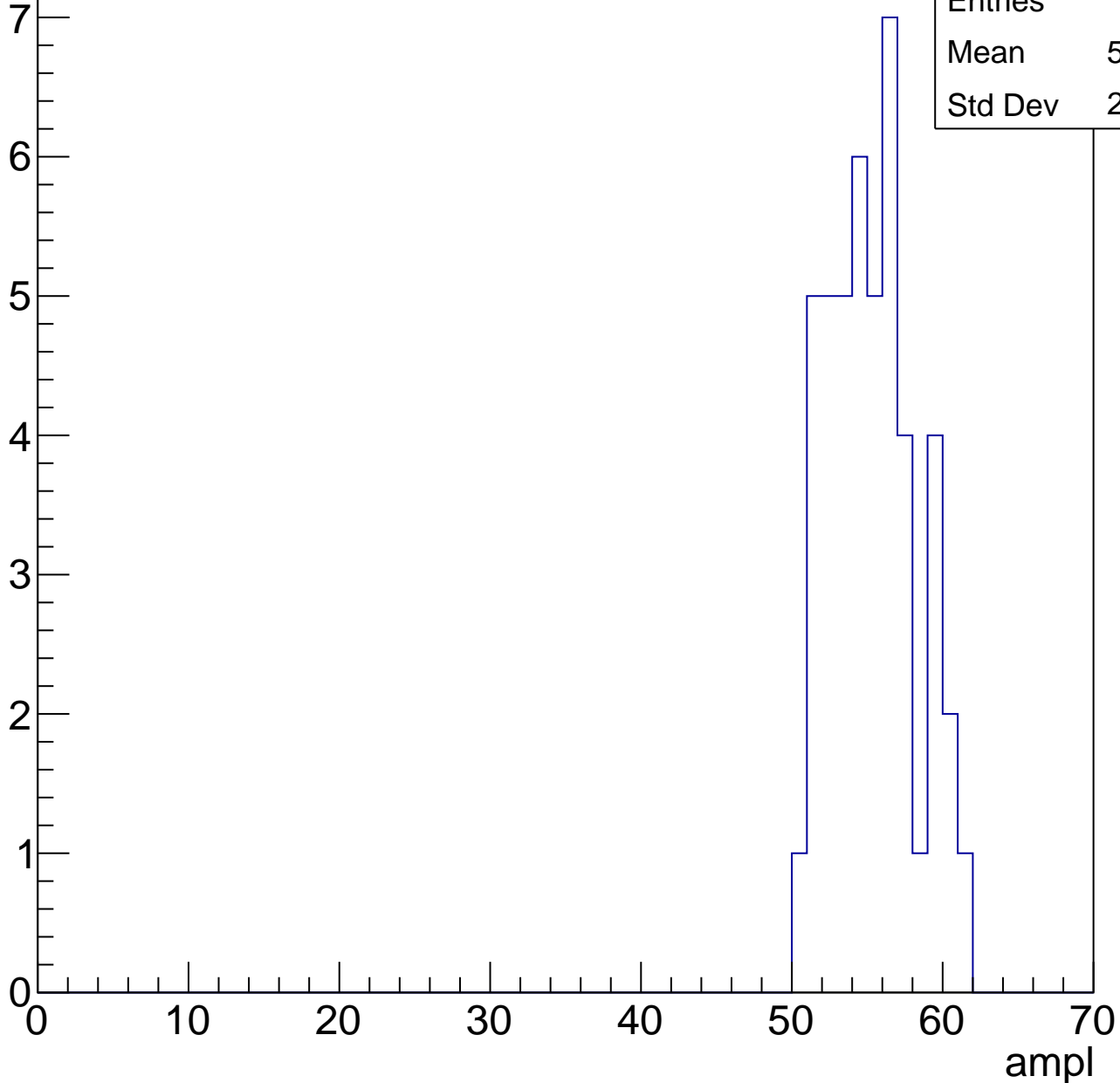


B1L101S, U15-ch75, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	54.87
Std Dev	2.787

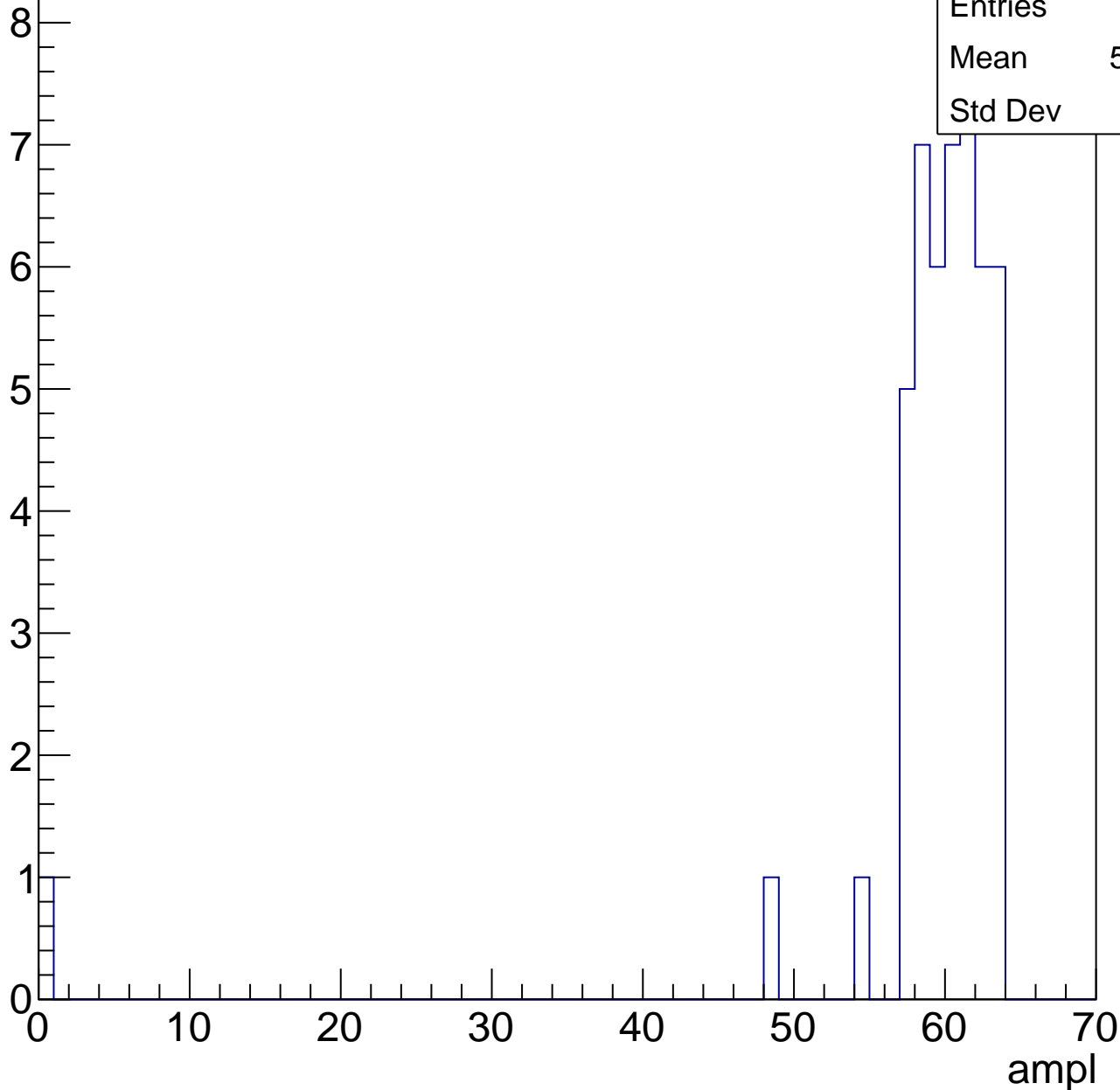


B1L101S, U15-ch75, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	58.44
Std Dev	8.93



B1L101S, U15-ch75, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch75, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch76, adc0

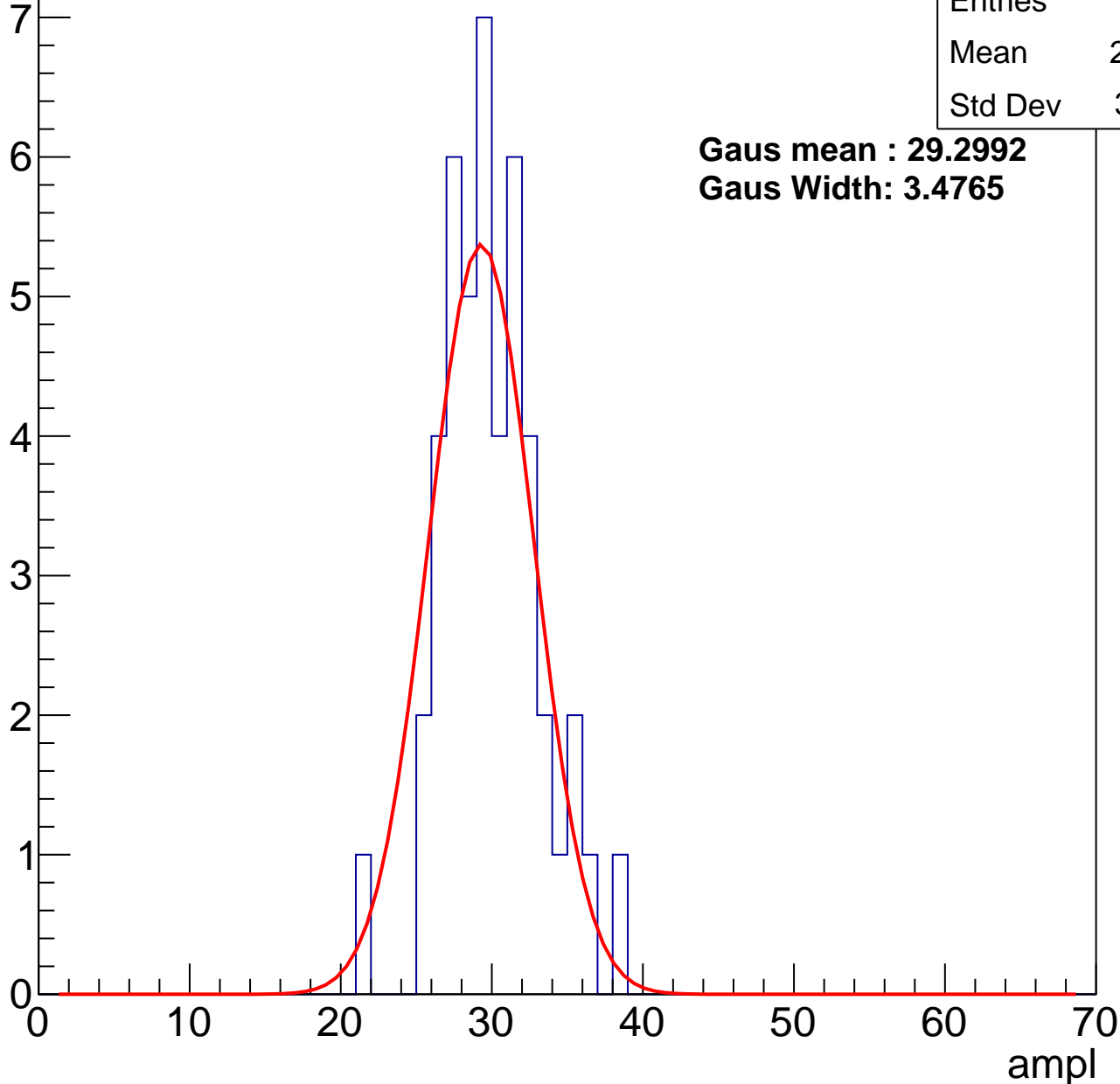
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	29.52
Std Dev	3.201

Gaus mean : 29.2992

Gaus Width: 3.4765



B1L101S, U15-ch76, adc1

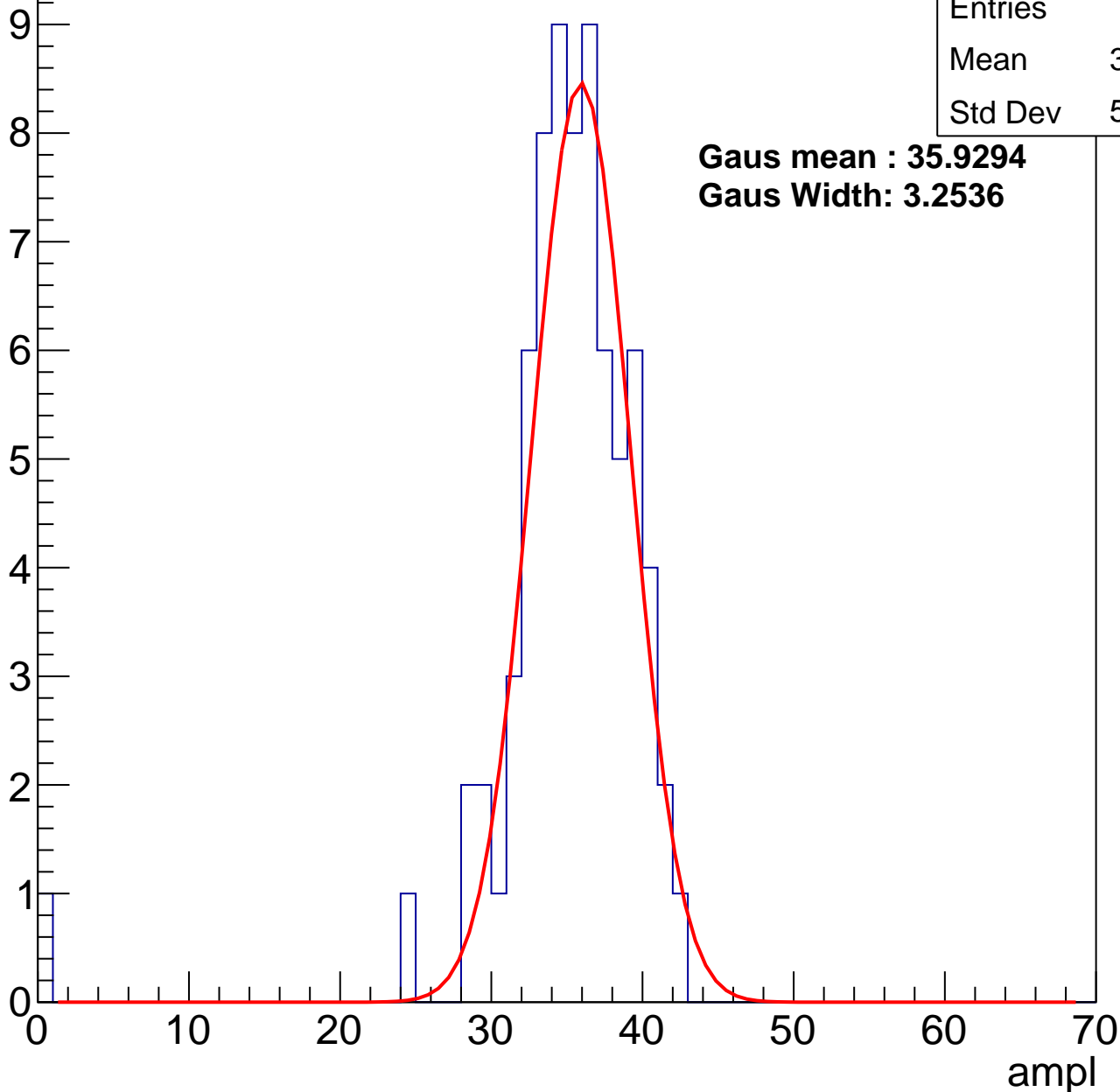
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	34.55
Std Dev	5.282

Gaus mean : 35.9294

Gaus Width: 3.2536



B1L101S, U15-ch76, adc2

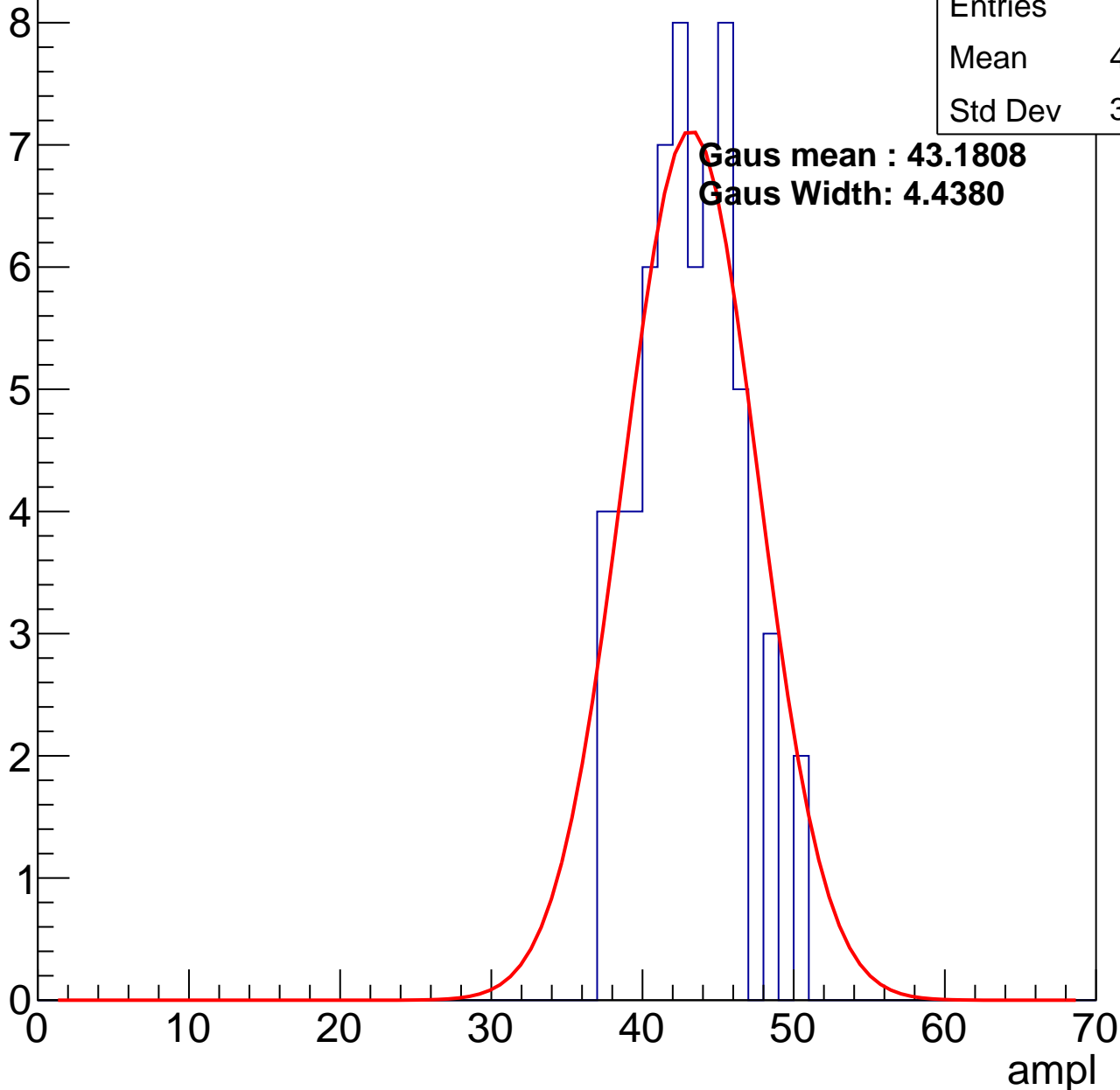
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.48
Std Dev	3.157

Gaus mean : 43.1808

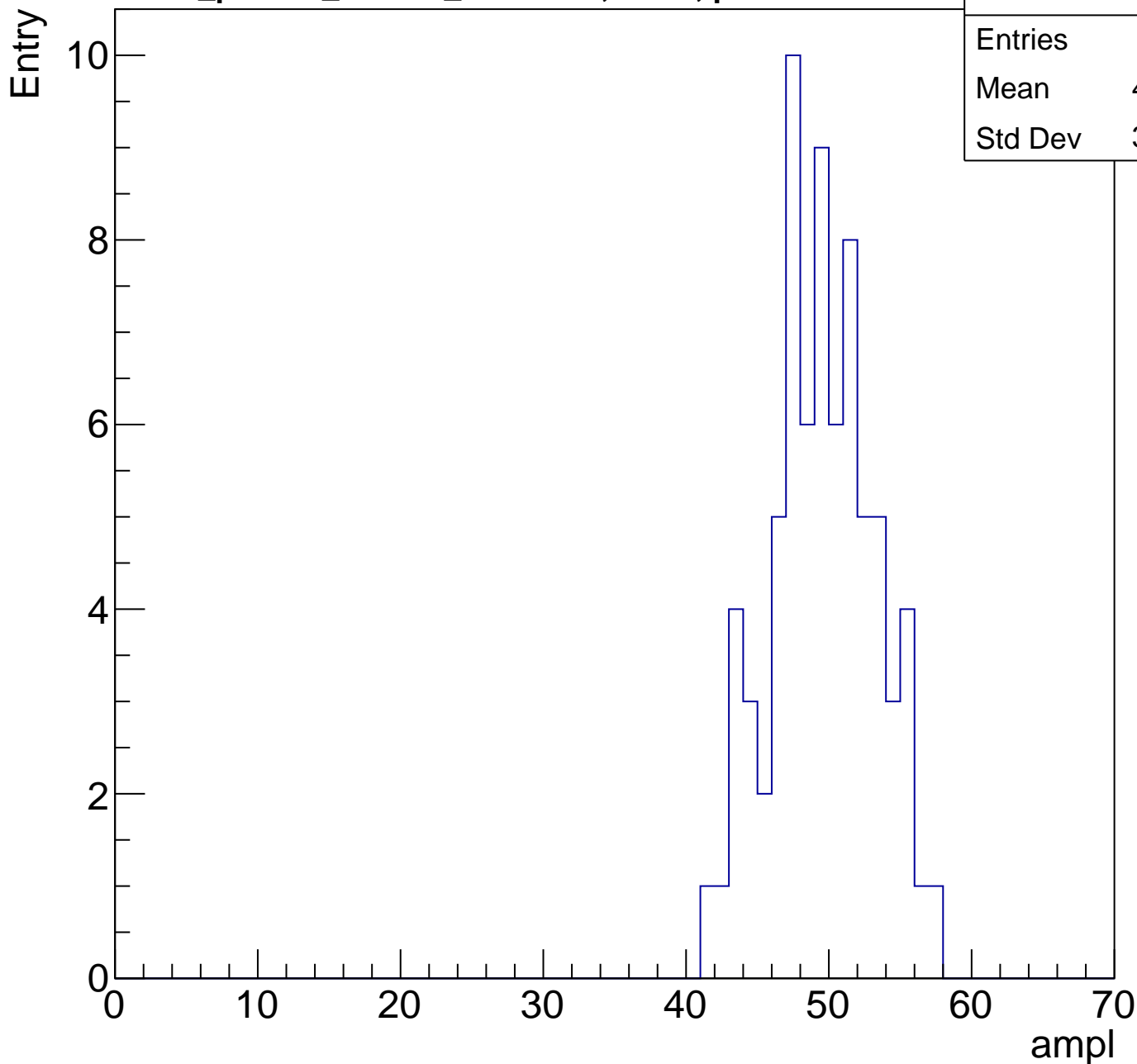
Gaus Width: 4.4380



B1L101S, U15-ch76, adc3

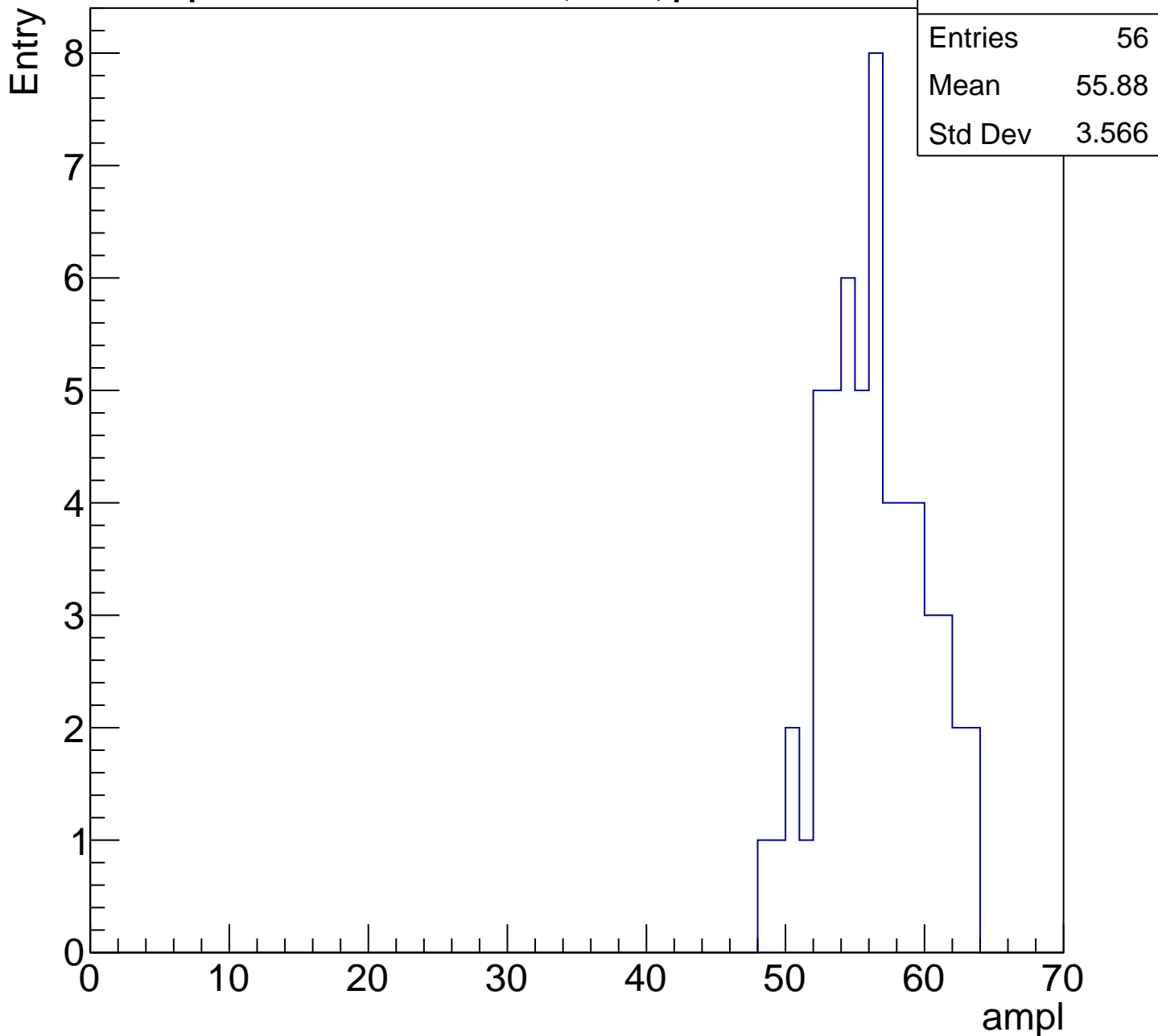
calib_packv5_042523_0143.root, FC#0, port D2

Entries	74
Mean	49.11
Std Dev	3.581



B1L101S, U15-ch76, adc4

calib_packv5_042523_0143.root, FC#0, port D2

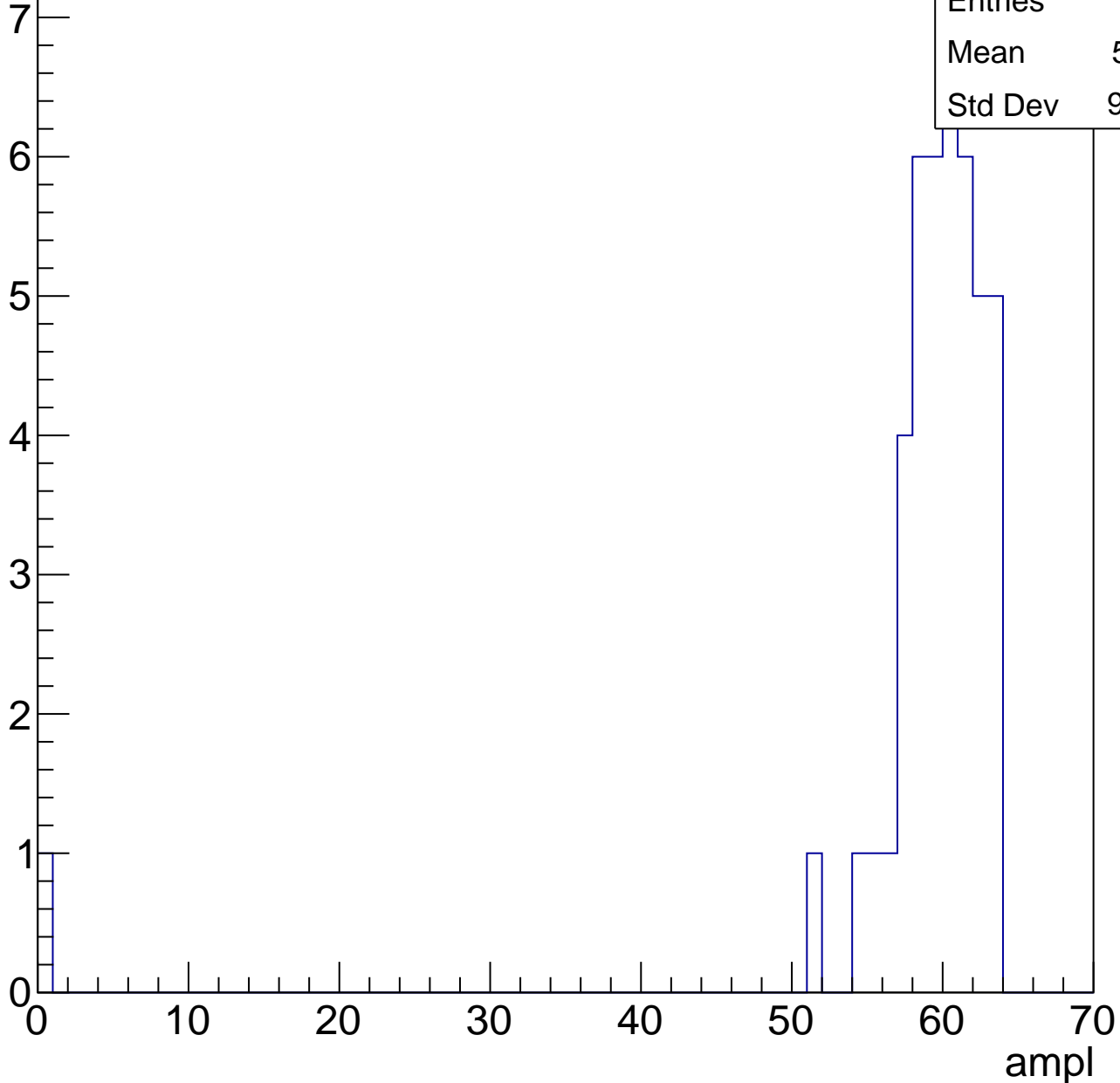


B1L101S, U15-ch76, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

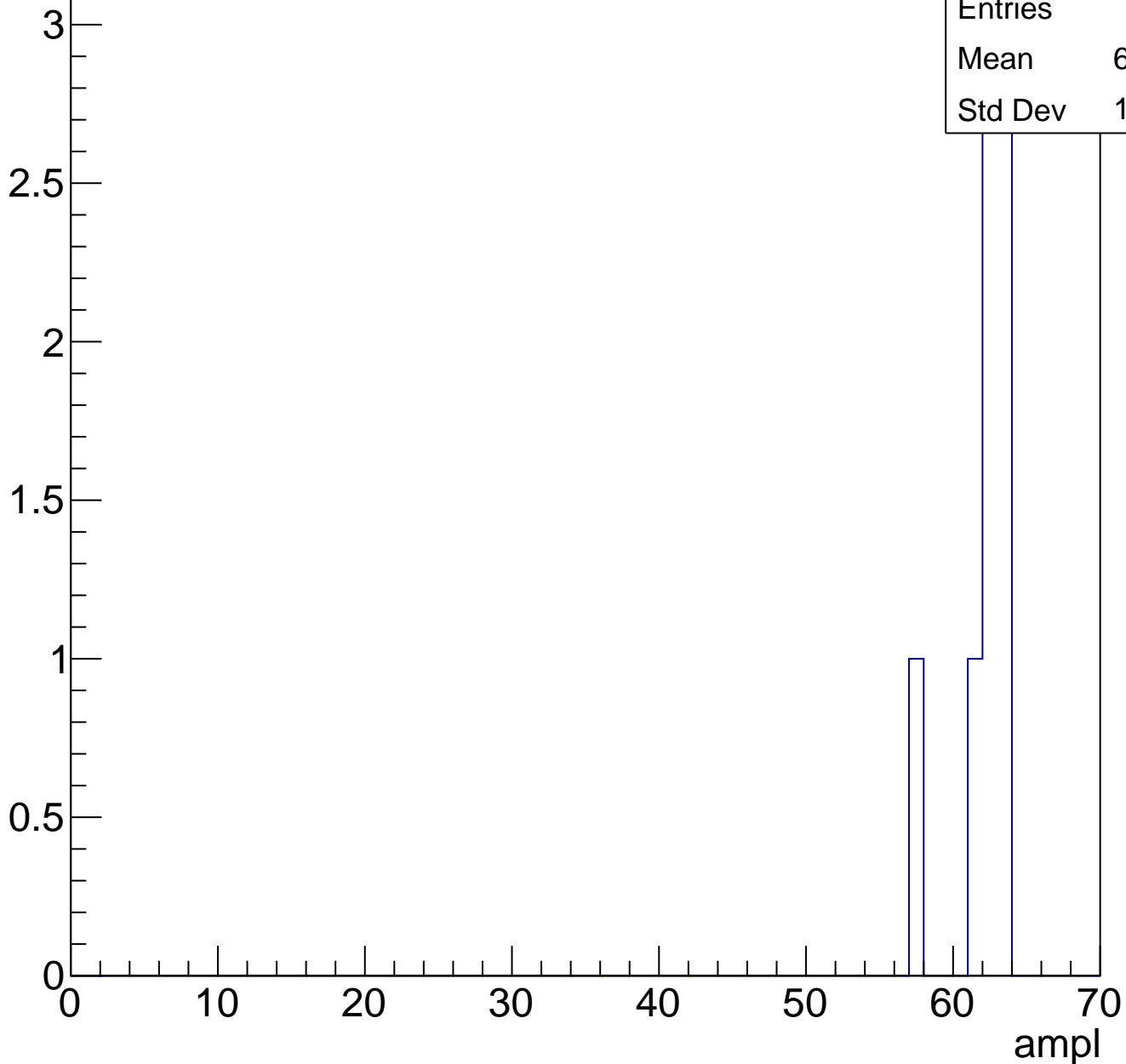
Entries	44
Mean	58.11
Std Dev	9.218



B1L101S, U15-ch76, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch76, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch77, adc0

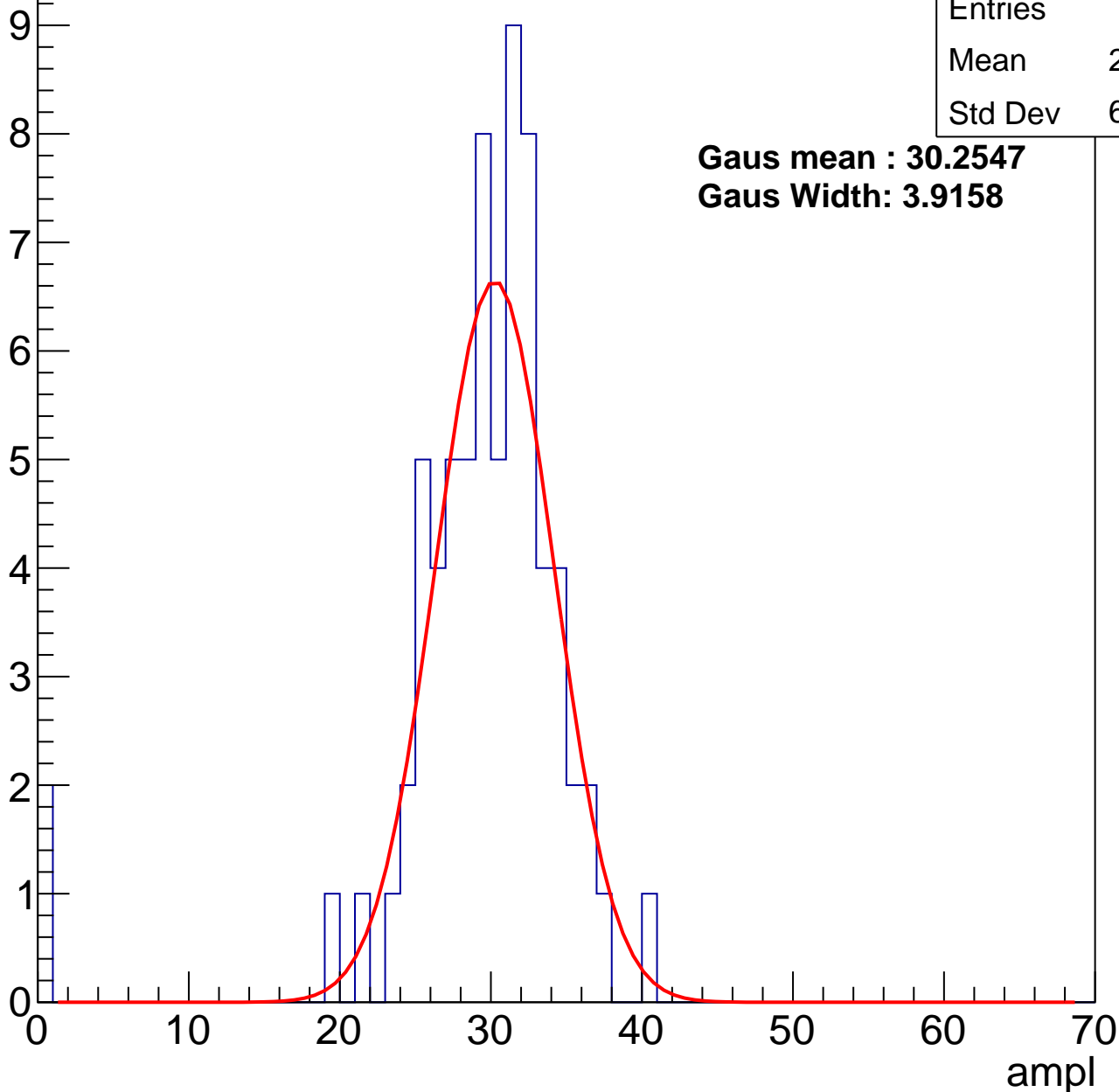
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.84
Std Dev	6.214

Gaus mean : 30.2547

Gaus Width: 3.9158



B1L101S, U15-ch77, adc1

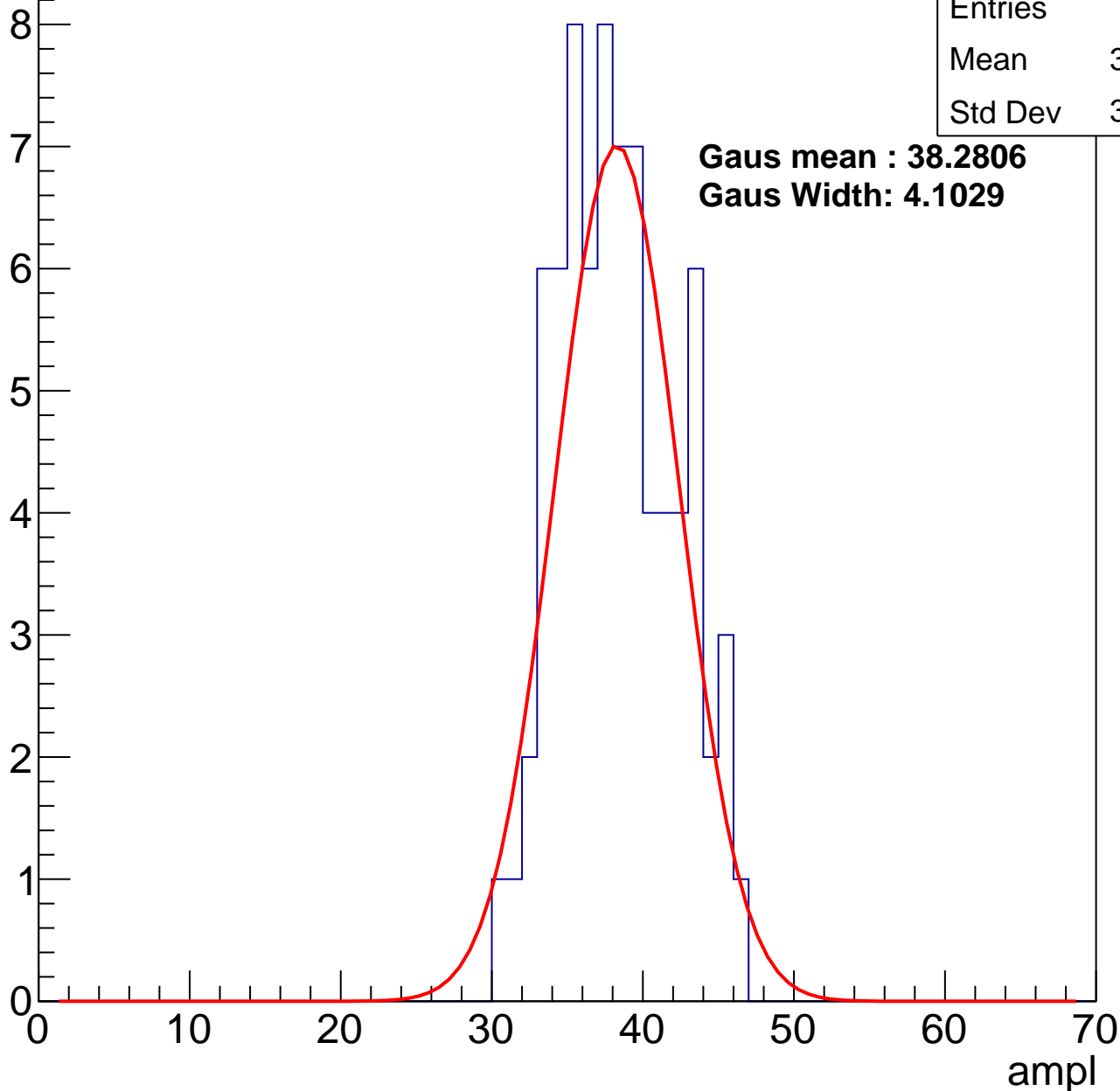
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	37.86
Std Dev	3.786

Gaus mean : 38.2806

Gaus Width: 4.1029



B1L101S, U15-ch77, adc2

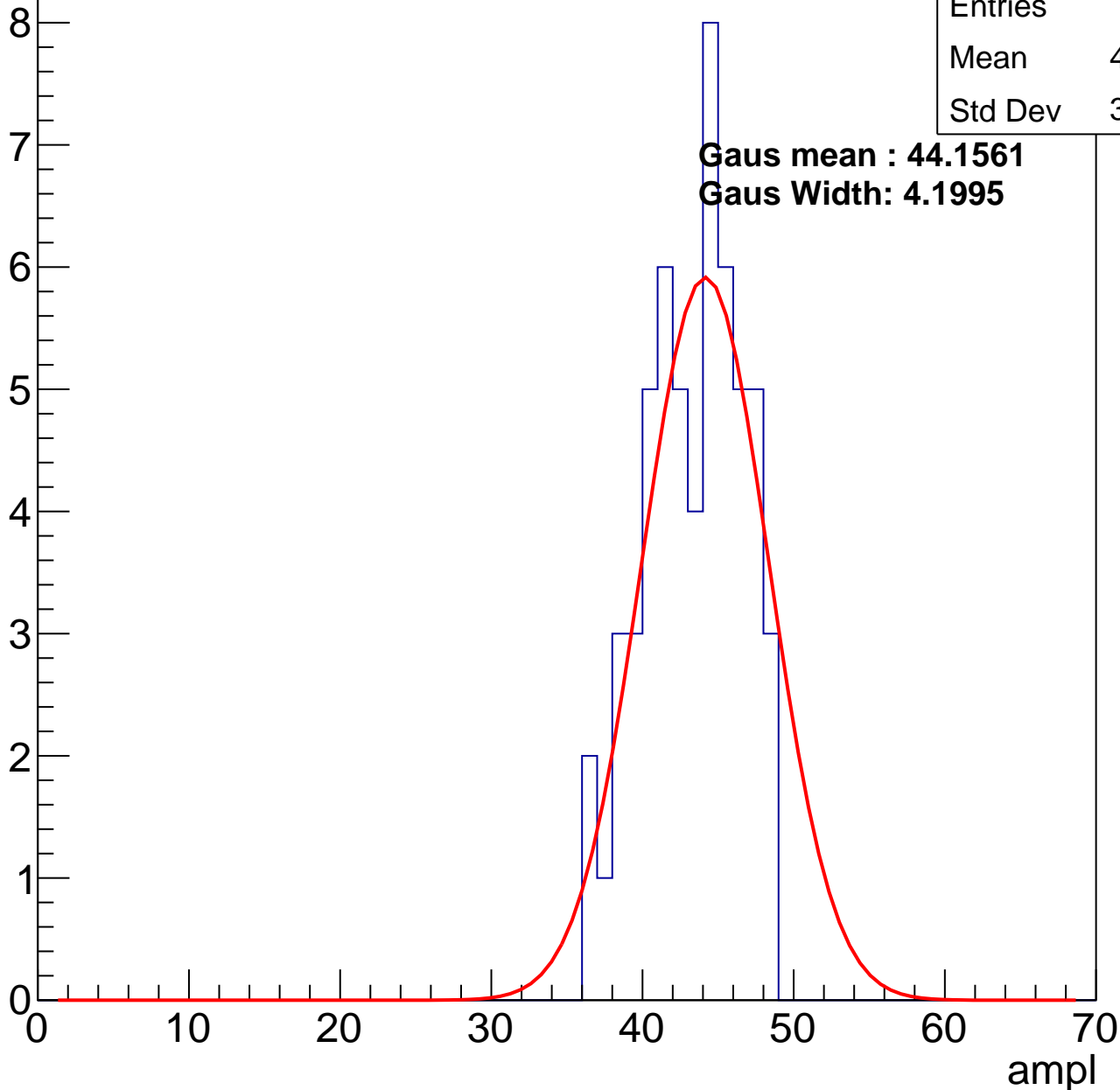
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.84
Std Dev	3.167

Gaus mean : 44.1561

Gaus Width: 4.1995

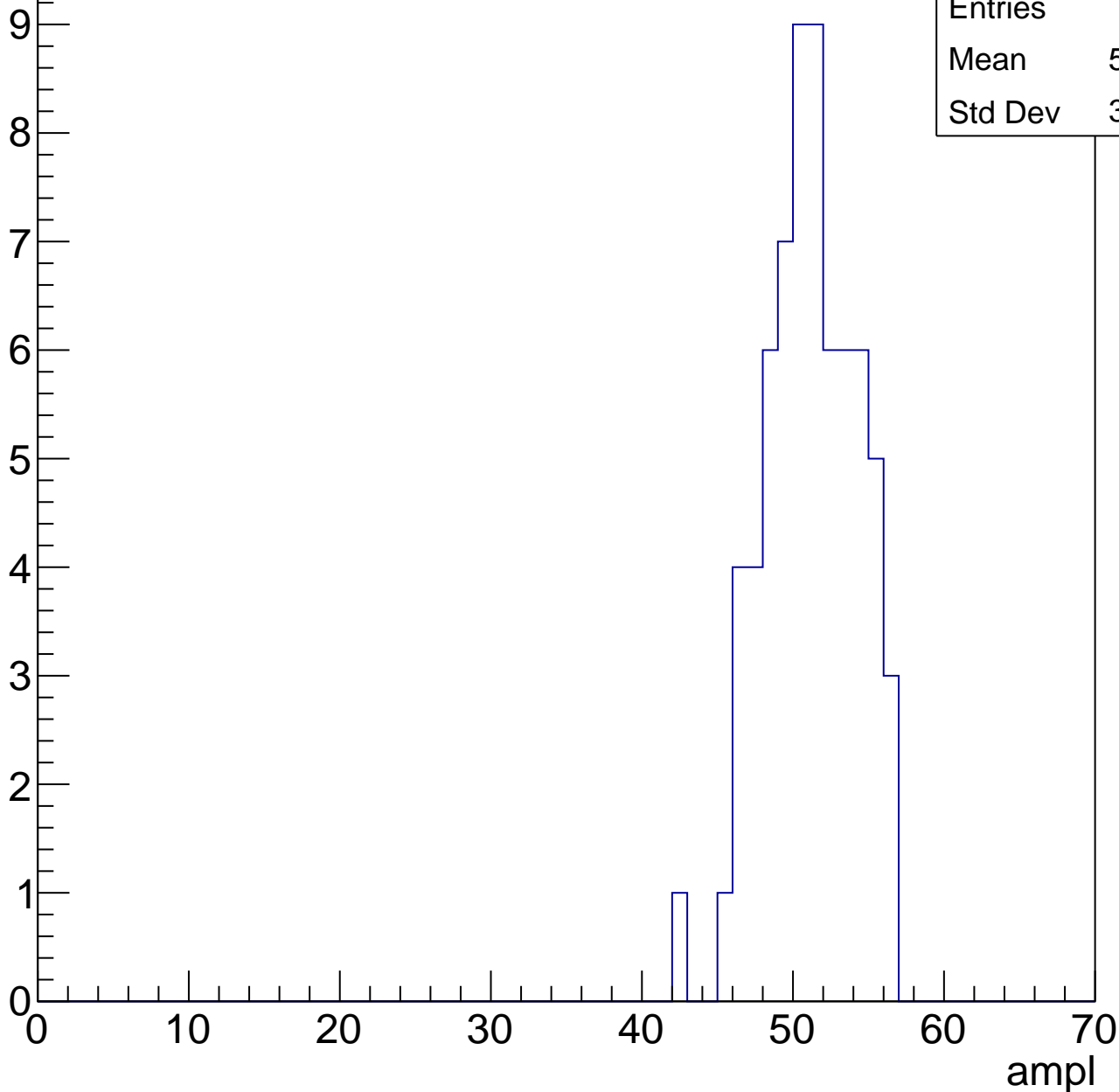


B1L101S, U15-ch77, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

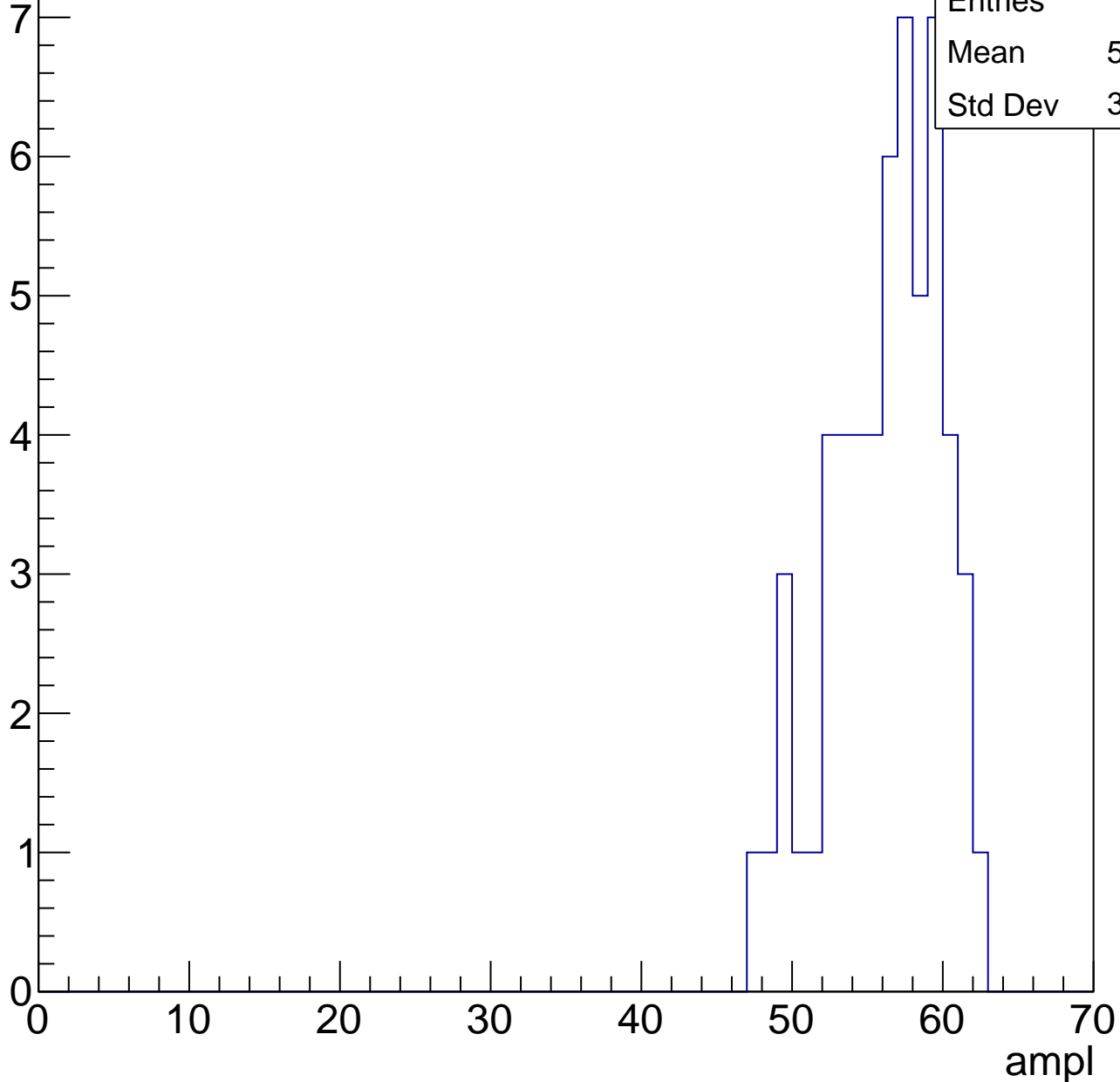
Entries	67
Mean	50.69
Std Dev	3.004



B1L101S, U15-ch77, adc4

calib_packv5_042523_0143.root, FC#0, port D2

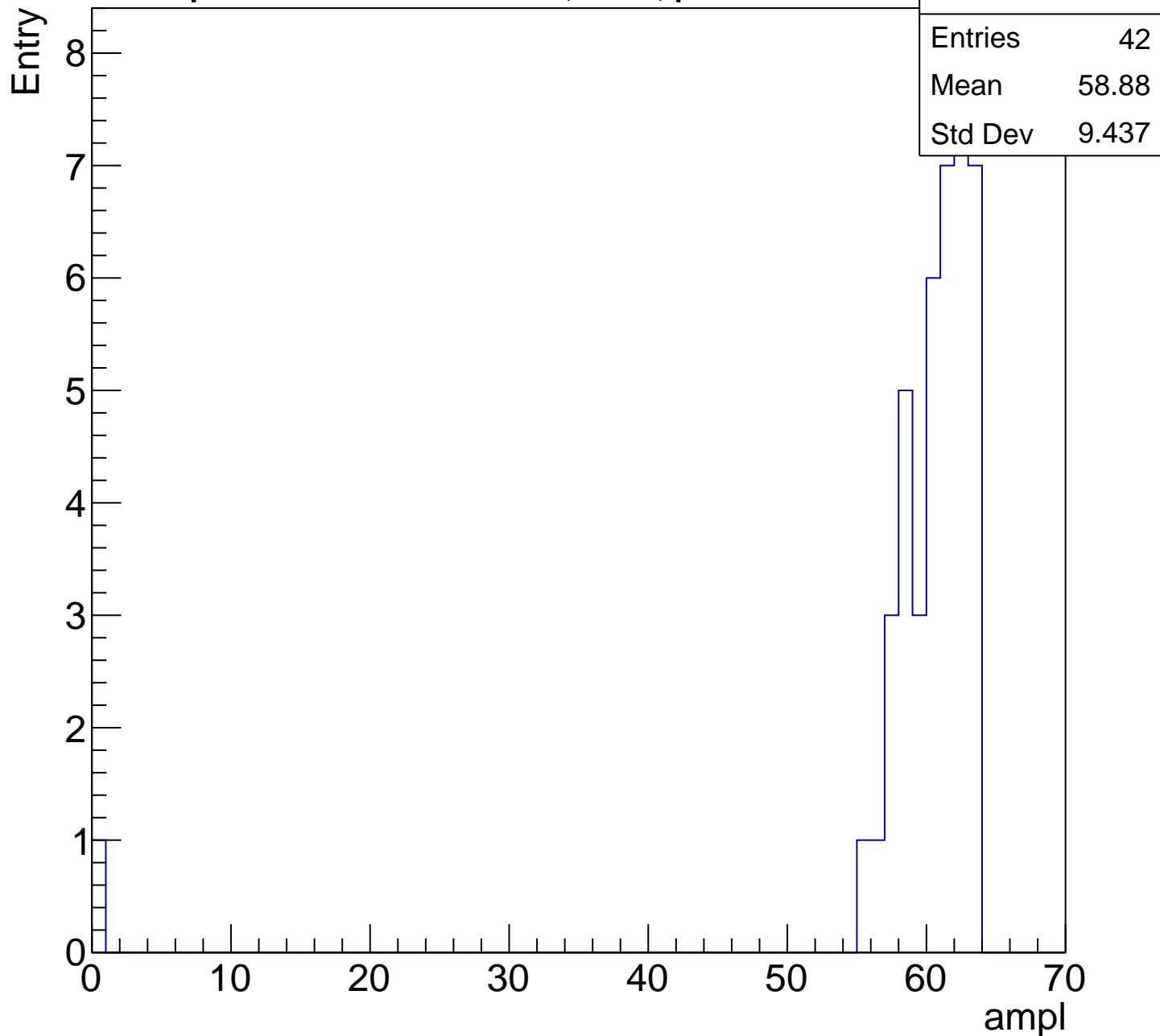
Entry



Entries	56
Mean	55.75
Std Dev	3.622

B1L101S, U15-ch77, adc5

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch77, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch77, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch78, adc0

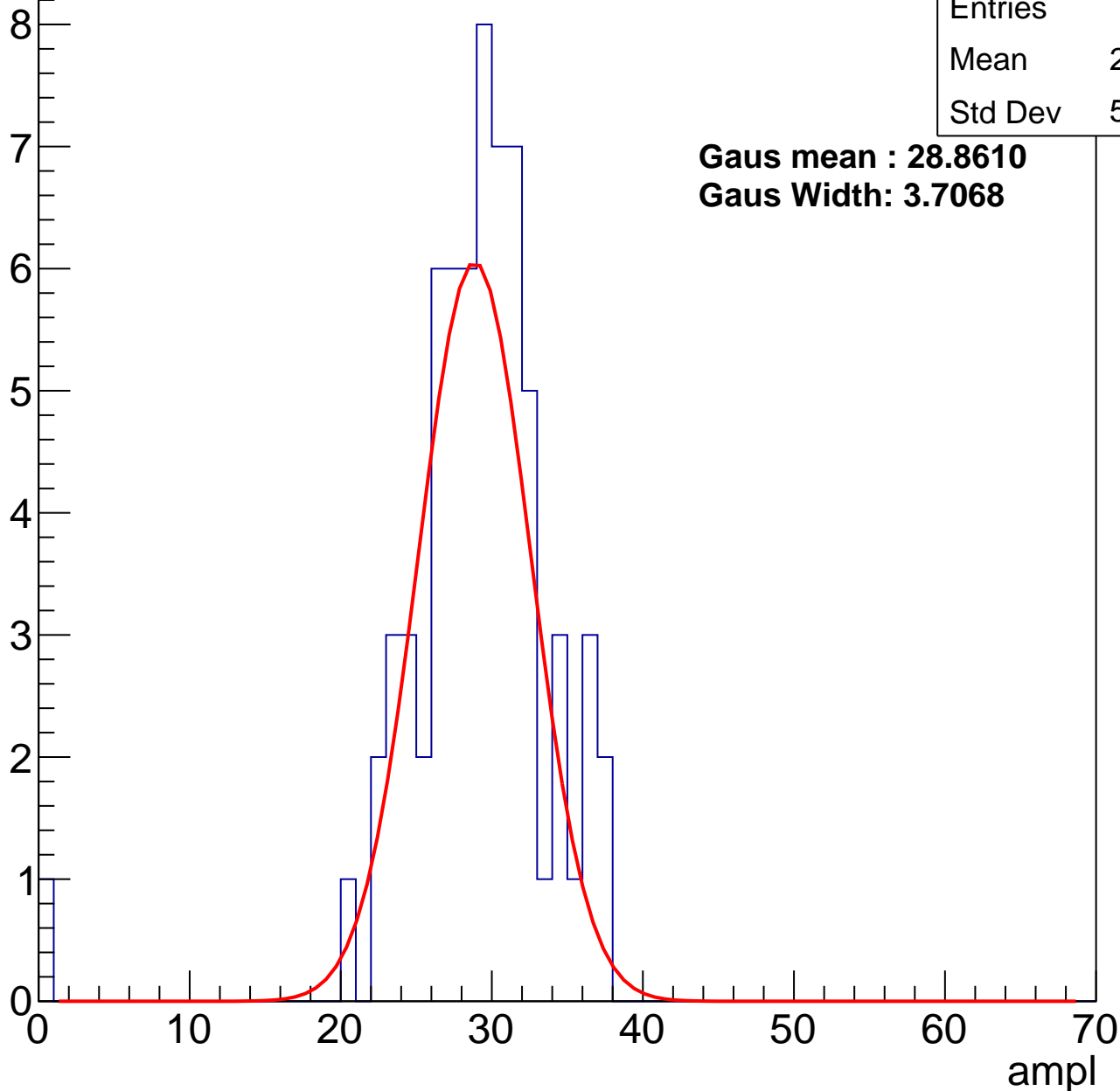
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	28.54
Std Dev	5.164

Gaus mean : 28.8610

Gaus Width: 3.7068



B1L101S, U15-ch78, adc1

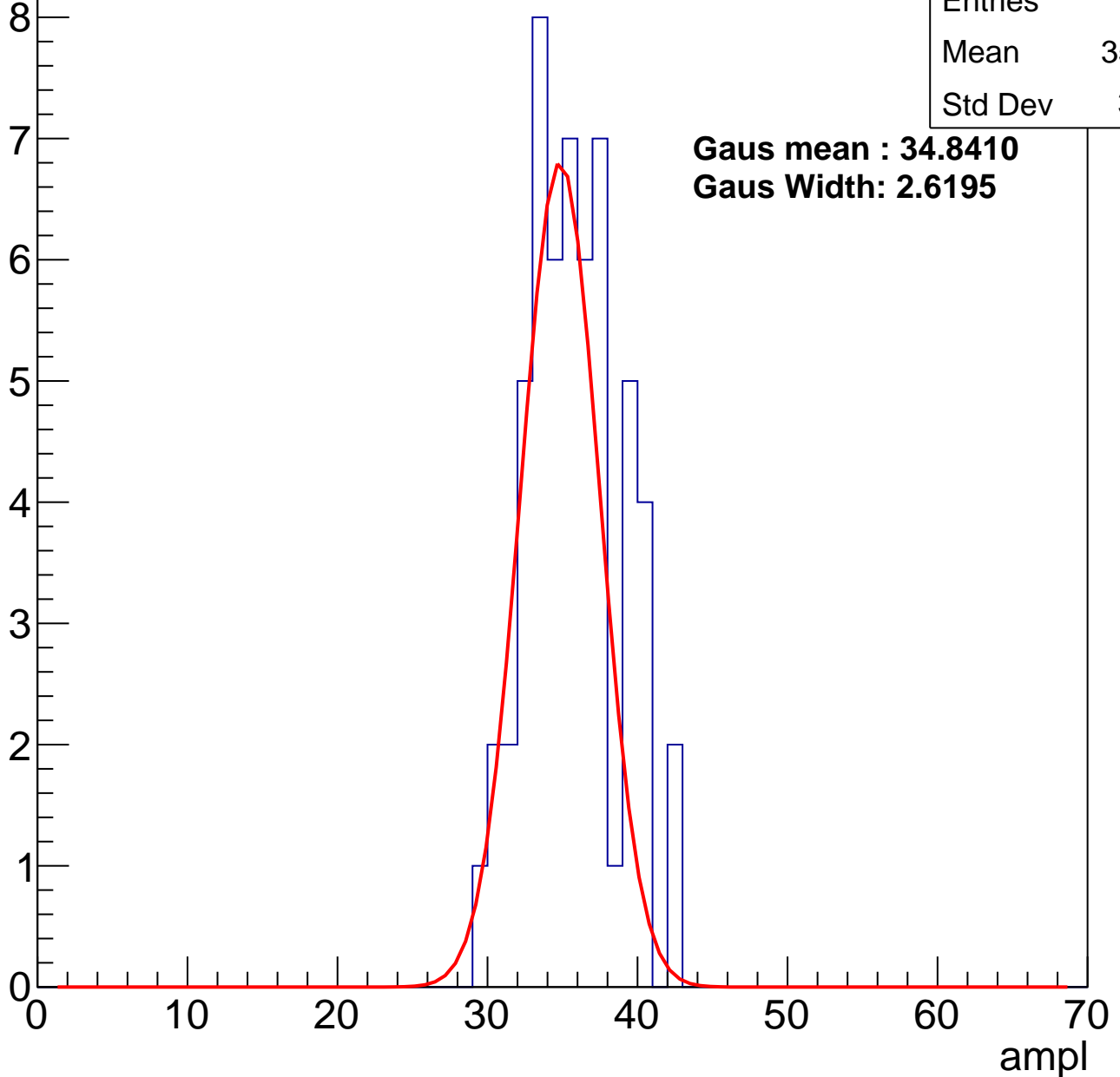
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.29
Std Dev	3.04

Gaus mean : 34.8410

Gaus Width: 2.6195



B1L101S, U15-ch78, adc2

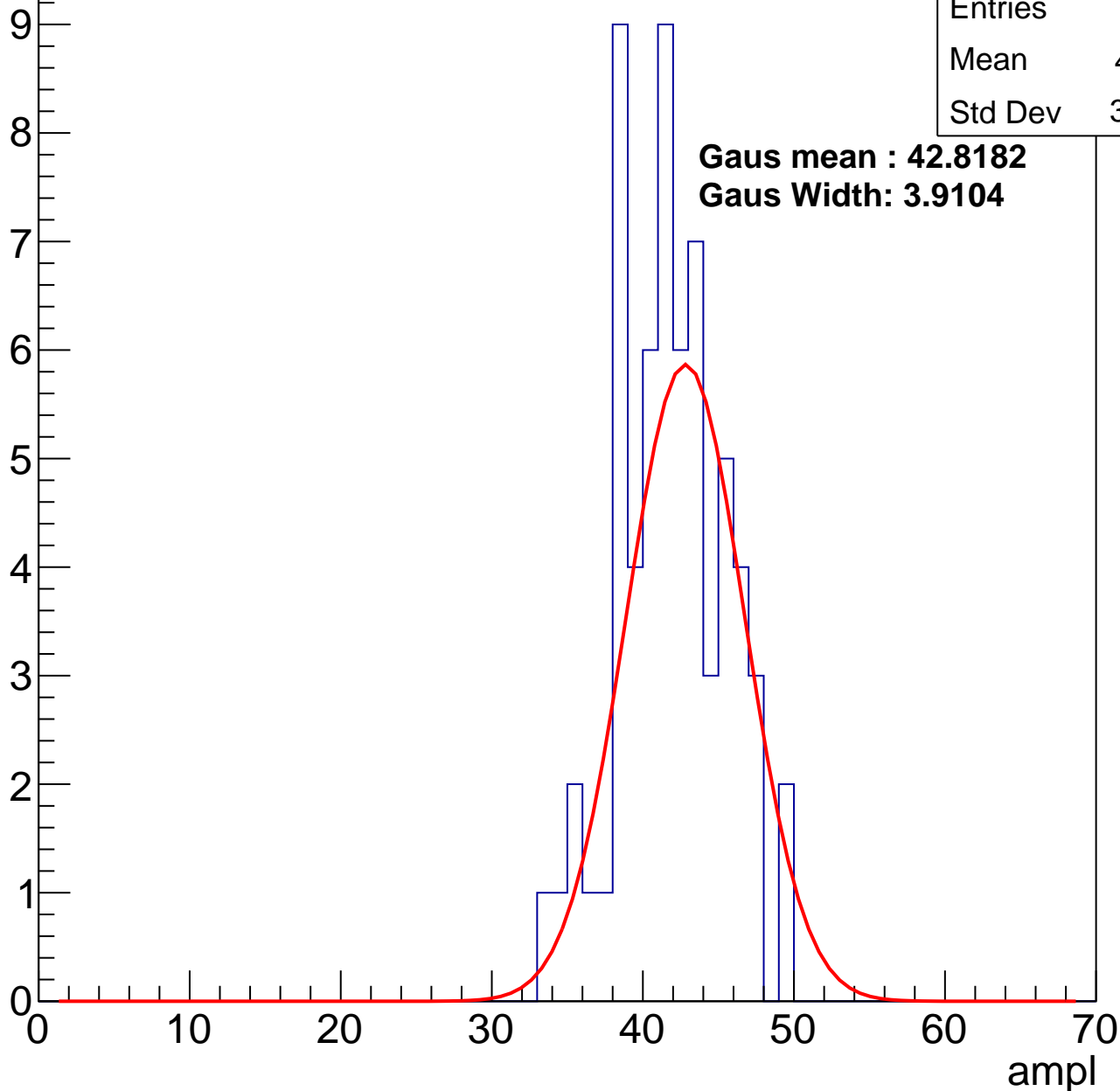
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	41.41
Std Dev	3.525

Gaus mean : 42.8182

Gaus Width: 3.9104

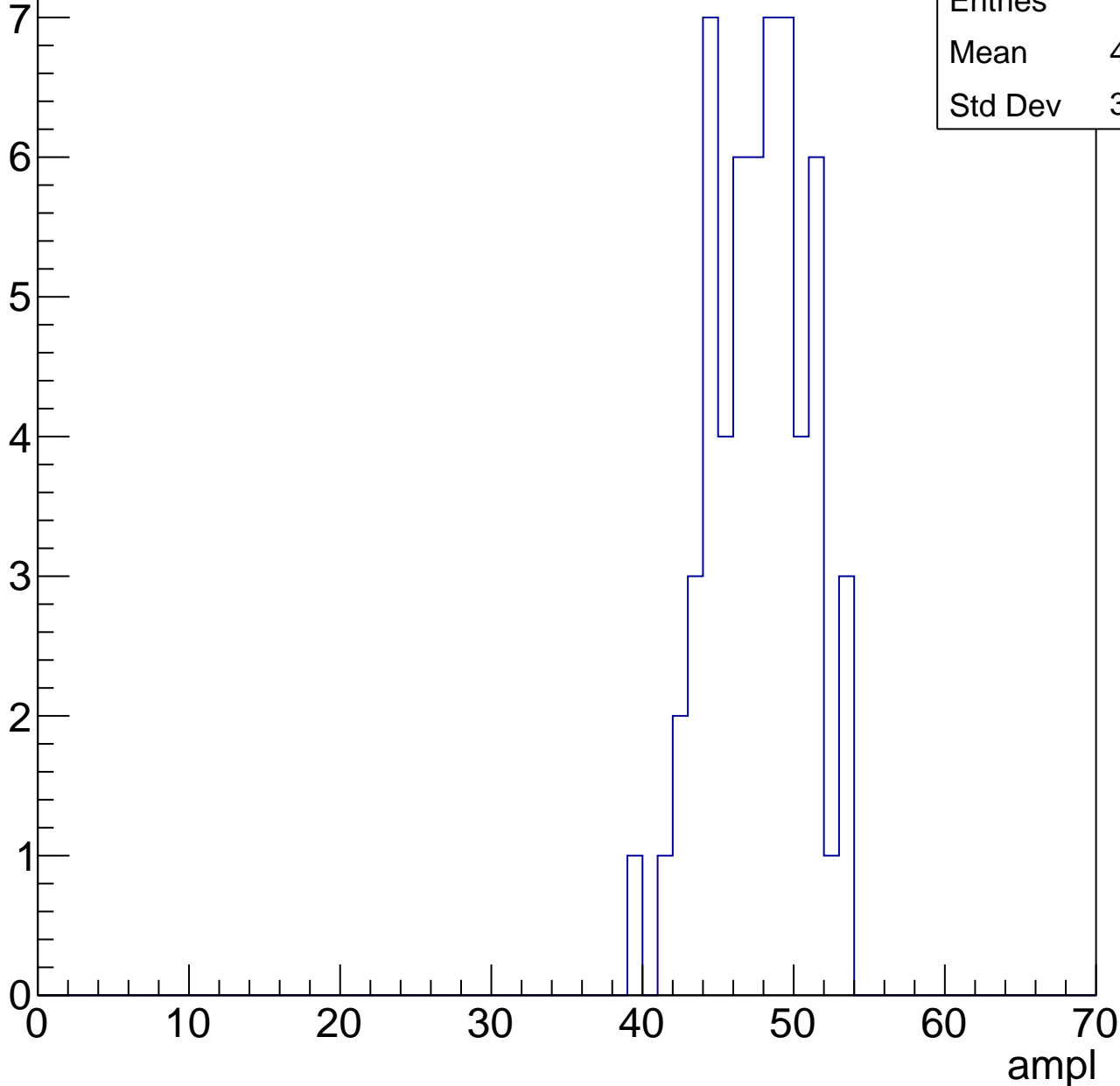


B1L101S, U15-ch78, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	47.16
Std Dev	3.167

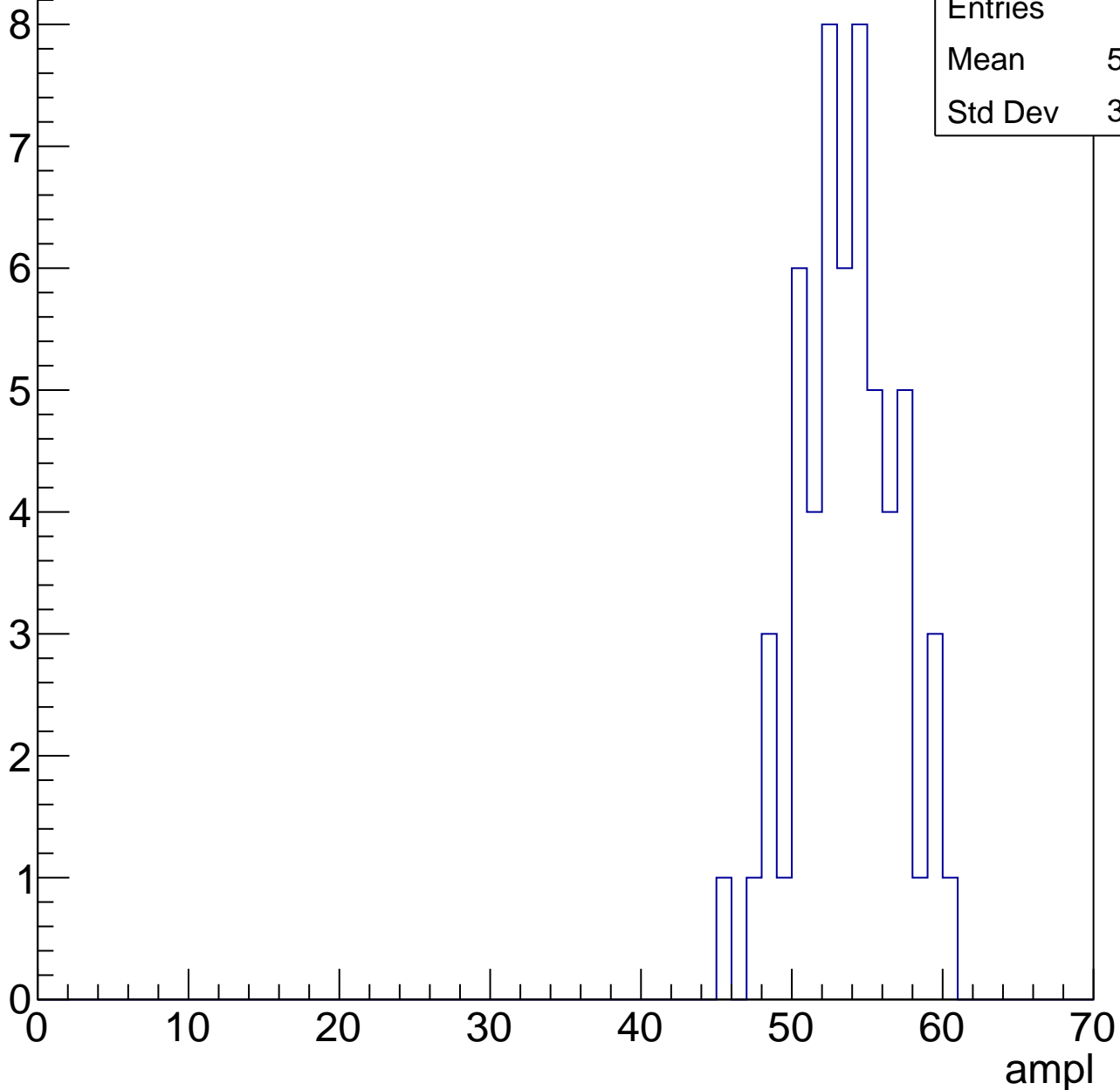


B1L101S, U15-ch78, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

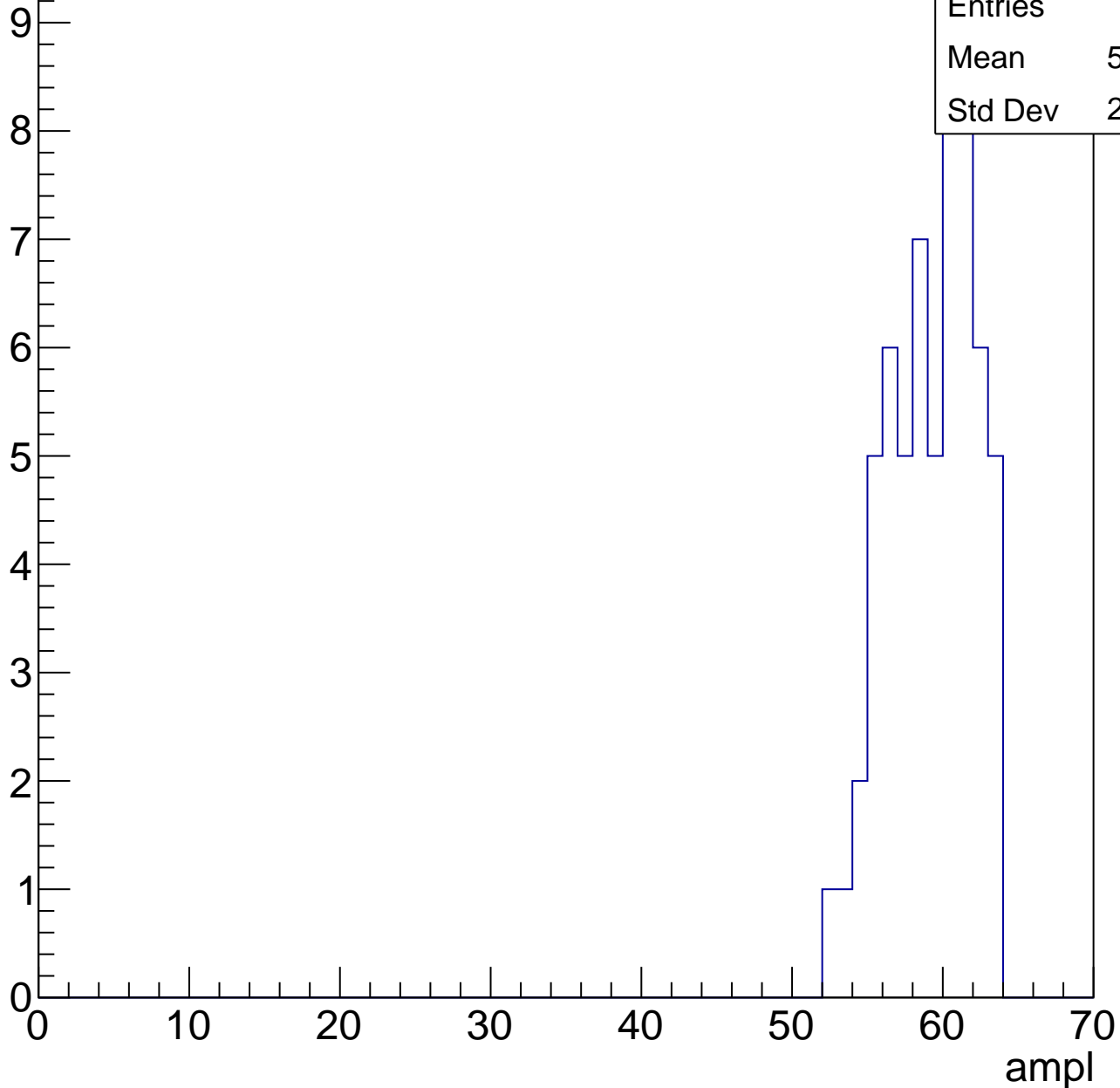
Entries	57
Mean	53.23
Std Dev	3.223



B1L101S, U15-ch78, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



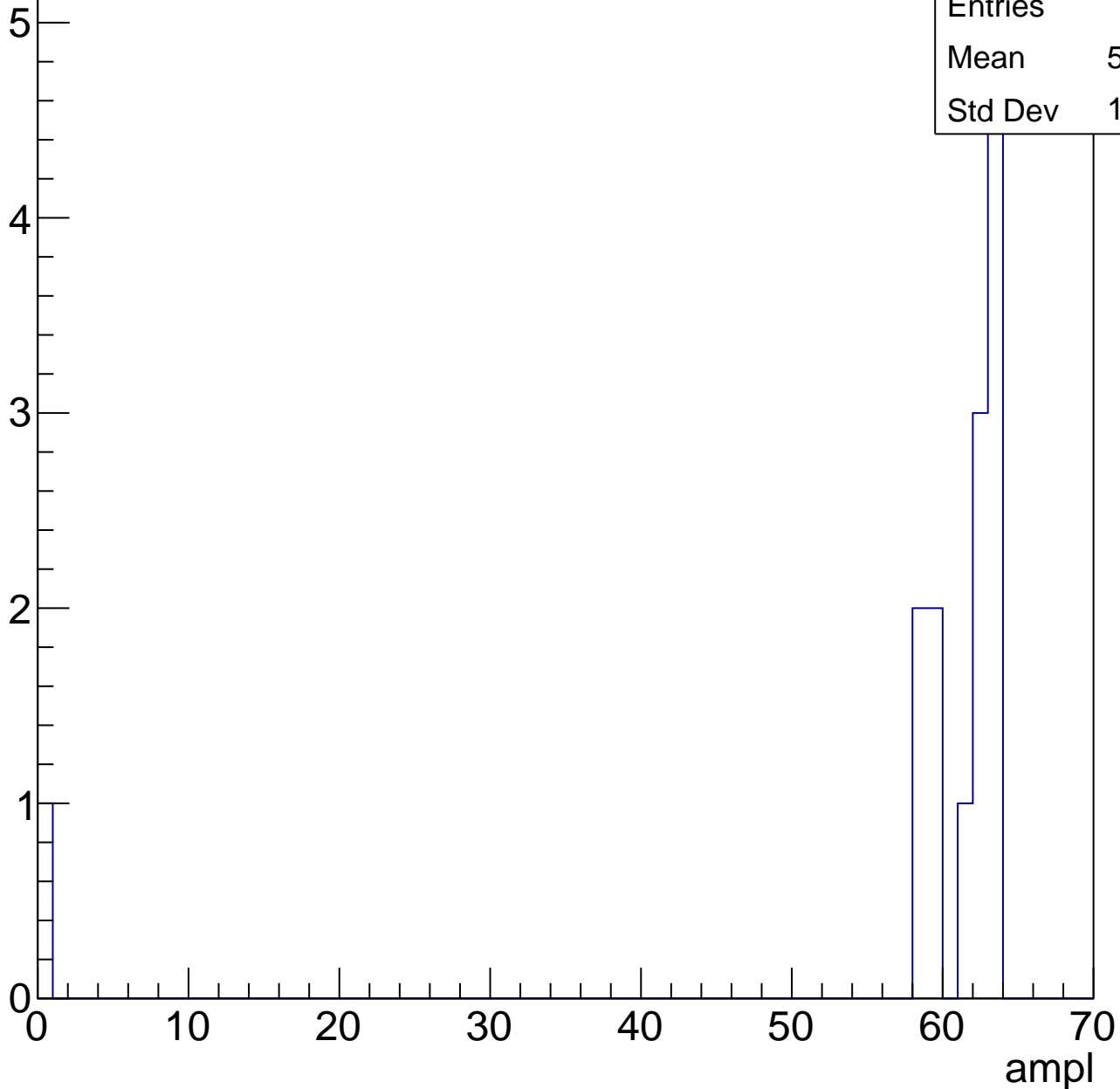
Entries	61
Mean	58.79
Std Dev	2.782

B1L101S, U15-ch78, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	56.86
Std Dev	15.88



B1L101S, U15-ch78, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch79, adc0

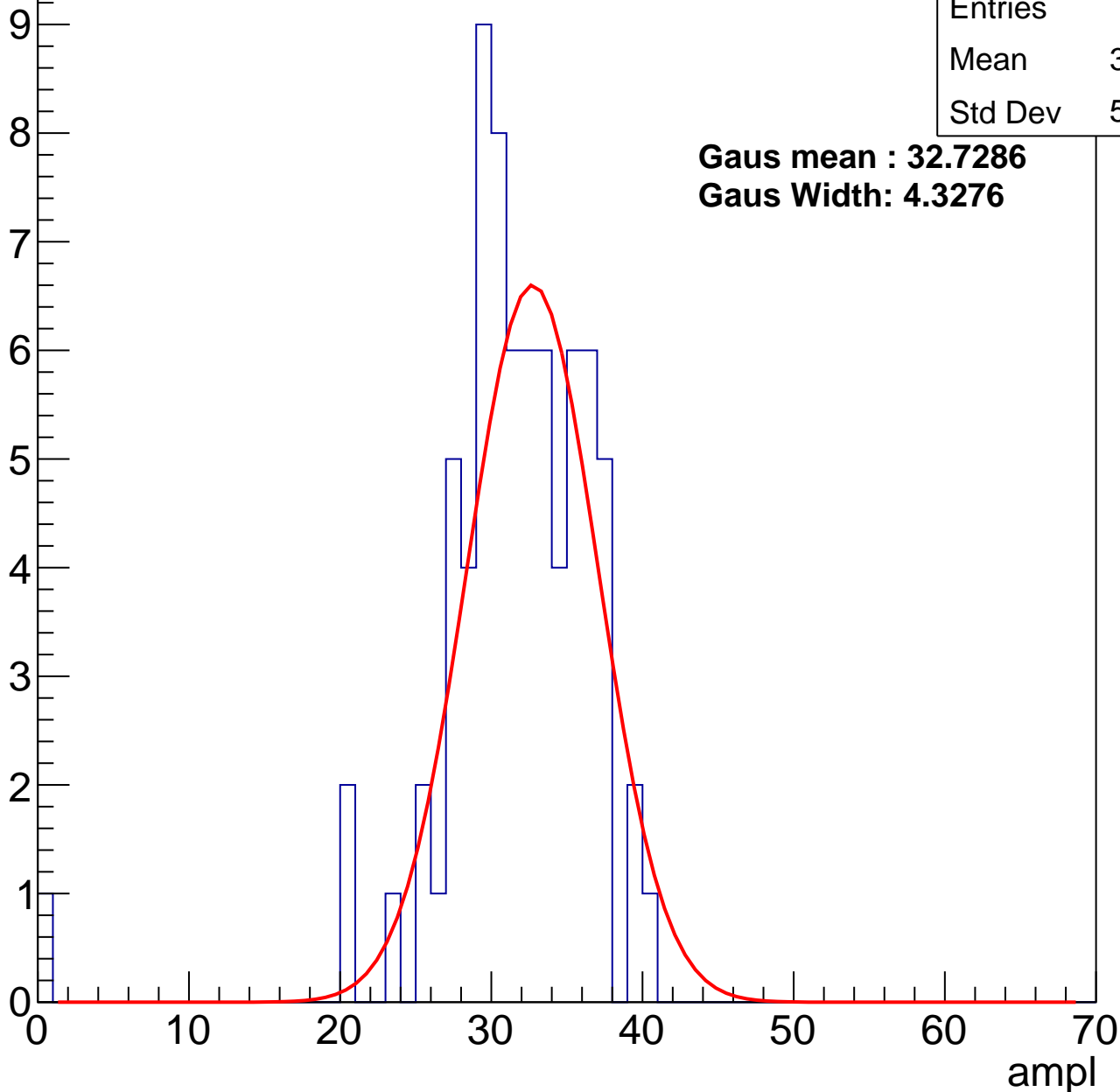
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	31.04
Std Dev	5.447

Gaus mean : 32.7286

Gaus Width: 4.3276



B1L101S, U15-ch79, adc1

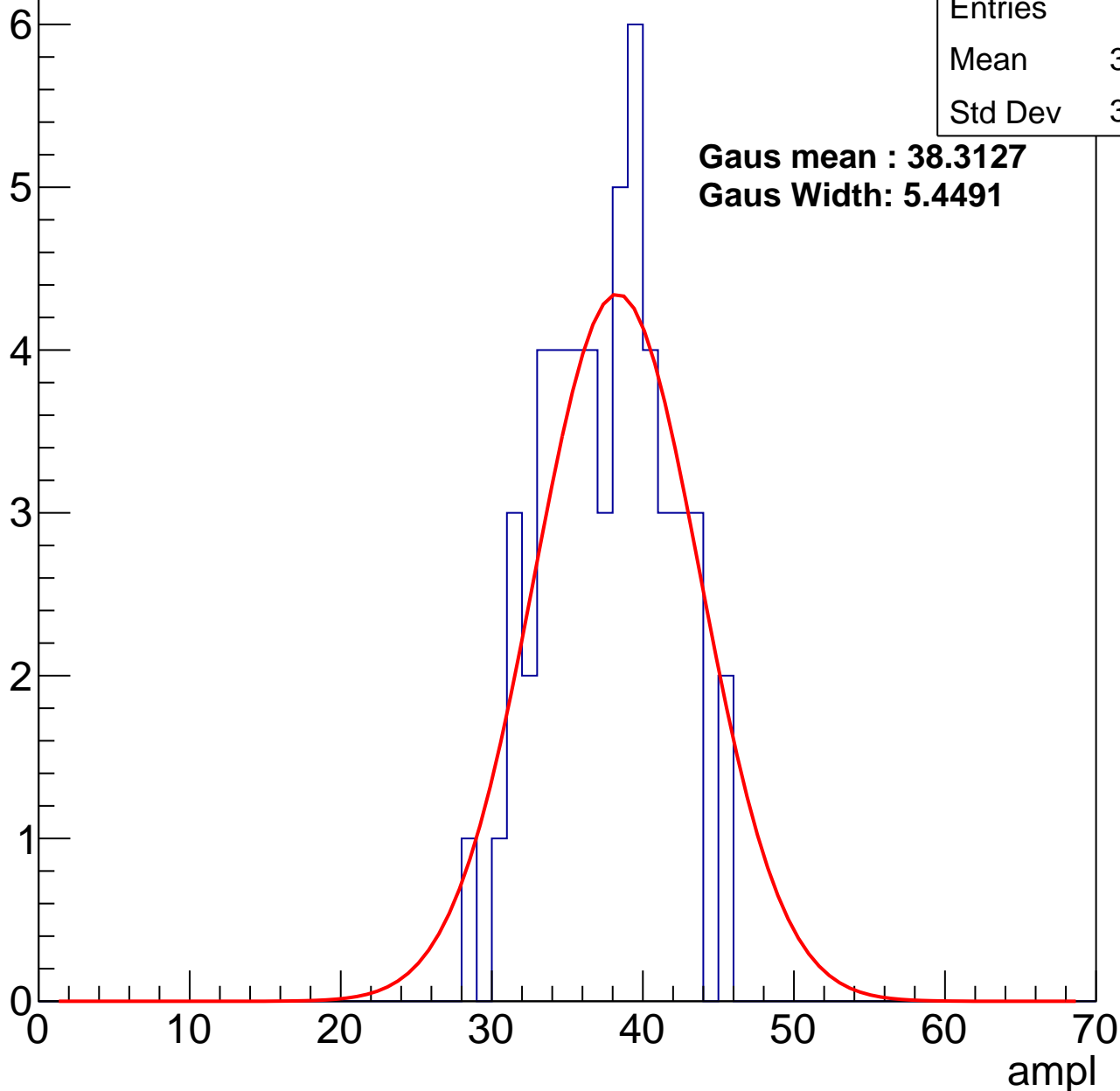
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	37.12
Std Dev	3.998

Gaus mean : 38.3127

Gaus Width: 5.4491



B1L101S, U15-ch79, adc2

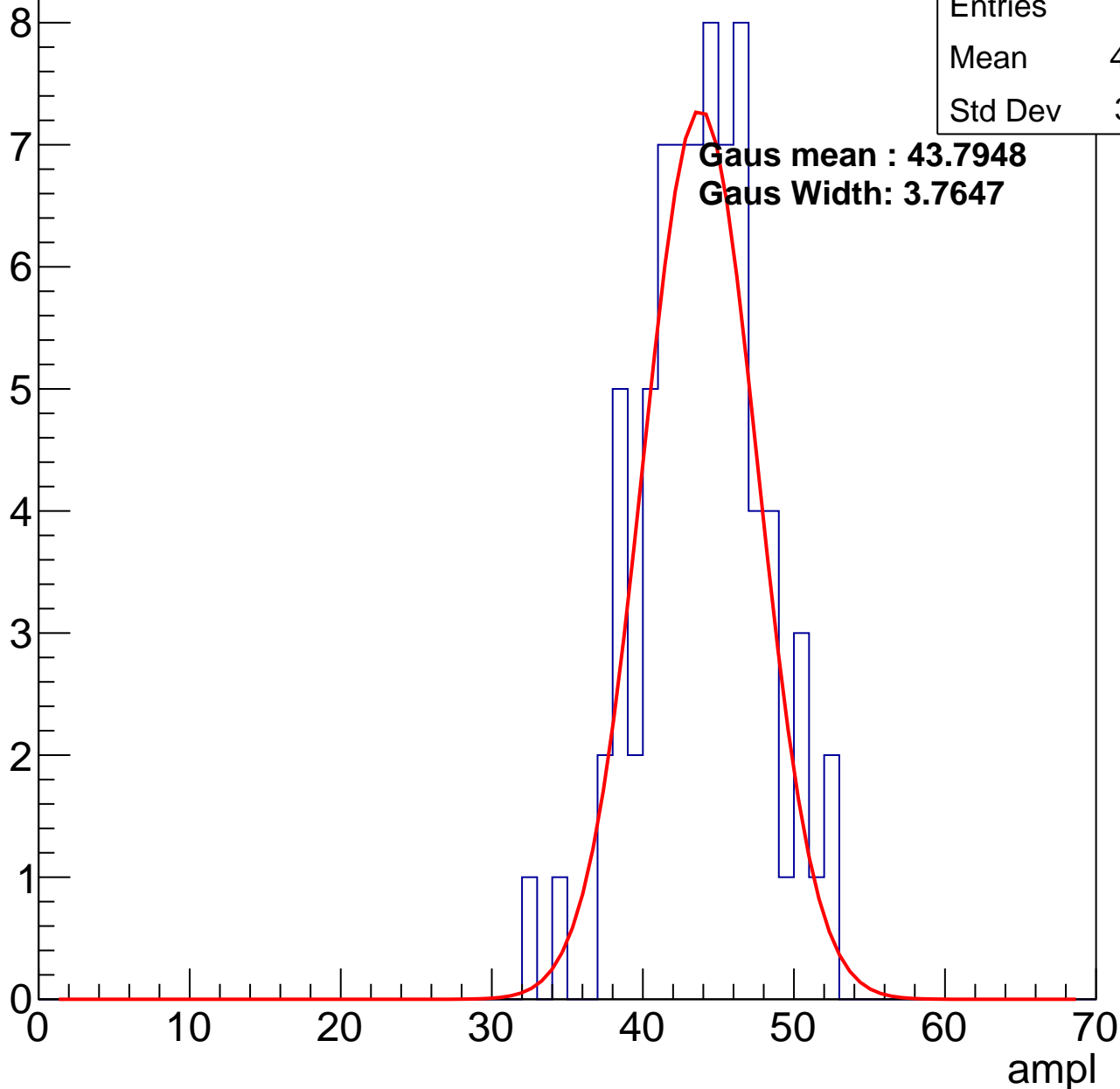
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	43.45
Std Dev	3.961

Gaus mean : 43.7948

Gaus Width: 3.7647

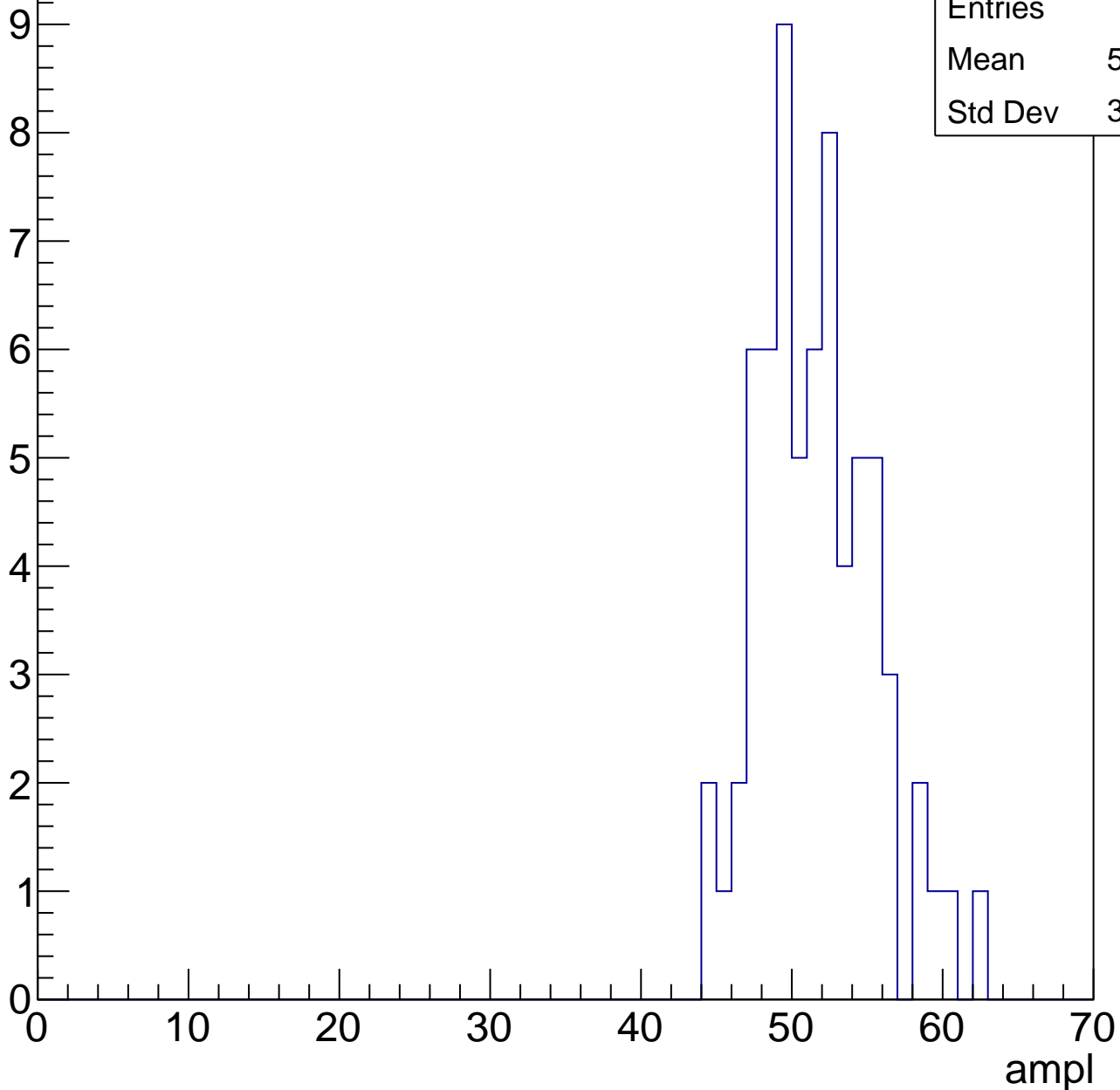


B1L101S, U15-ch79, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.19
Std Dev	3.786

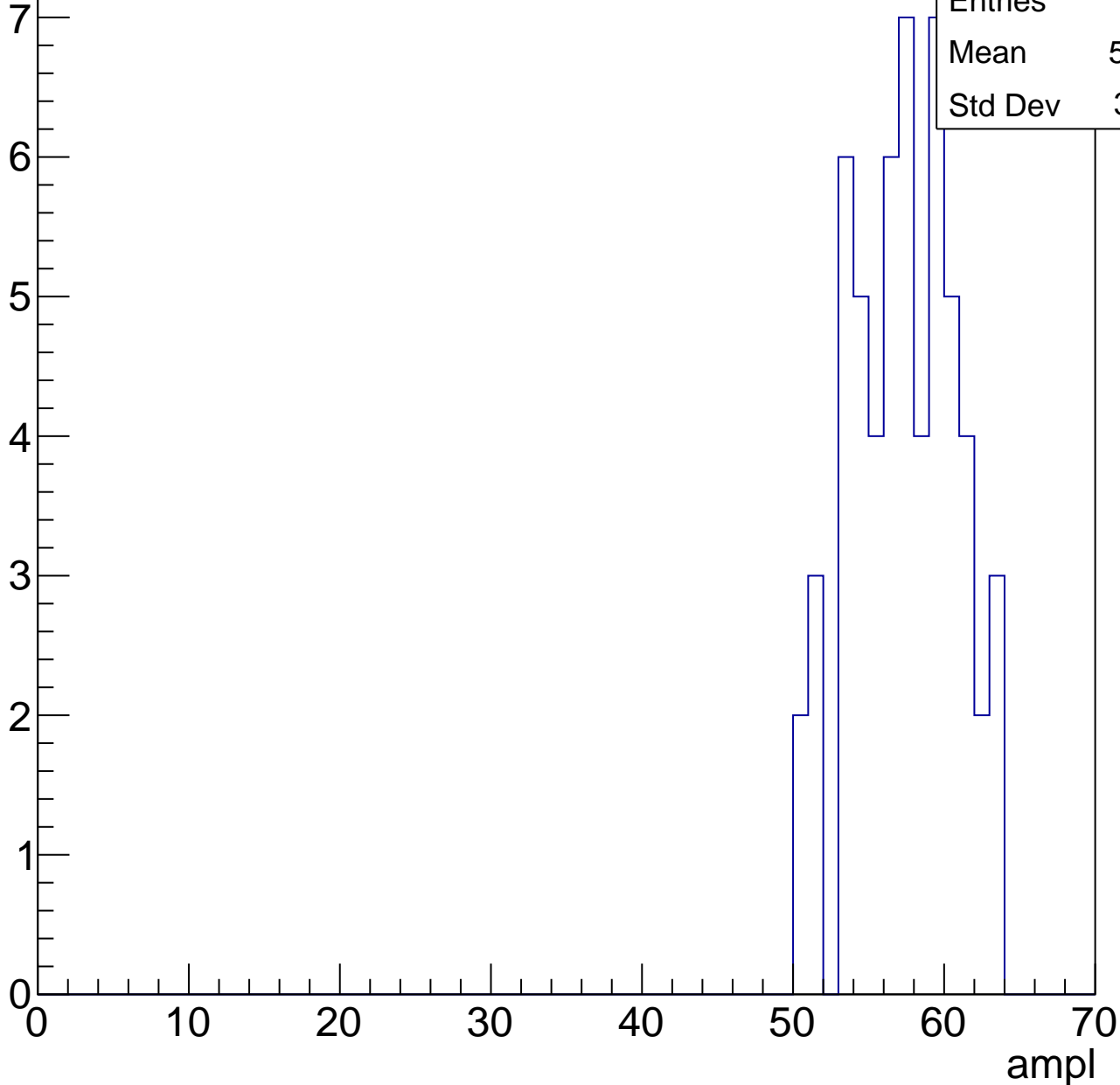


B1L101S, U15-ch79, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	56.86
Std Dev	3.391

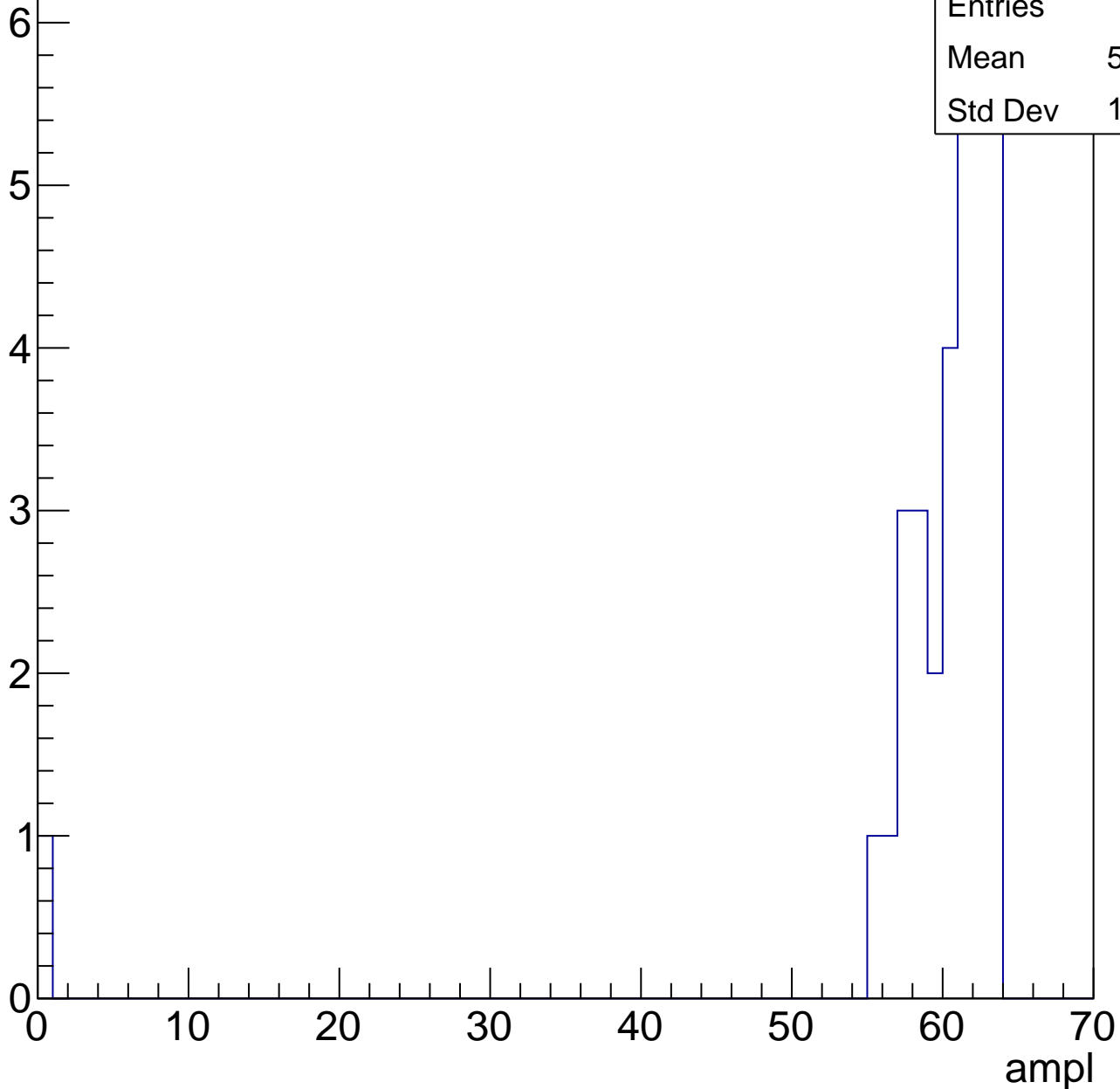


B1L101S, U15-ch79, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

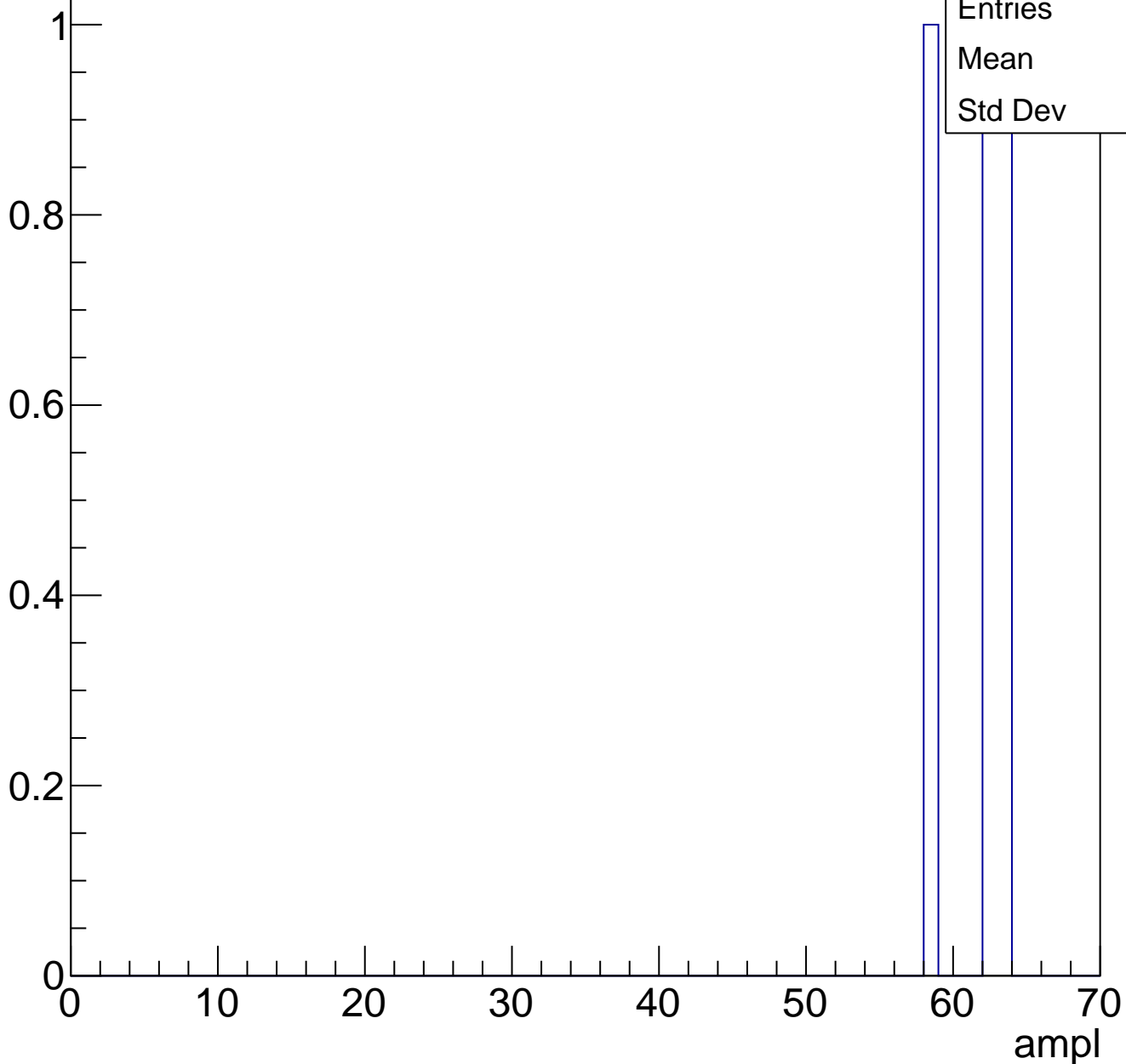
Entries	33
Mean	58.48
Std Dev	10.57



B1L101S, U15-ch79, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch79, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch80, adc0

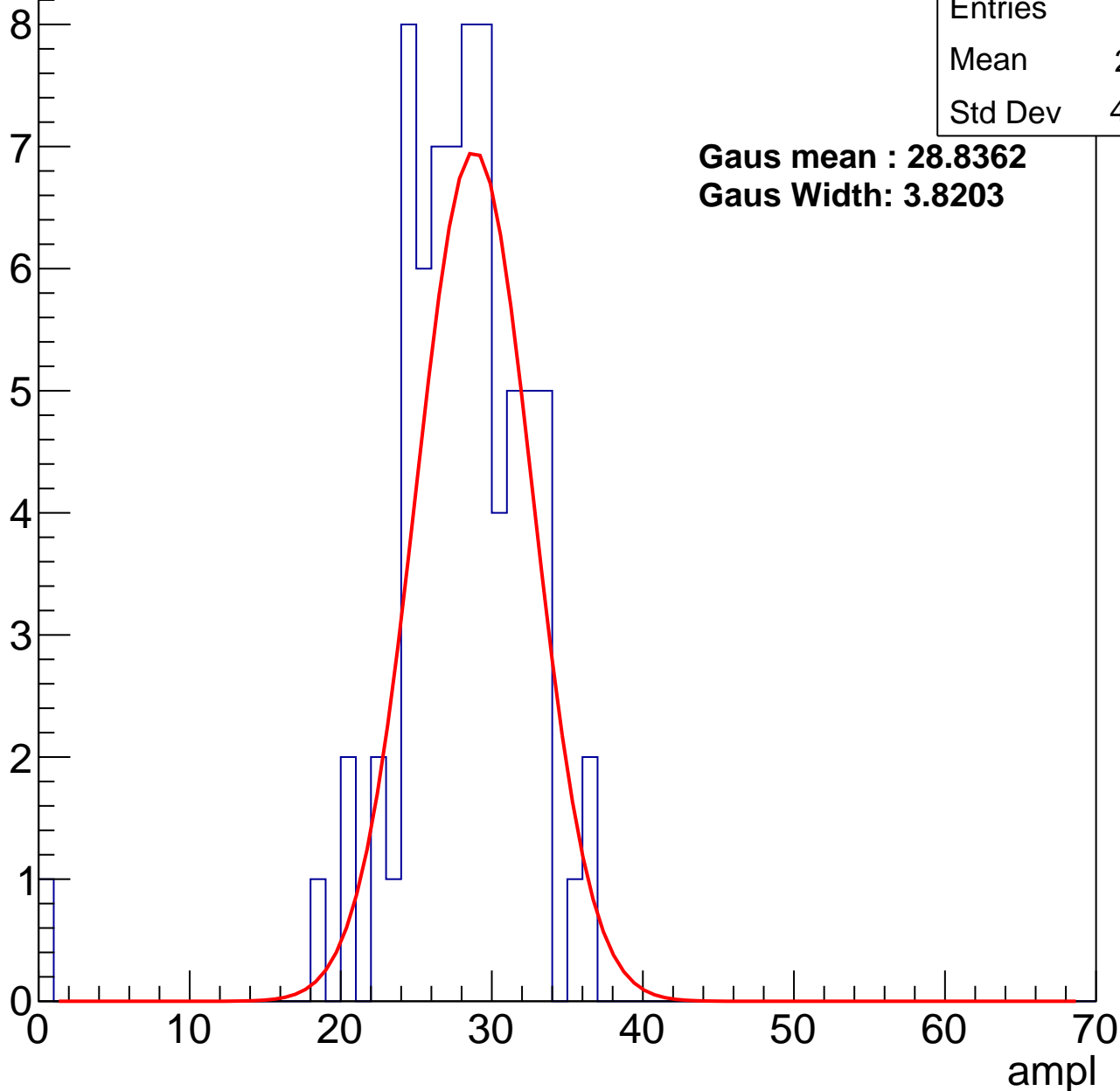
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.41
Std Dev	4.899

Gaus mean : 28.8362

Gaus Width: 3.8203



B1L101S, U15-ch80, adc1

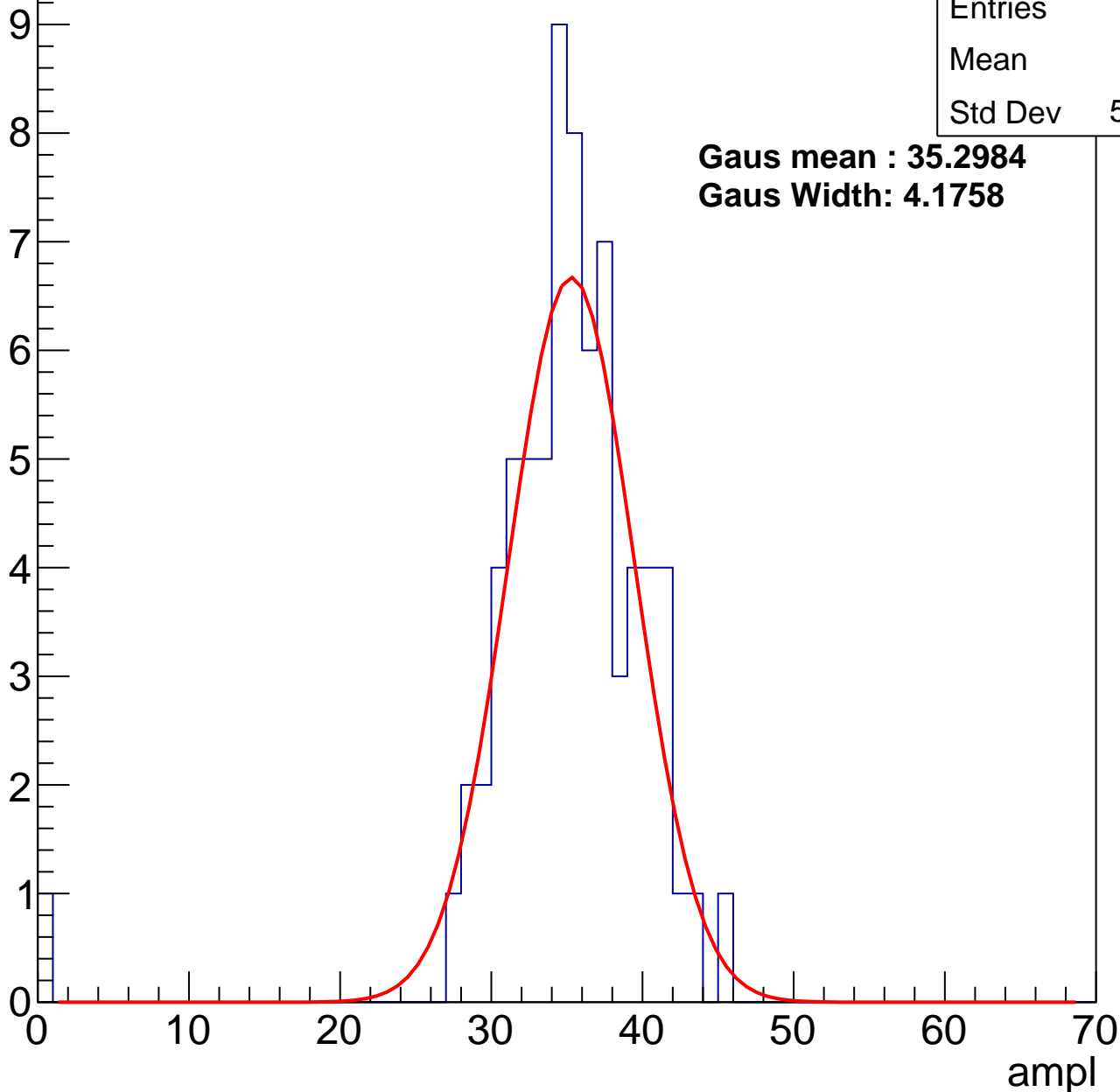
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	34.6
Std Dev	5.593

Gaus mean : 35.2984

Gaus Width: 4.1758



B1L101S, U15-ch80, adc2

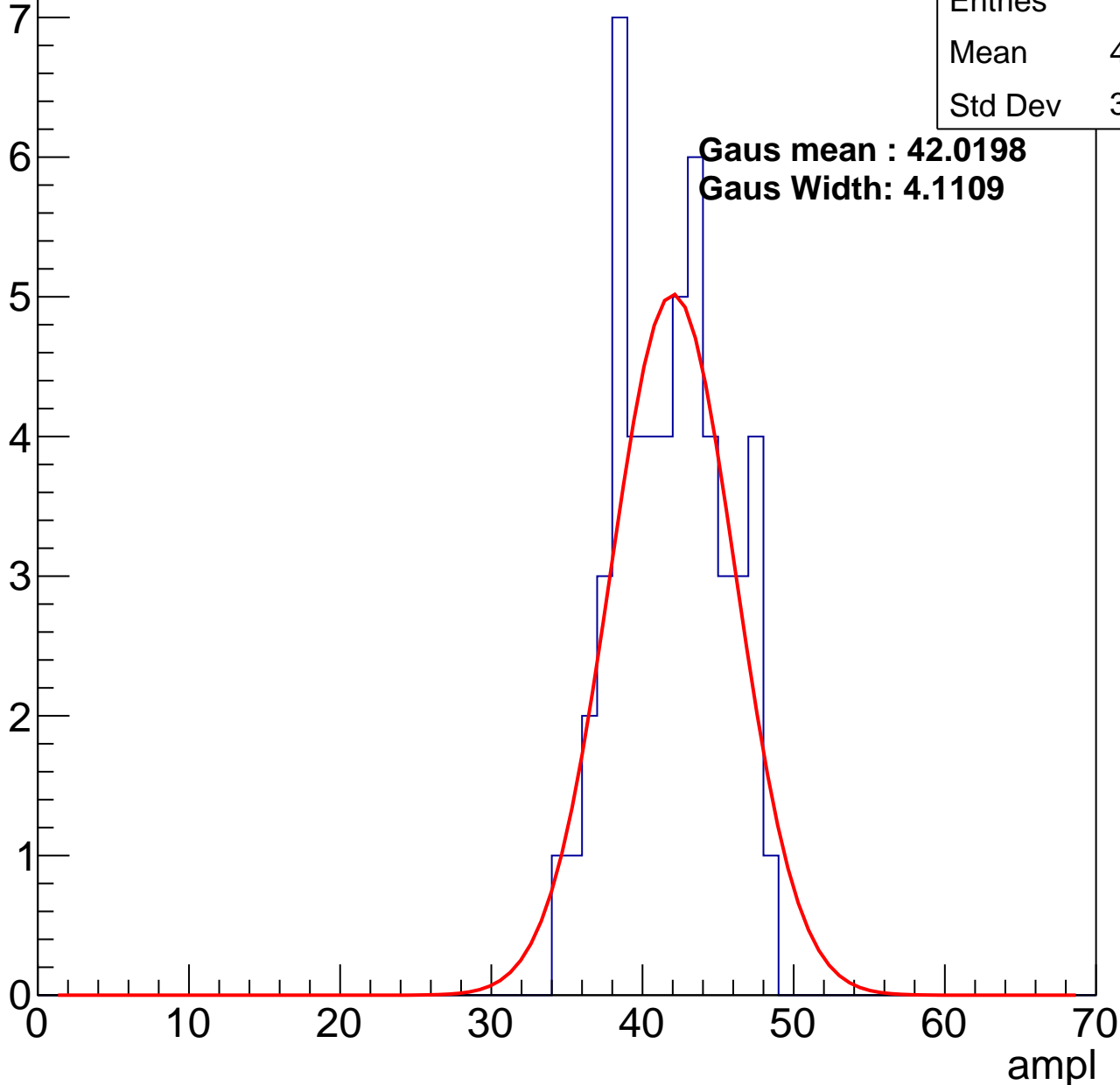
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.37
Std Dev	3.508

Gaus mean : 42.0198

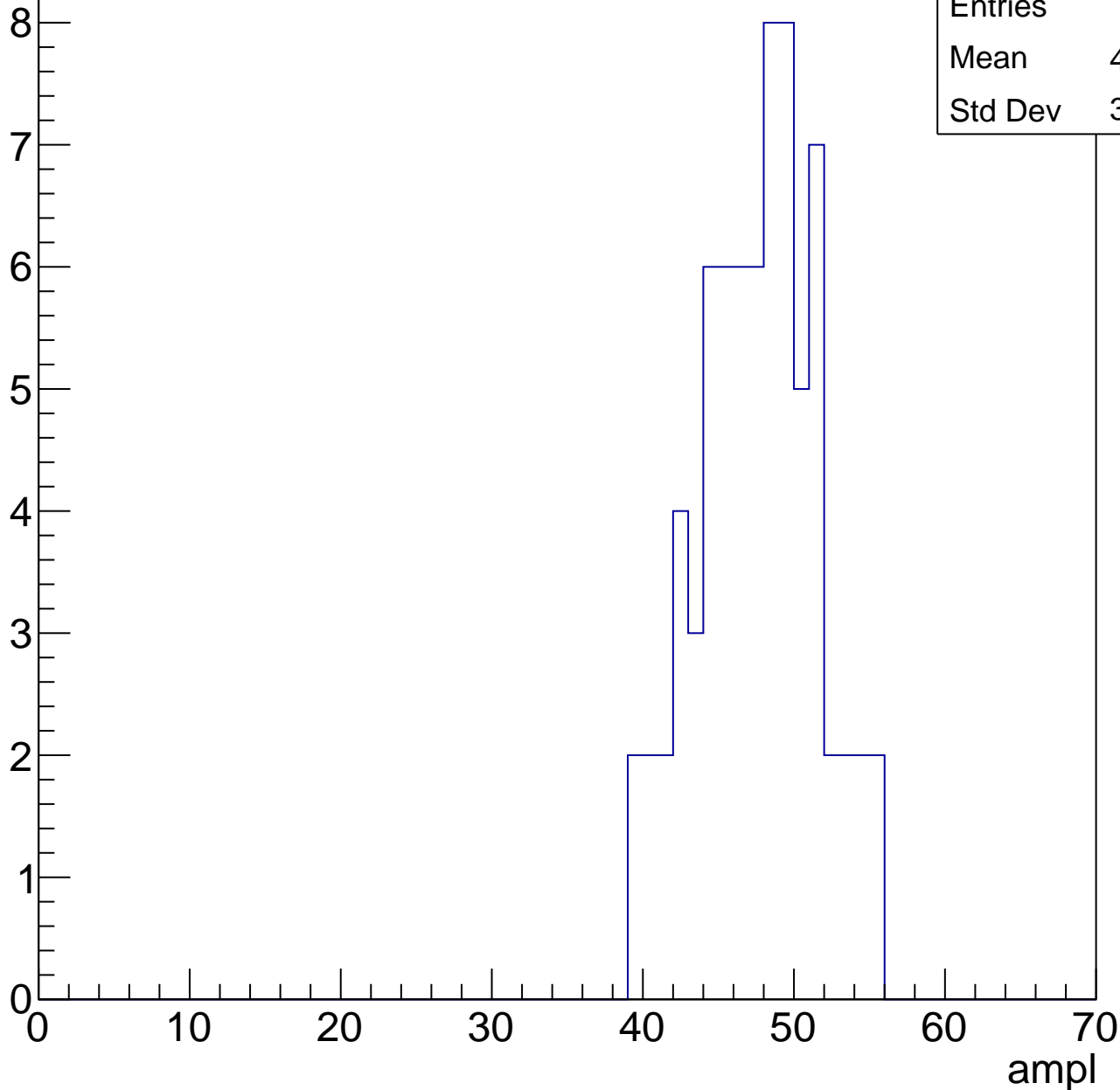
Gaus Width: 4.1109



B1L101S, U15-ch80, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

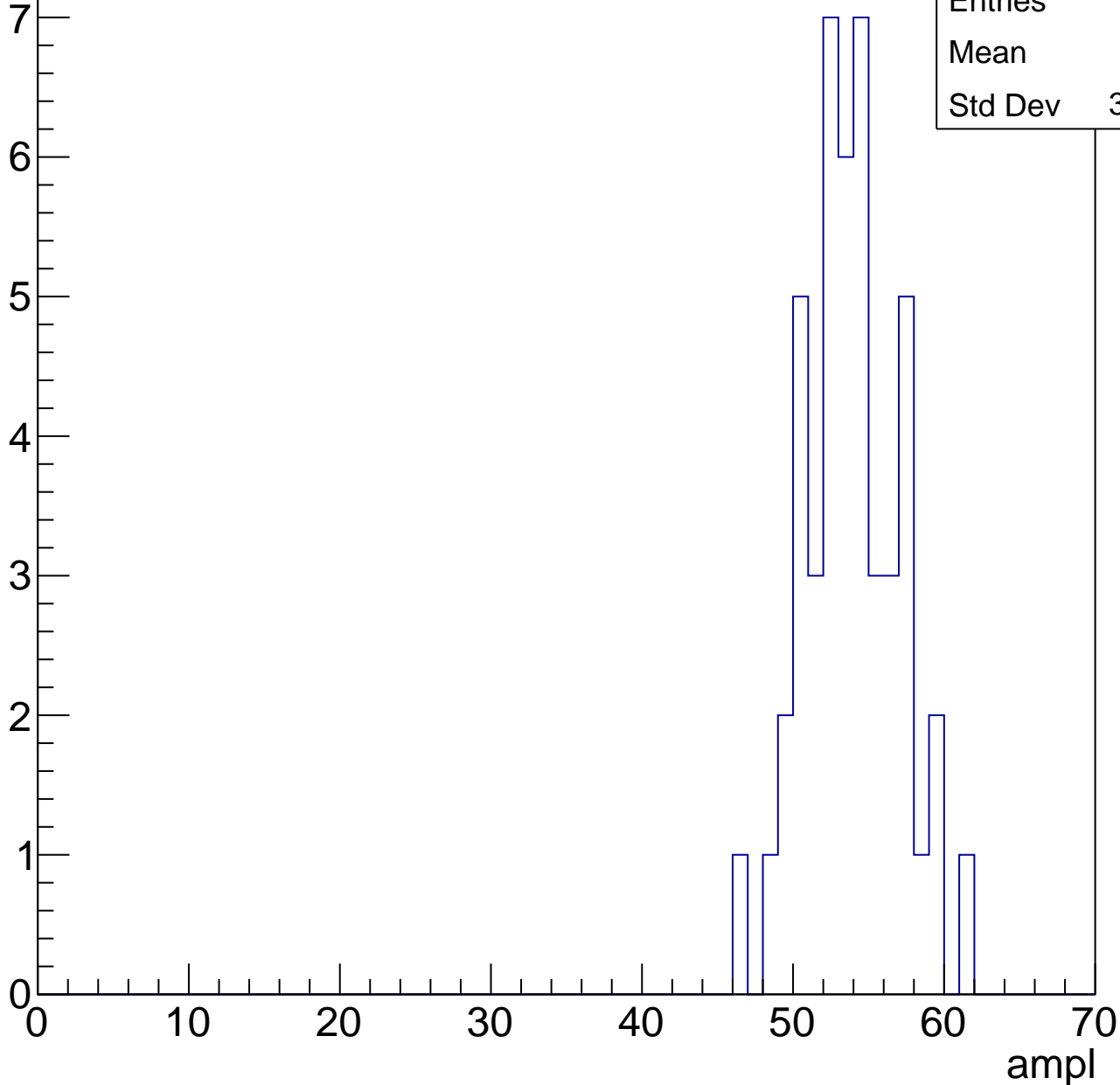


B1L101S, U15-ch80, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	53.4
Std Dev	3.092



B1L101S, U15-ch80, adc5

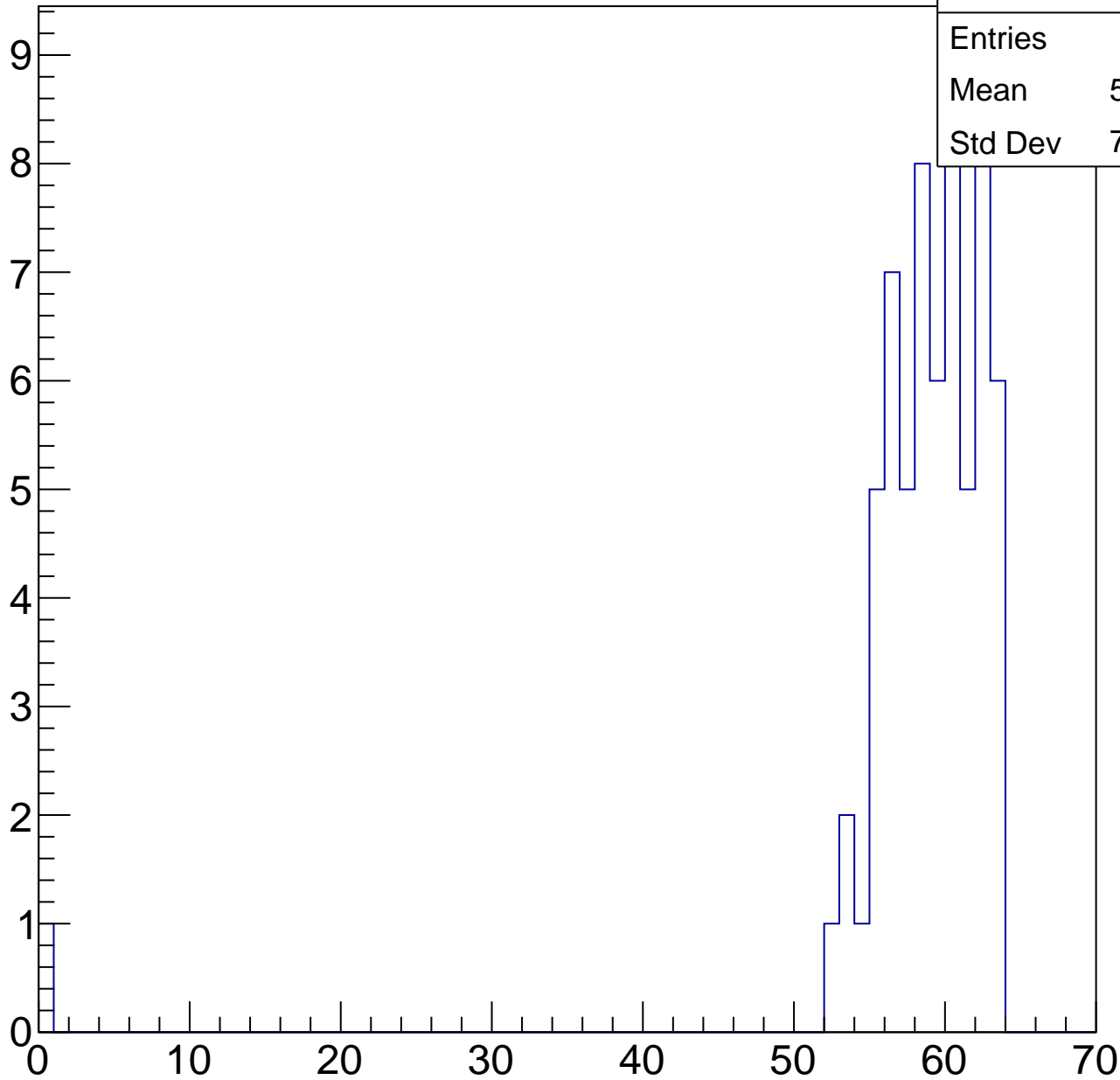
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	57.83
Std Dev	7.813

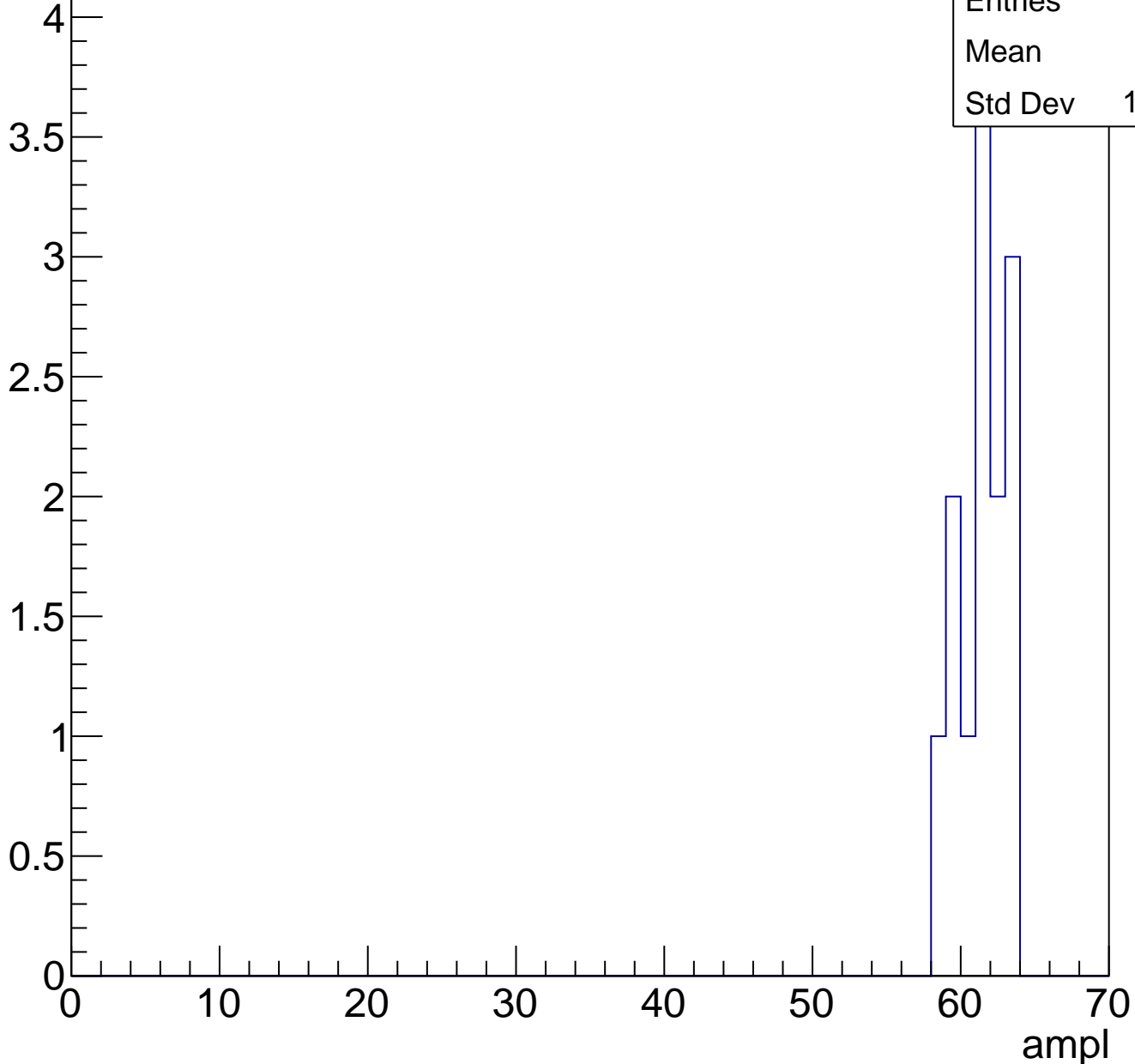
ampl



B1L101S, U15-ch80, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	13
Mean	61
Std Dev	1.569

B1L101S, U15-ch80, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch81, adc0

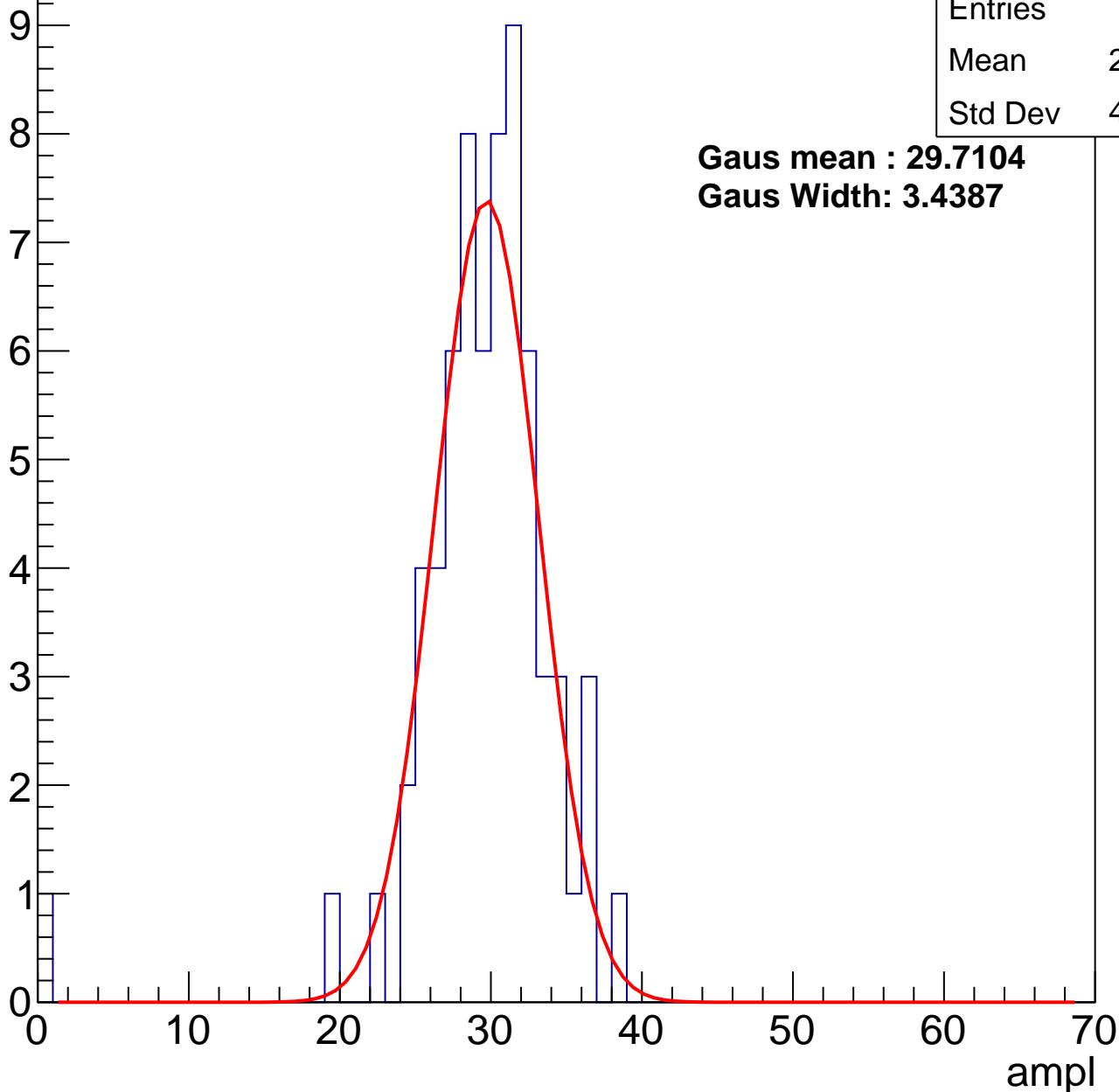
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.04
Std Dev	4.973

Gaus mean : 29.7104

Gaus Width: 3.4387



B1L101S, U15-ch81, adc1

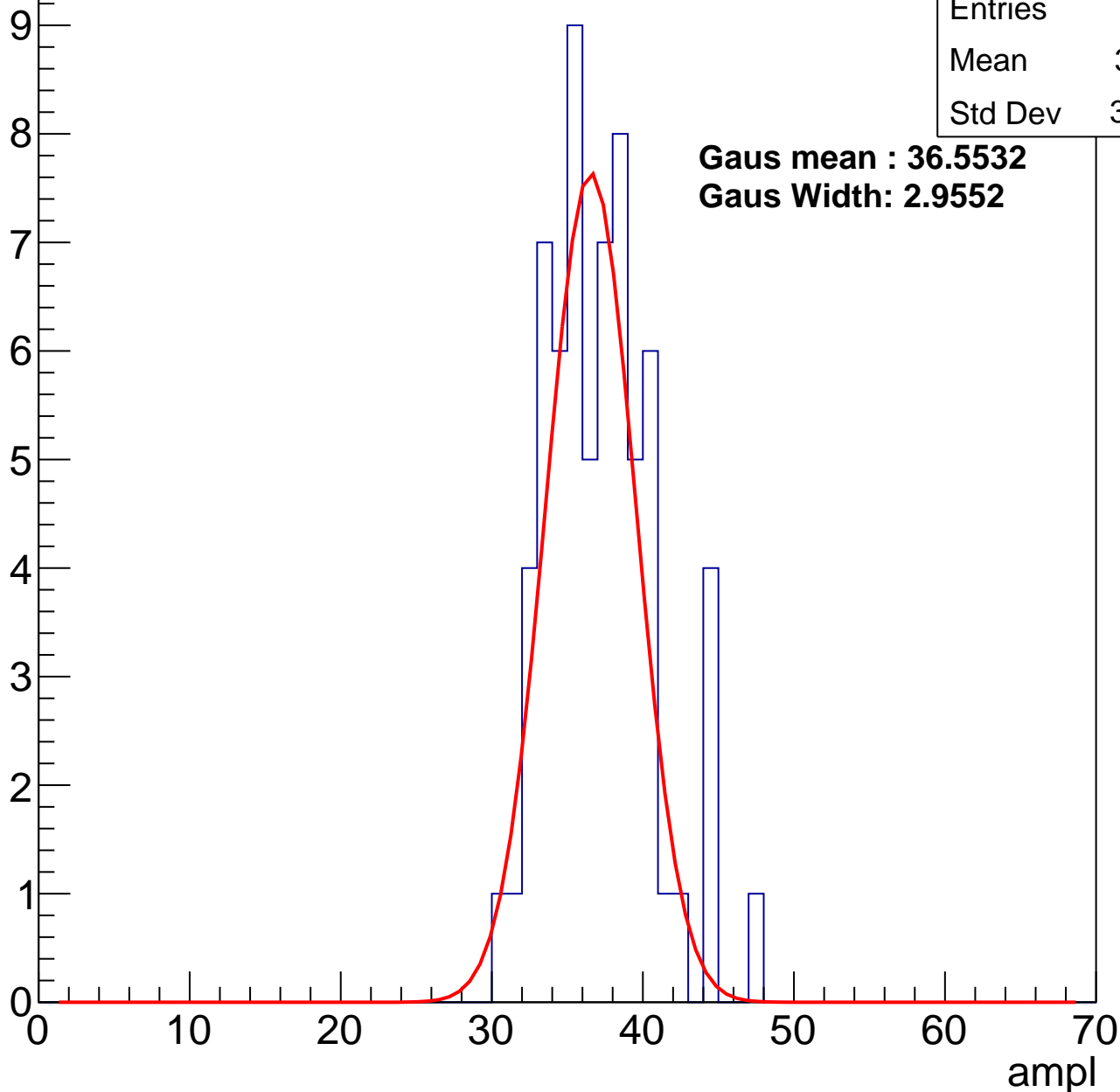
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.71
Std Dev	3.494

Gaus mean : 36.5532

Gaus Width: 2.9552



B1L101S, U15-ch81, adc2

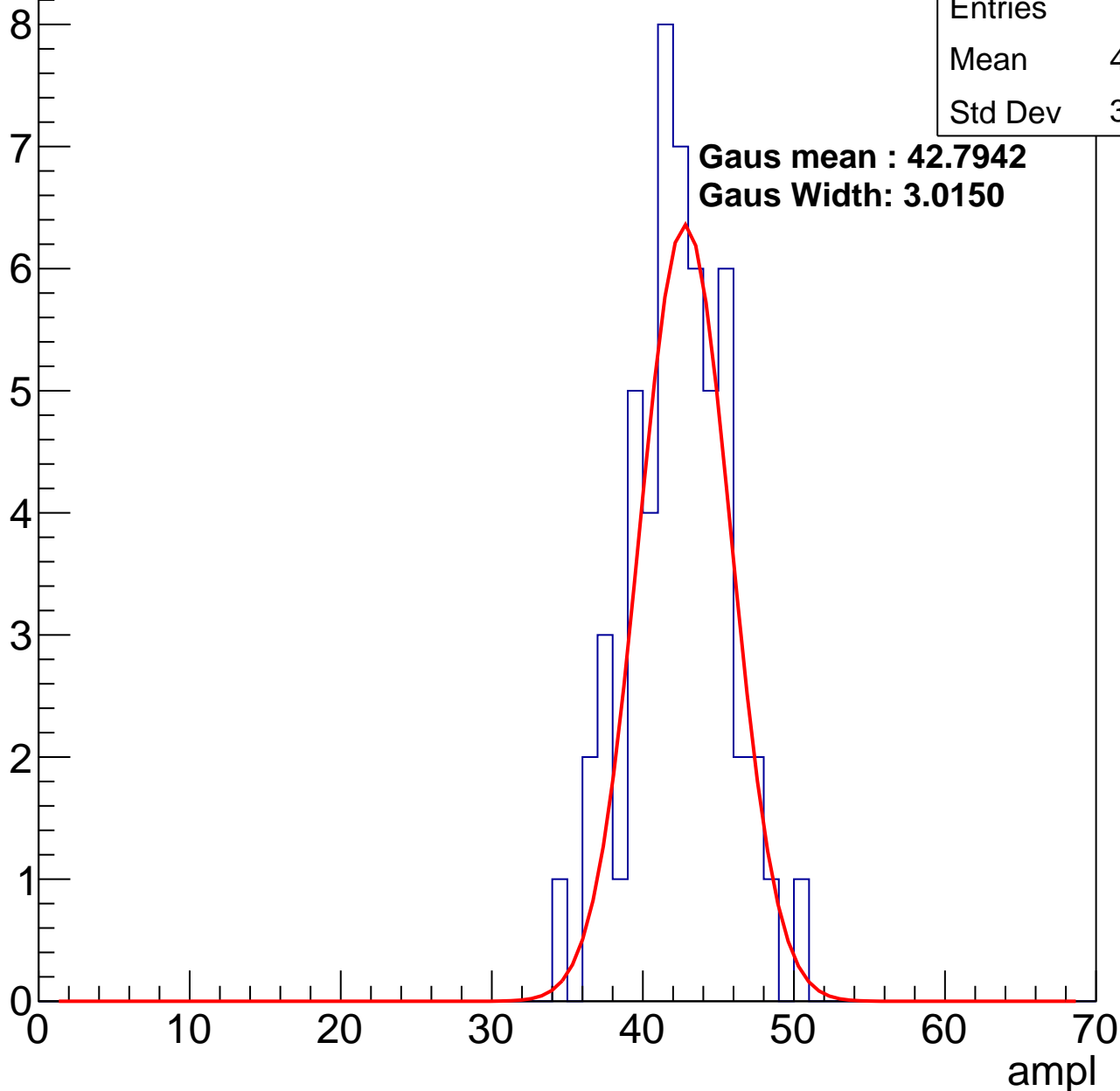
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	41.93
Std Dev	3.214

Gaus mean : 42.7942

Gaus Width: 3.0150



B1L101S, U15-ch81, adc3

calib_packv5_042523_0143.root, FC#0, port D2

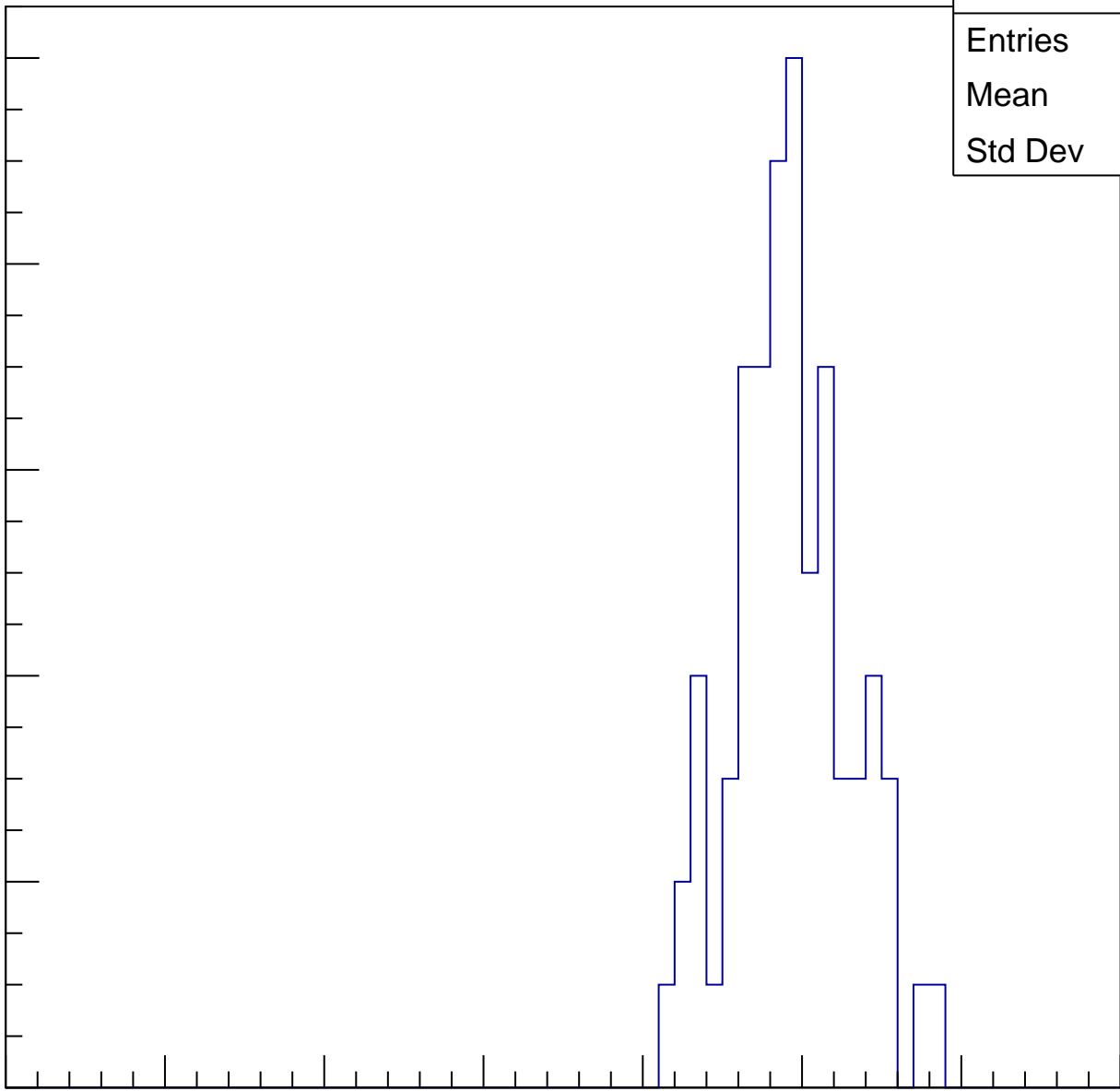
Entries	71
Mean	48.83
Std Dev	3.635

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

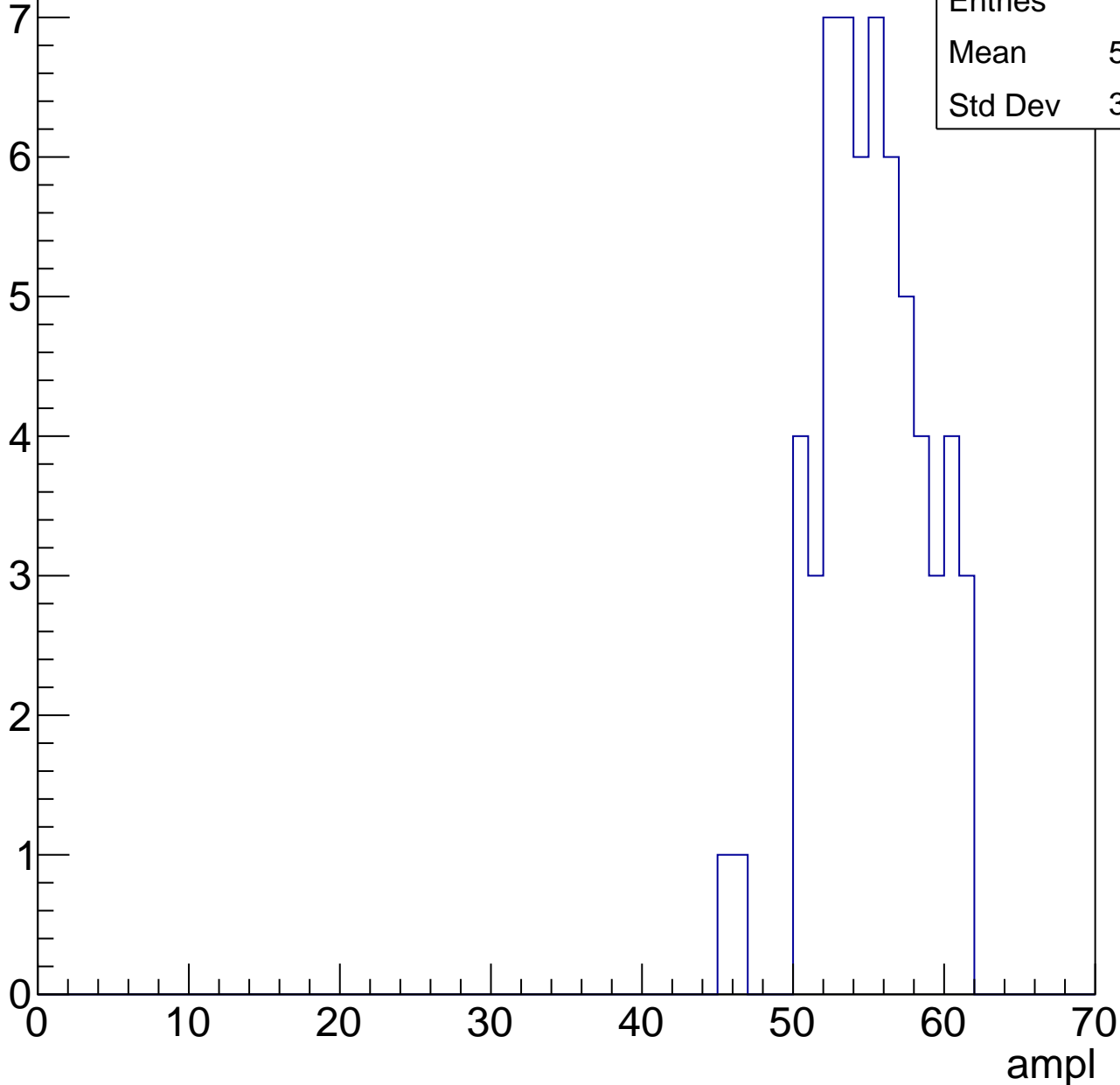


B1L101S, U15-ch81, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	54.77
Std Dev	3.485

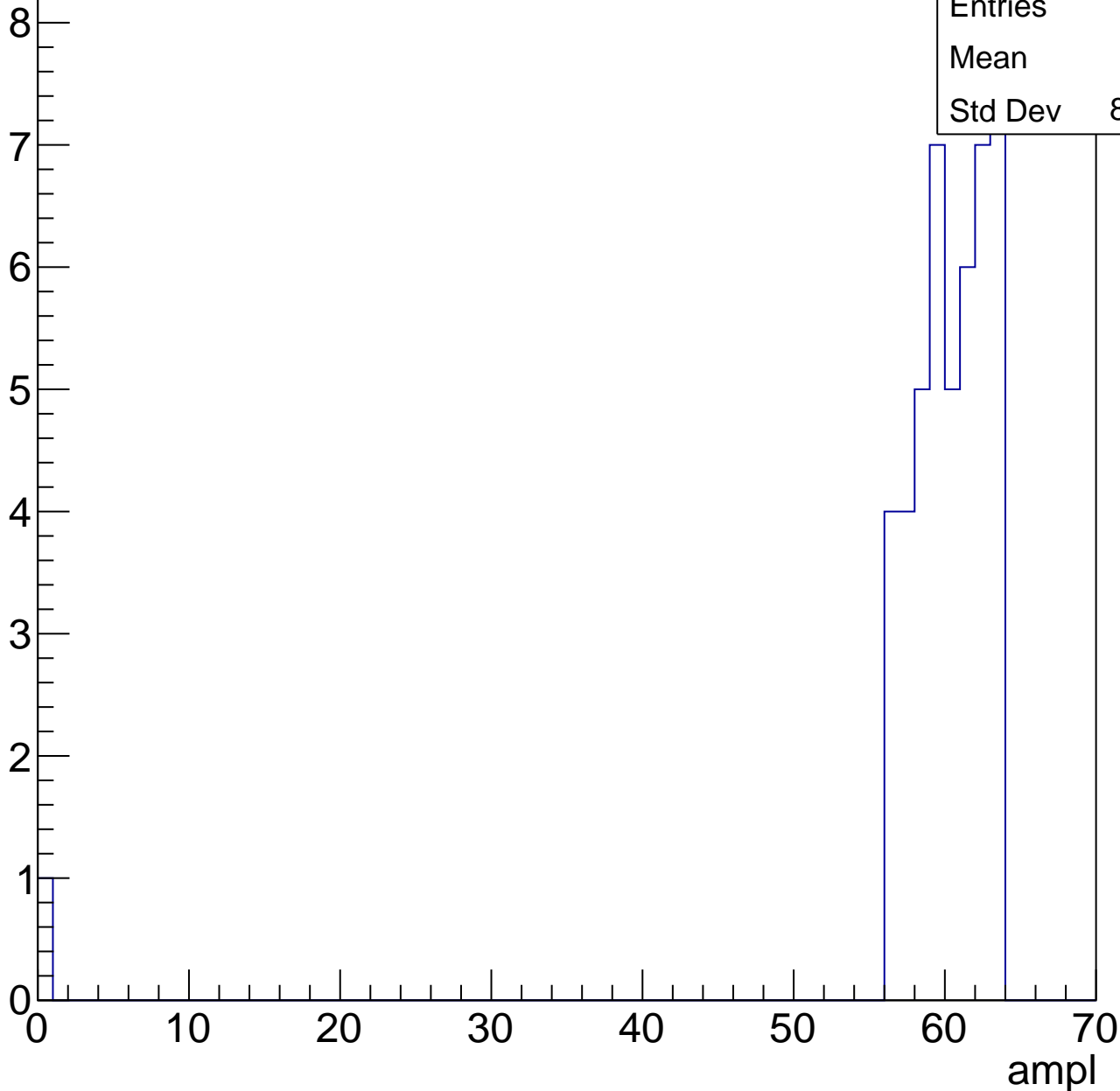


B1L101S, U15-ch81, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.7
Std Dev	8.937



B1L101S, U15-ch81, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

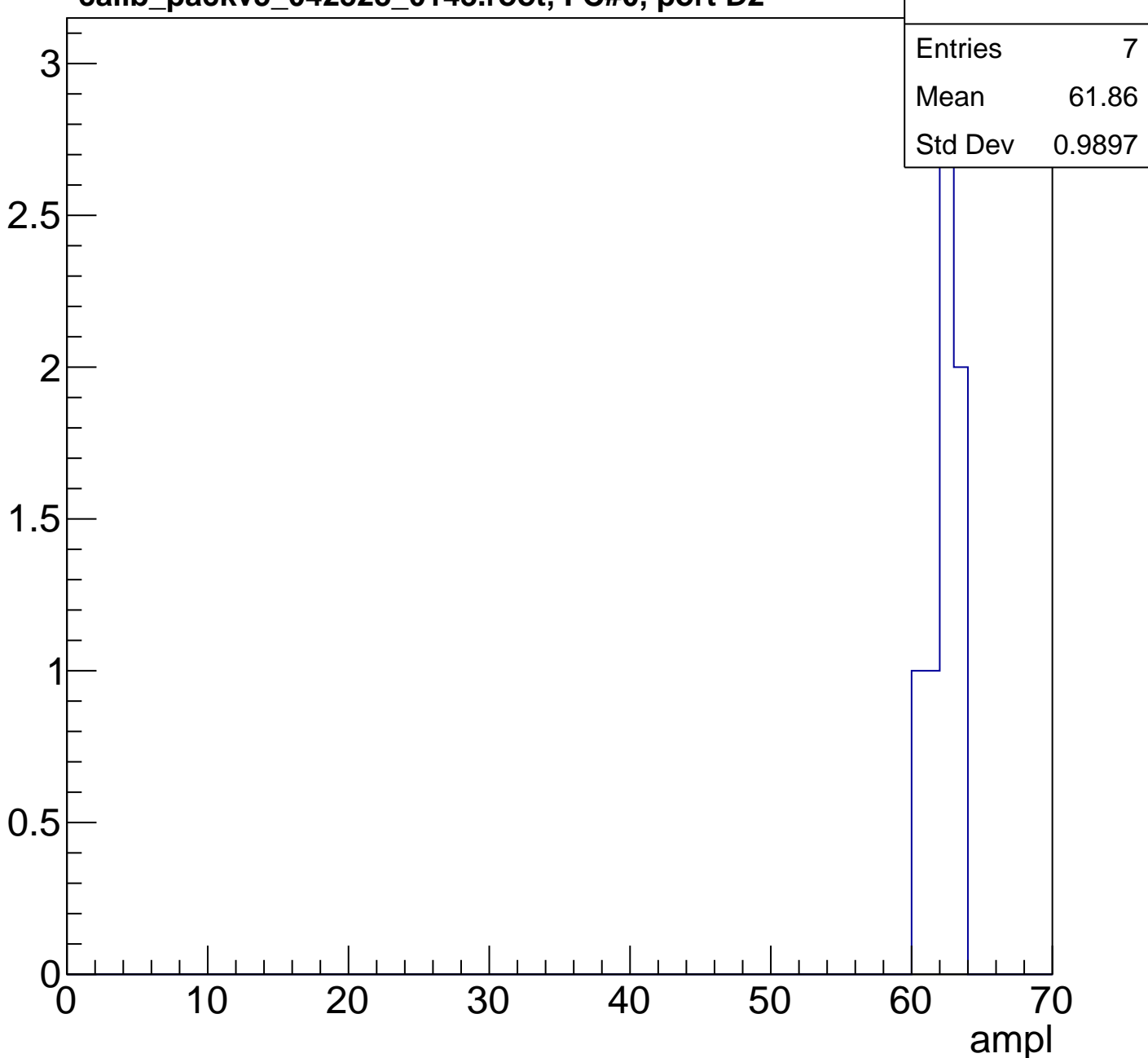
7

Mean

61.86

Std Dev

0.9897



B1L101S, U15-ch81, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch82, adc0

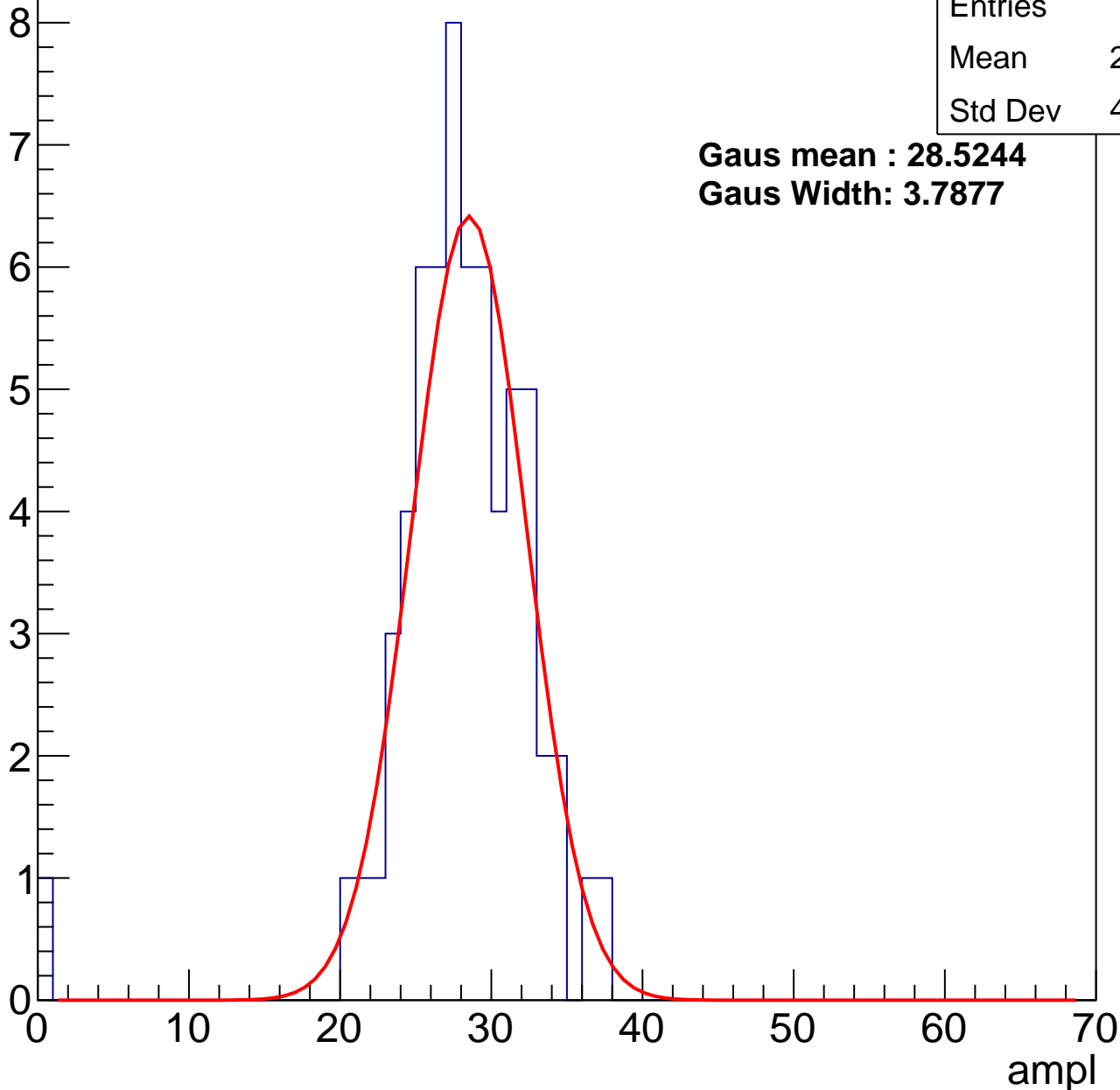
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	27.52
Std Dev	4.972

Gaus mean : 28.5244

Gaus Width: 3.7877



B1L101S, U15-ch82, adc1

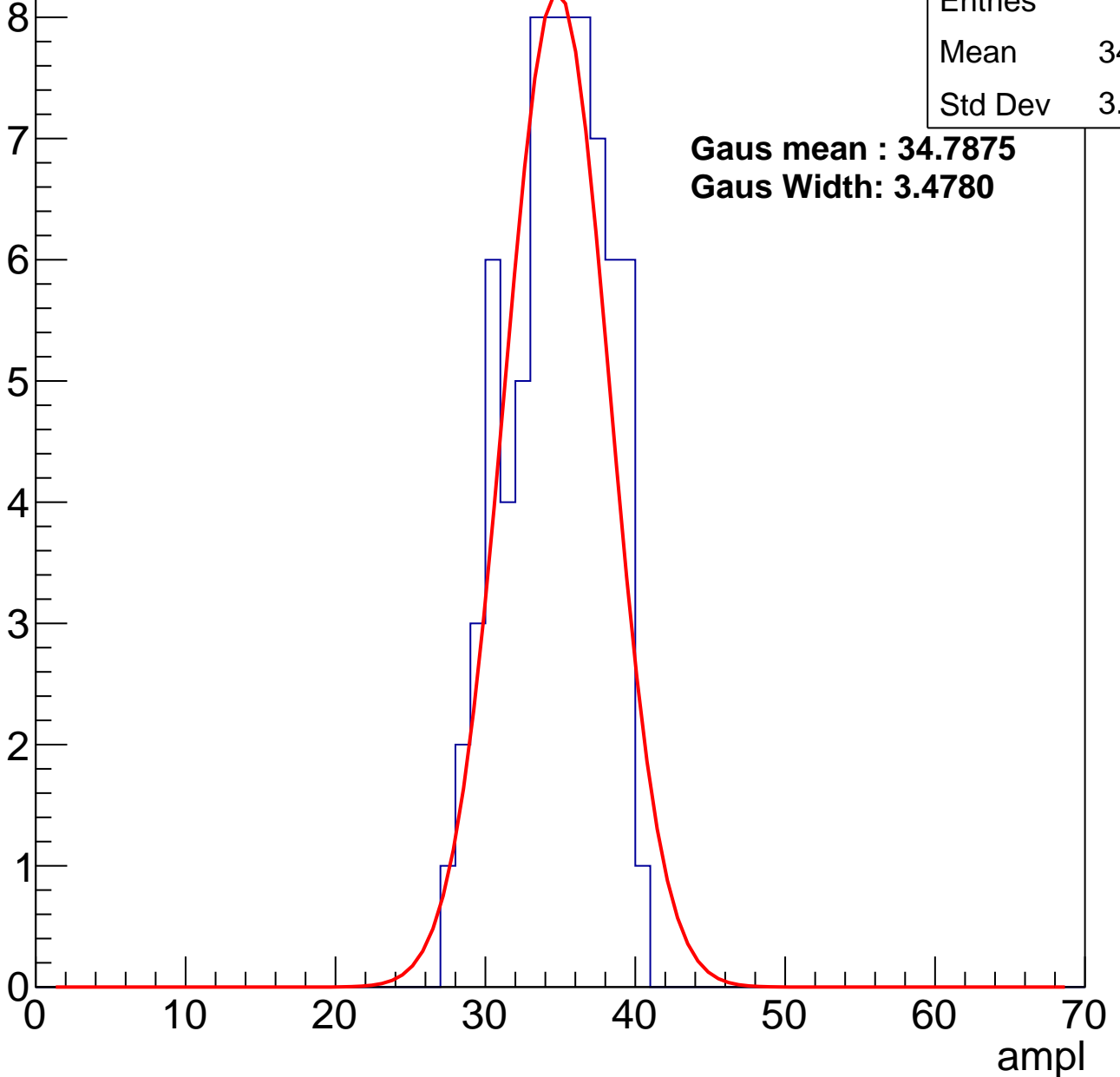
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	34.23
Std Dev	3.177

Gaus mean : 34.7875

Gaus Width: 3.4780



B1L101S, U15-ch82, adc2

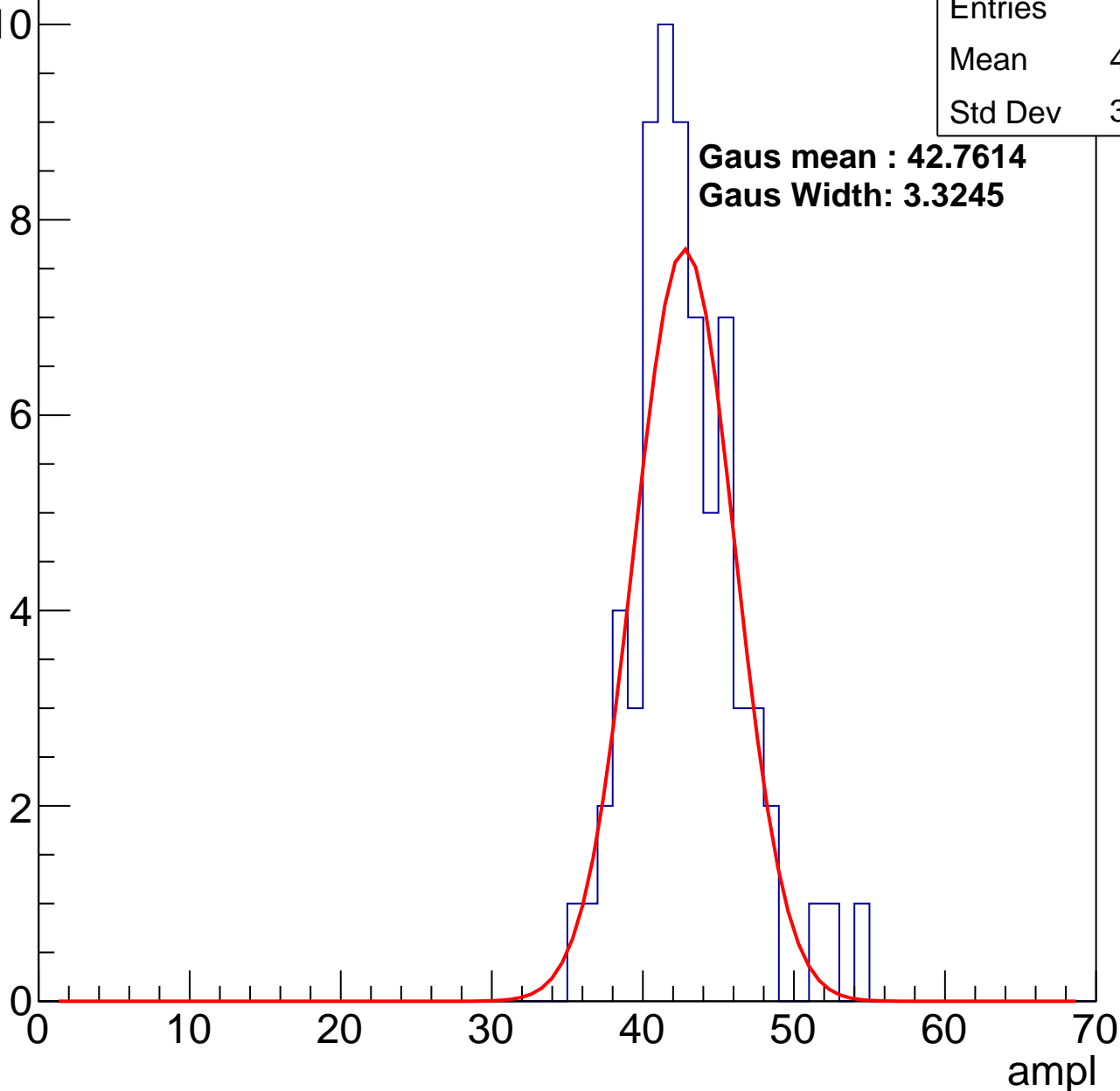
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.46
Std Dev	3.545

Gaus mean : 42.7614

Gaus Width: 3.3245

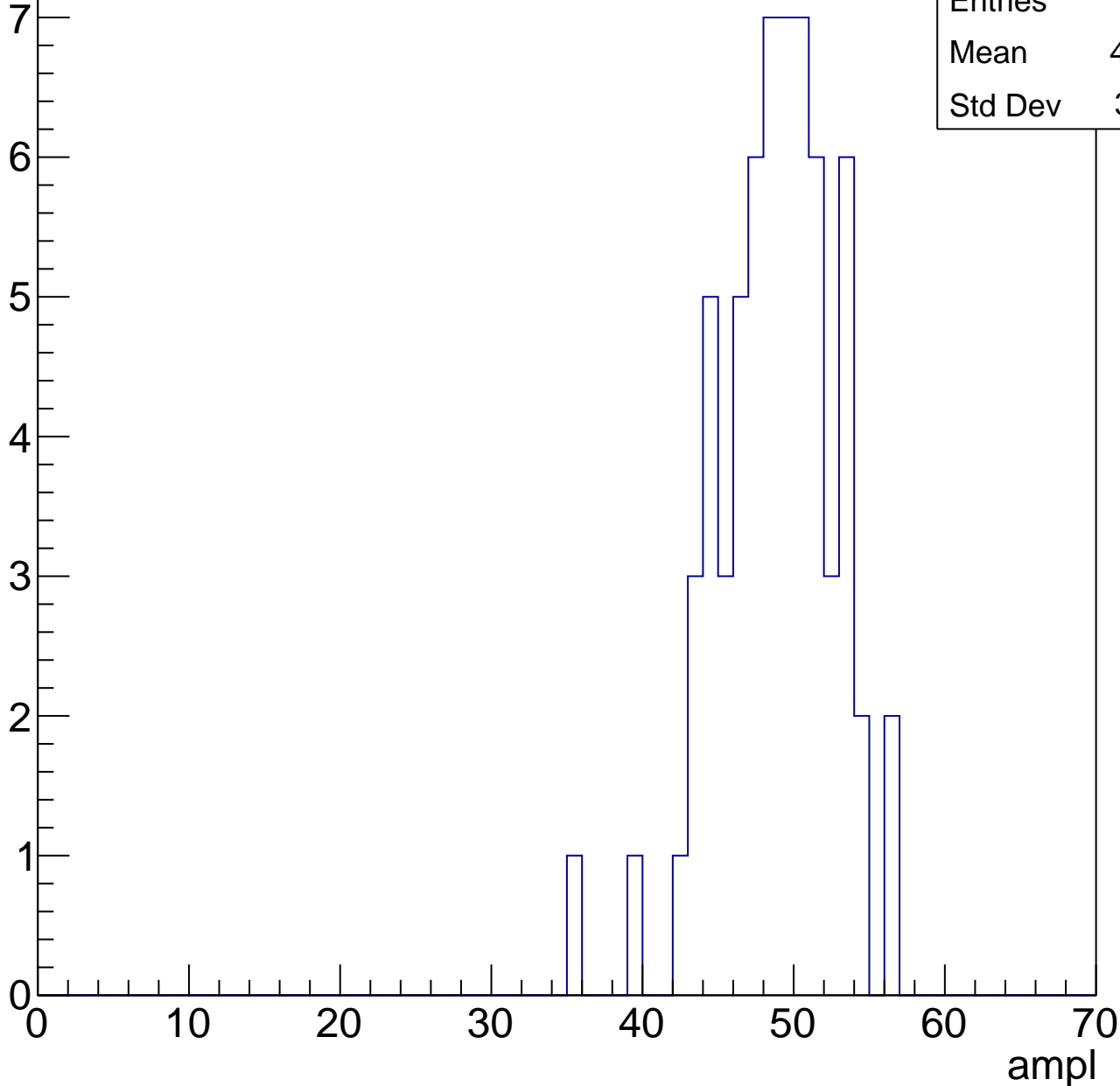


B1L101S, U15-ch82, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	48.32
Std Dev	3.871

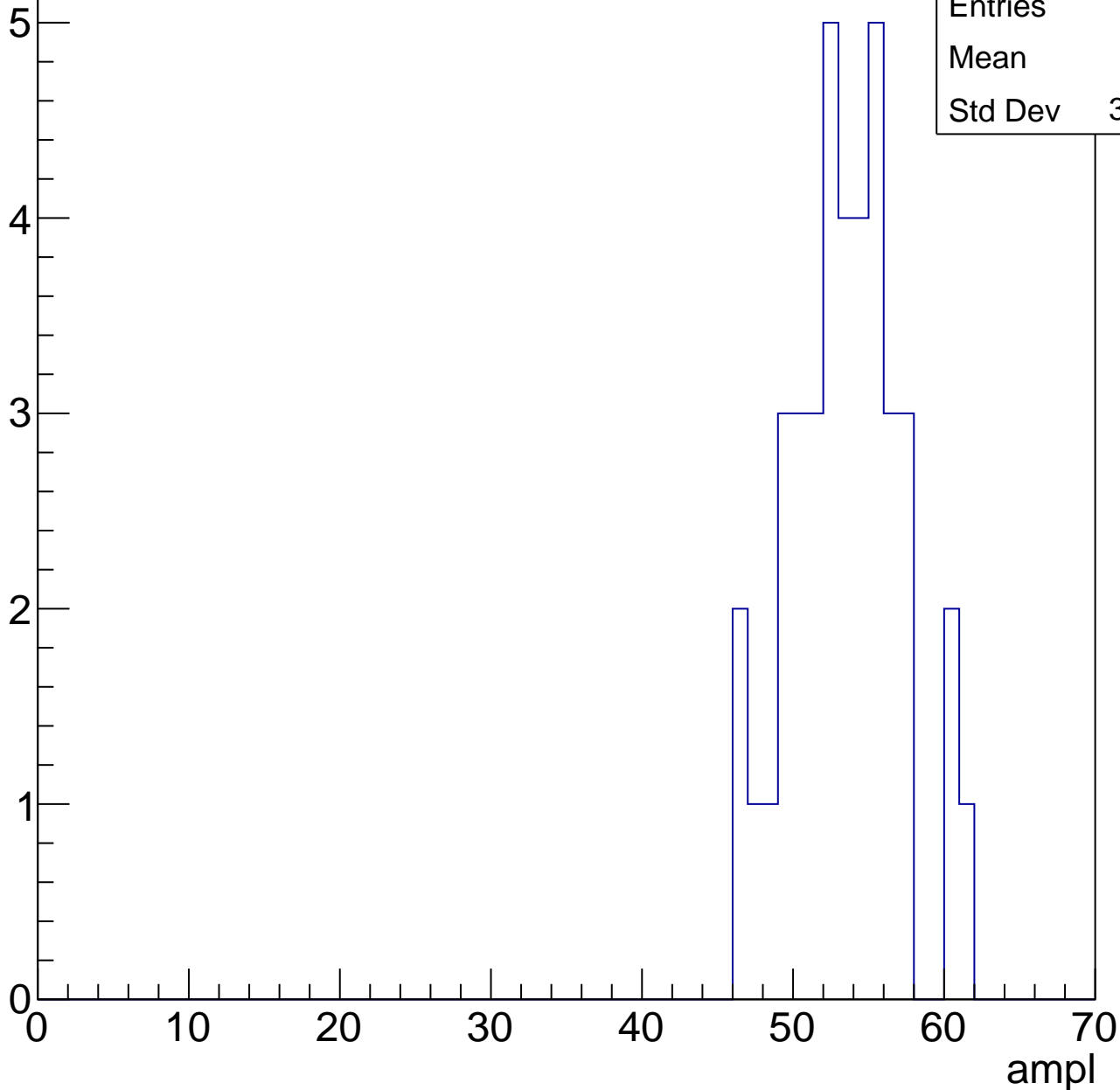


B1L101S, U15-ch82, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	40
Mean	53
Std Dev	3.578



B1L101S, U15-ch82, adc5

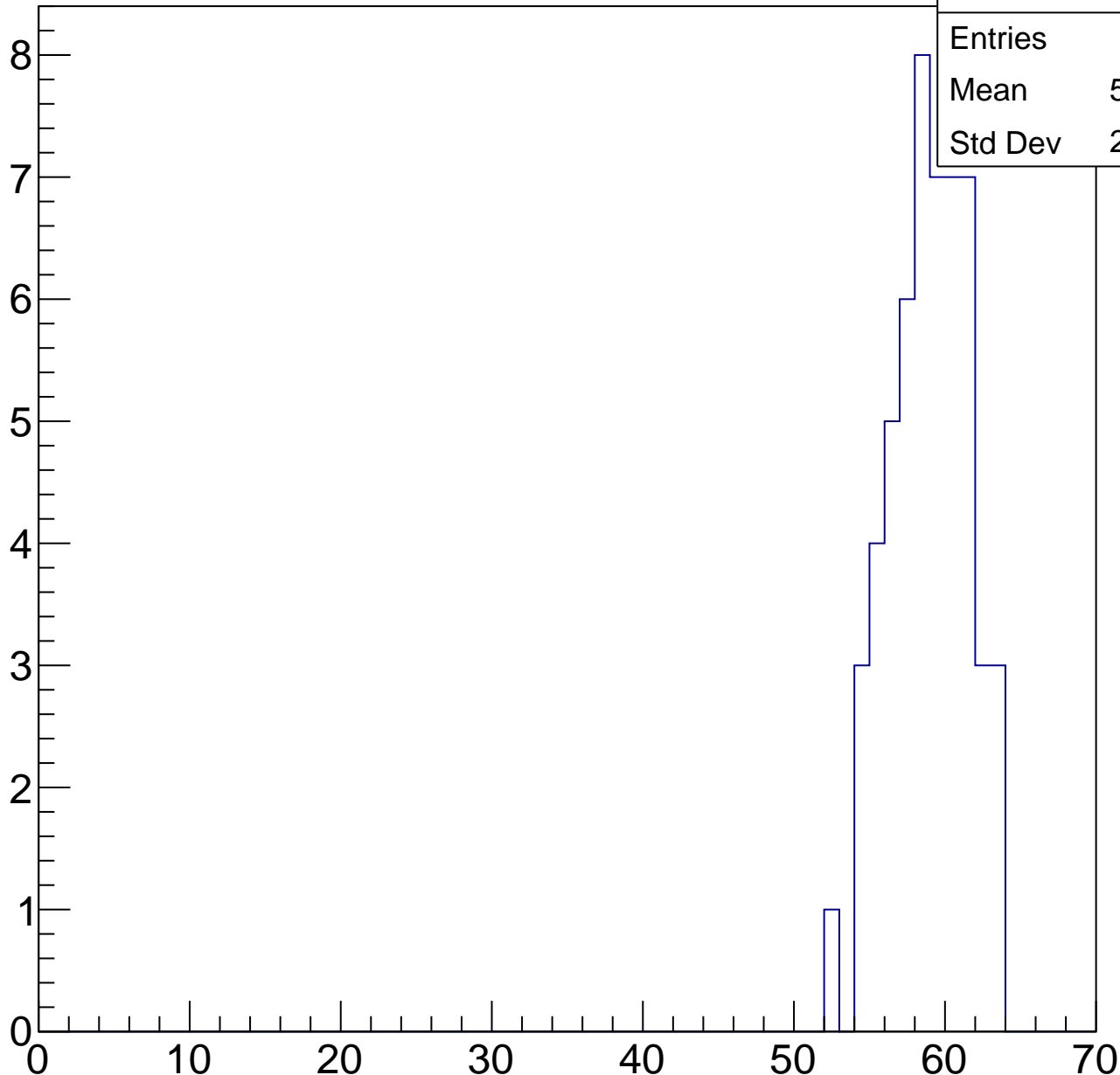
calib_packv5_042523_0143.root, FC#0, port D2

Entry

8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.43
Std Dev	2.572

ampl

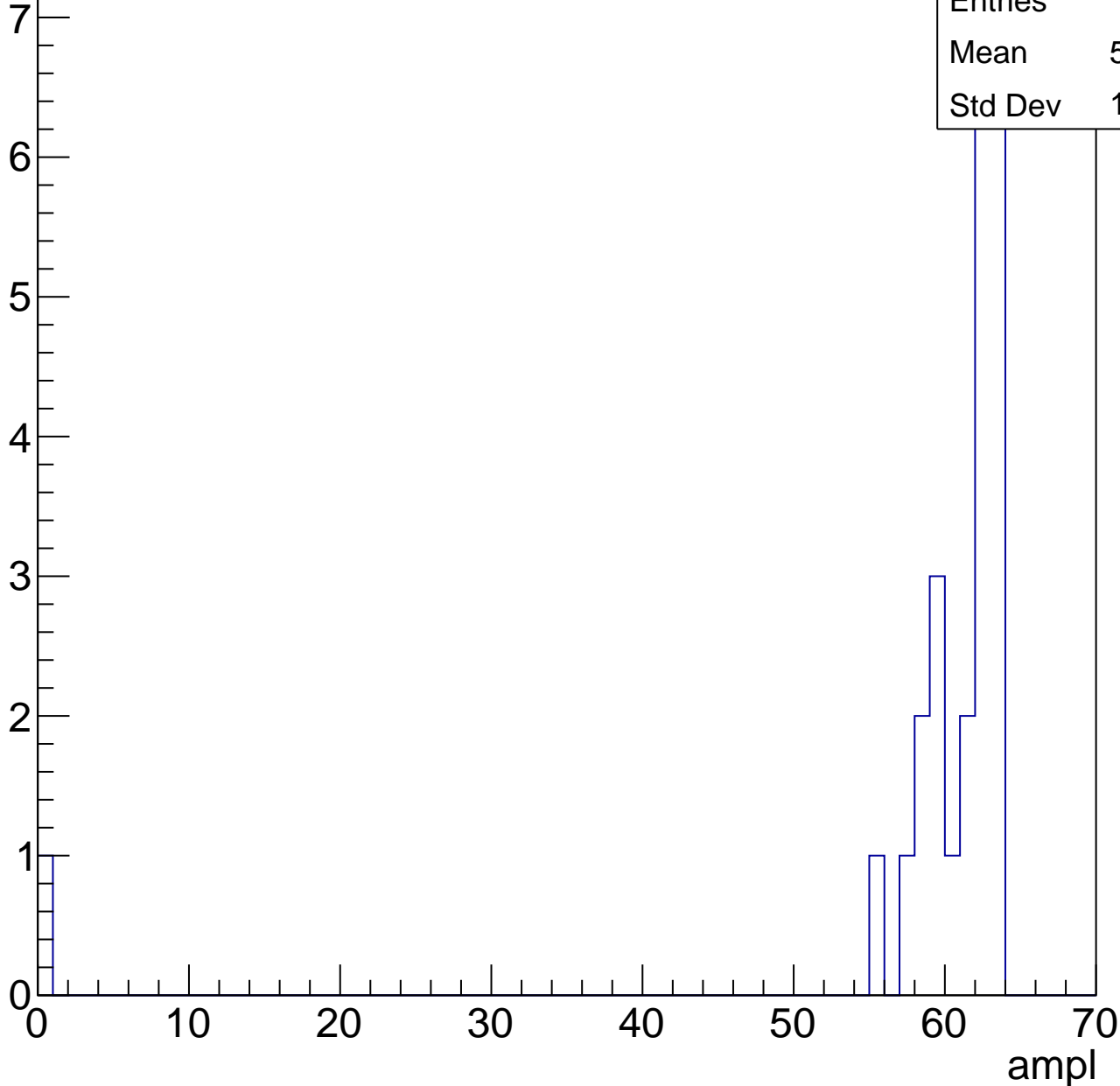


B1L101S, U15-ch82, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	25
Mean	58.48
Std Dev	12.13



B1L101S, U15-ch82, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch83, adc0

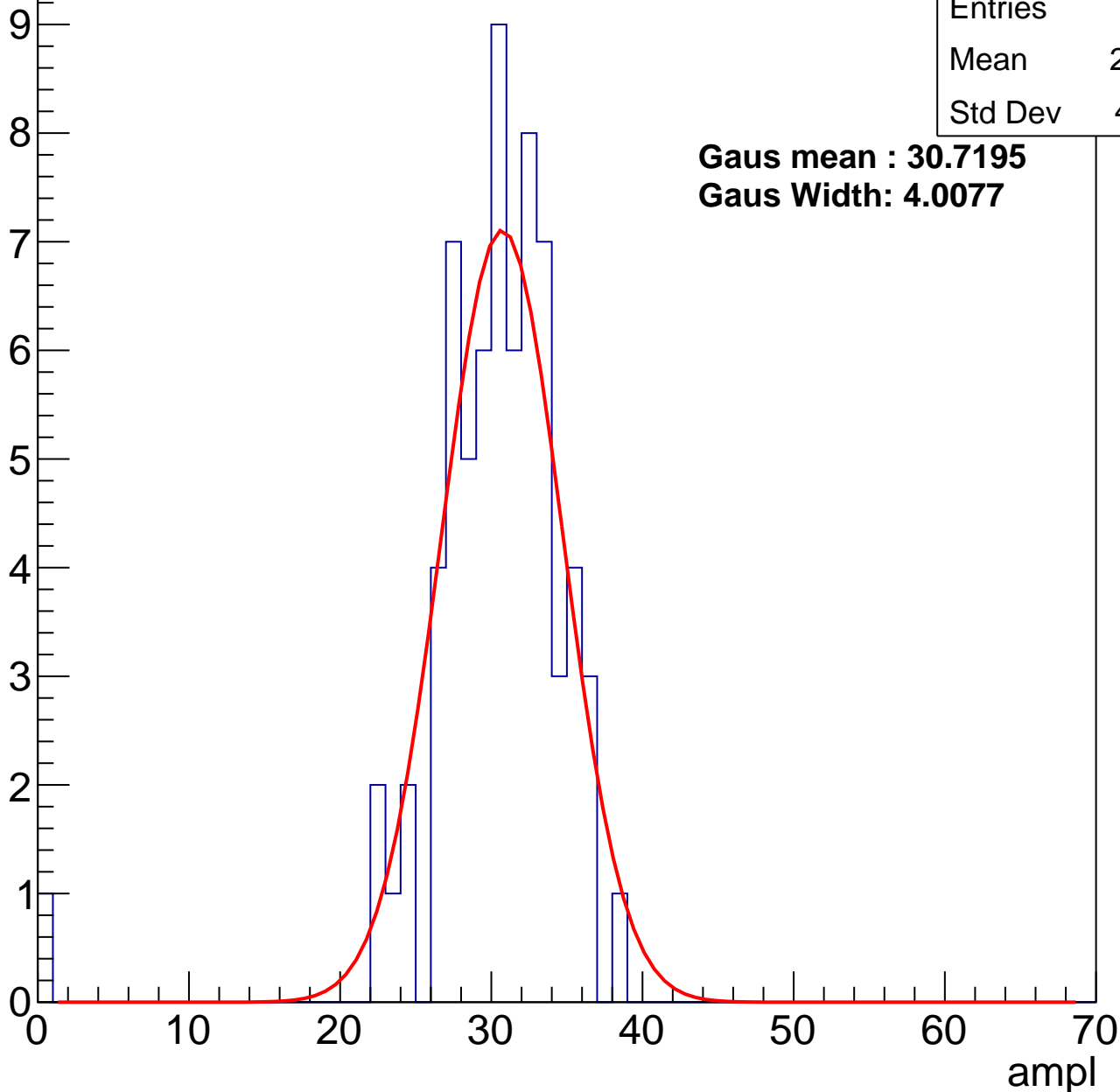
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	29.75
Std Dev	4.991

Gaus mean : 30.7195

Gaus Width: 4.0077



B1L101S, U15-ch83, adc1

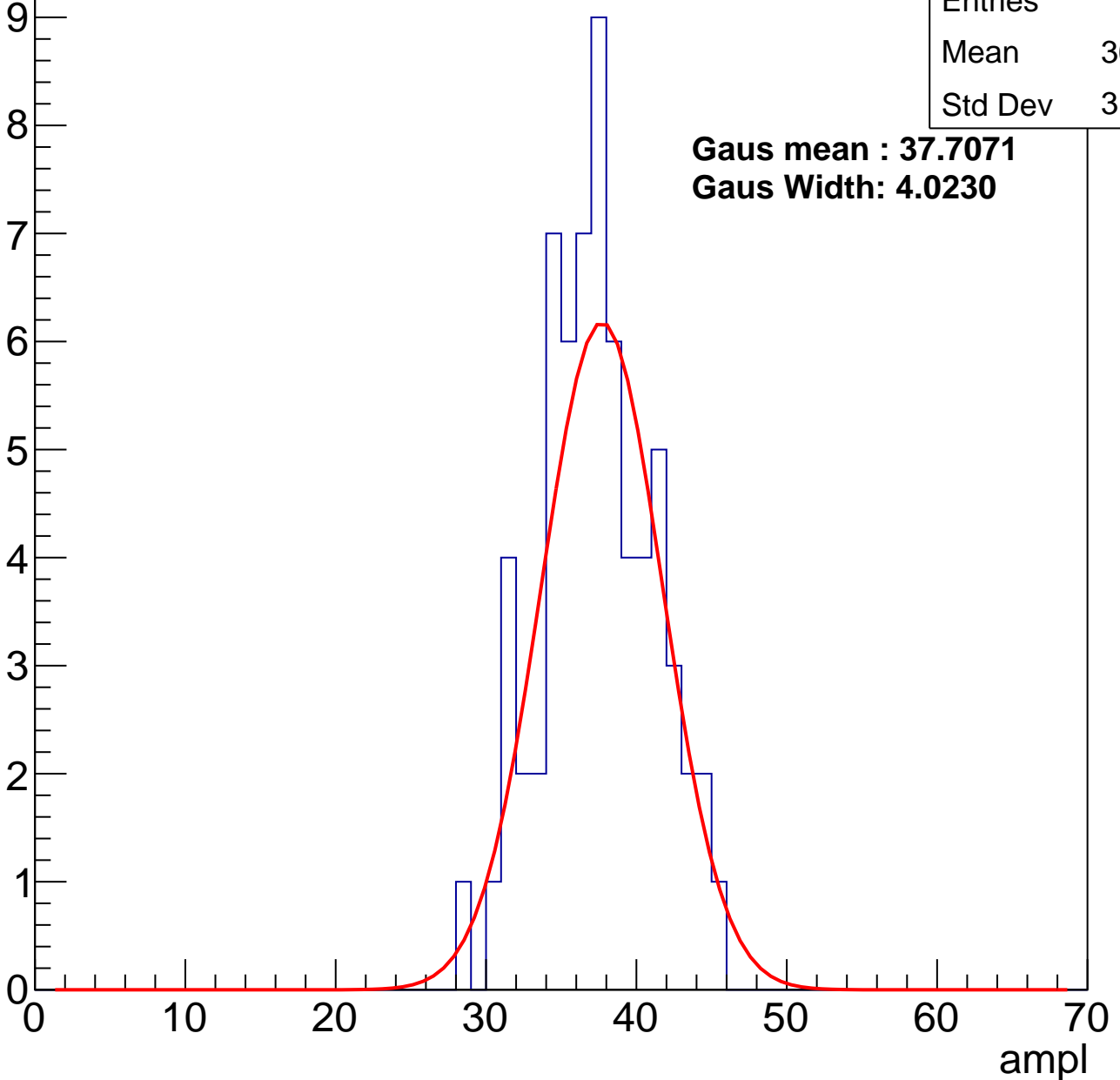
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	36.95
Std Dev	3.686

Gaus mean : 37.7071

Gaus Width: 4.0230

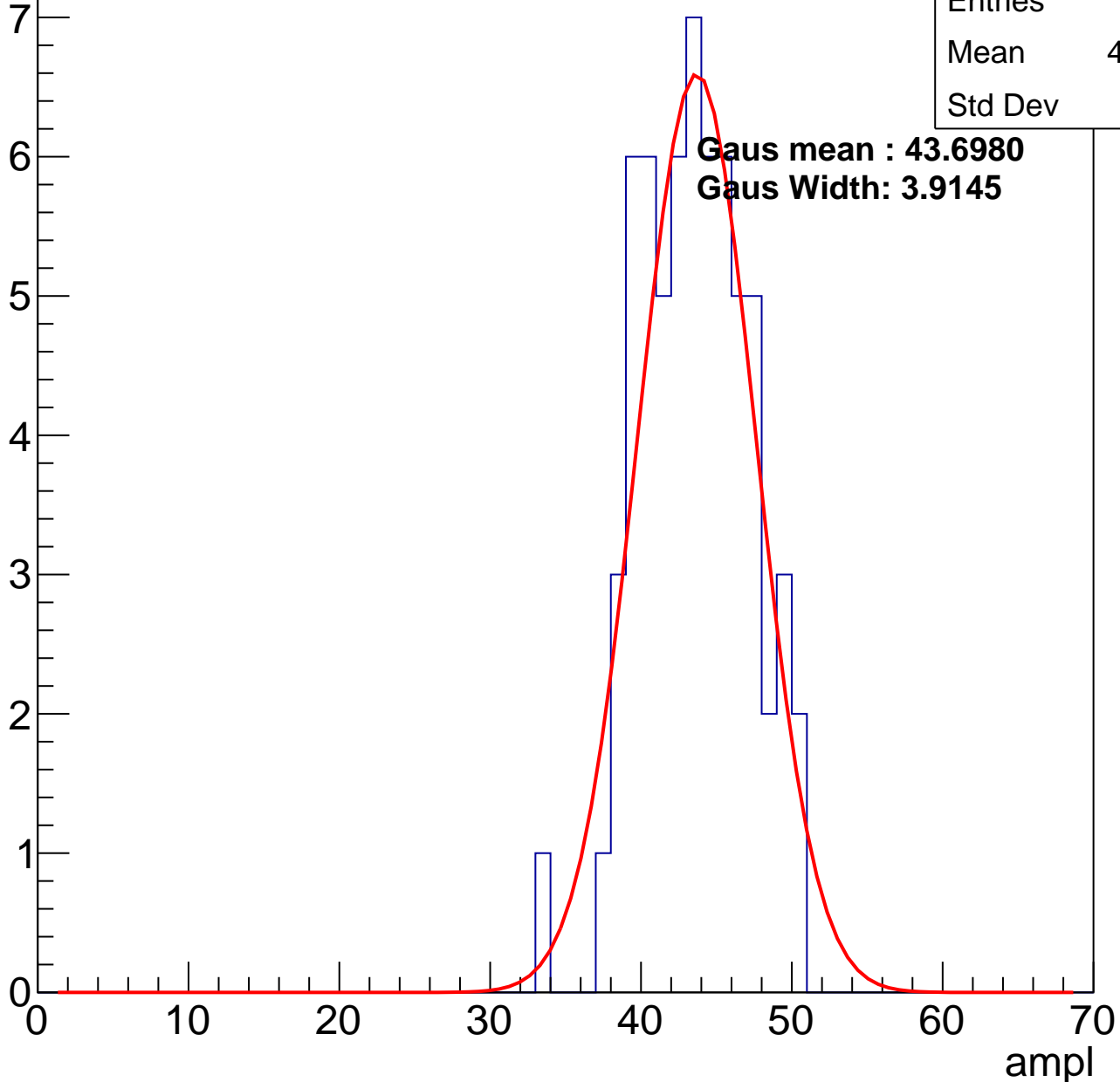


B1L101S, U15-ch83, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	43.09
Std Dev	3.53

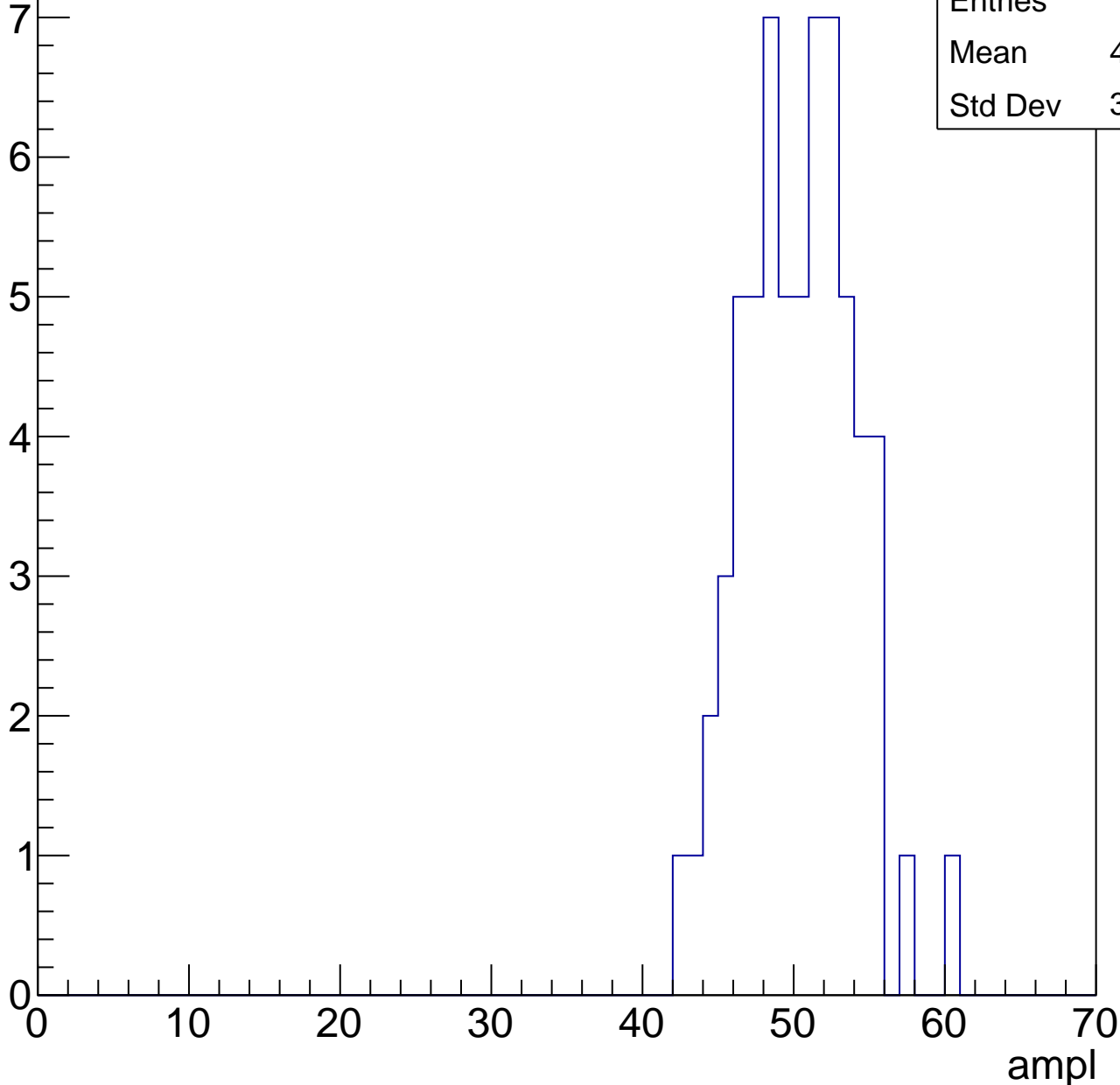


B1L101S, U15-ch83, adc3

calib_packv5_042523_0143.root, FC#0, port D2

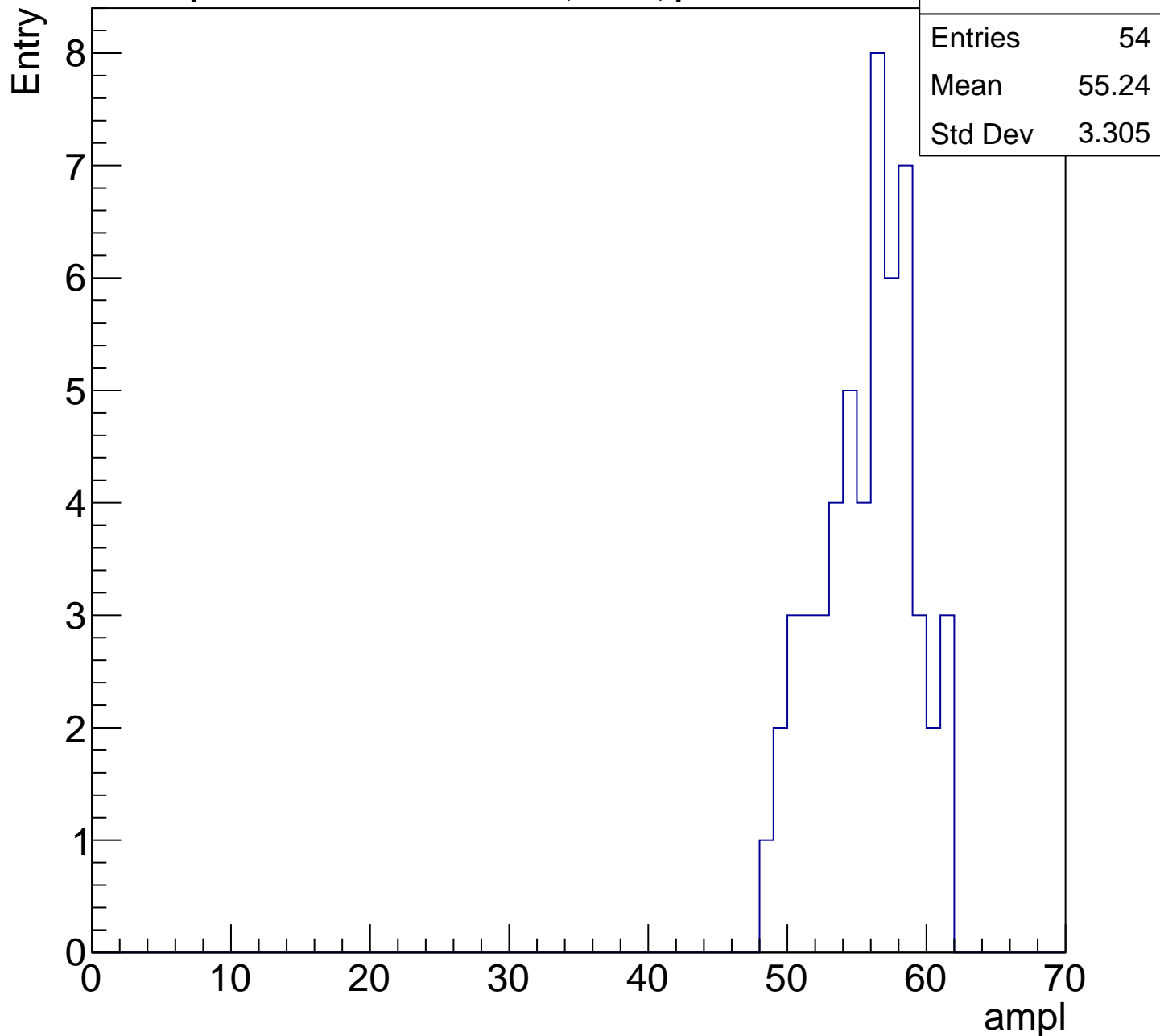
Entry

Entries	63
Mean	49.89
Std Dev	3.586



B1L101S, U15-ch83, adc4

calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch83, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 44

Mean 58.25

Std Dev 9.242

ampl

0

10

20

30

40

50

60

70

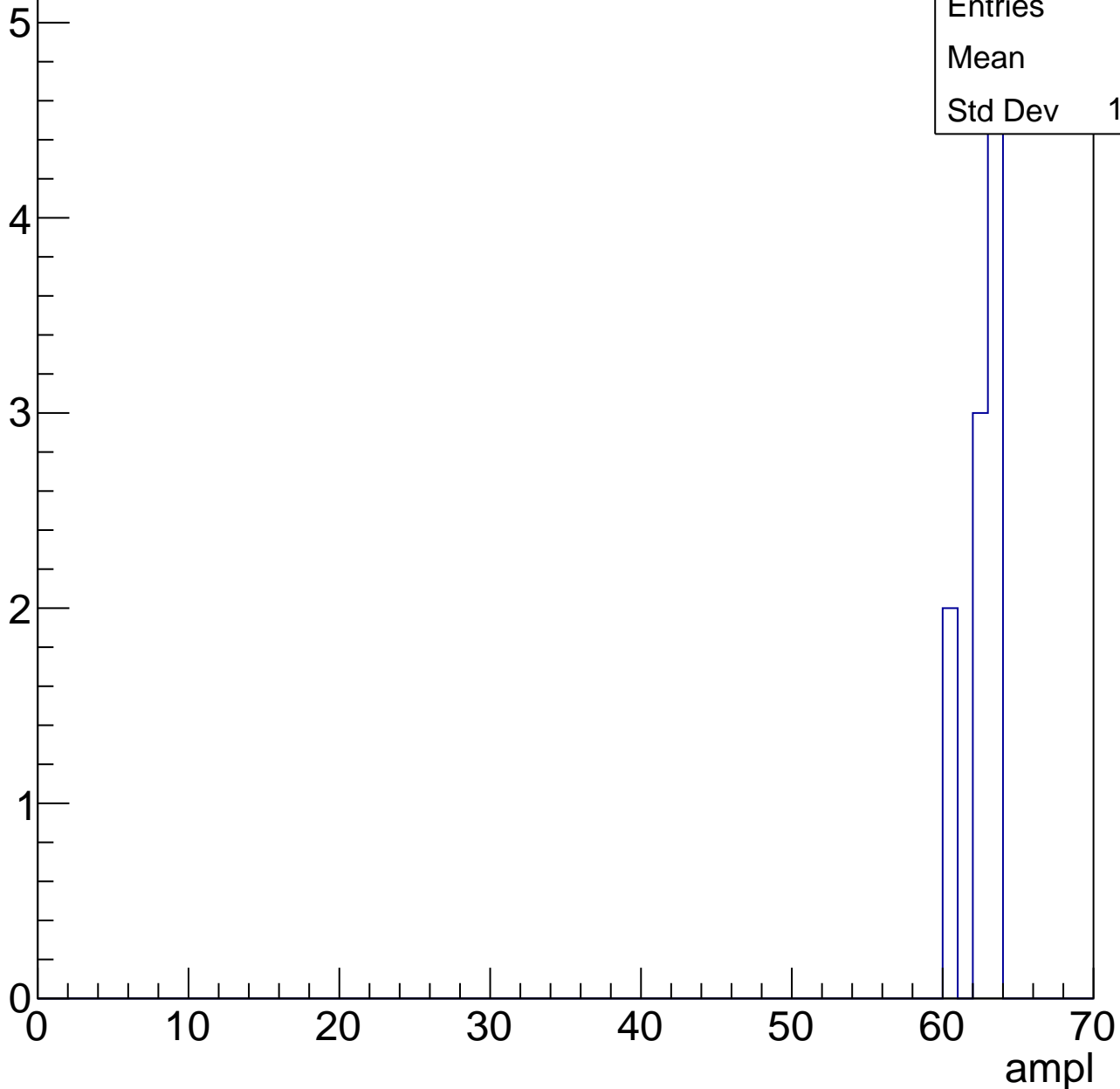
0

B1L101S, U15-ch83, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	10
Mean	62.1
Std Dev	1.136



B1L101S, U15-ch83, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch84, adc0

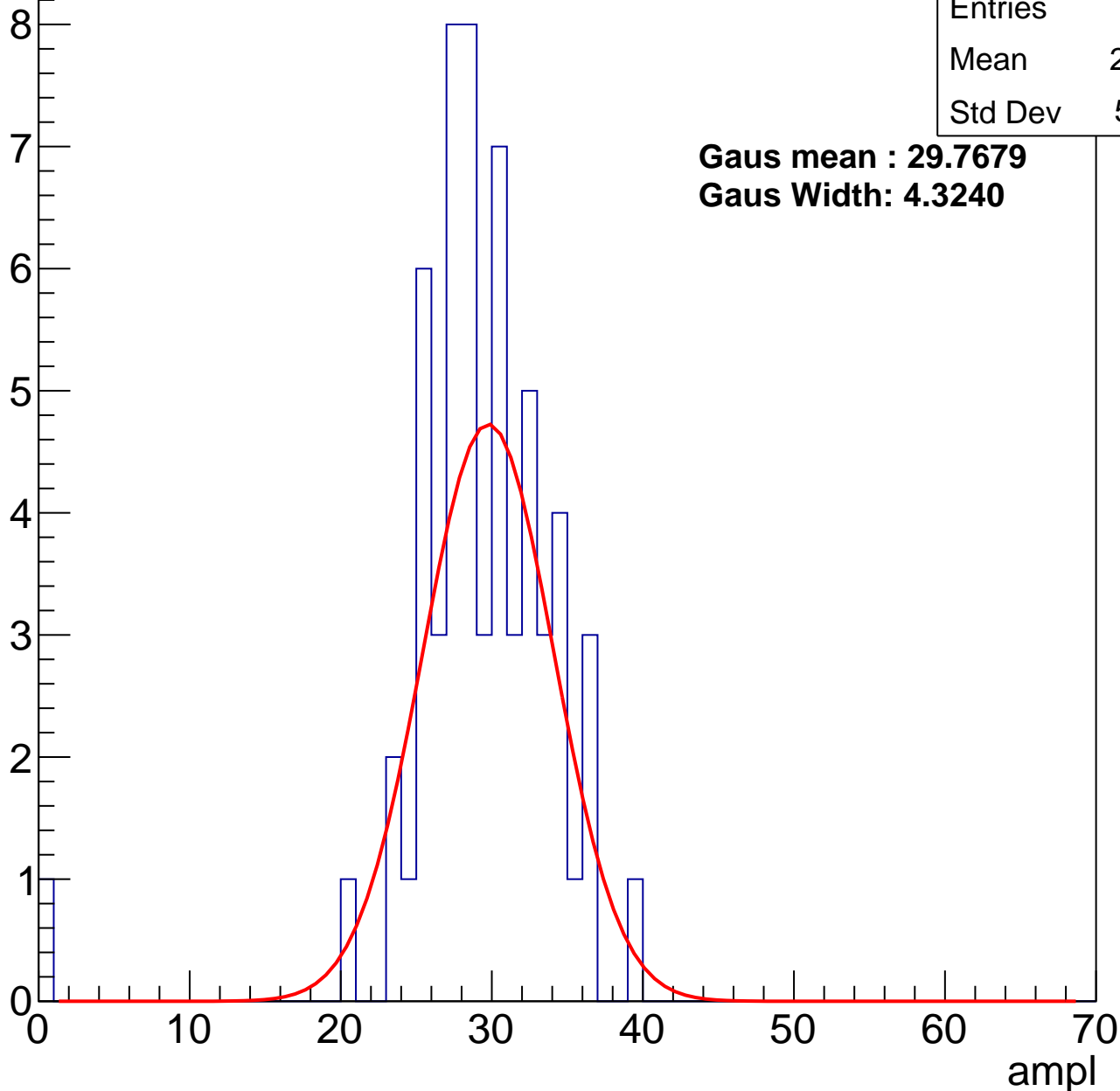
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.75
Std Dev	5.281

Gaus mean : 29.7679

Gaus Width: 4.3240



B1L101S, U15-ch84, adc1

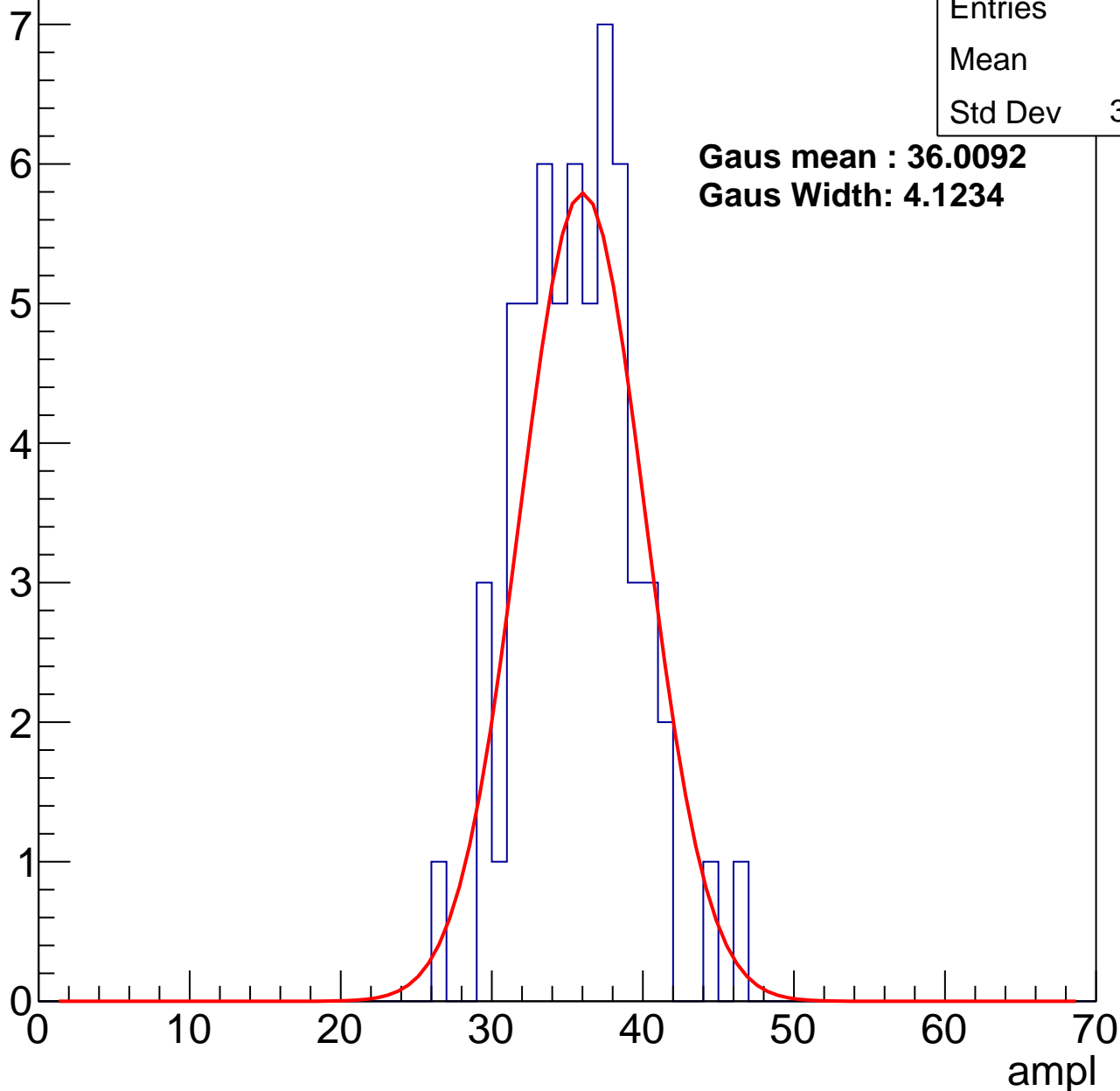
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	35.2
Std Dev	3.763

Gaus mean : 36.0092

Gaus Width: 4.1234



B1L101S, U15-ch84, adc2

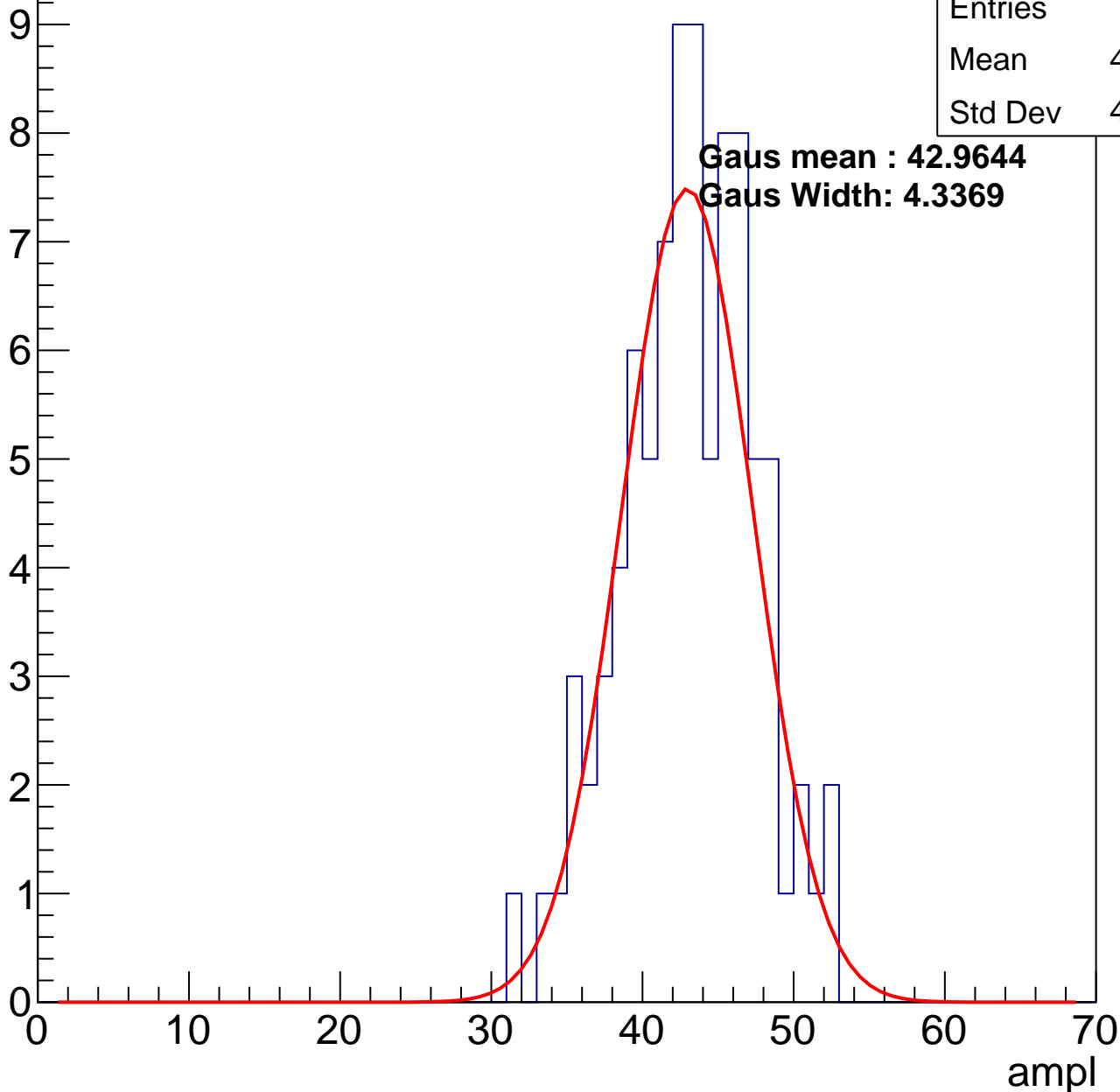
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	88
Mean	42.62
Std Dev	4.347

Gaus mean : 42.9644

Gaus Width: 4.3369

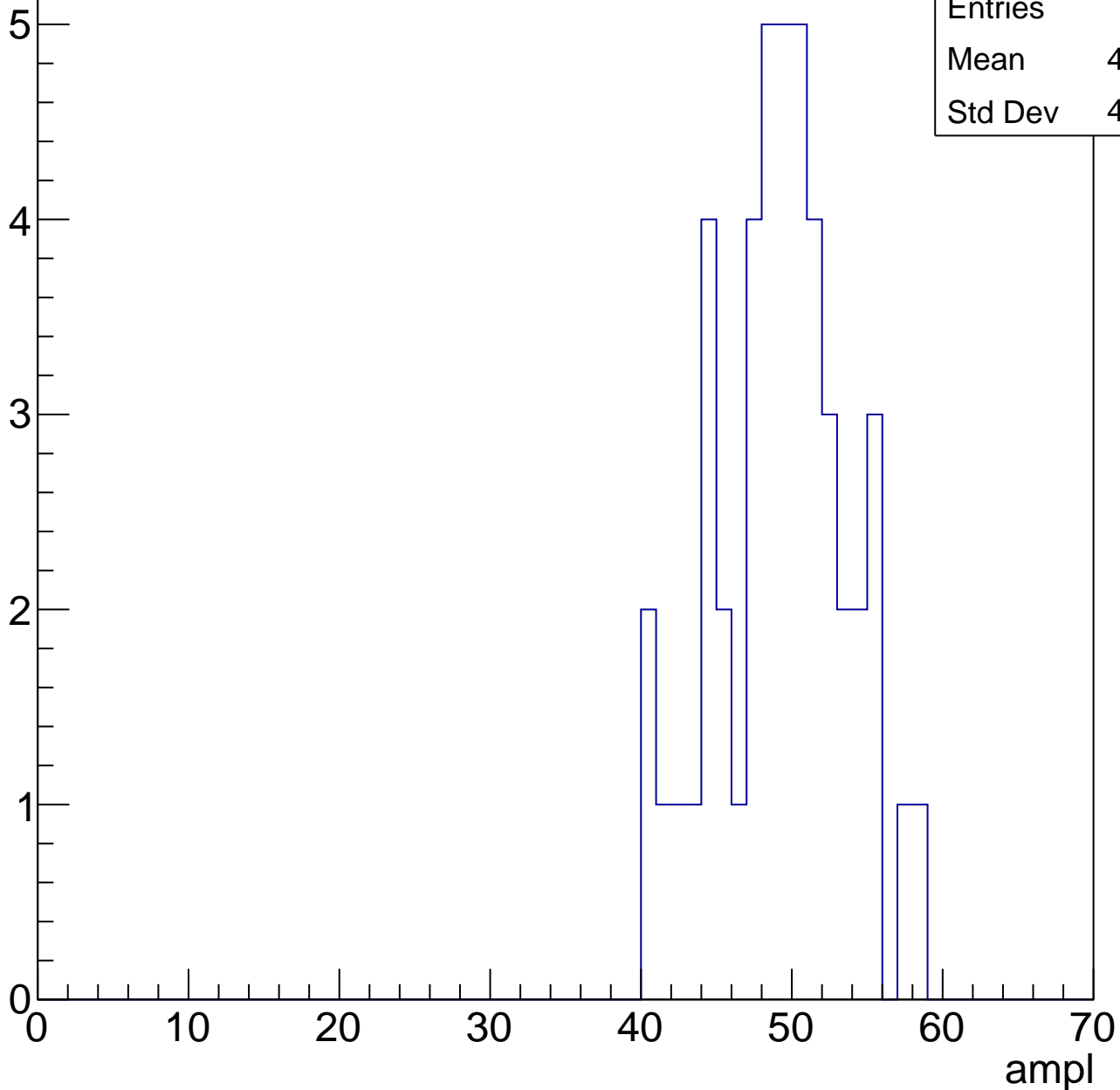


B1L101S, U15-ch84, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	48.83
Std Dev	4.264

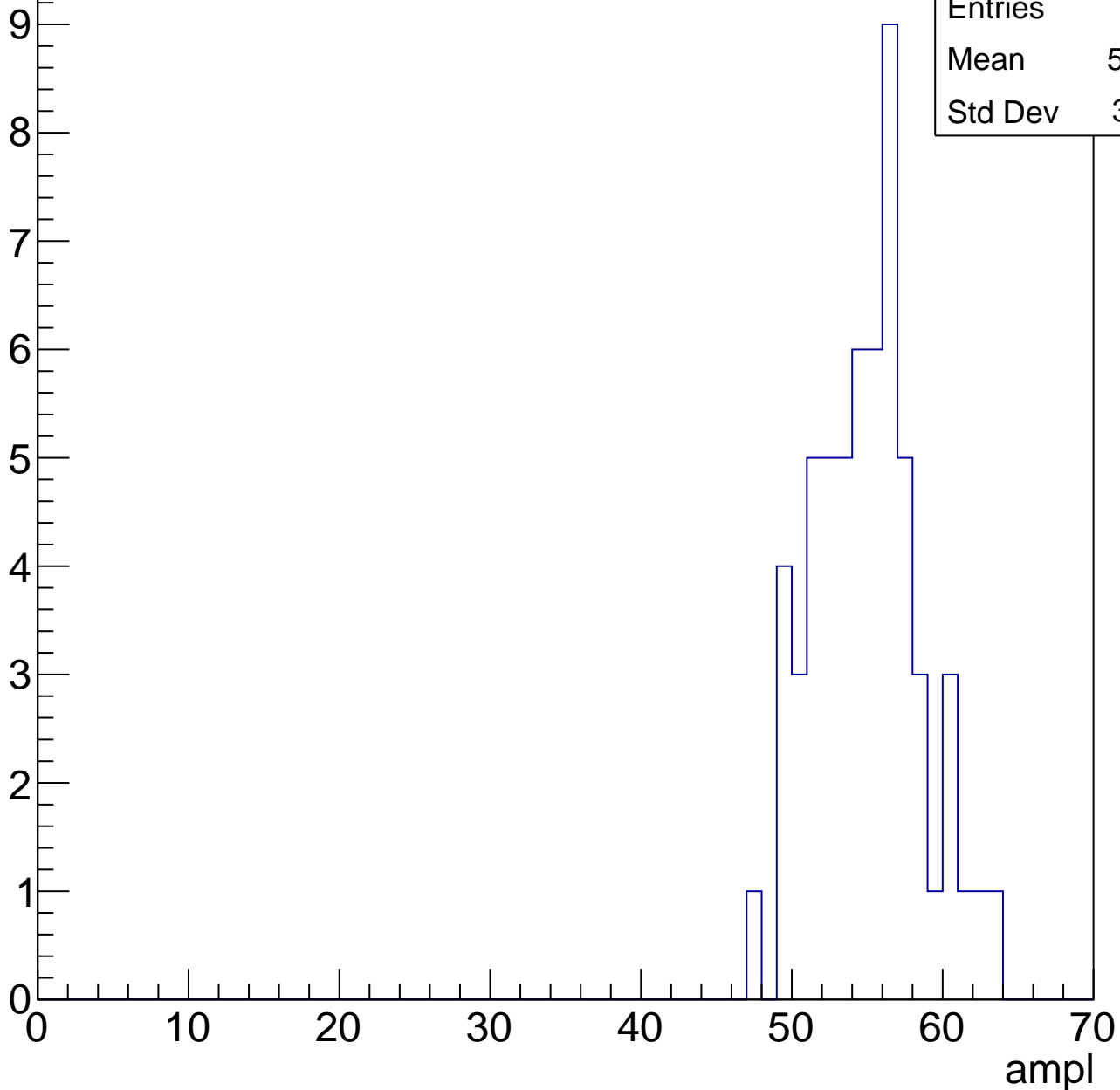


B1L101S, U15-ch84, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.49
Std Dev	3.481



B1L101S, U15-ch84, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

Entries 50

Mean 58.16

Std Dev 8.767

ampl

0

10

20

30

40

50

60

70

B1L101S, U15-ch84, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.67
Std Dev	1.7

ampl

0 10 20 30 40 50 60 70

B1L101S, U15-ch84, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch85, adc0

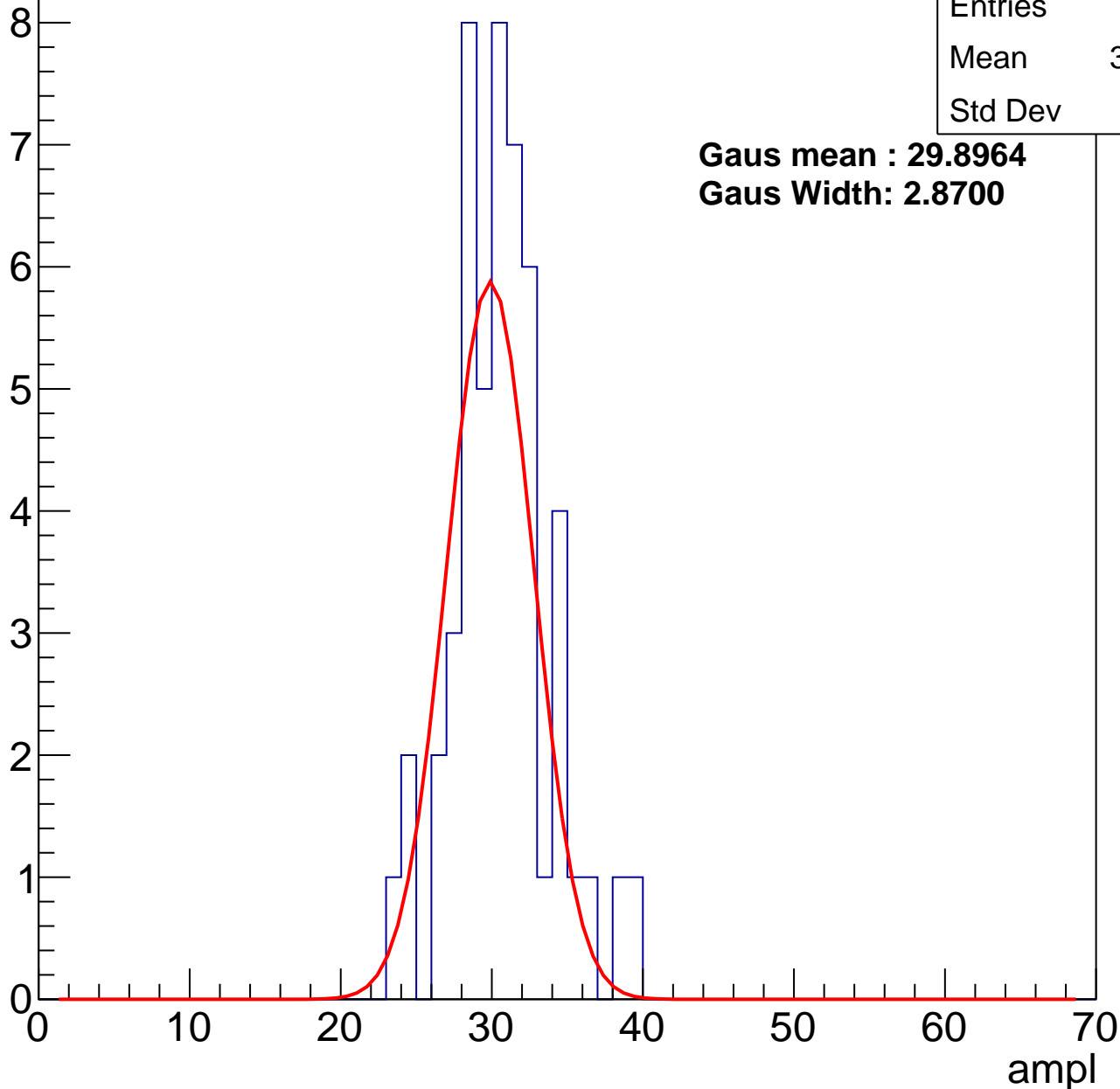
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	30.18
Std Dev	3.21

Gaus mean : 29.8964

Gaus Width: 2.8700



B1L101S, U15-ch85, adc1

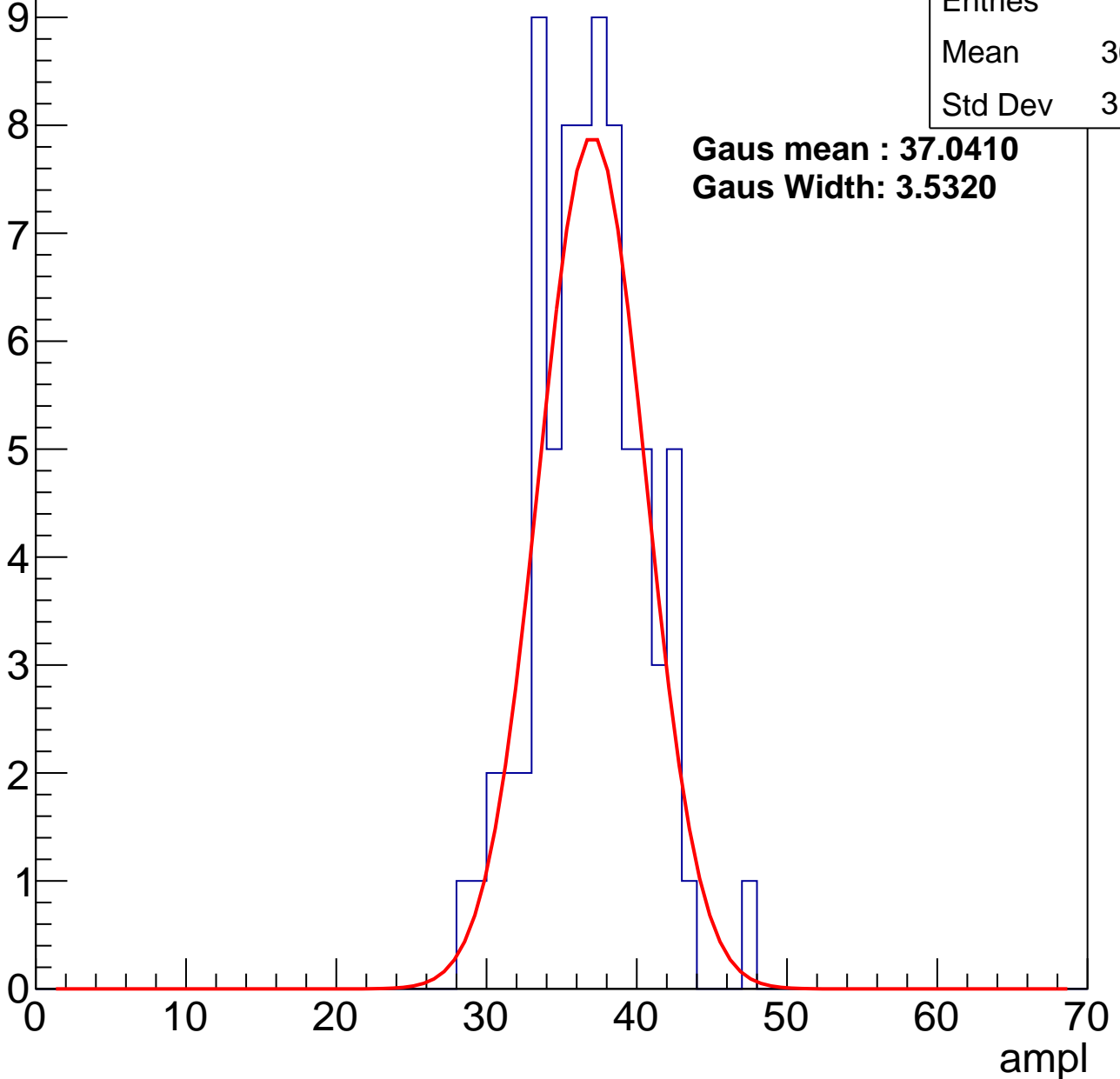
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	36.44
Std Dev	3.564

Gaus mean : 37.0410

Gaus Width: 3.5320



B1L101S, U15-ch85, adc2

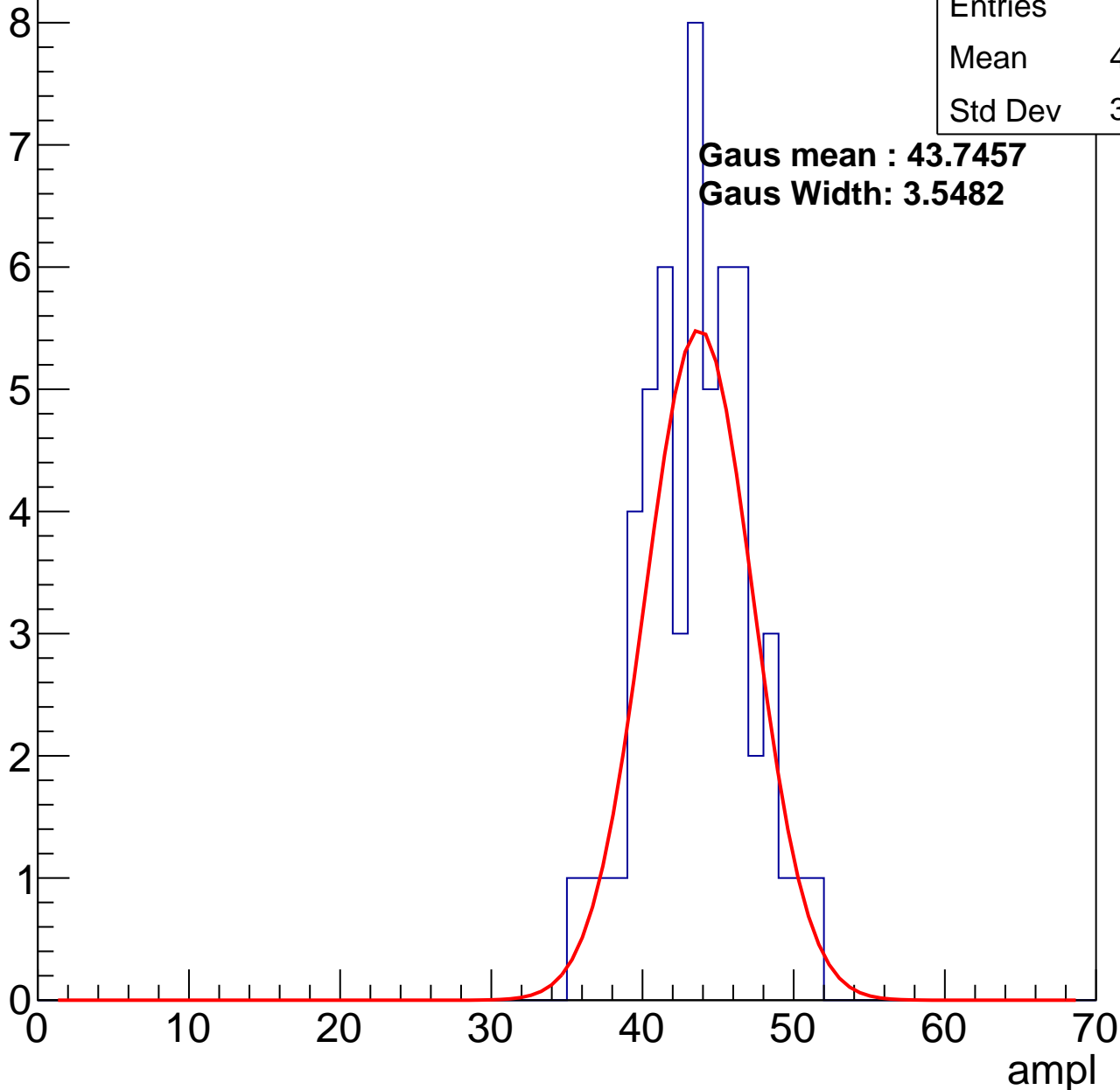
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	43.13
Std Dev	3.433

Gaus mean : 43.7457

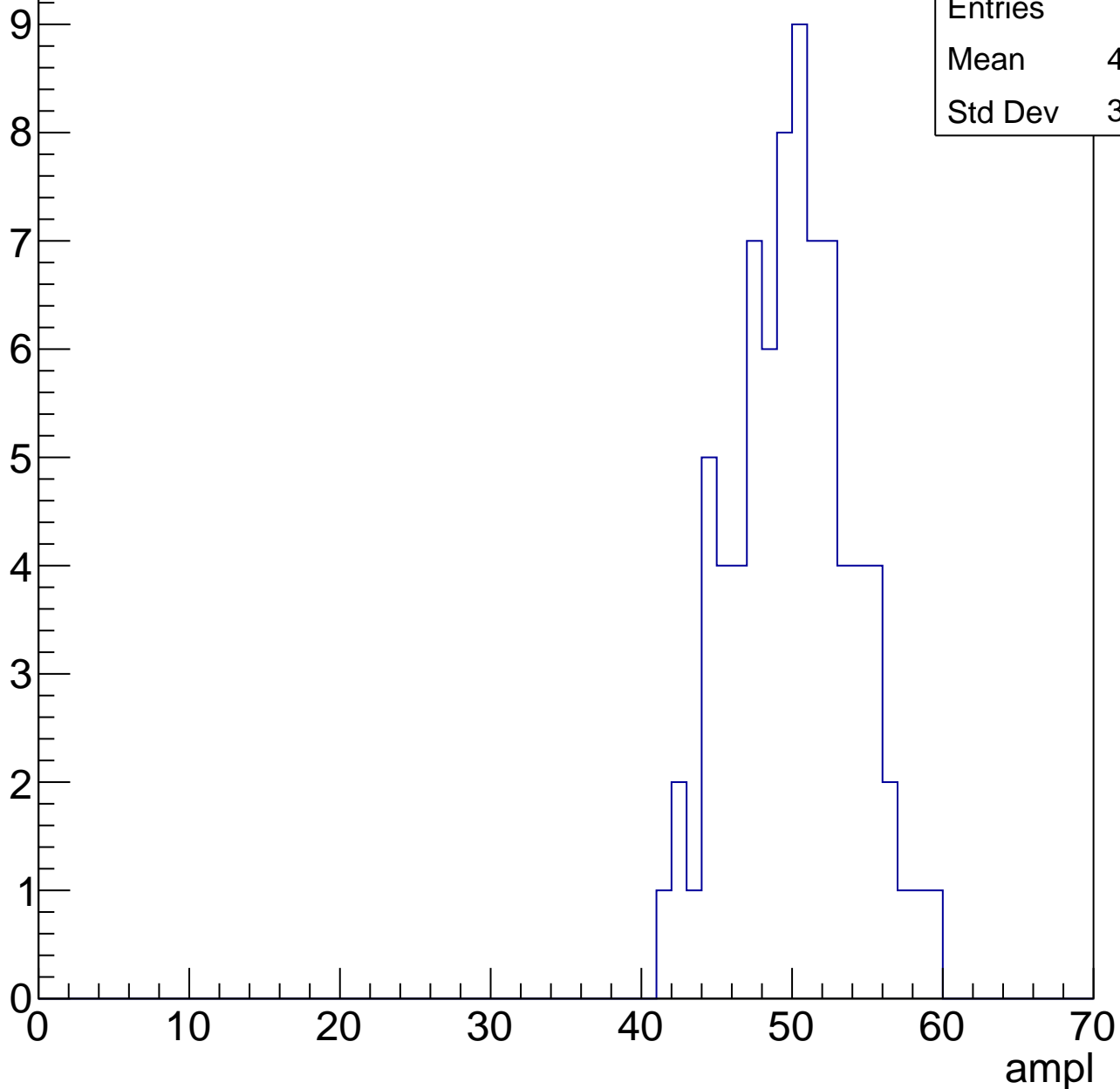
Gaus Width: 3.5482



B1L101S, U15-ch85, adc3

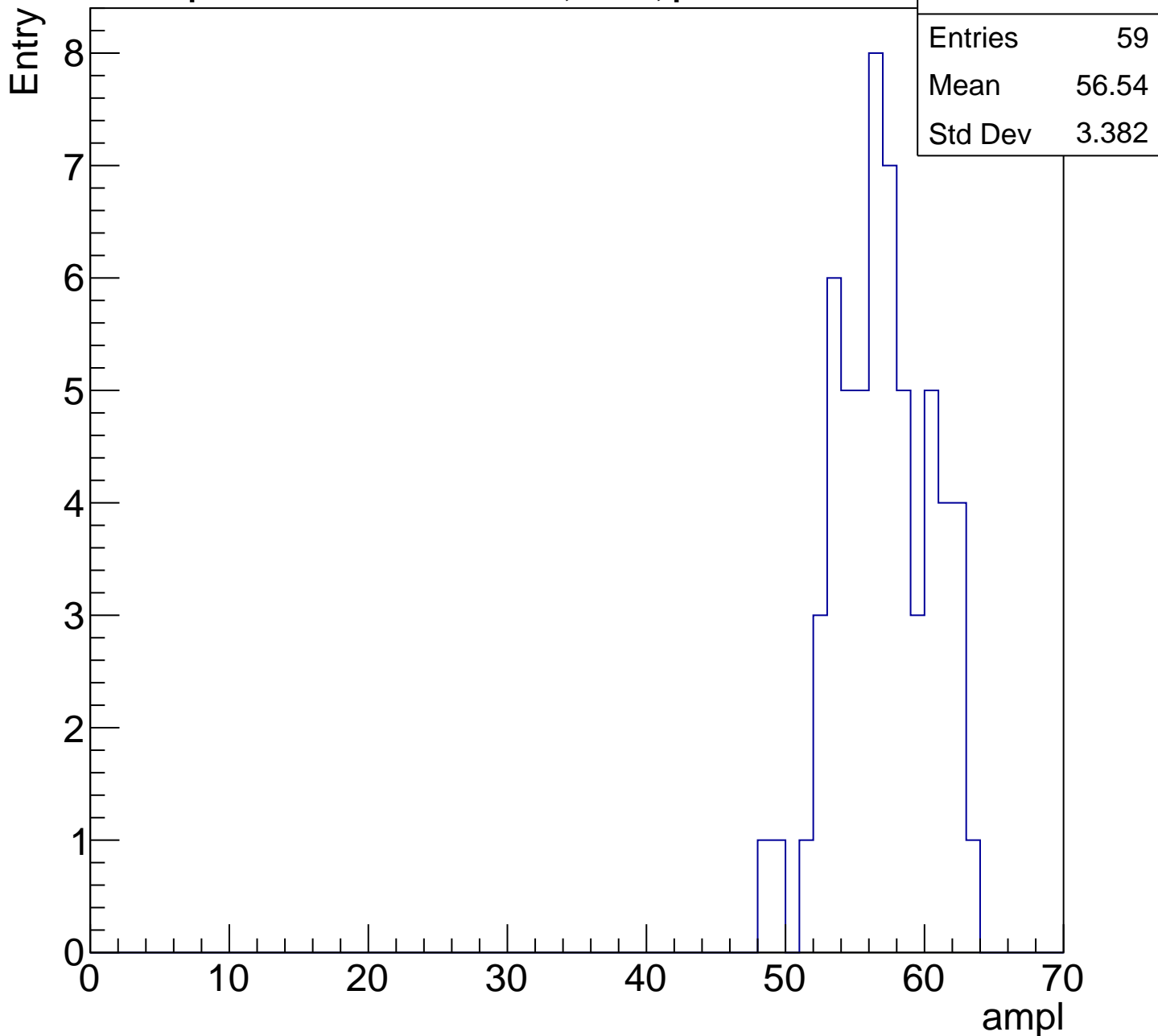
calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch85, adc4

calib_packv5_042523_0143.root, FC#0, port D2

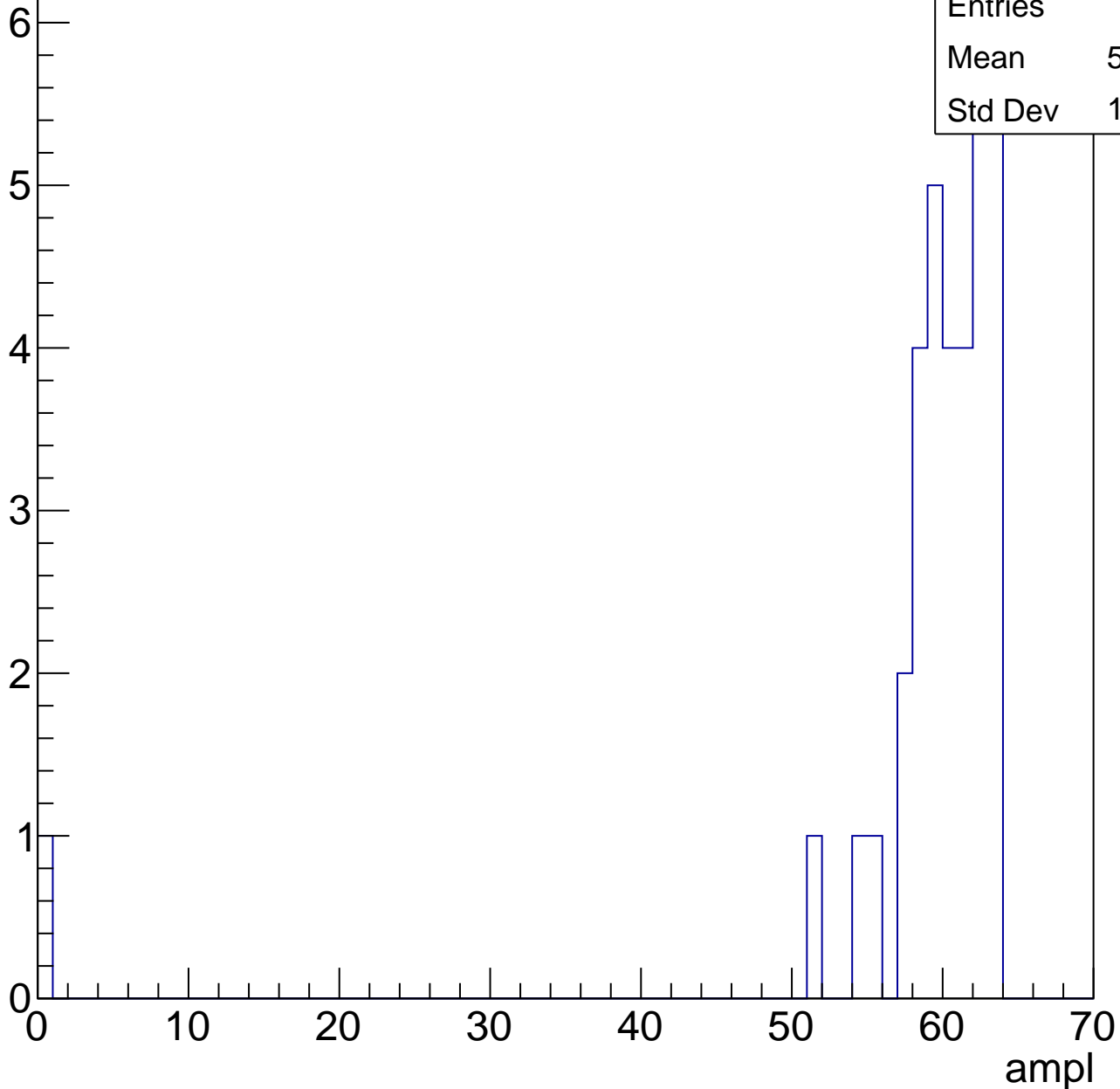


B1L101S, U15-ch85, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

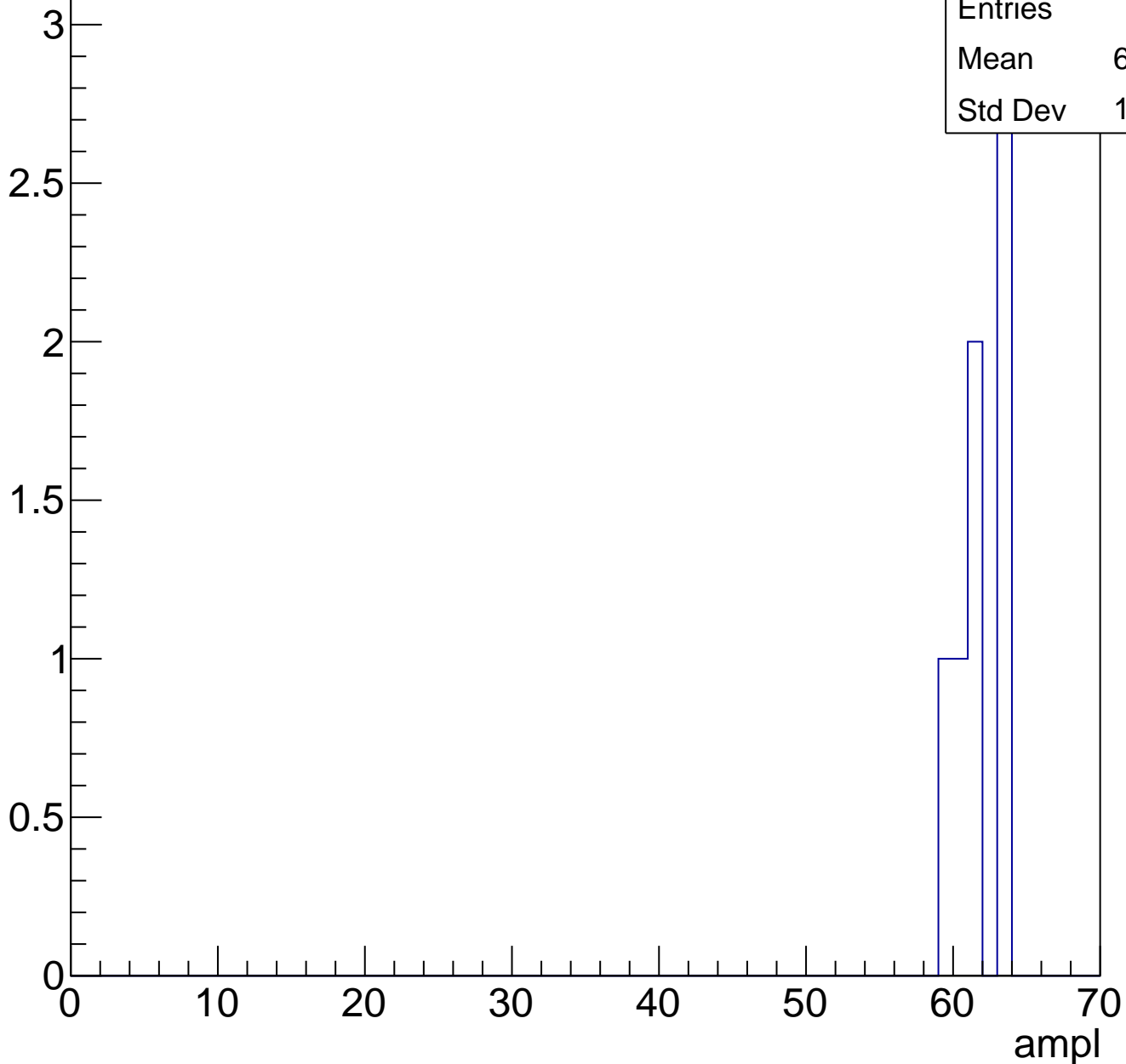
Entries	35
Mean	58.14
Std Dev	10.34



B1L101S, U15-ch85, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch85, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch86, adc0

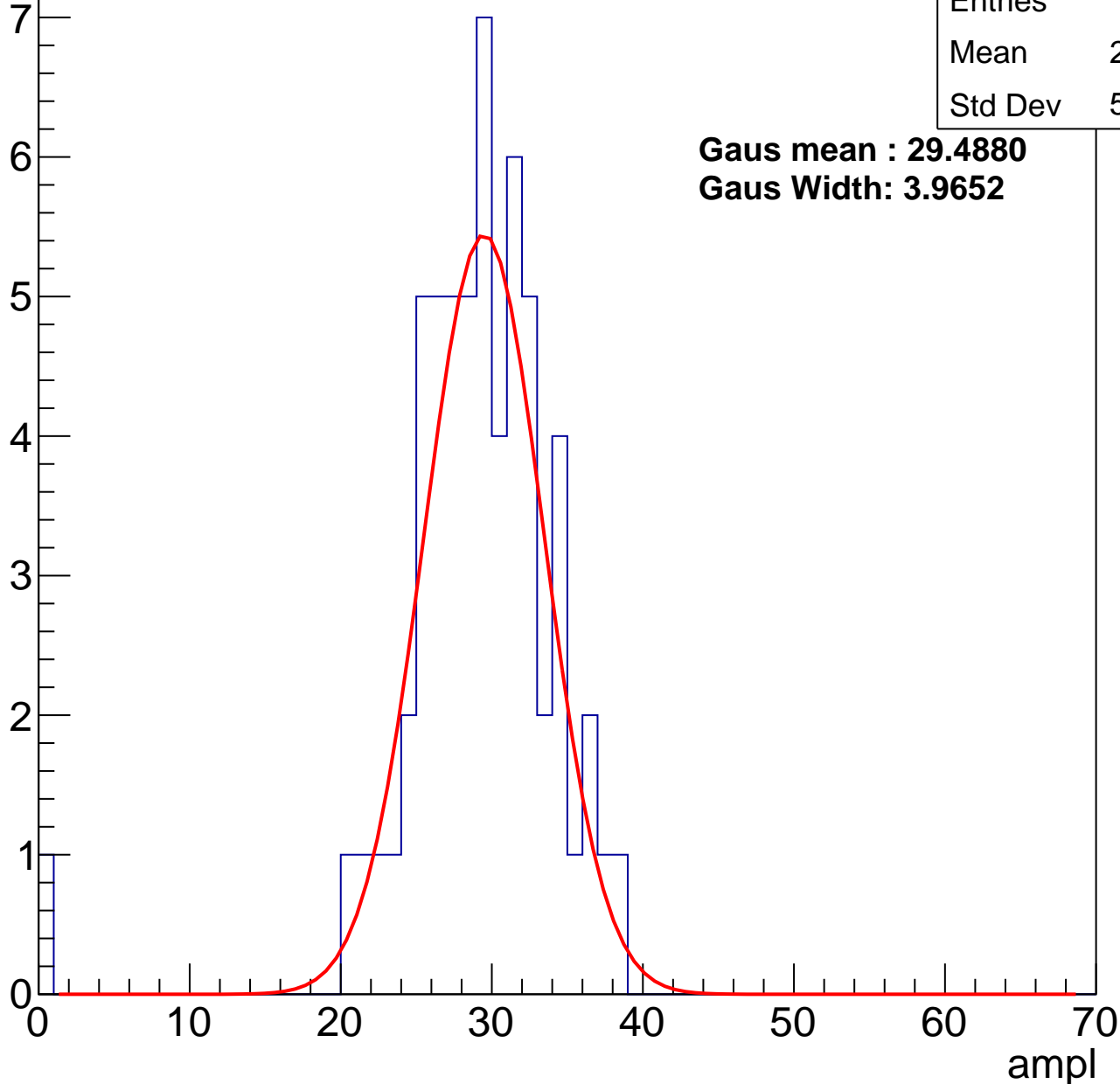
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.62
Std Dev	5.395

Gaus mean : 29.4880

Gaus Width: 3.9652



B1L101S, U15-ch86, adc1

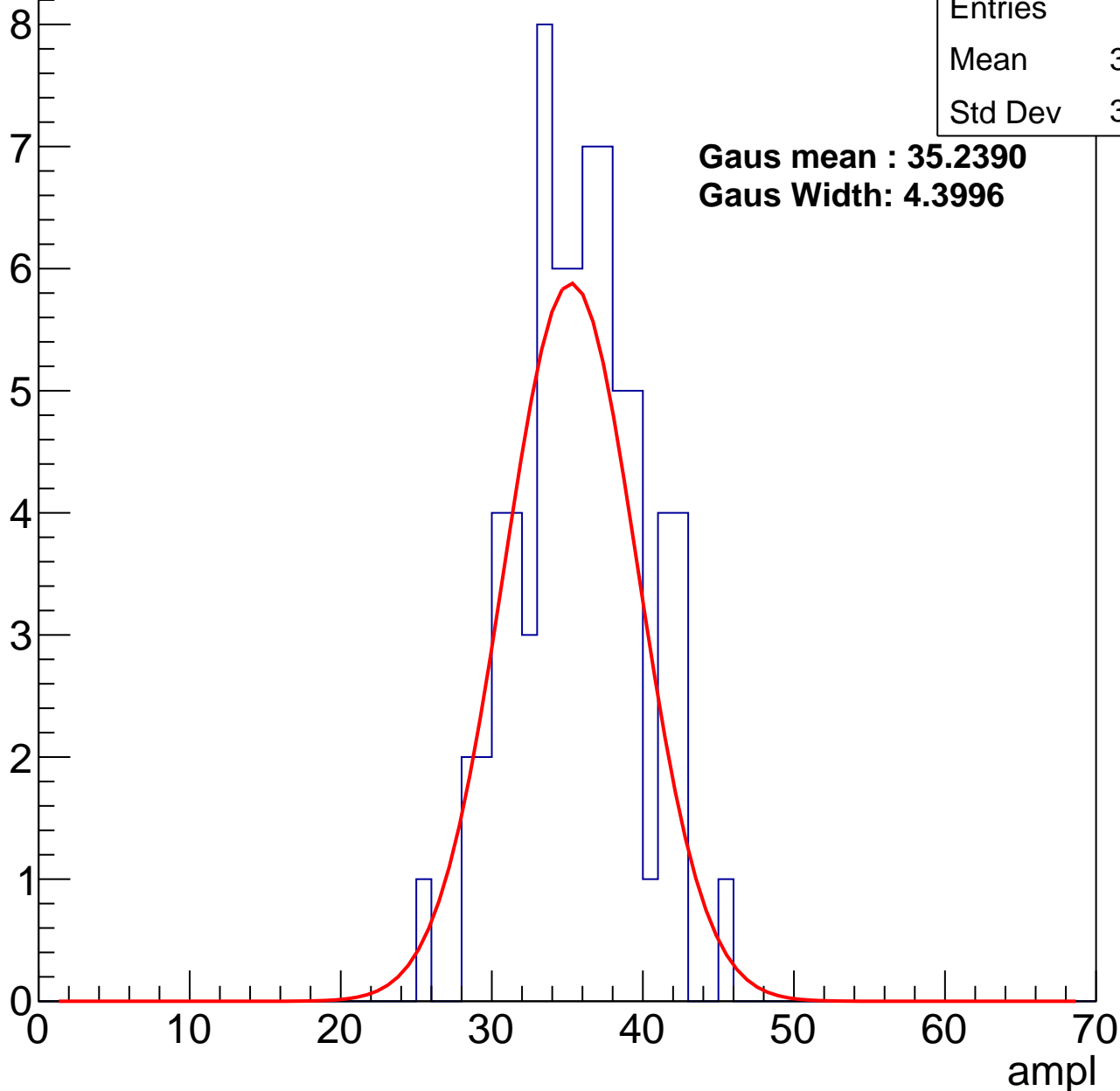
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	35.29
Std Dev	3.997

Gaus mean : 35.2390

Gaus Width: 4.3996



B1L101S, U15-ch86, adc2

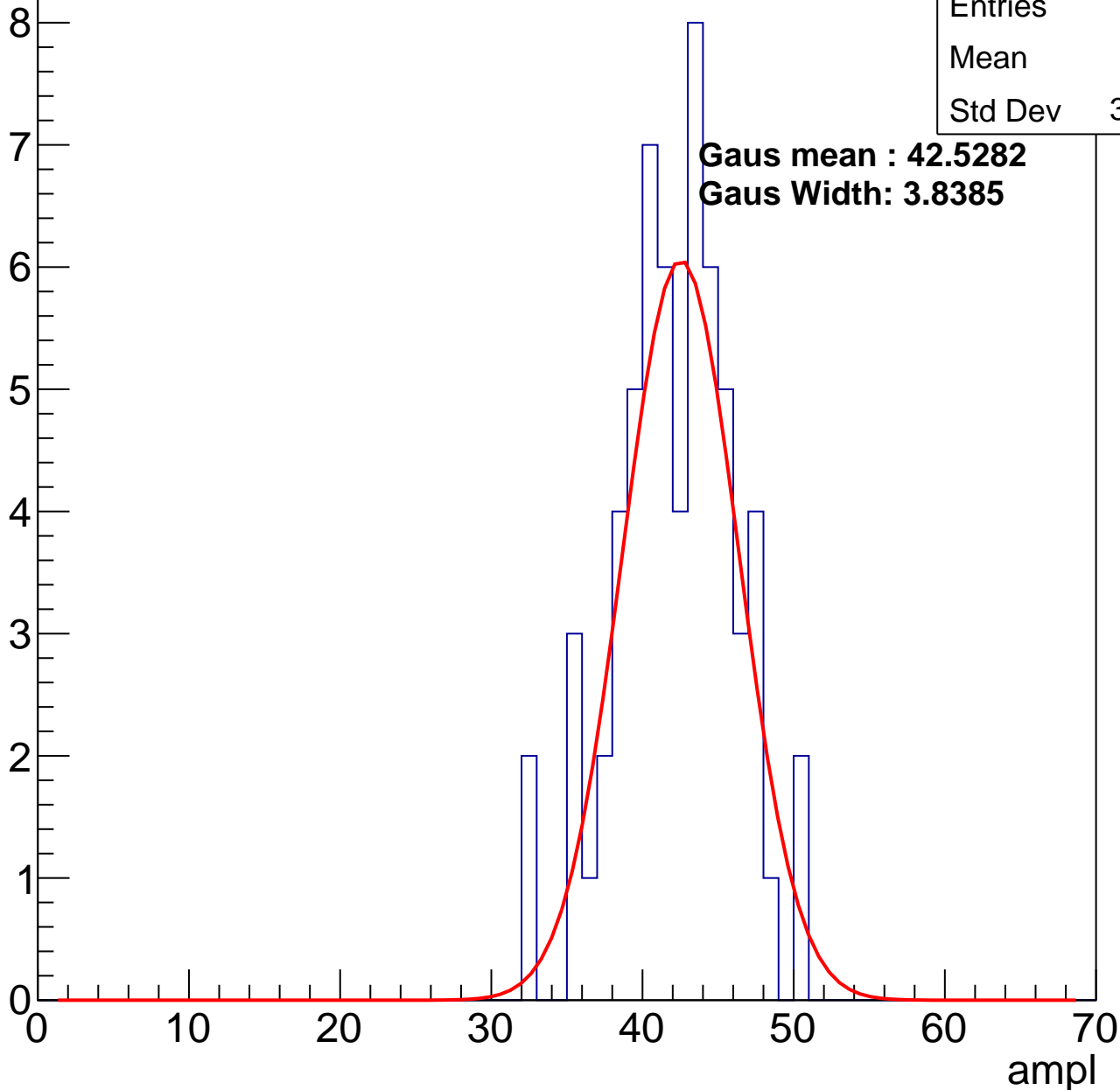
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	41.7
Std Dev	3.902

Gaus mean : 42.5282

Gaus Width: 3.8385

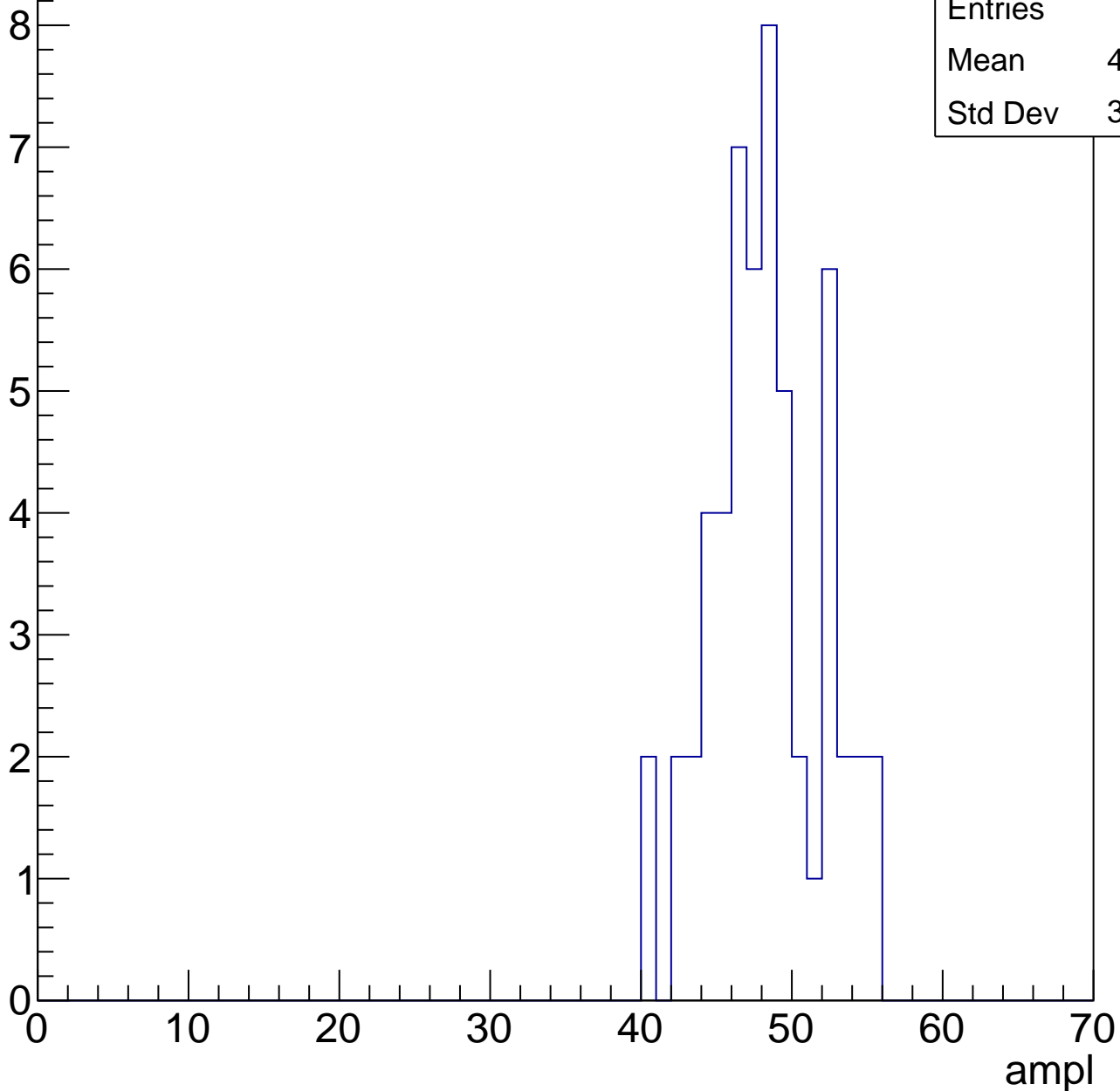


B1L101S, U15-ch86, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	47.75
Std Dev	3.614

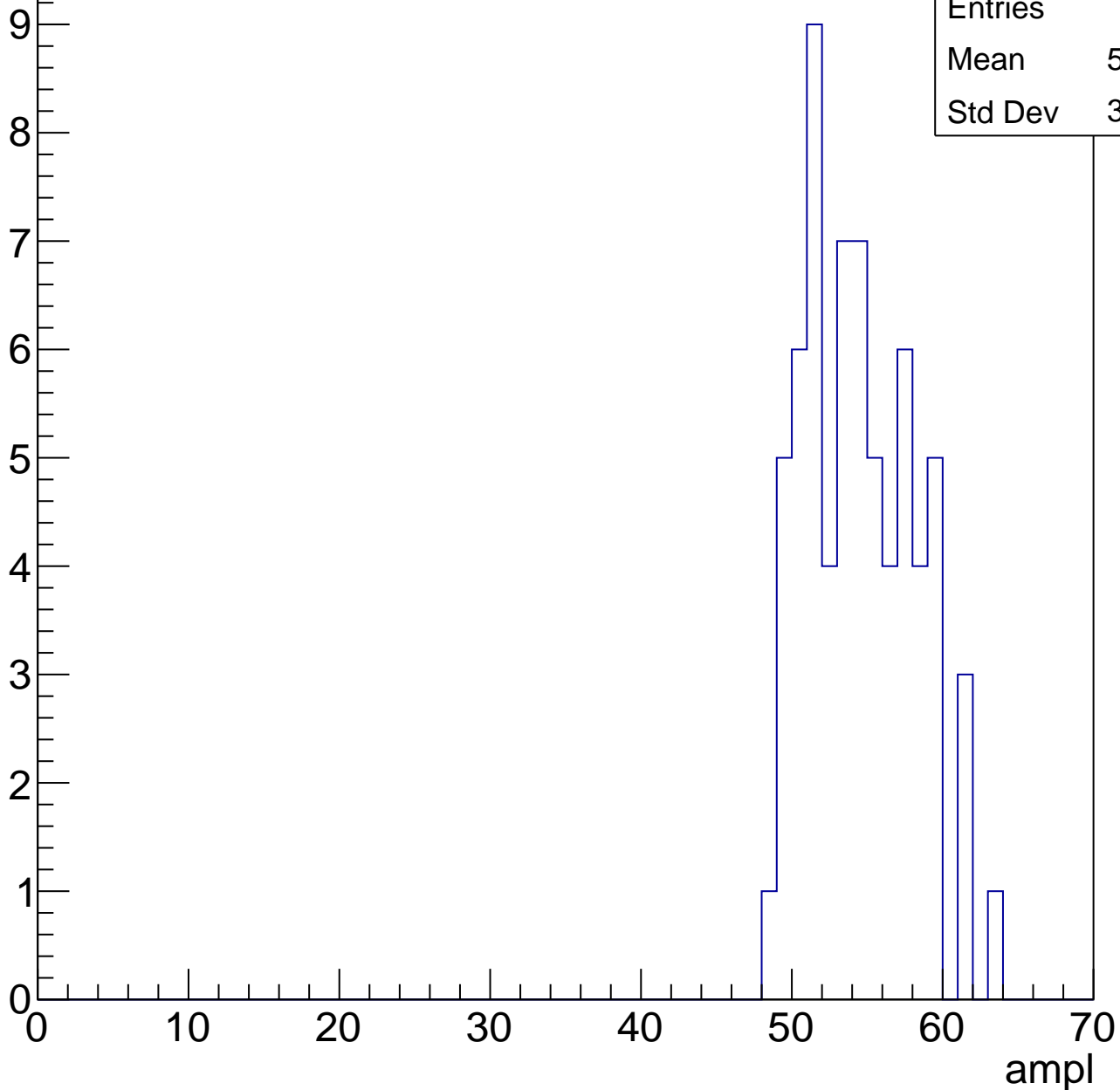


B1L101S, U15-ch86, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	54.07
Std Dev	3.567

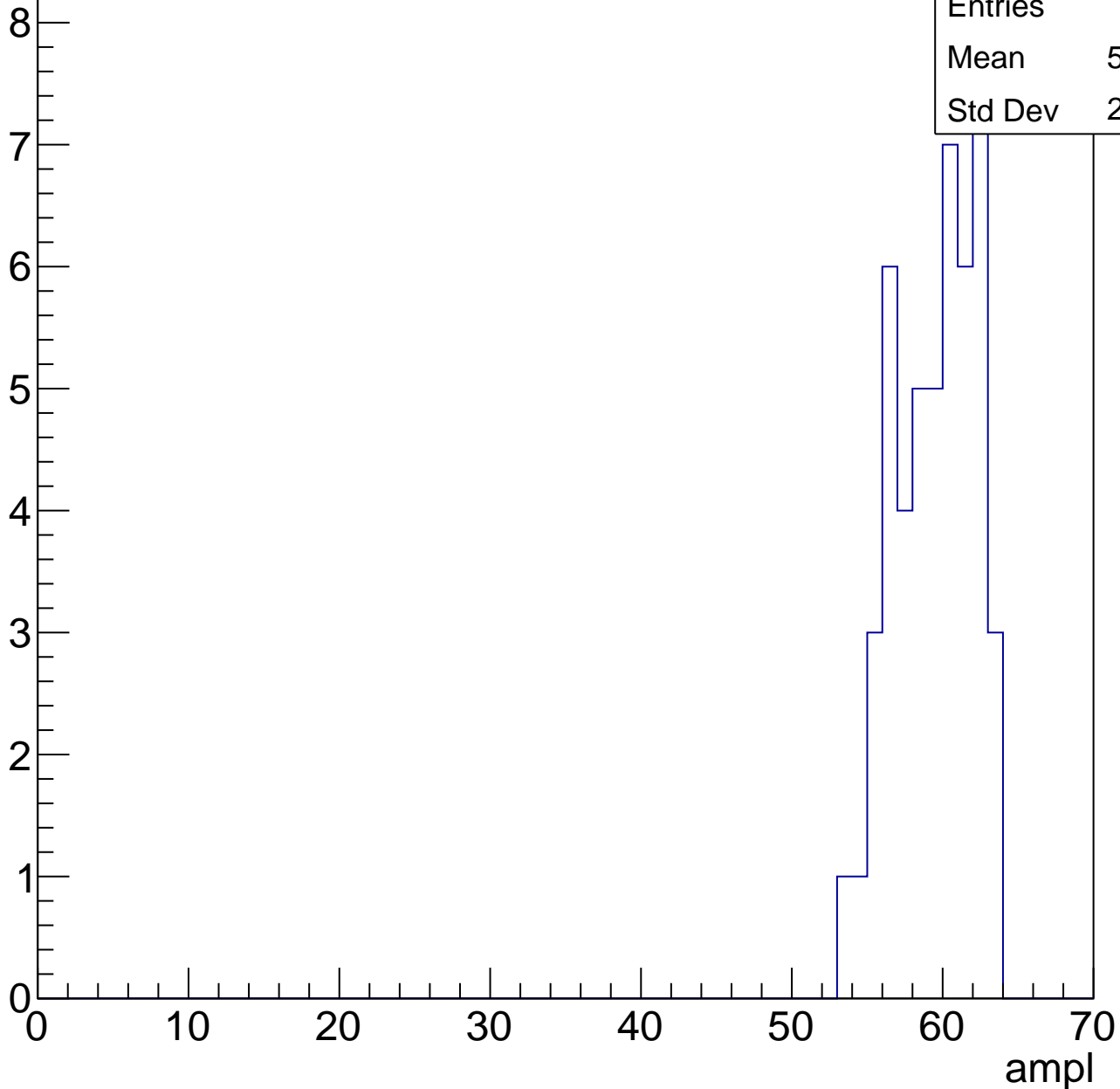


B1L101S, U15-ch86, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	59.02
Std Dev	2.615

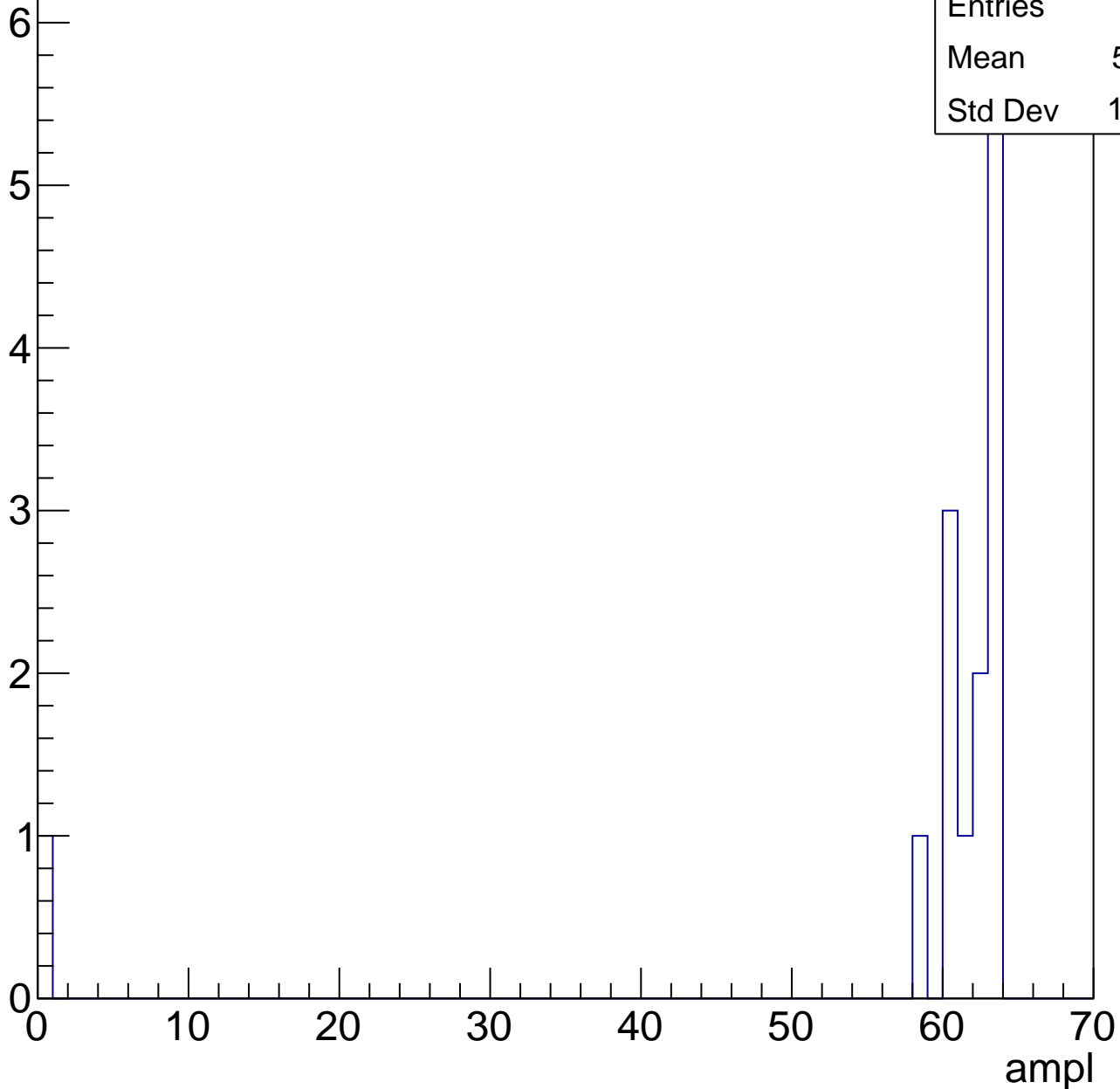


B1L101S, U15-ch86, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	14
Mean	57.21
Std Dev	15.94



B1L101S, U15-ch86, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch87, adc0

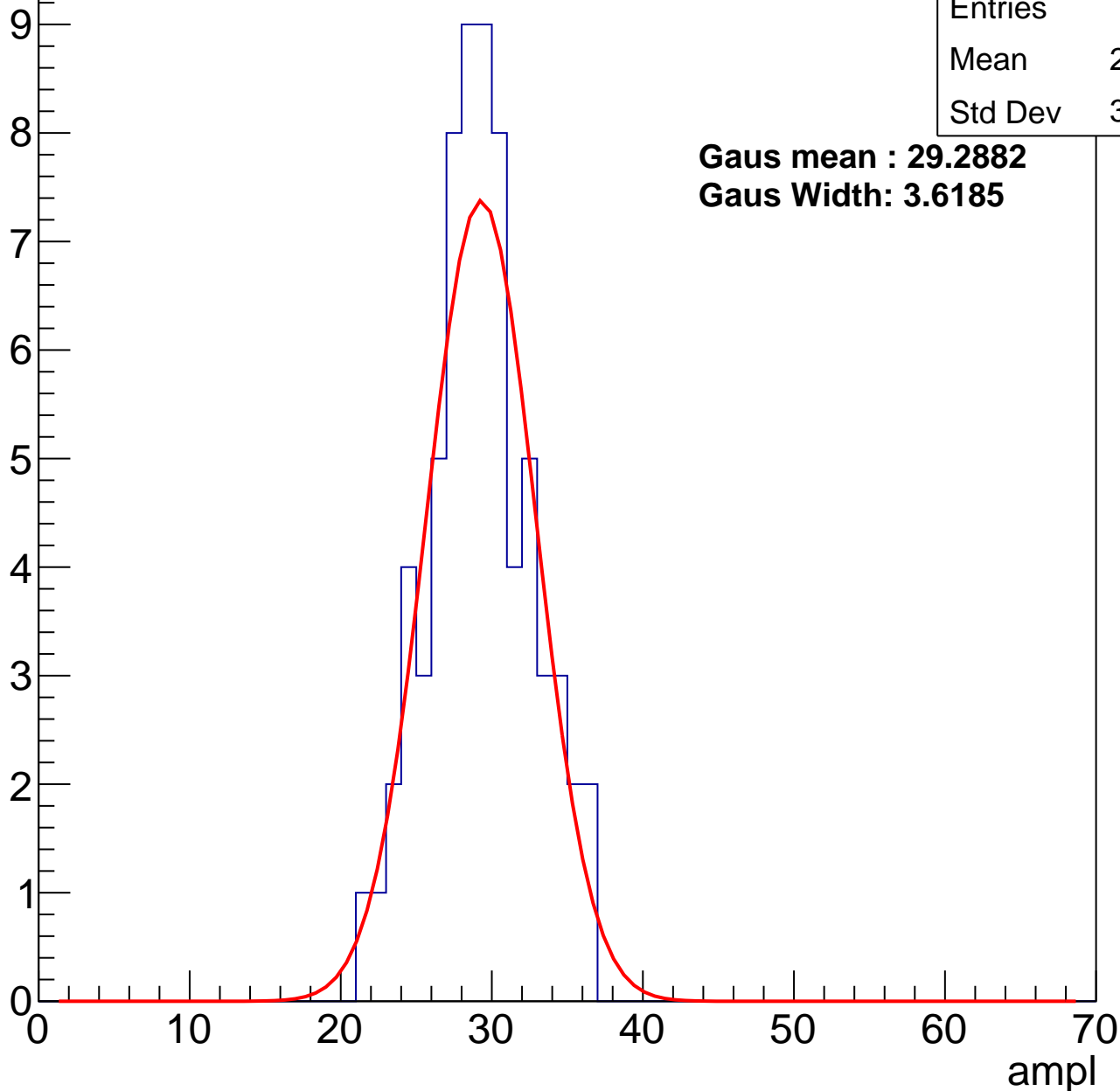
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.78
Std Dev	3.357

Gaus mean : 29.2882

Gaus Width: 3.6185



B1L101S, U15-ch87, adc1

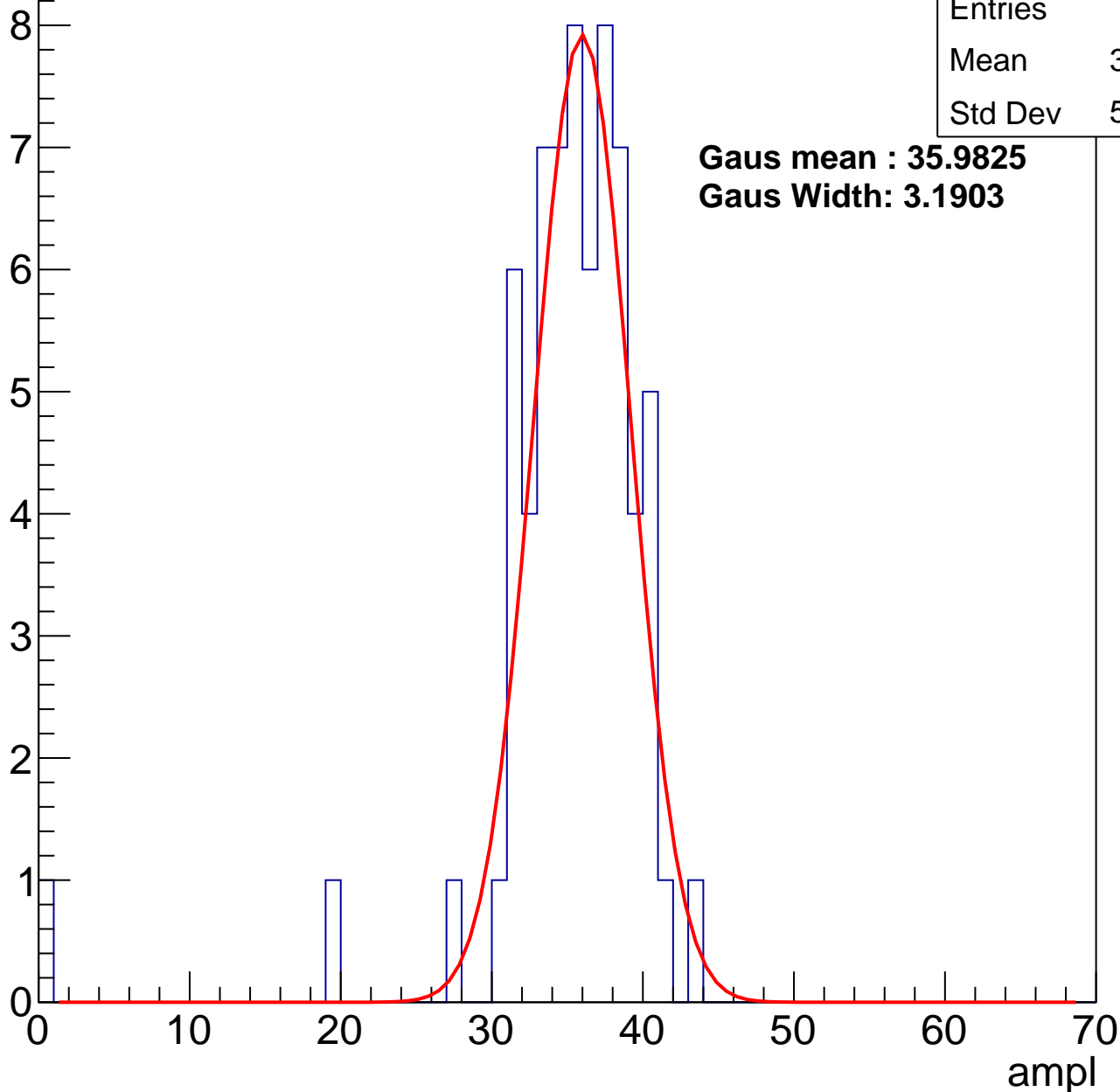
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	34.66
Std Dev	5.583

Gaus mean : 35.9825

Gaus Width: 3.1903



B1L101S, U15-ch87, adc2

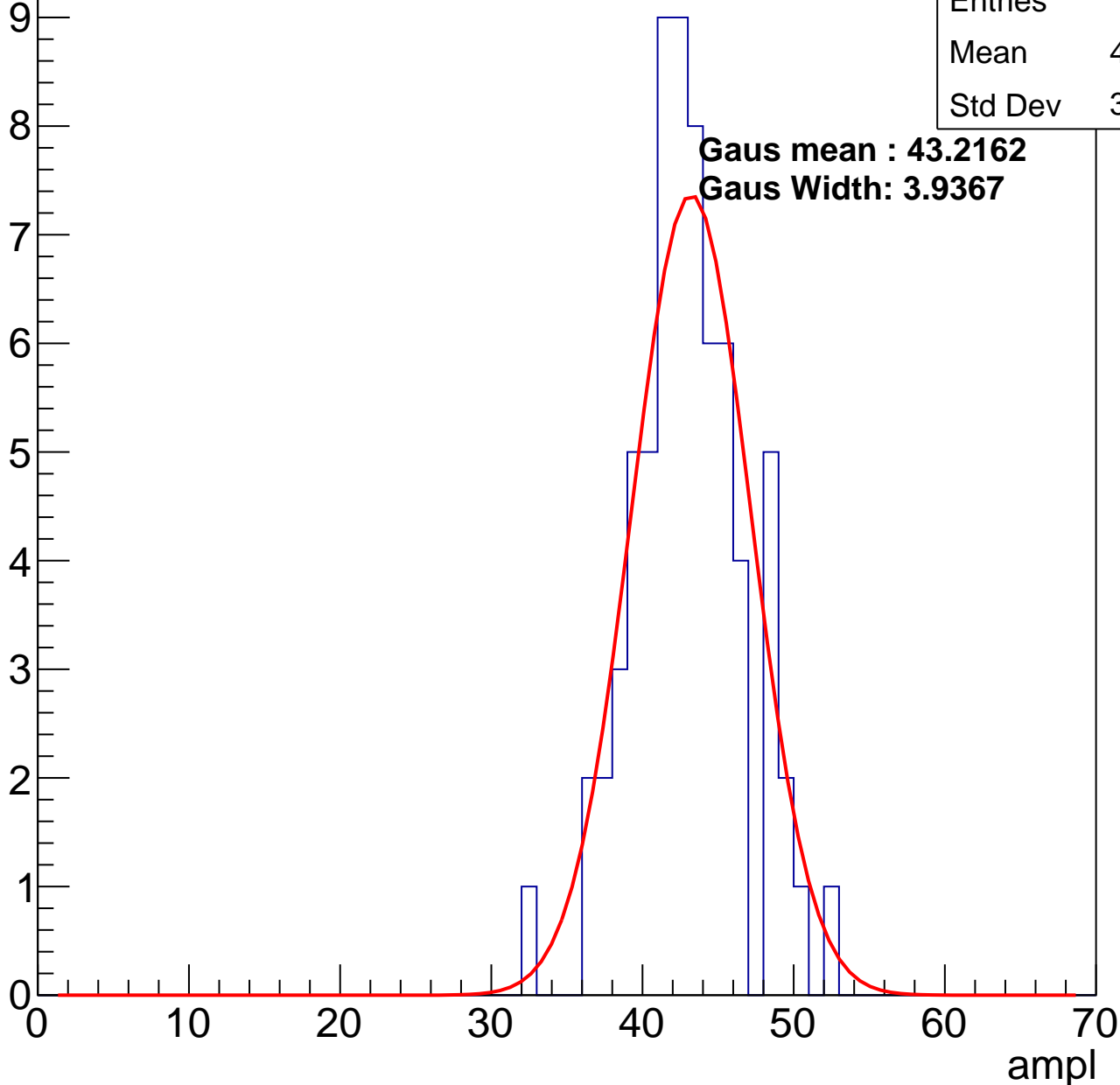
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.55
Std Dev	3.642

Gaus mean : 43.2162

Gaus Width: 3.9367

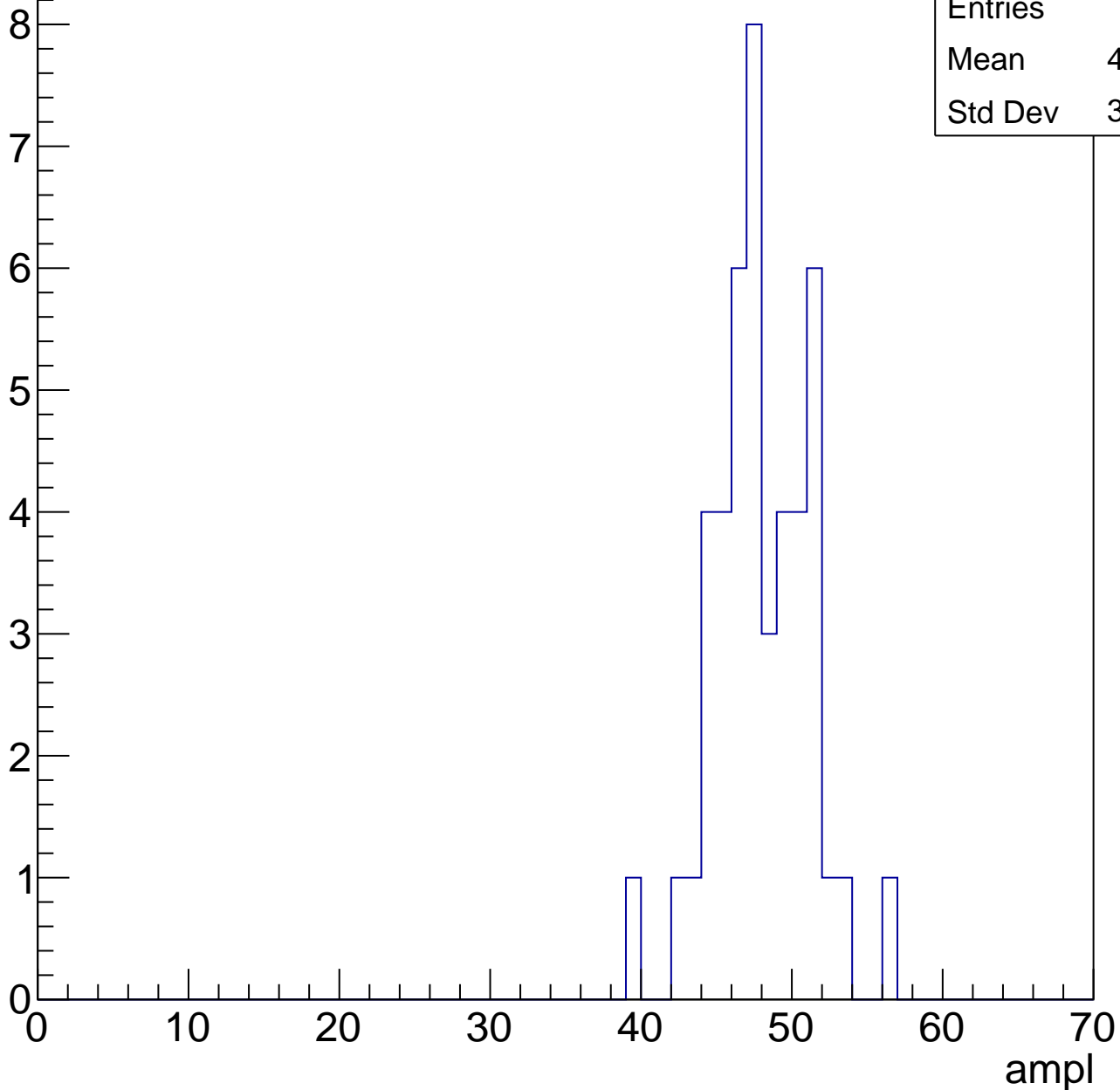


B1L101S, U15-ch87, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	45
Mean	47.53
Std Dev	3.138



B1L101S, U15-ch87, adc4

calib_packv5_042523_0143.root, FC#0, port D2

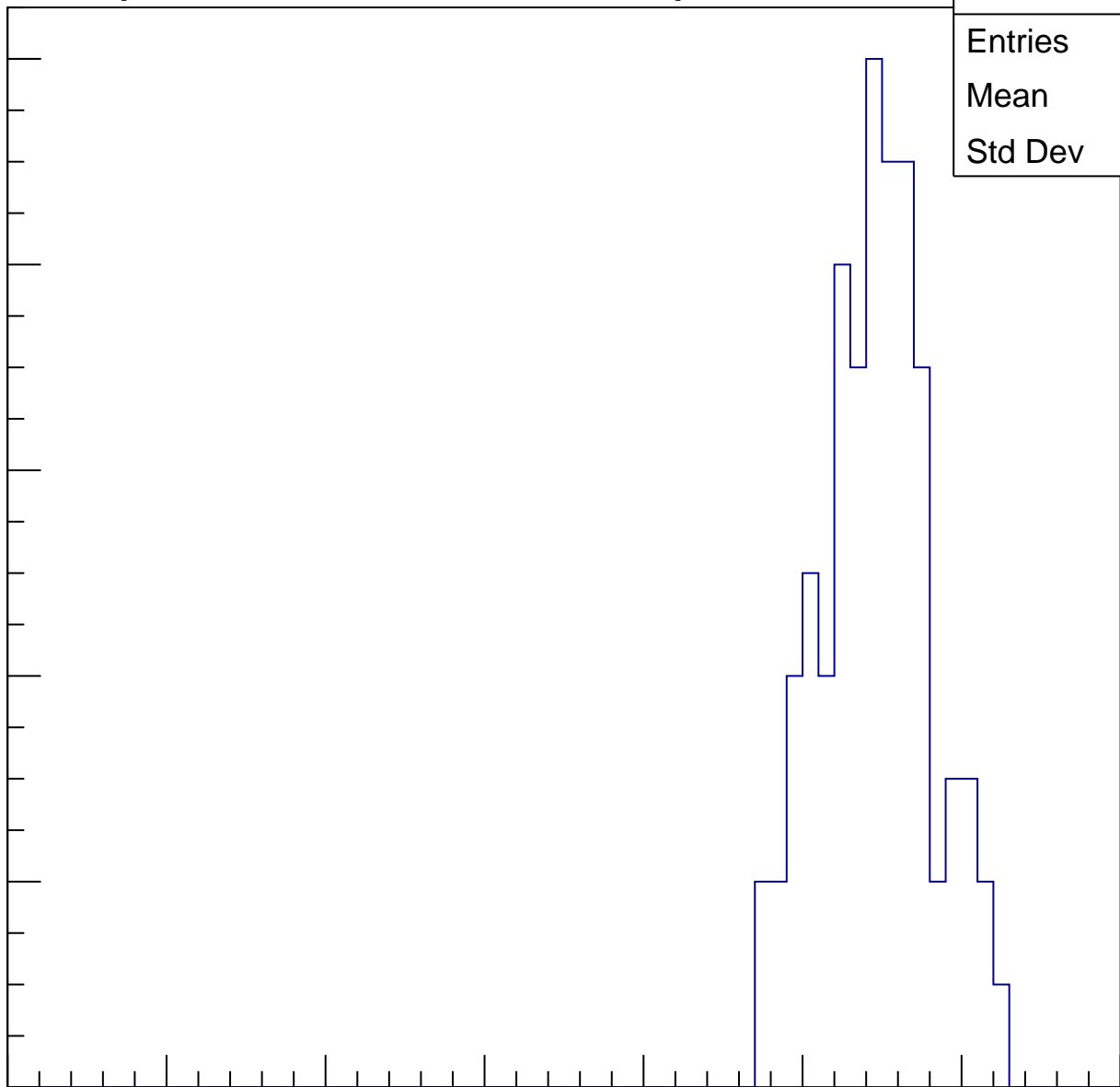
Entries	78
Mean	54.13
Std Dev	3.413

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

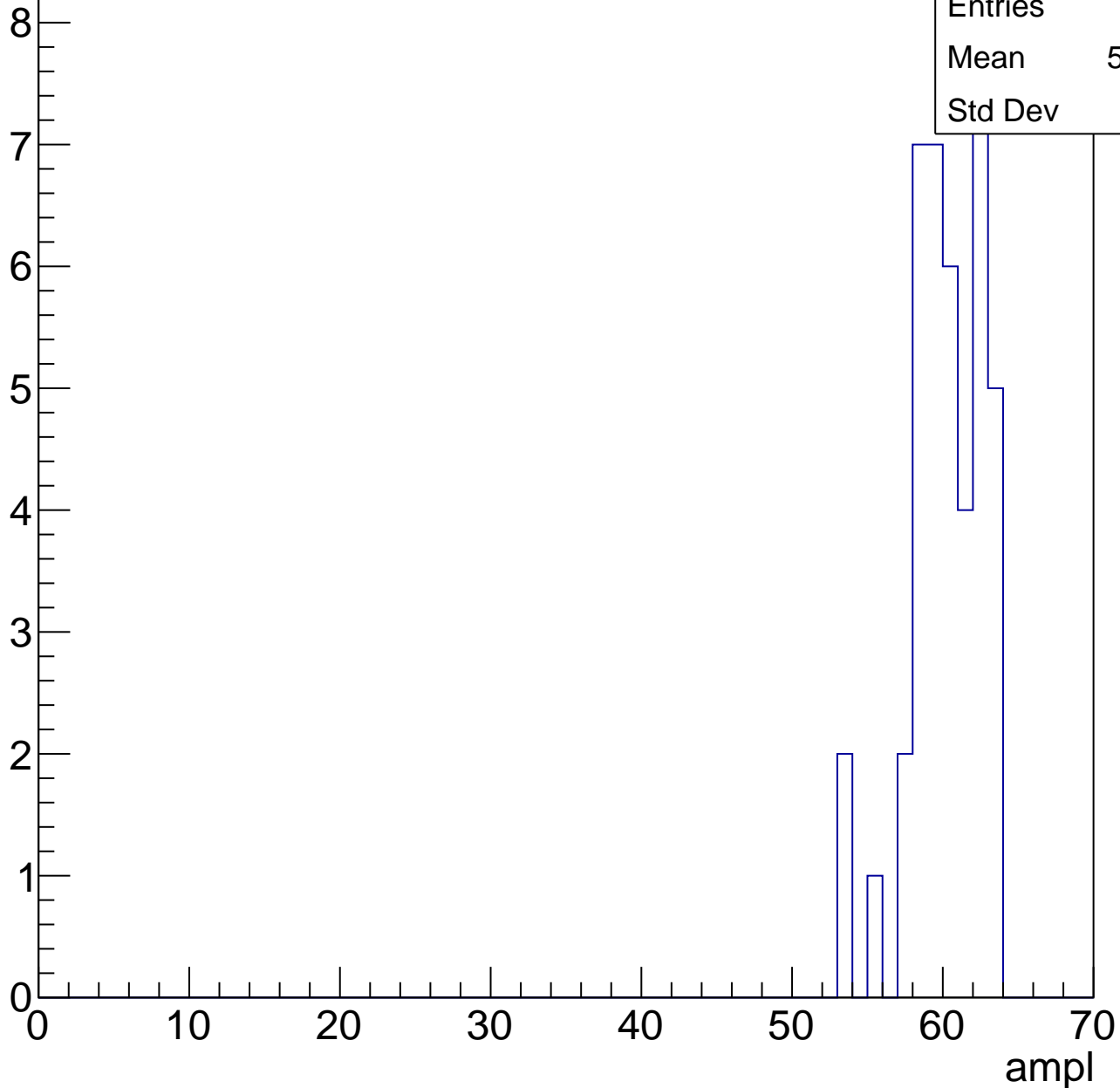


B1L101S, U15-ch87, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

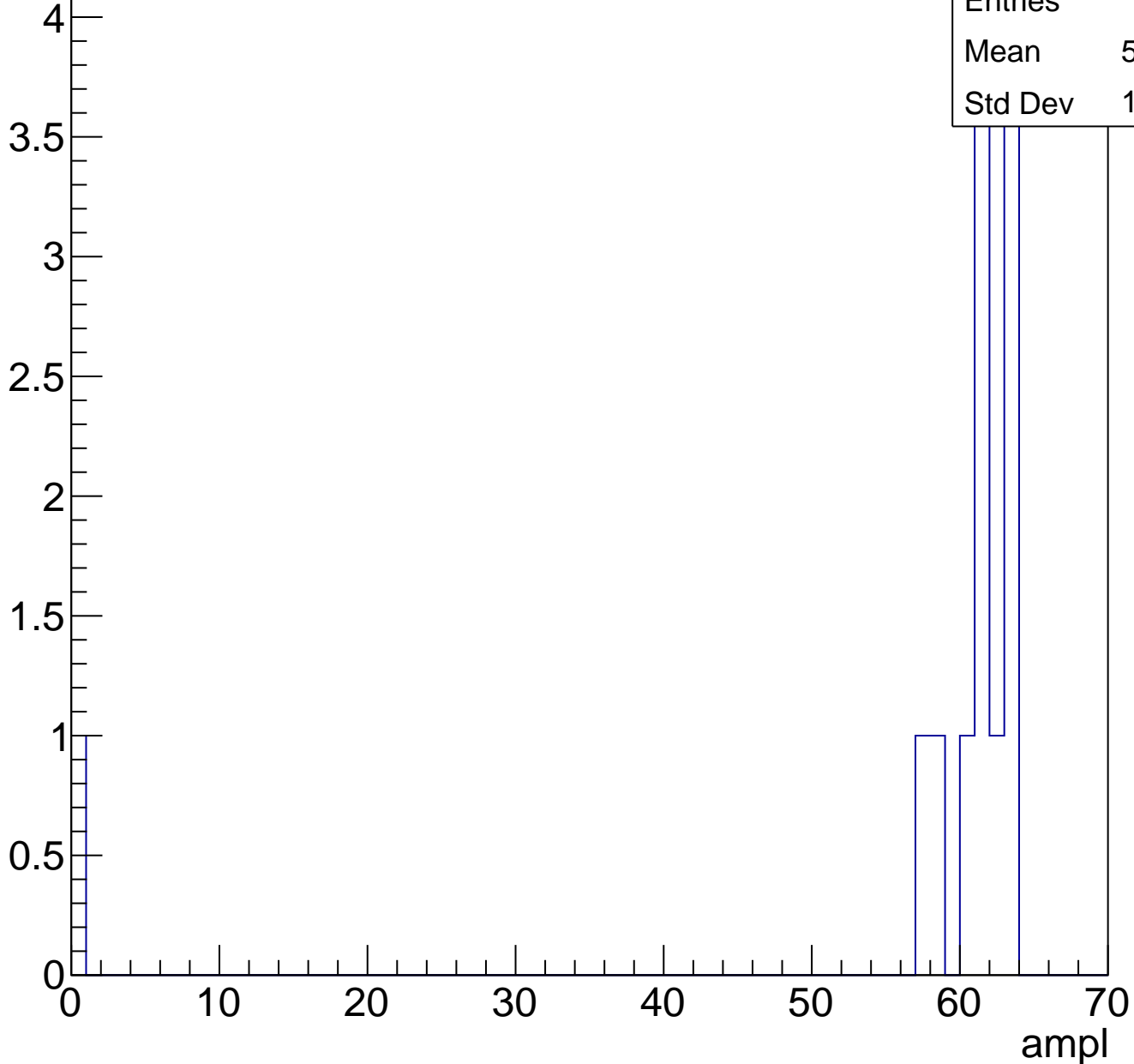
Entries	42
Mean	59.74
Std Dev	2.46



B1L101S, U15-ch87, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	13
Mean	56.38
Std Dev	16.38

B1L101S, U15-ch87, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L101S, U15-ch88, adc0

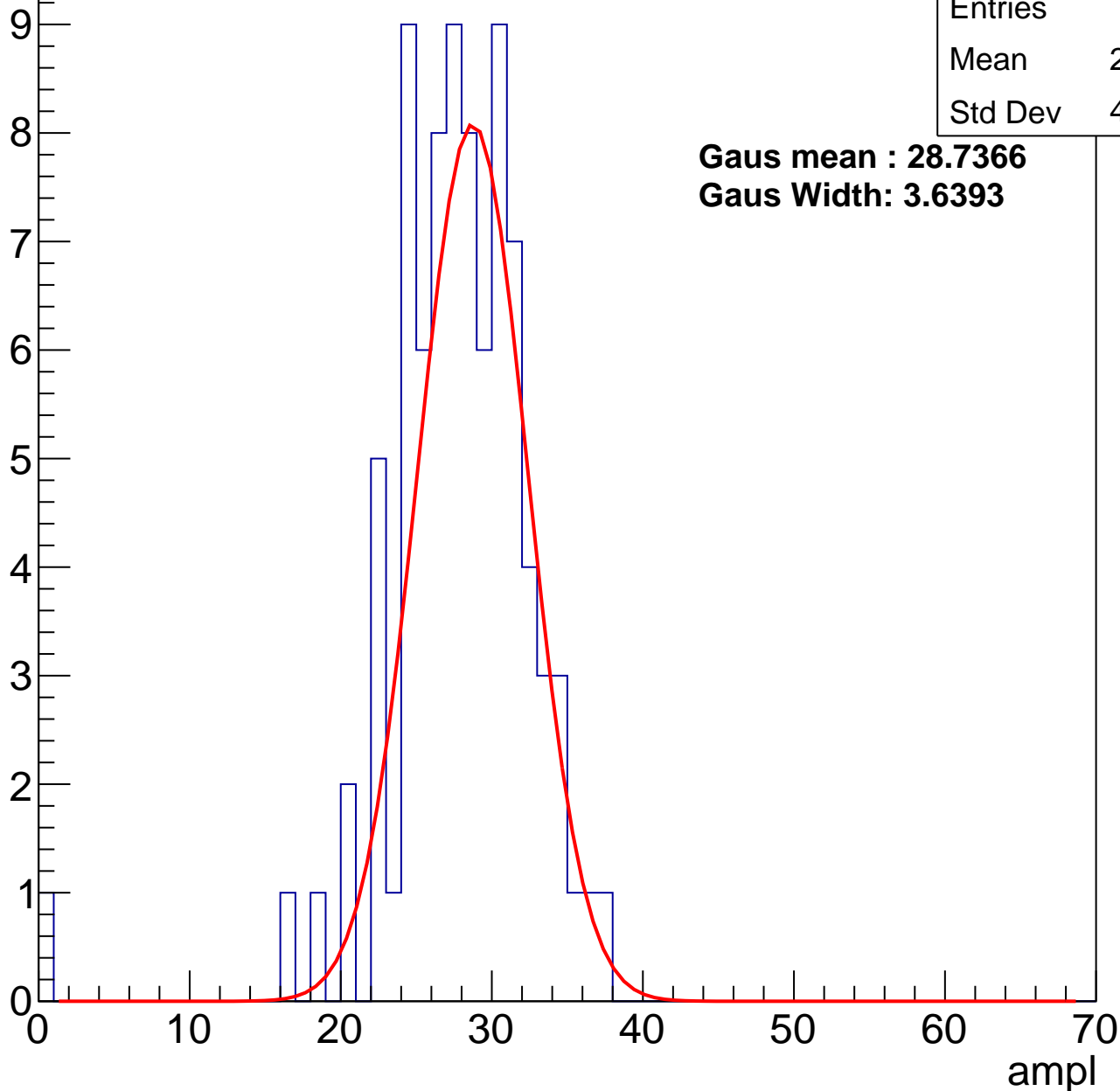
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	86
Mean	27.28
Std Dev	4.943

Gaus mean : 28.7366

Gaus Width: 3.6393



B1L101S, U15-ch88, adc1

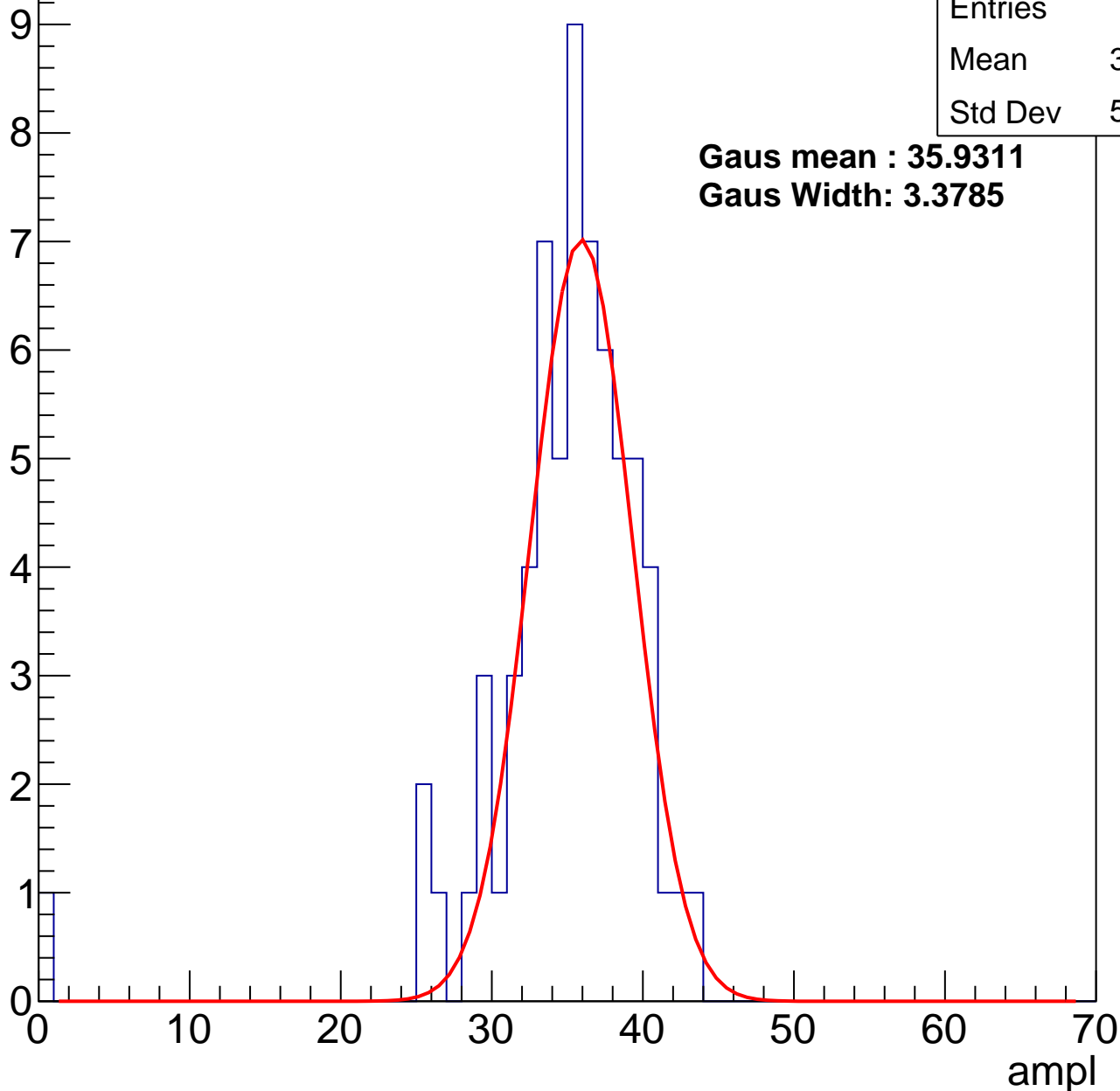
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	34.37
Std Dev	5.709

Gaus mean : 35.9311

Gaus Width: 3.3785



B1L101S, U15-ch88, adc2

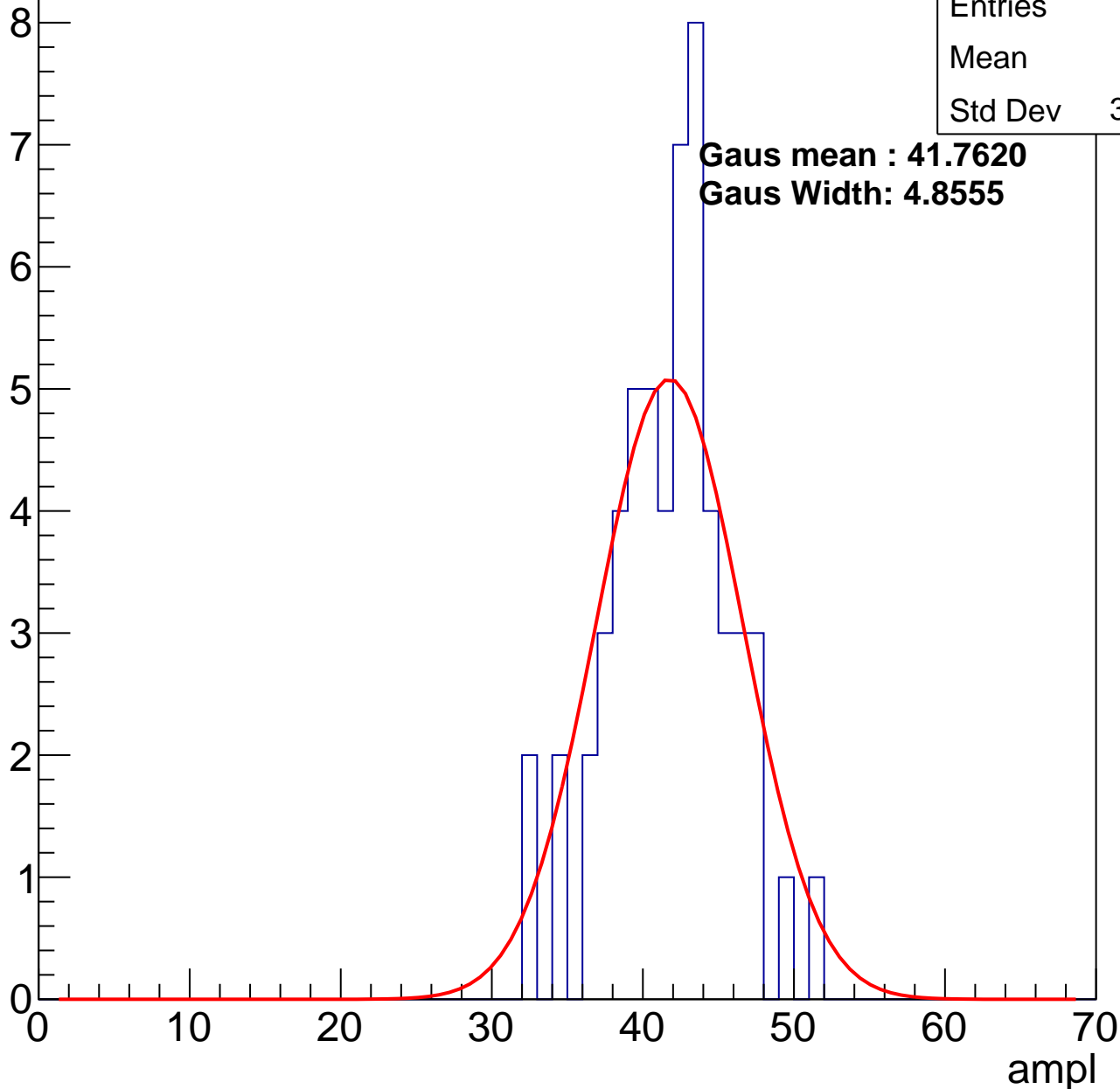
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	41.3
Std Dev	3.938

Gaus mean : 41.7620

Gaus Width: 4.8555

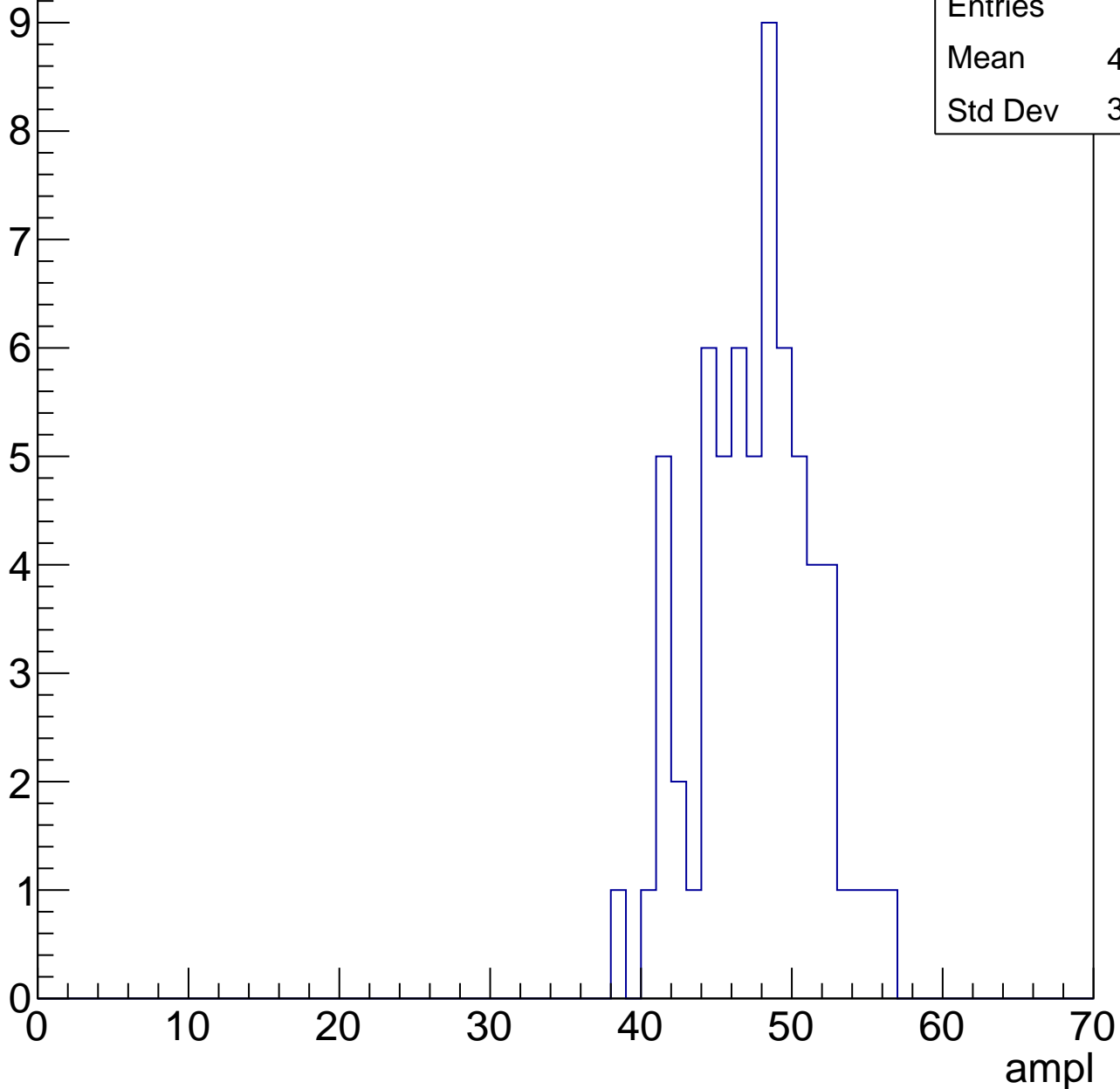


B1L101S, U15-ch88, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	47.12
Std Dev	3.806

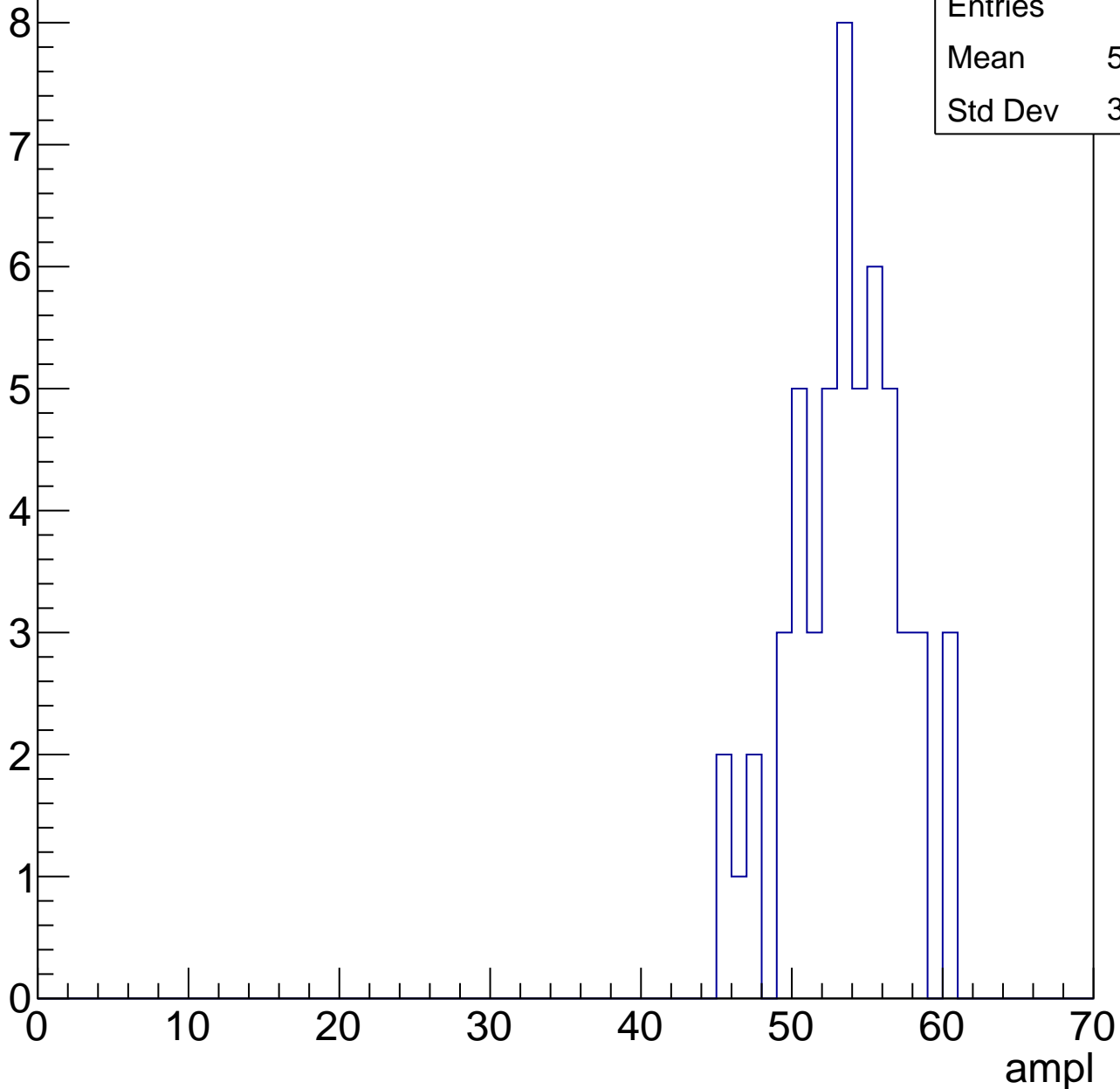


B1L101S, U15-ch88, adc4

calib_packv5_042523_0143.root, FC#0, port D2

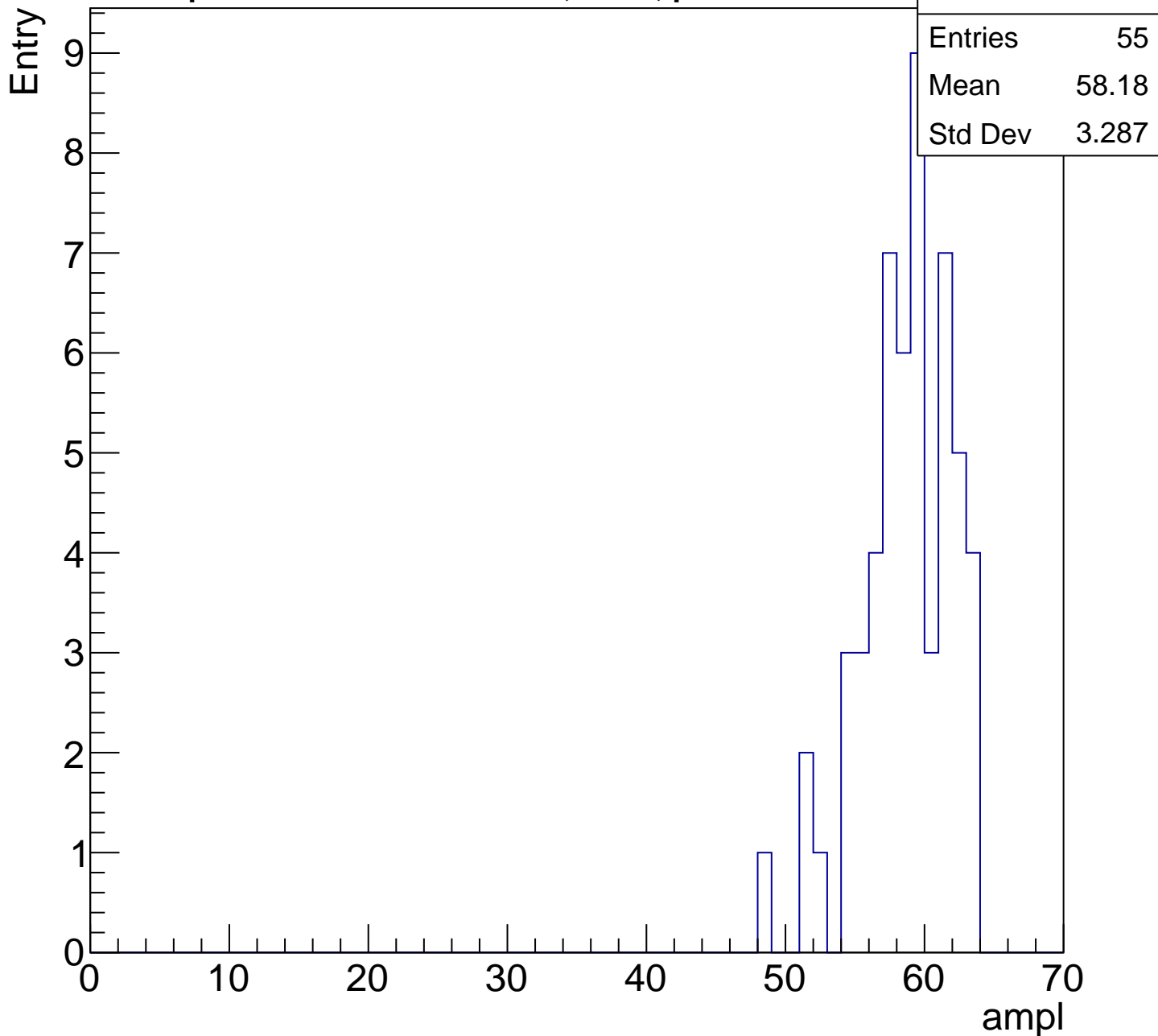
Entry

Entries	54
Mean	53.13
Std Dev	3.606



B1L101S, U15-ch88, adc5

calib_packv5_042523_0143.root, FC#0, port D2

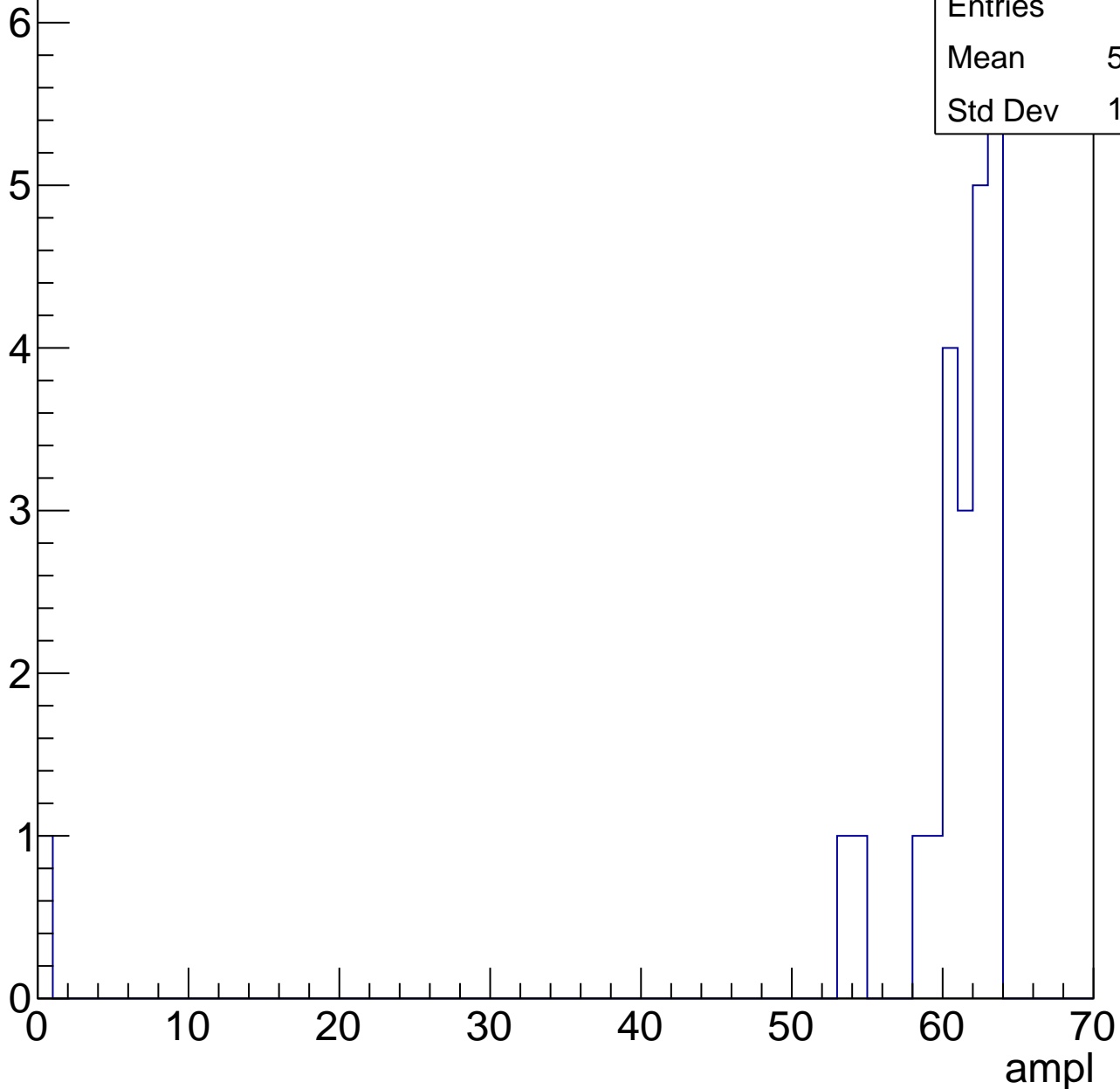


B1L101S, U15-ch88, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	23
Mean	58.04
Std Dev	12.65



B1L101S, U15-ch88, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch89, adc0

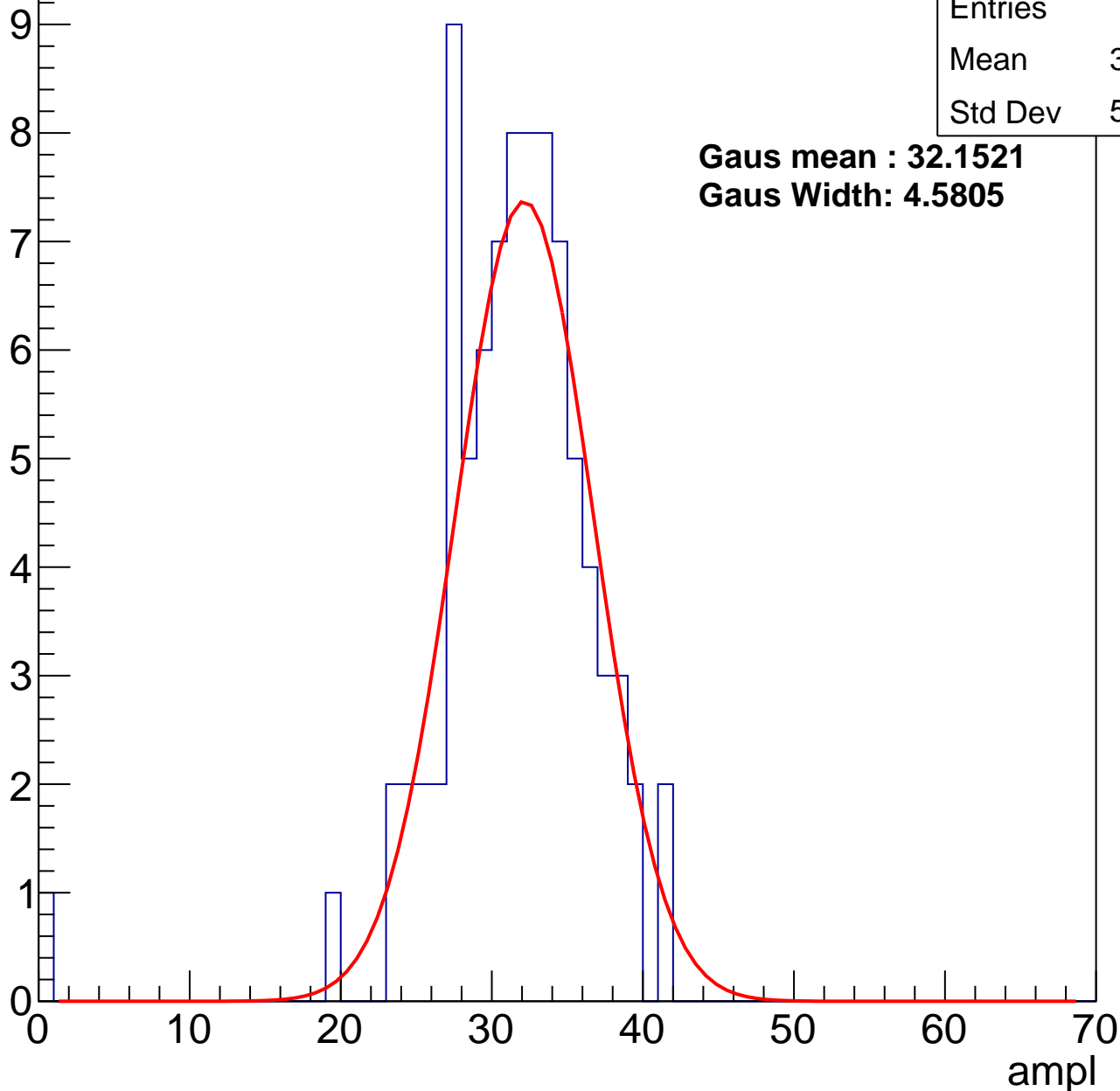
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	87
Mean	30.94
Std Dev	5.402

Gaus mean : 32.1521

Gaus Width: 4.5805



B1L101S, U15-ch89, adc1

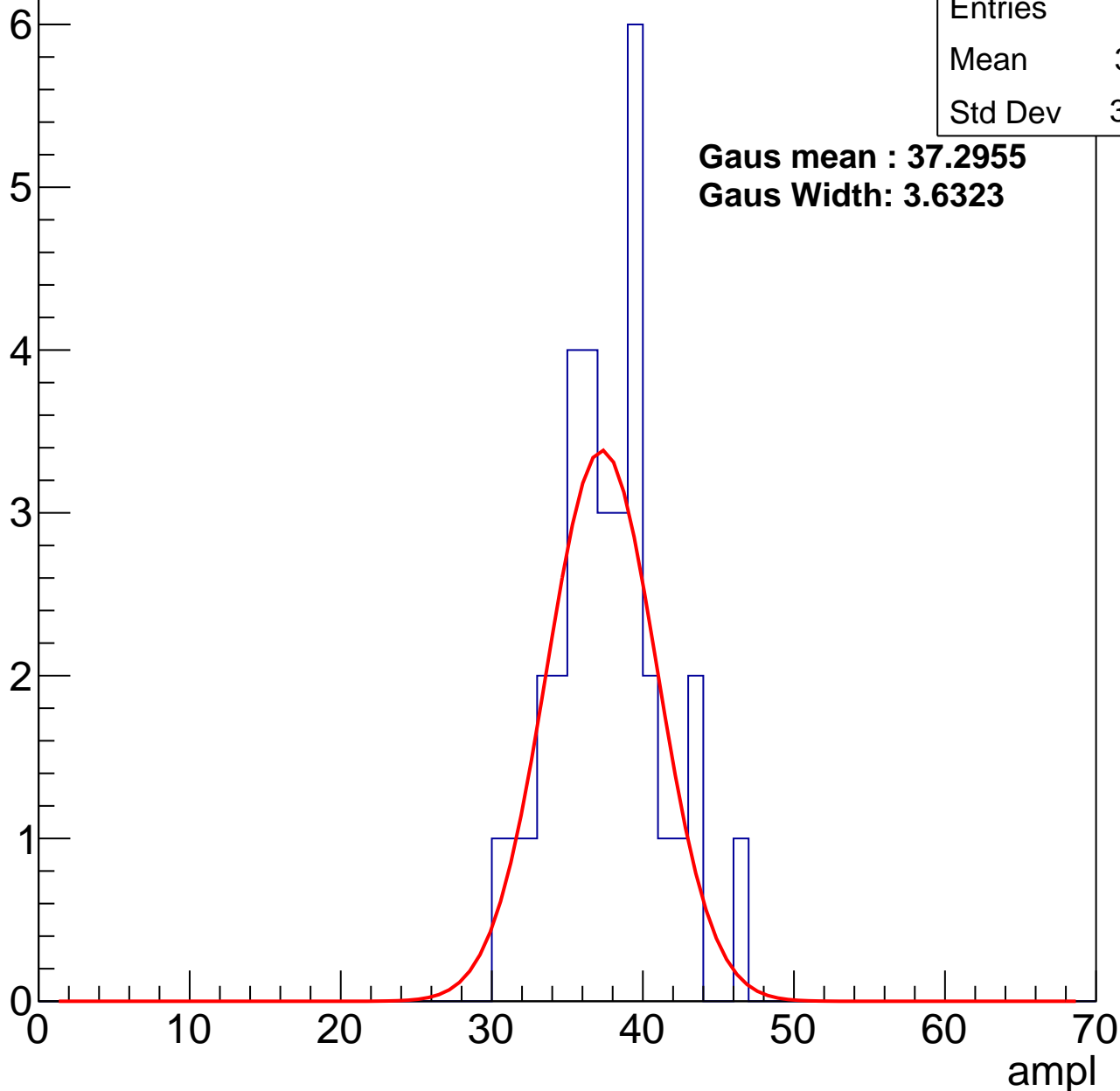
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	34
Mean	37.21
Std Dev	3.504

Gaus mean : 37.2955

Gaus Width: 3.6323



B1L101S, U15-ch89, adc2

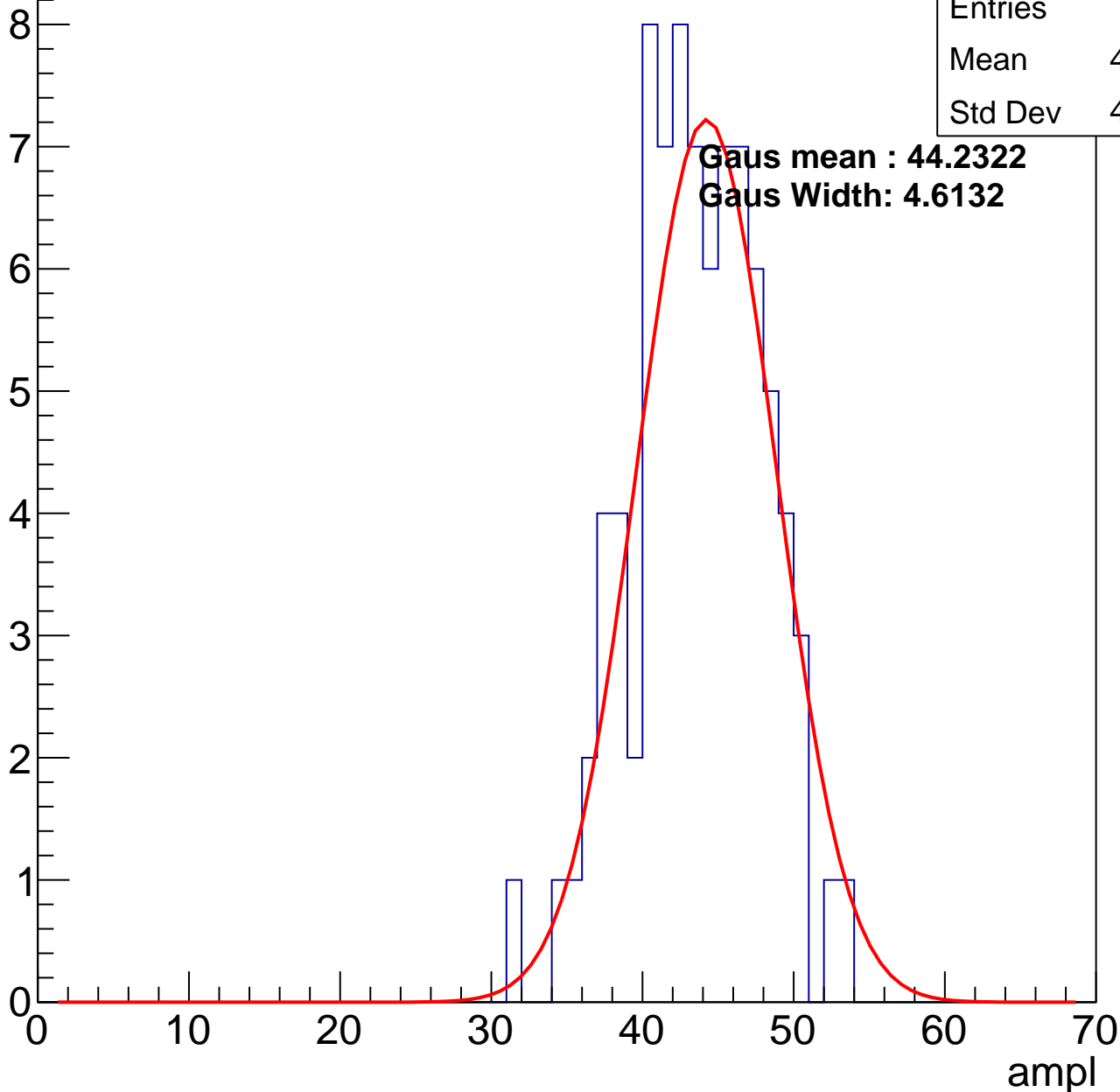
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	43.15
Std Dev	4.283

Gaus mean : 44.2322

Gaus Width: 4.6132

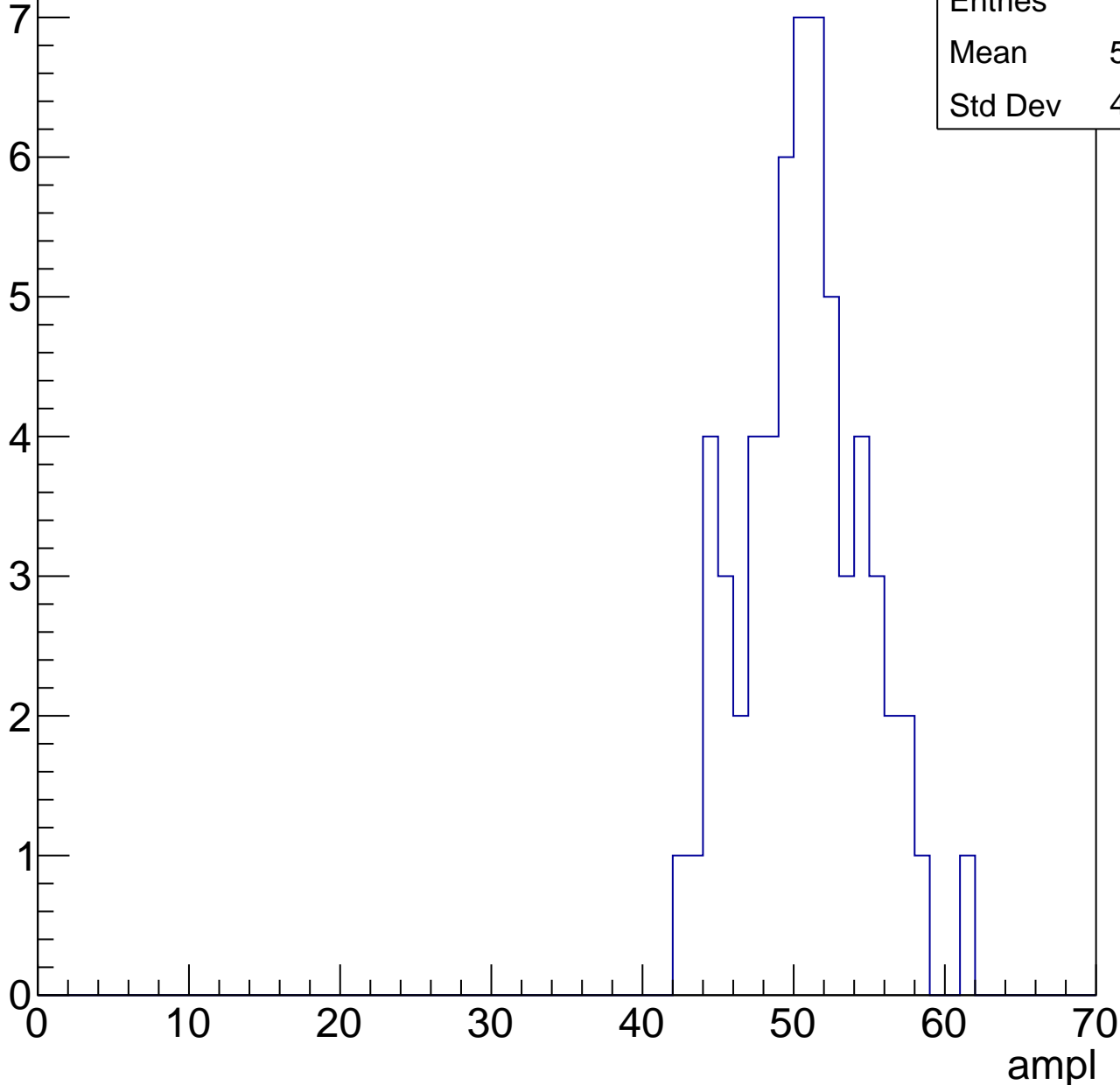


B1L101S, U15-ch89, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	50.23
Std Dev	4.014

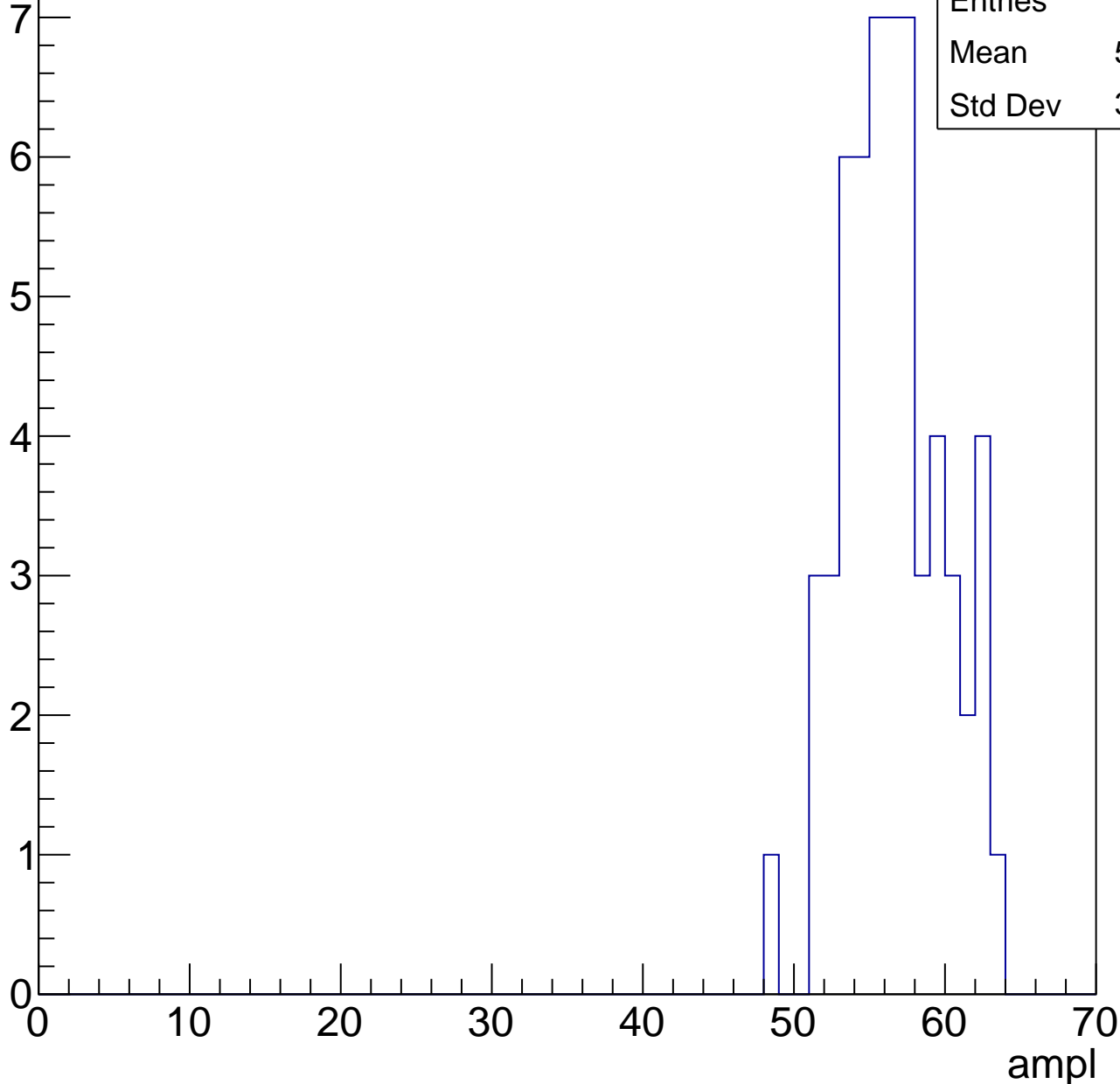


B1L101S, U15-ch89, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	56.11
Std Dev	3.291



B1L101S, U15-ch89, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

7

6

5

4

3

2

1

0

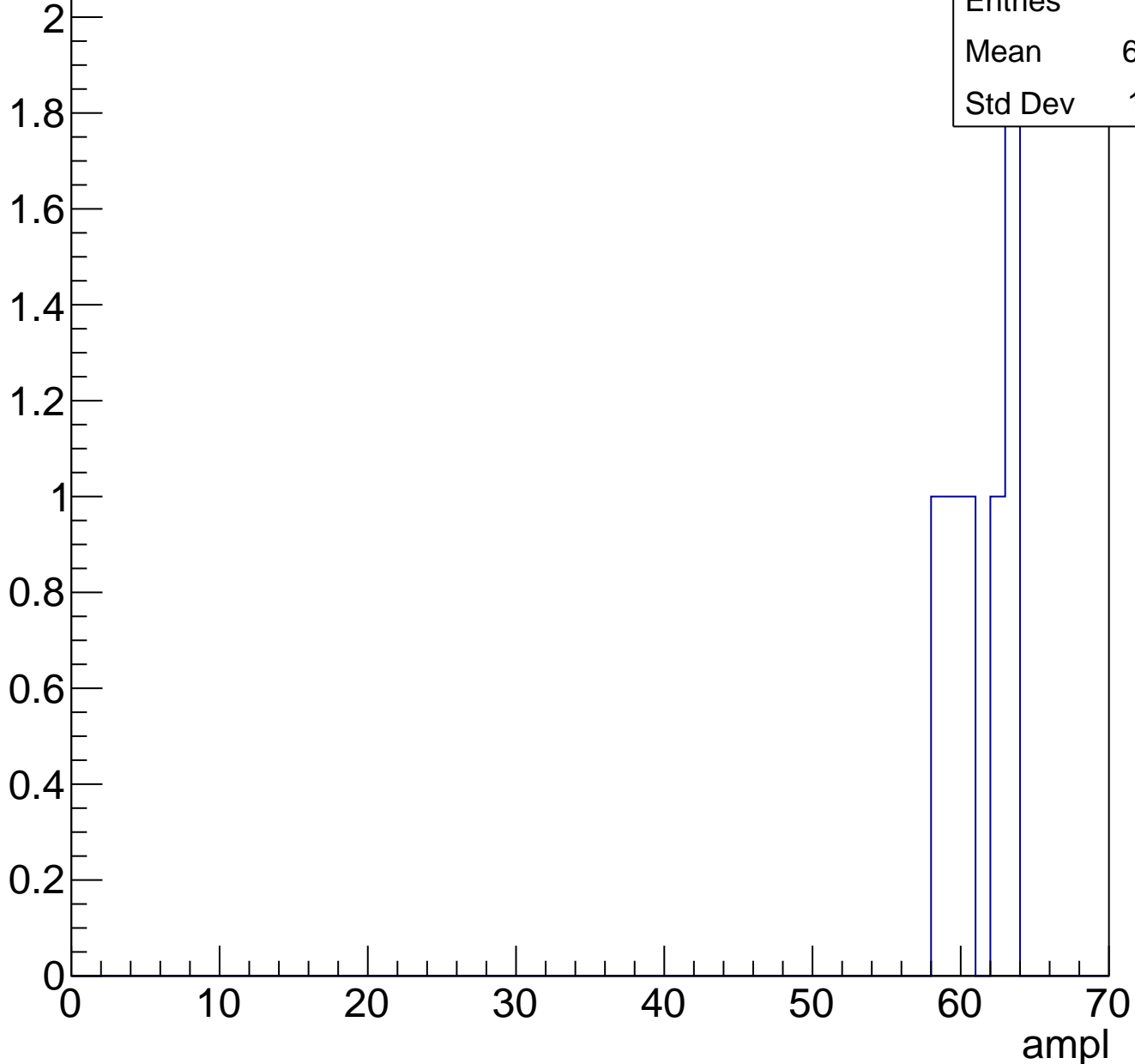
Entries	39
Mean	58.67
Std Dev	9.786

ampl

B1L101S, U15-ch89, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch89, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch90, adc0

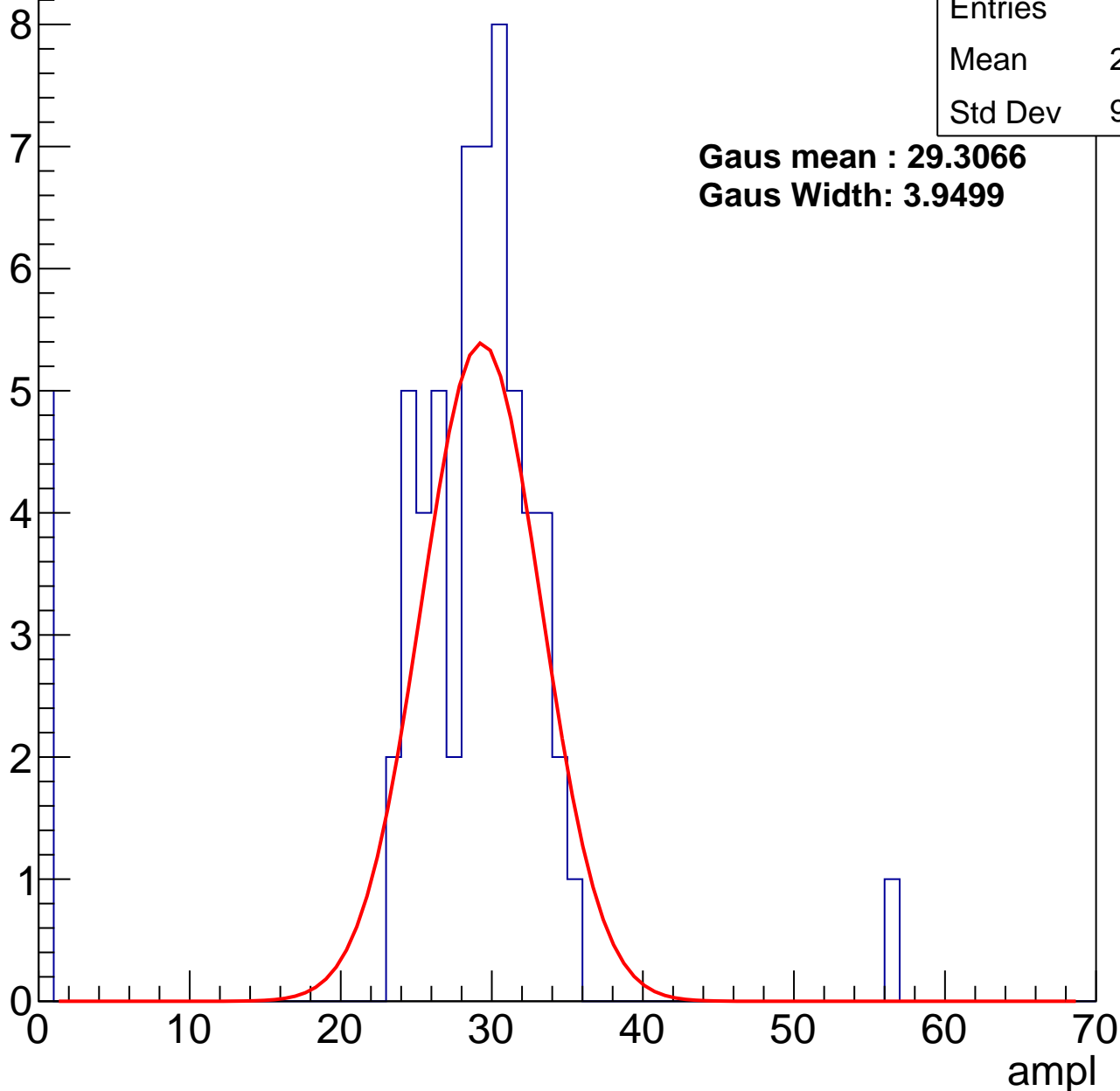
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	26.82
Std Dev	9.136

Gaus mean : 29.3066

Gaus Width: 3.9499



B1L101S, U15-ch90, adc1

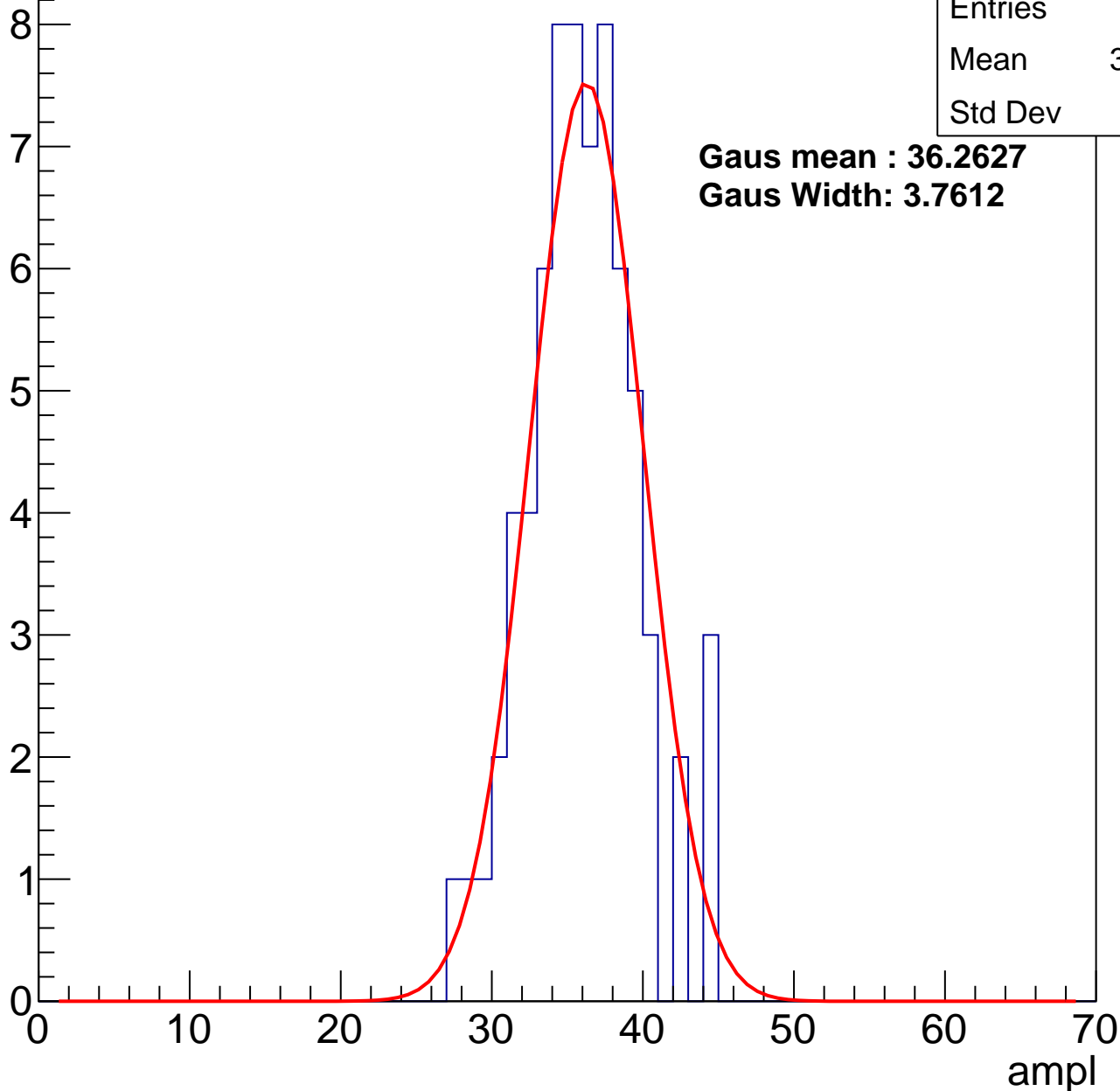
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.55
Std Dev	3.61

Gaus mean : 36.2627

Gaus Width: 3.7612



B1L101S, U15-ch90, adc2

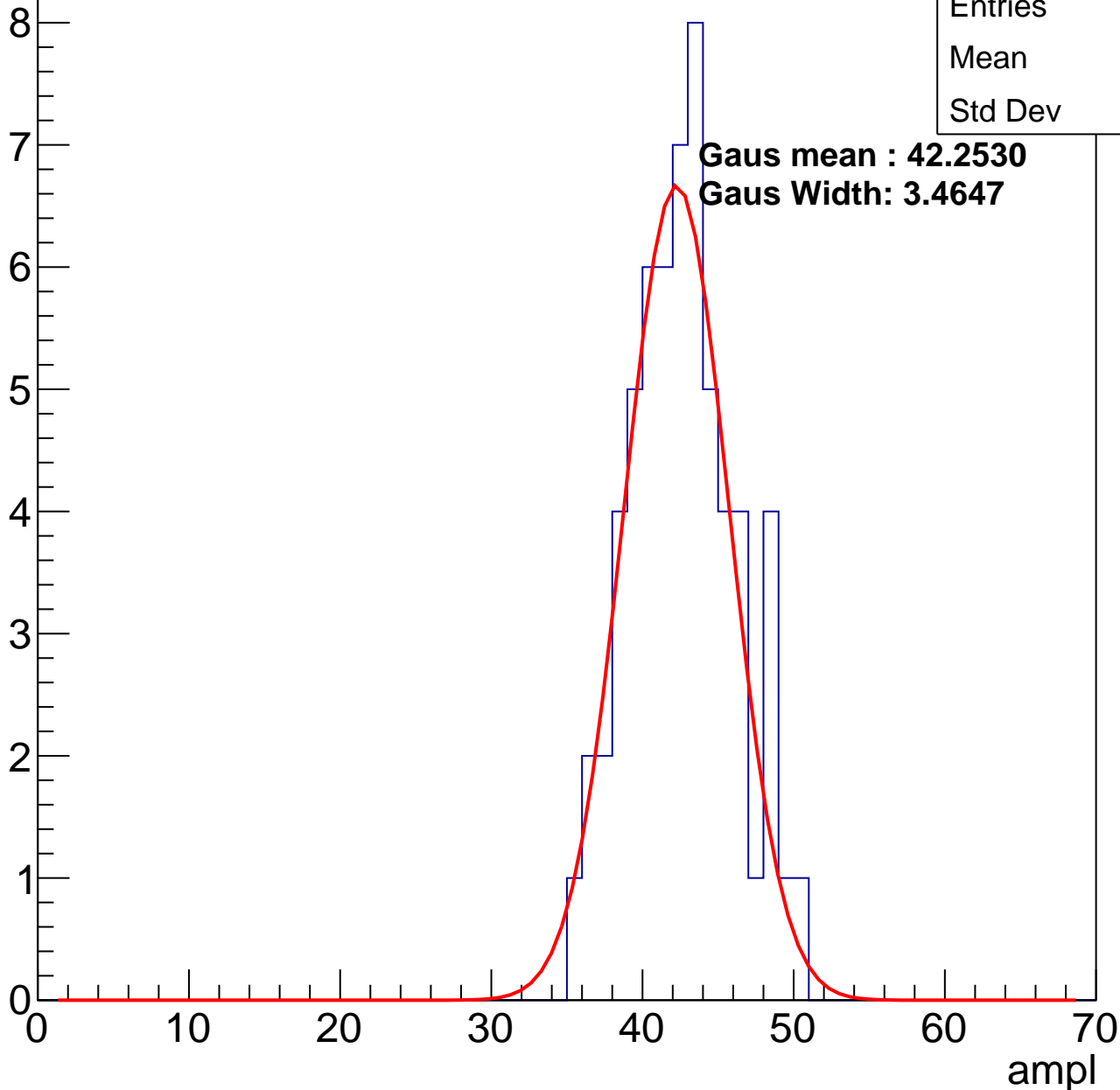
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.2
Std Dev	3.43

Gaus mean : 42.2530

Gaus Width: 3.4647

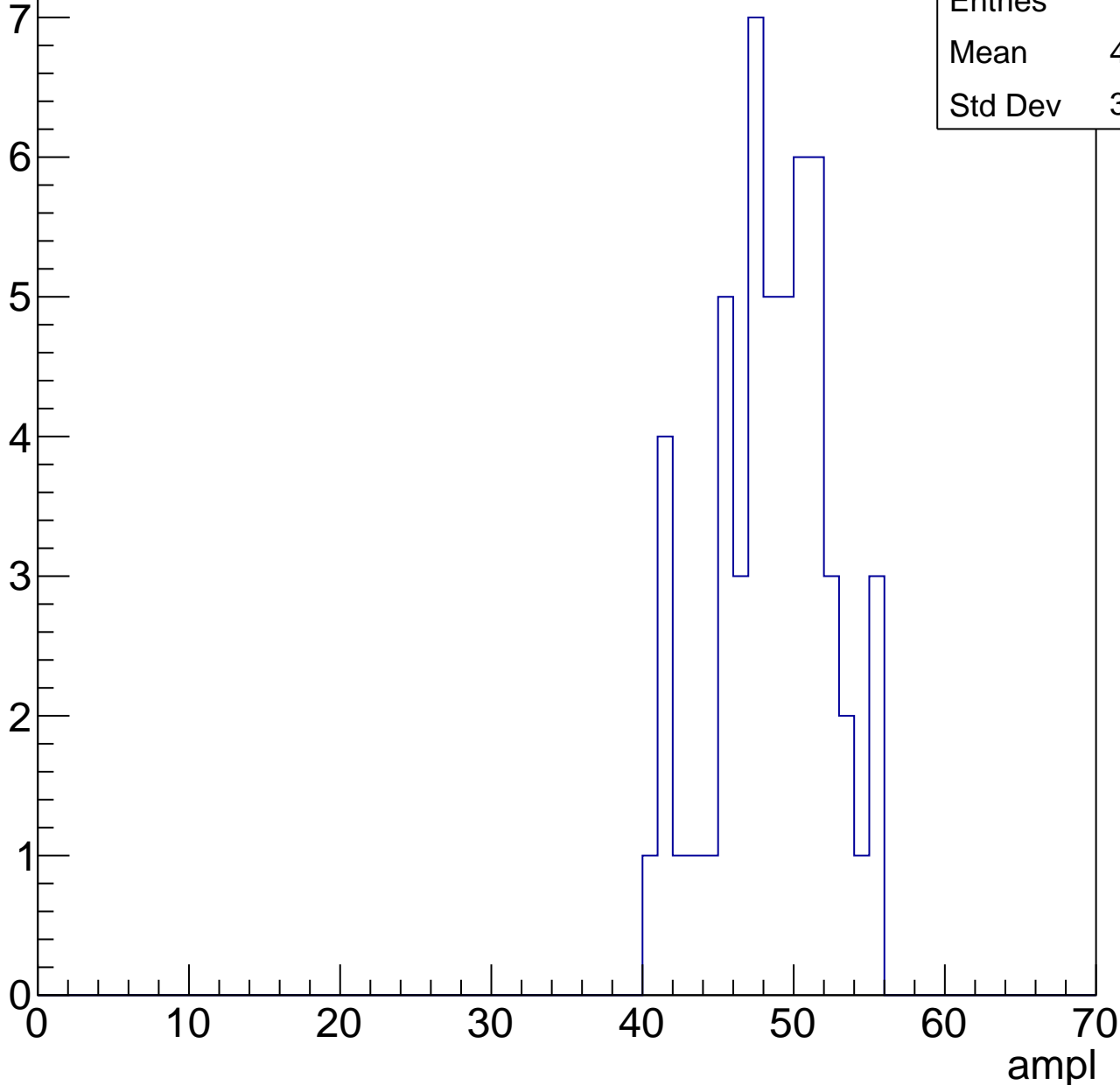


B1L101S, U15-ch90, adc3

calib_packv5_042523_0143.root, FC#0, port D2

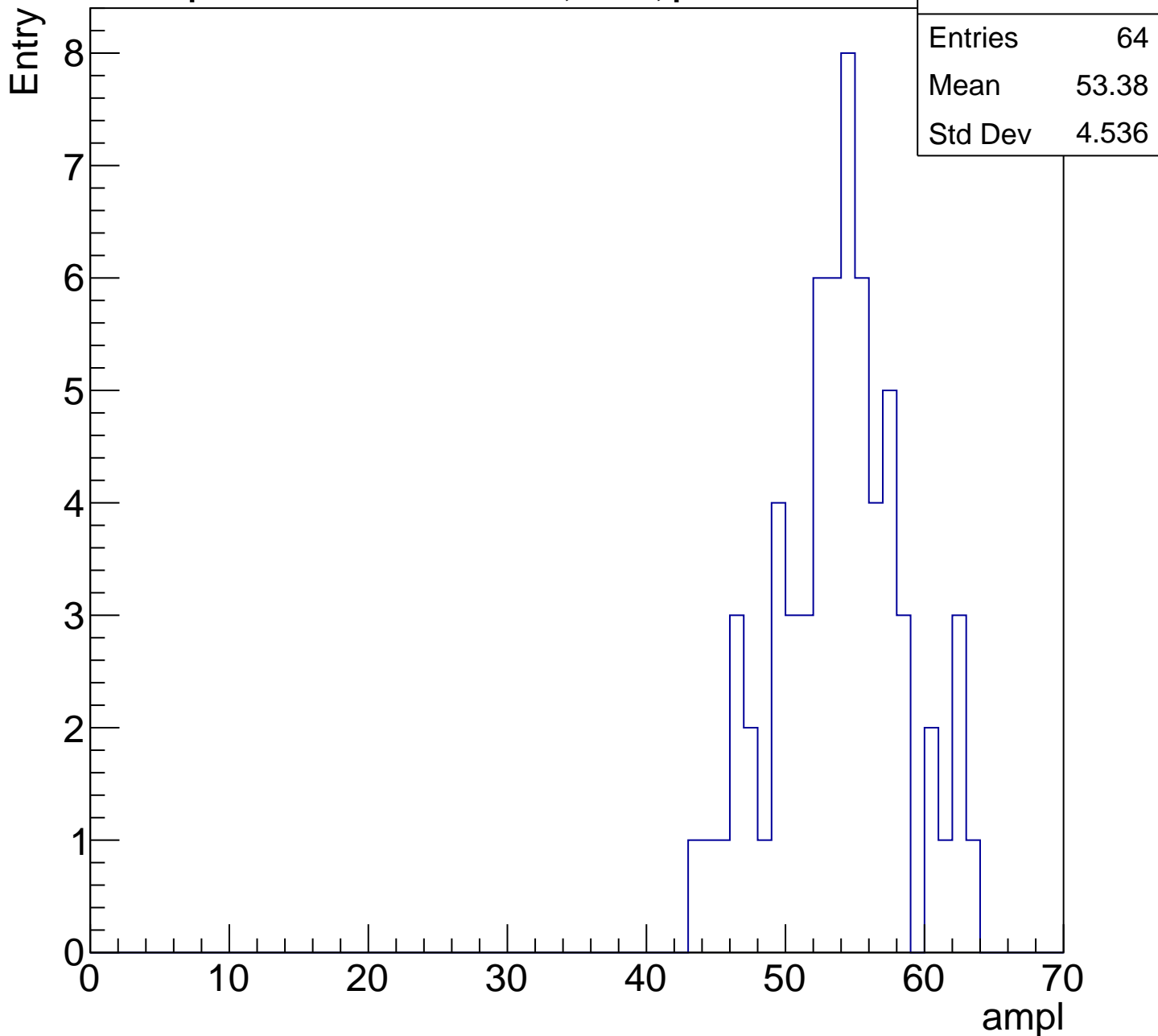
Entry

Entries	54
Mean	48.09
Std Dev	3.763



B1L101S, U15-ch90, adc4

calib_packv5_042523_0143.root, FC#0, port D2

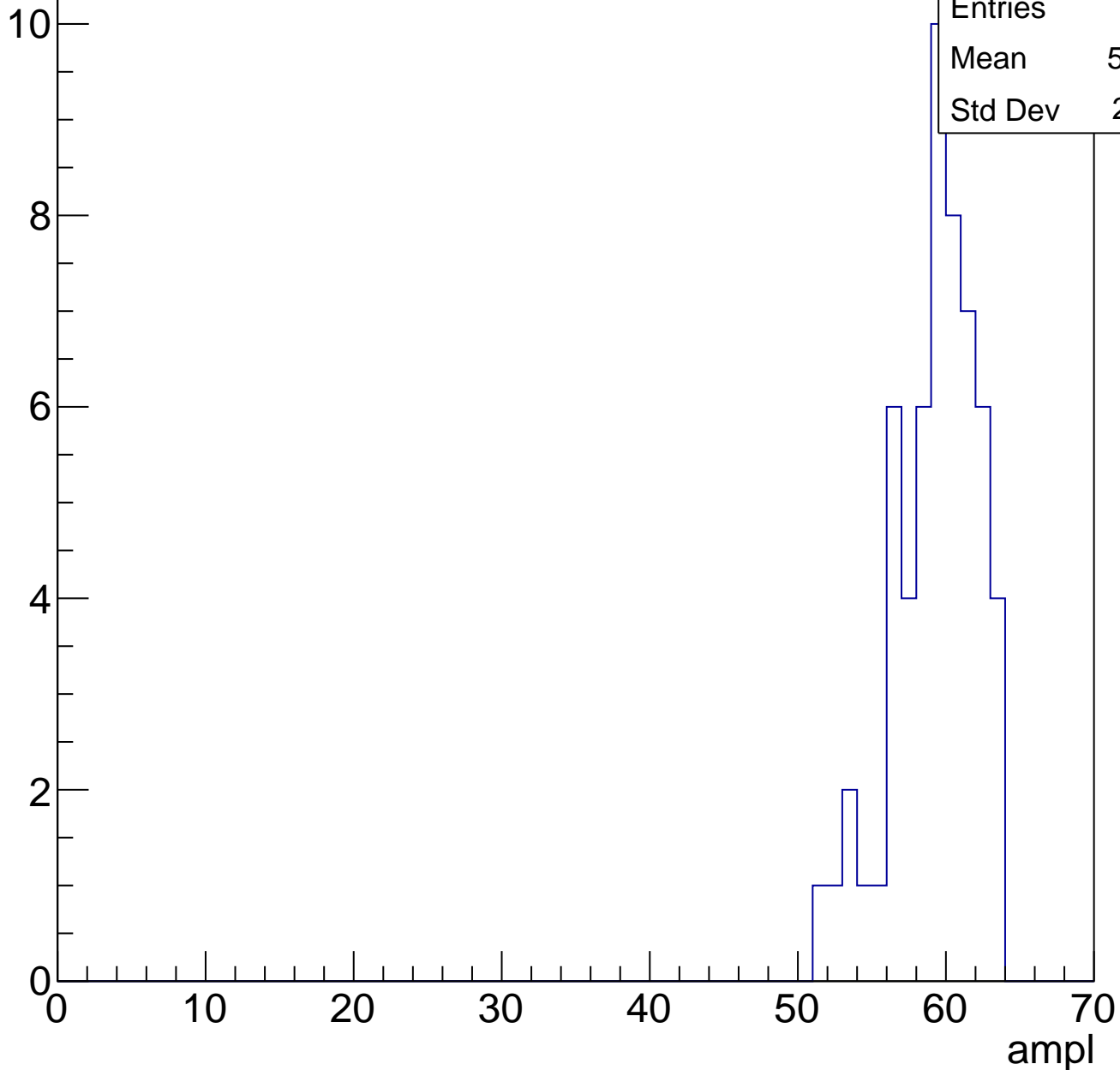


B1L101S, U15-ch90, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entries	57
Mean	58.79
Std Dev	2.821

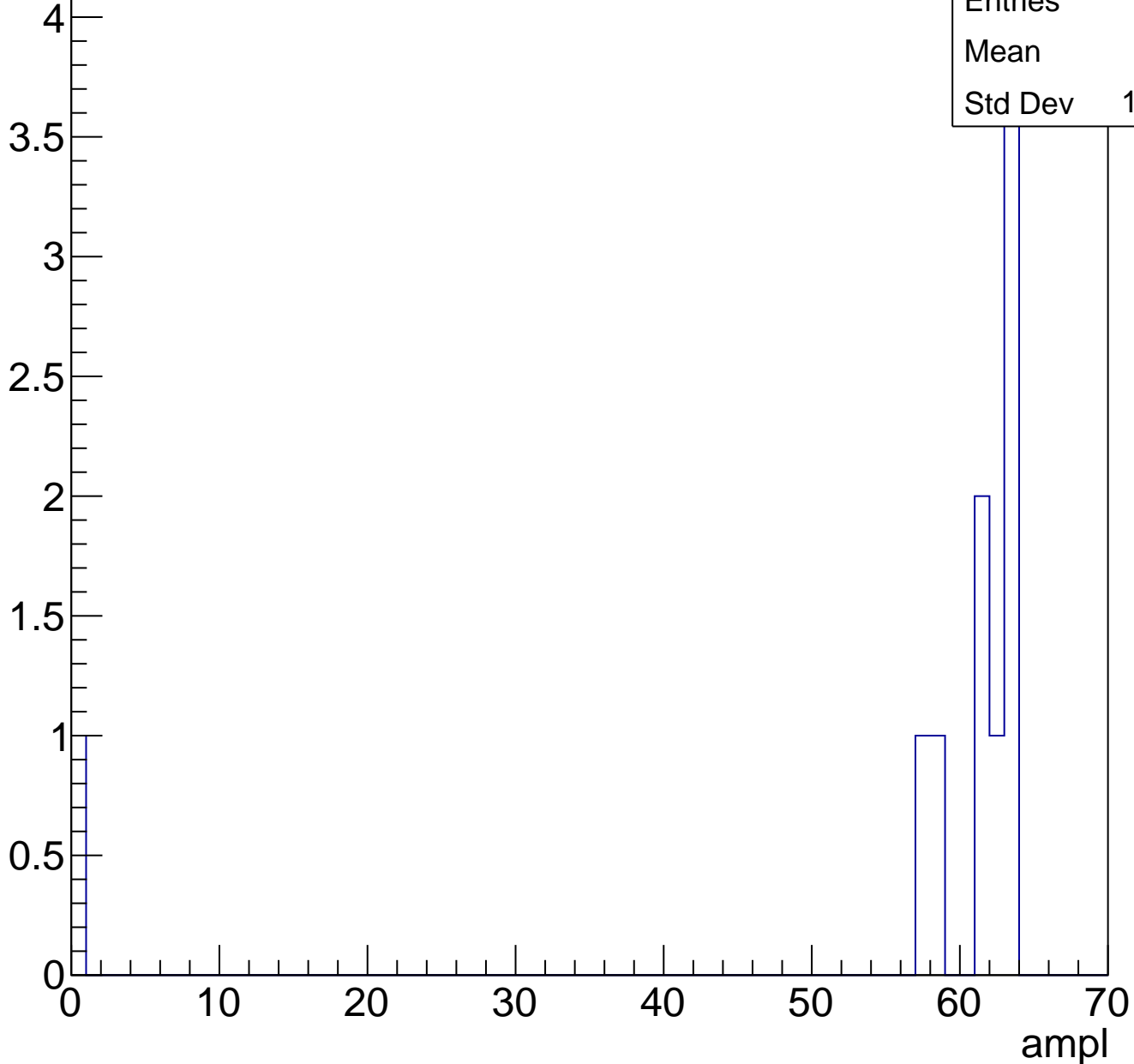
Entry



B1L101S, U15-ch90, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch90, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch91, adc0

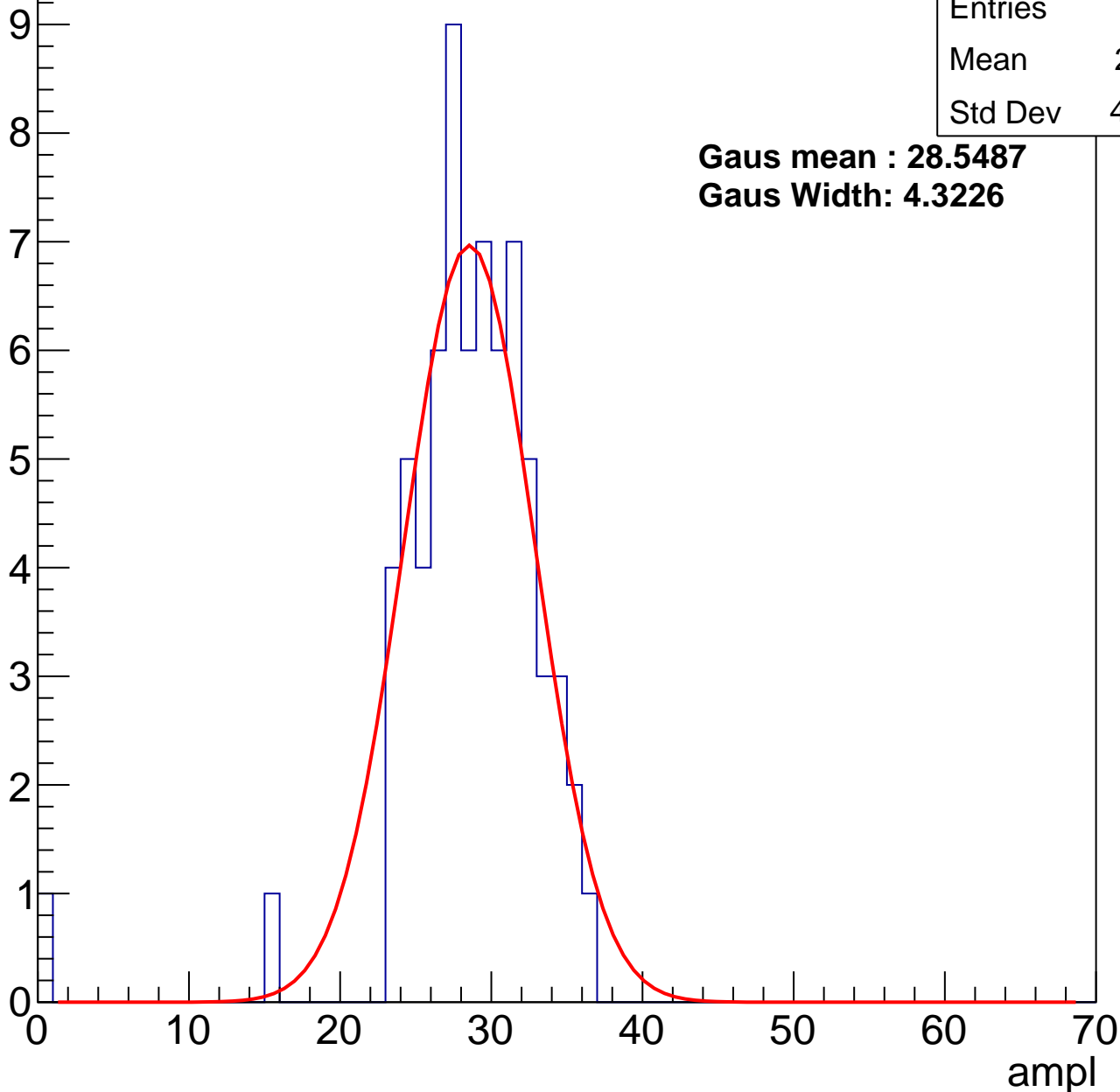
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.01
Std Dev	4.958

Gaus mean : 28.5487

Gaus Width: 4.3226



B1L101S, U15-ch91, adc1

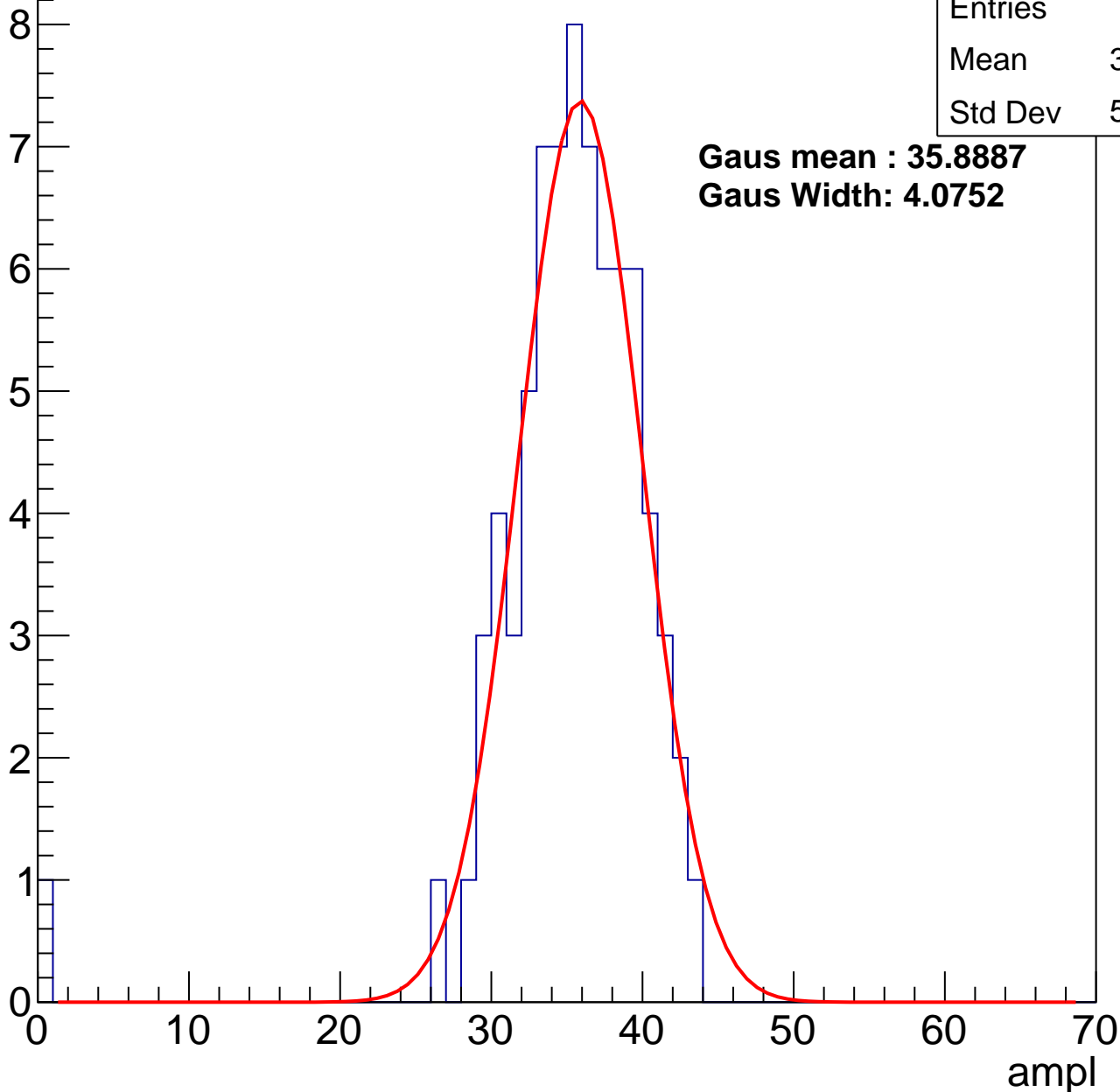
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	34.79
Std Dev	5.463

Gaus mean : 35.8887

Gaus Width: 4.0752



B1L101S, U15-ch91, adc2

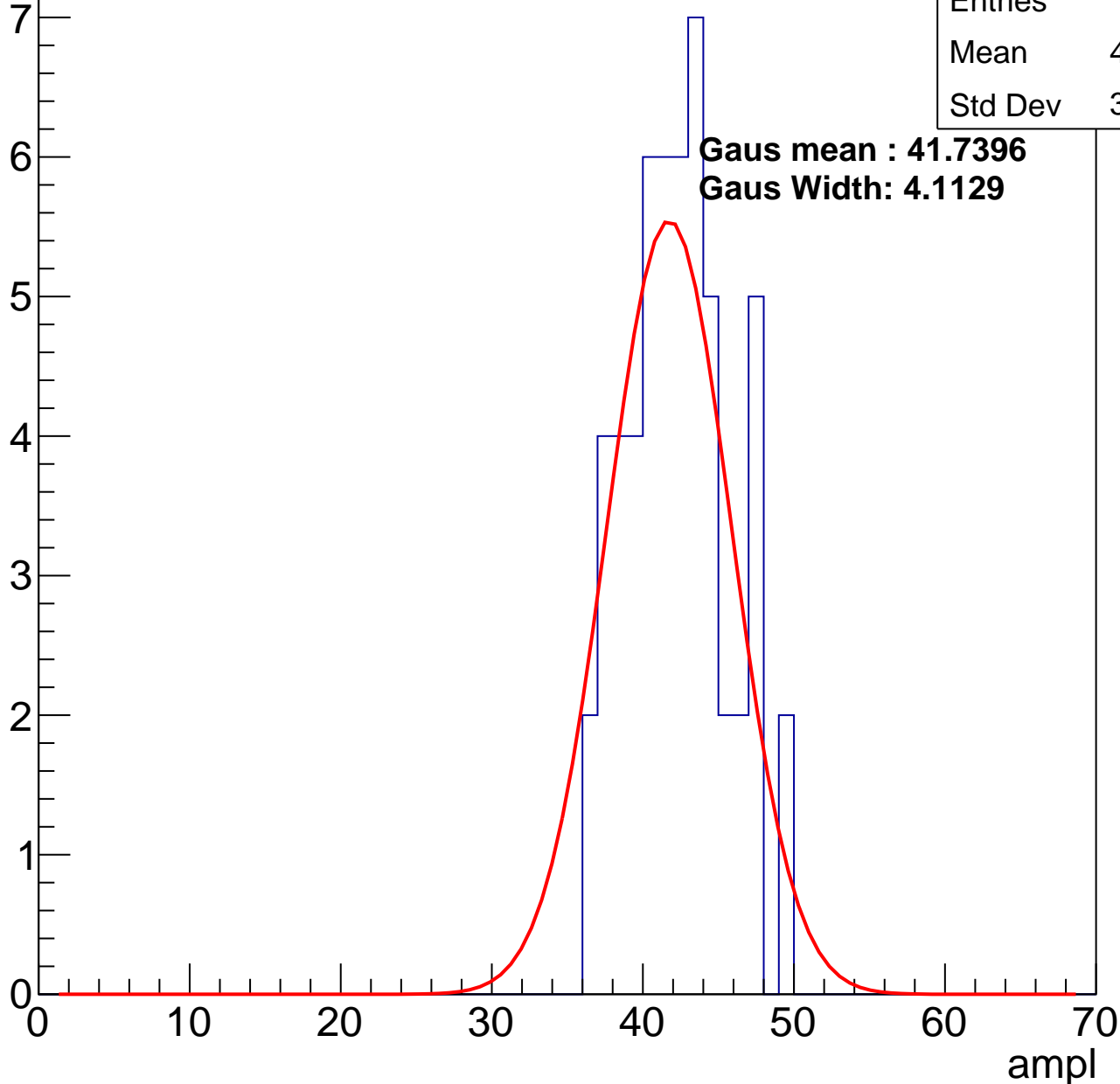
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	41.85
Std Dev	3.305

Gaus mean : 41.7396

Gaus Width: 4.1129

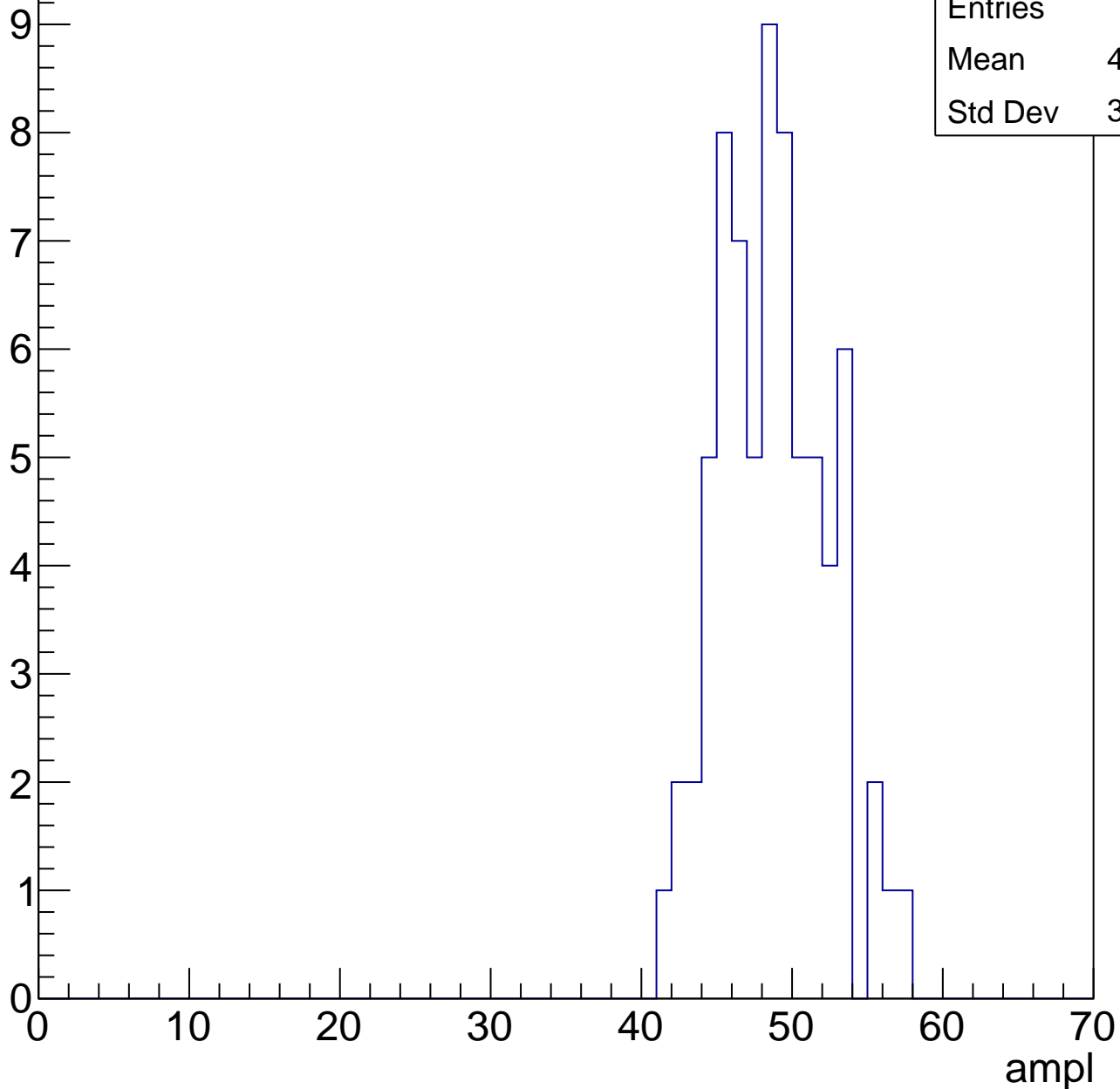


B1L101S, U15-ch91, adc3

calib_packv5_042523_0143.root, FC#0, port D2

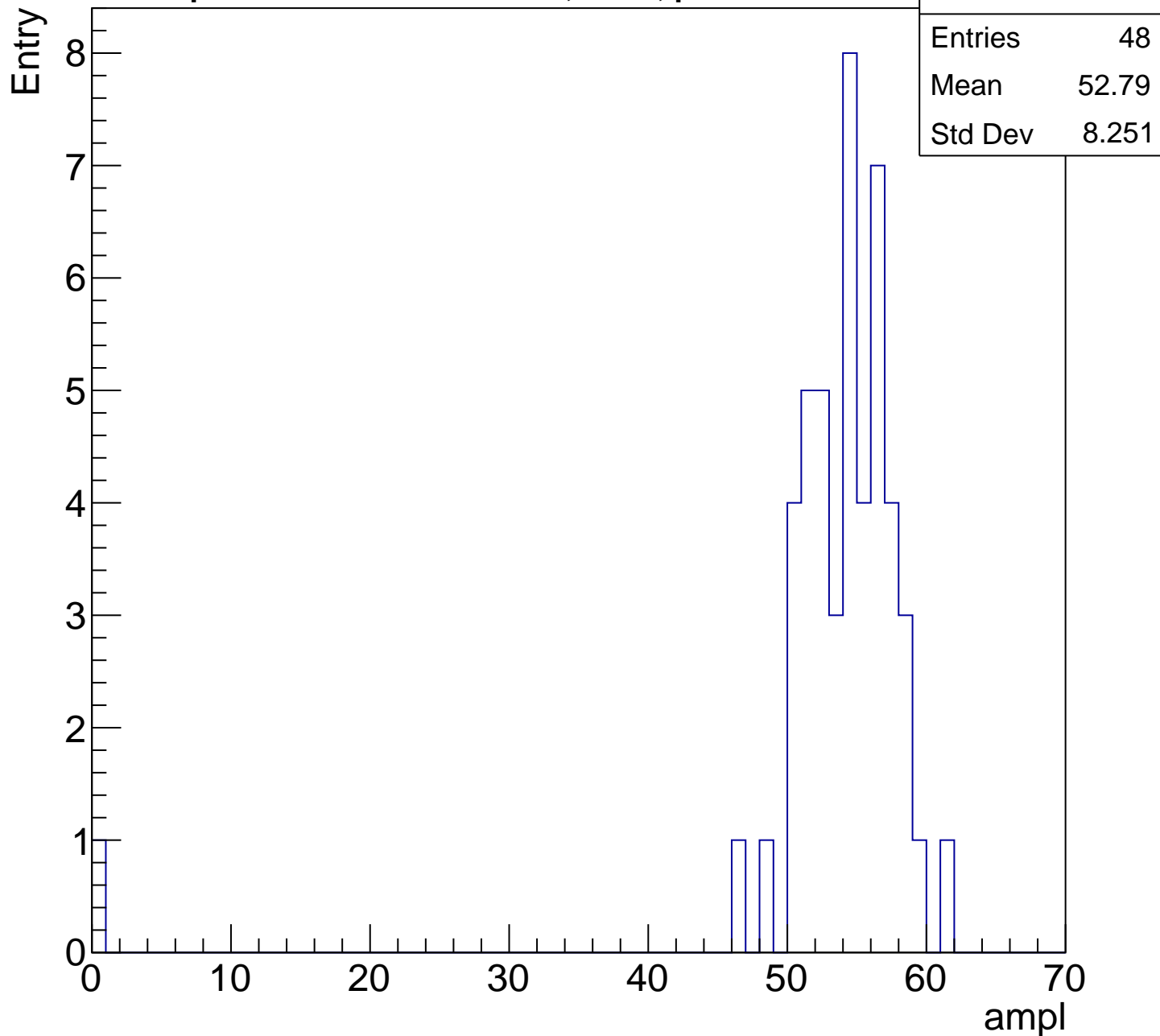
Entry

Entries	71
Mean	48.25
Std Dev	3.523



B1L101S, U15-ch91, adc4

calib_packv5_042523_0143.root, FC#0, port D2

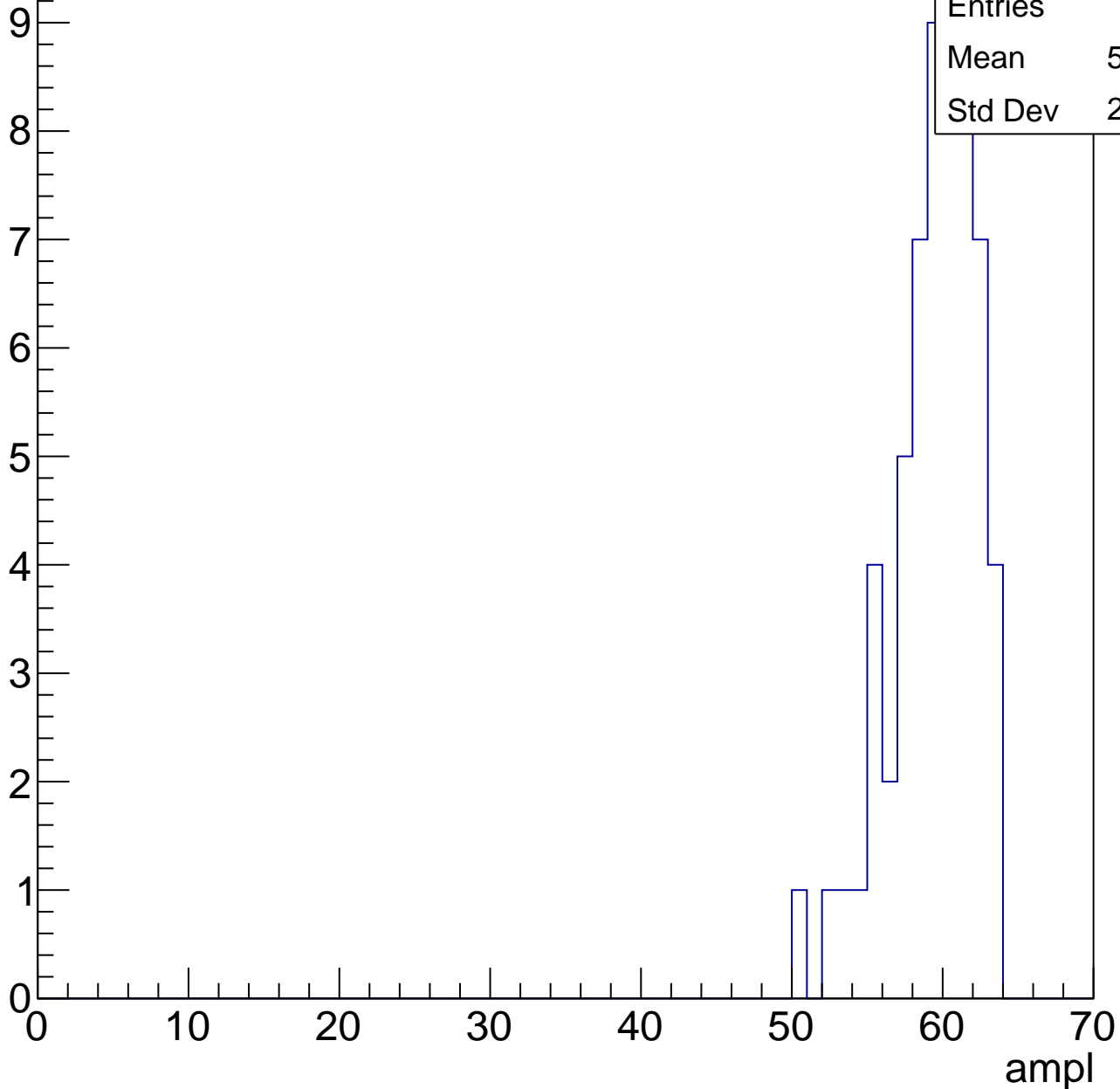


B1L101S, U15-ch91, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	58.95
Std Dev	2.825

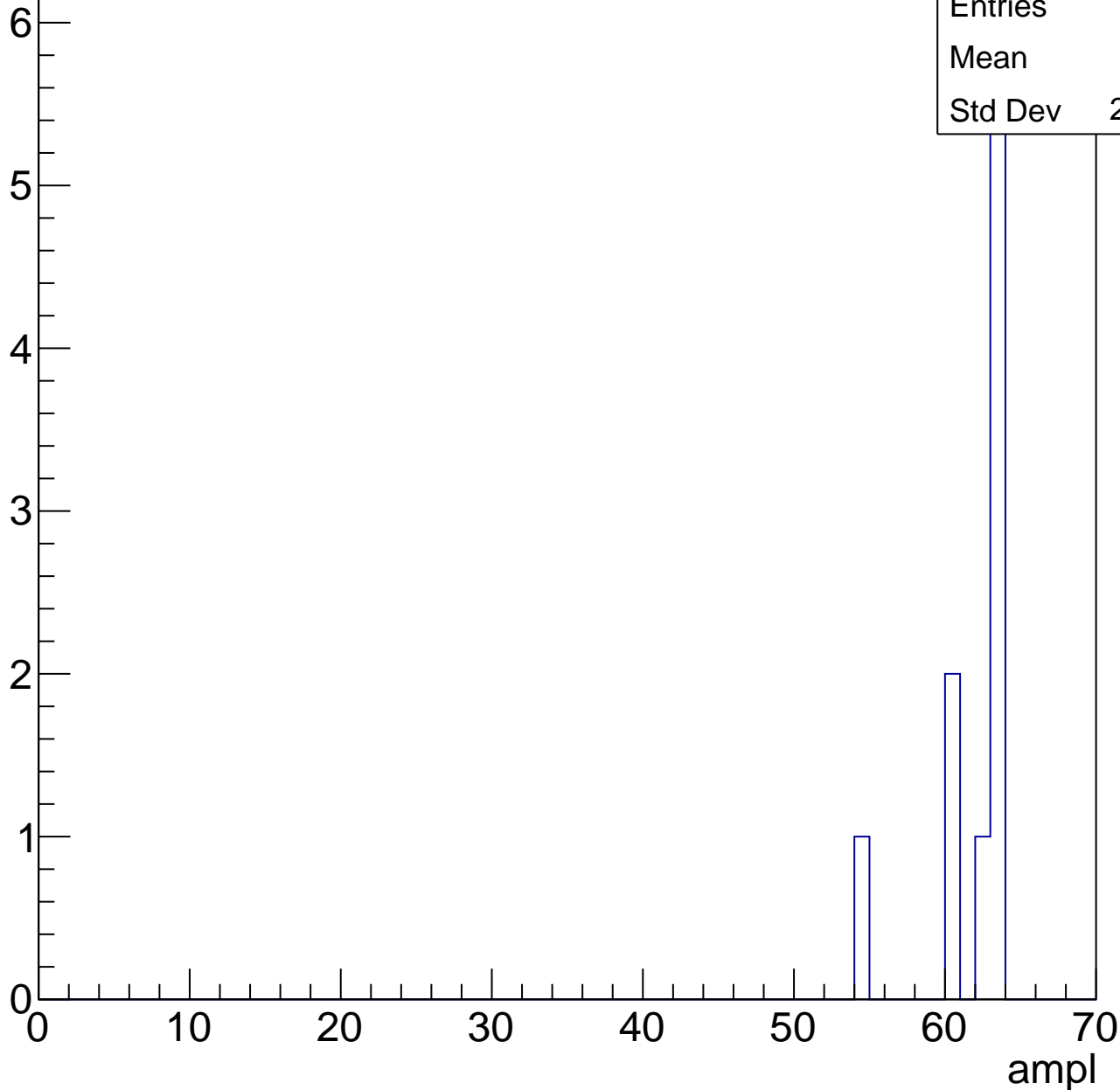


B1L101S, U15-ch91, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

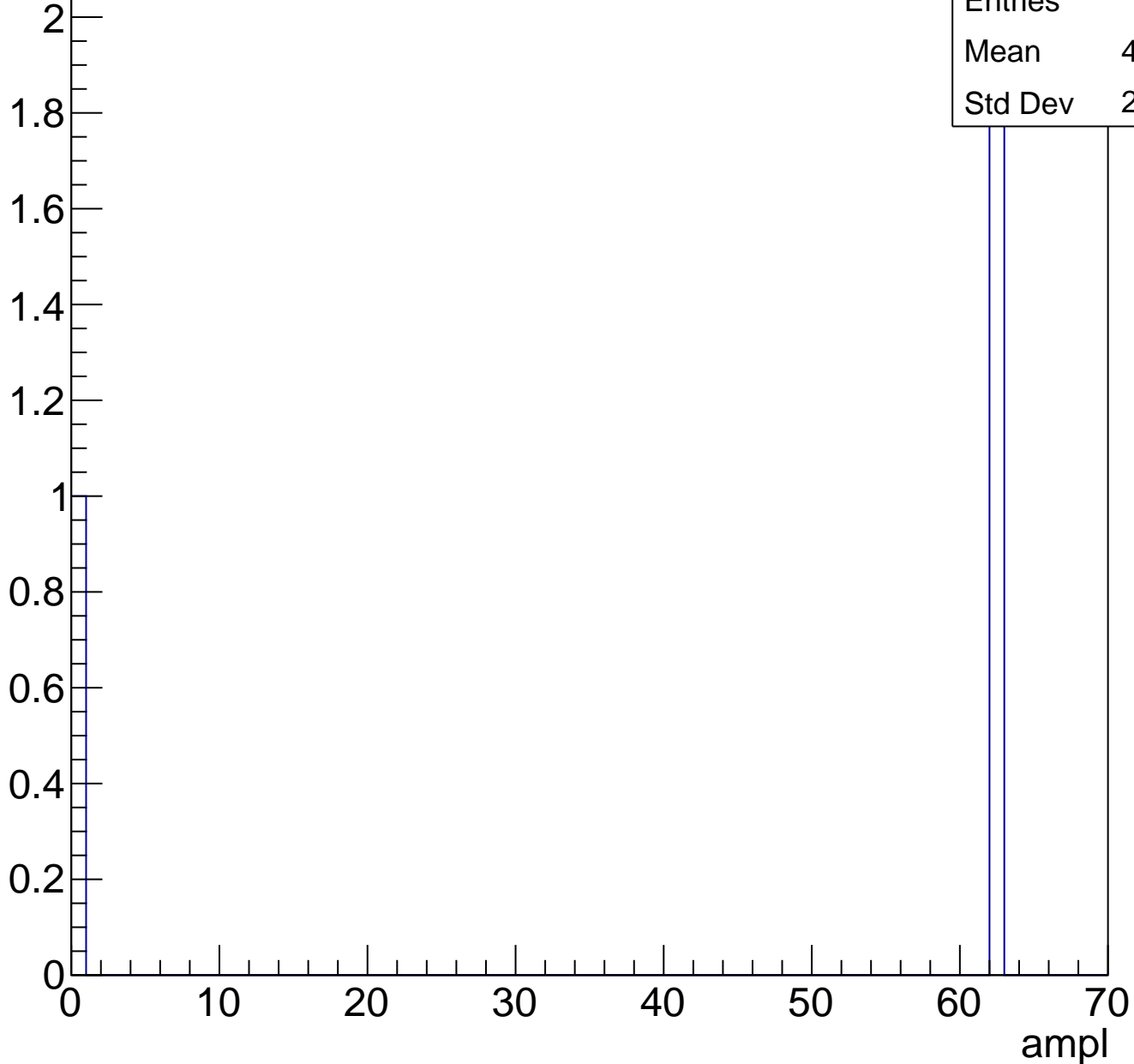
Entries	10
Mean	61.4
Std Dev	2.728



B1L101S, U15-ch91, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch92, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	71
Mean	28.73
Std Dev	4.821

Gaus mean : 29.3811

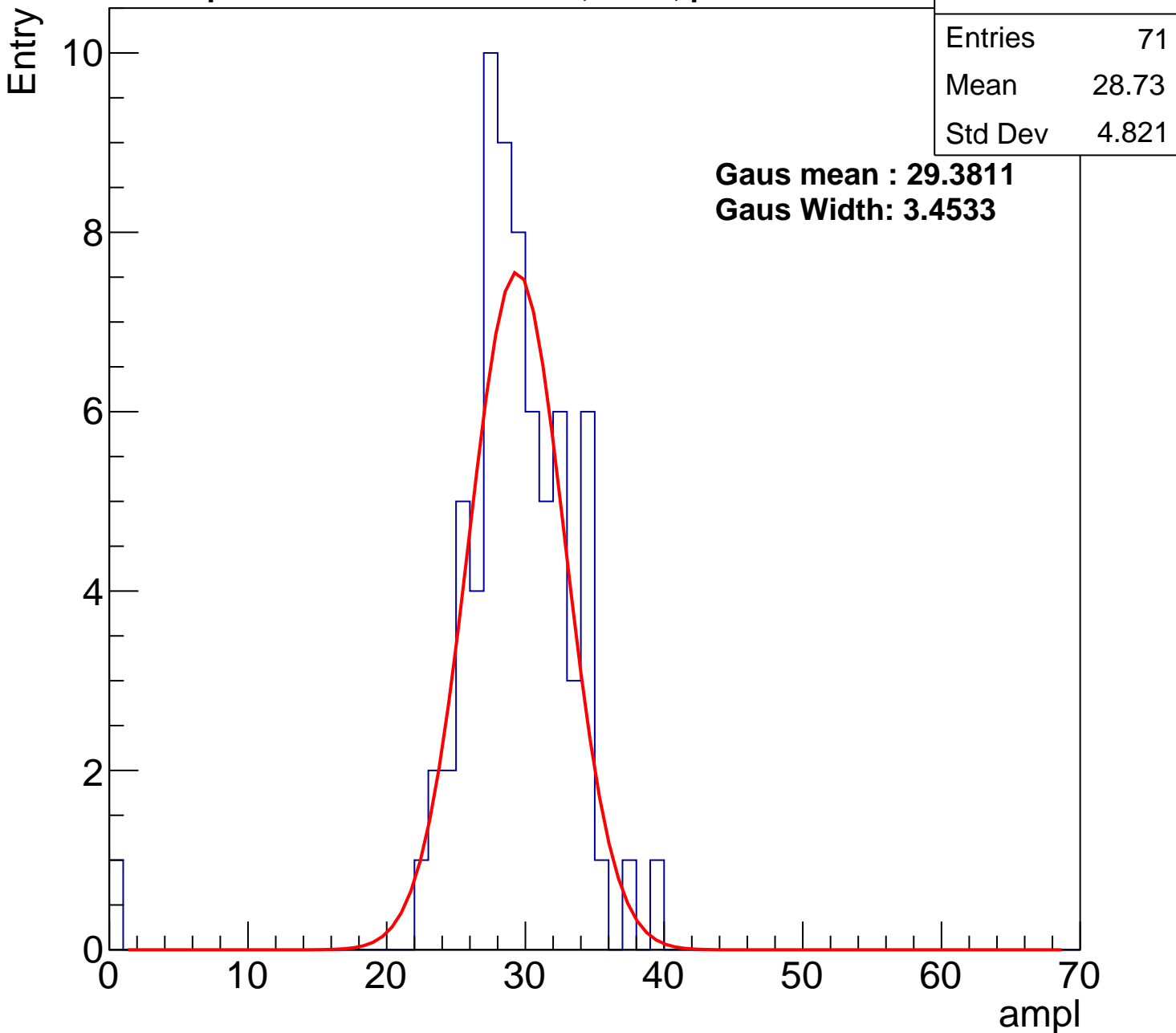
Gaus Width: 3.4533

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch92, adc1

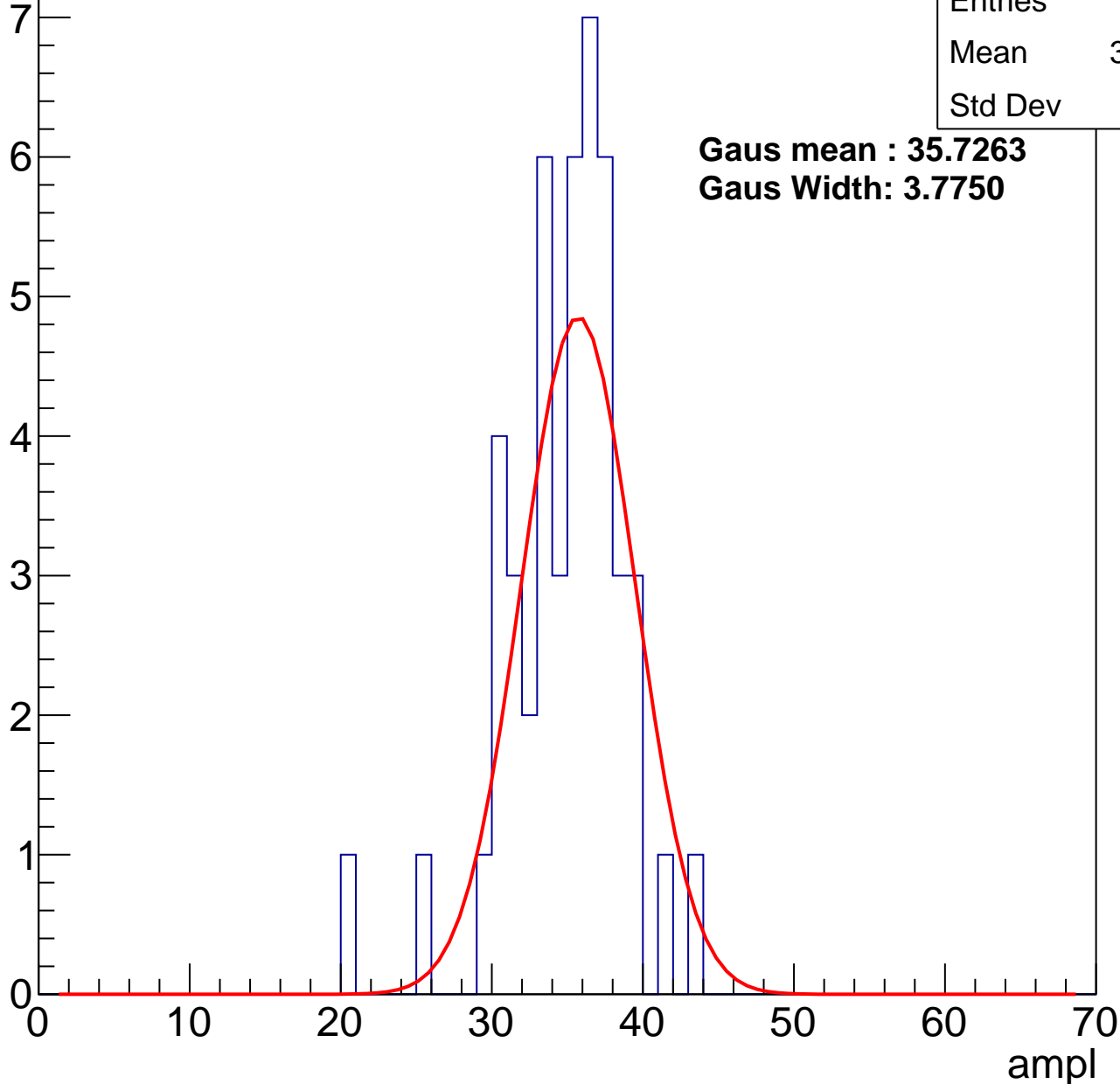
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	34.38
Std Dev	3.93

Gaus mean : 35.7263

Gaus Width: 3.7750



B1L101S, U15-ch92, adc2

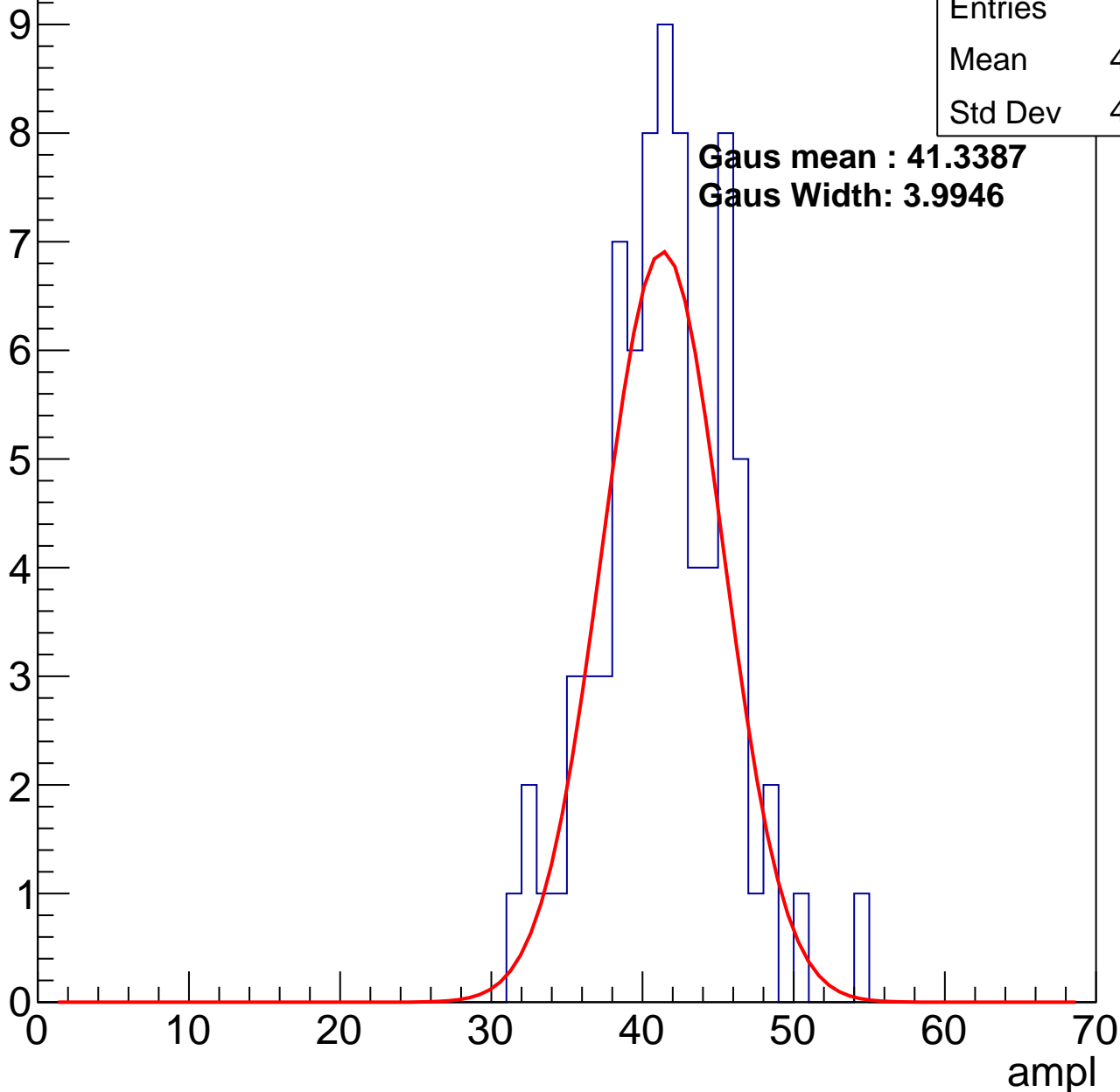
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	78
Mean	40.97
Std Dev	4.233

Gaus mean : 41.3387

Gaus Width: 3.9946

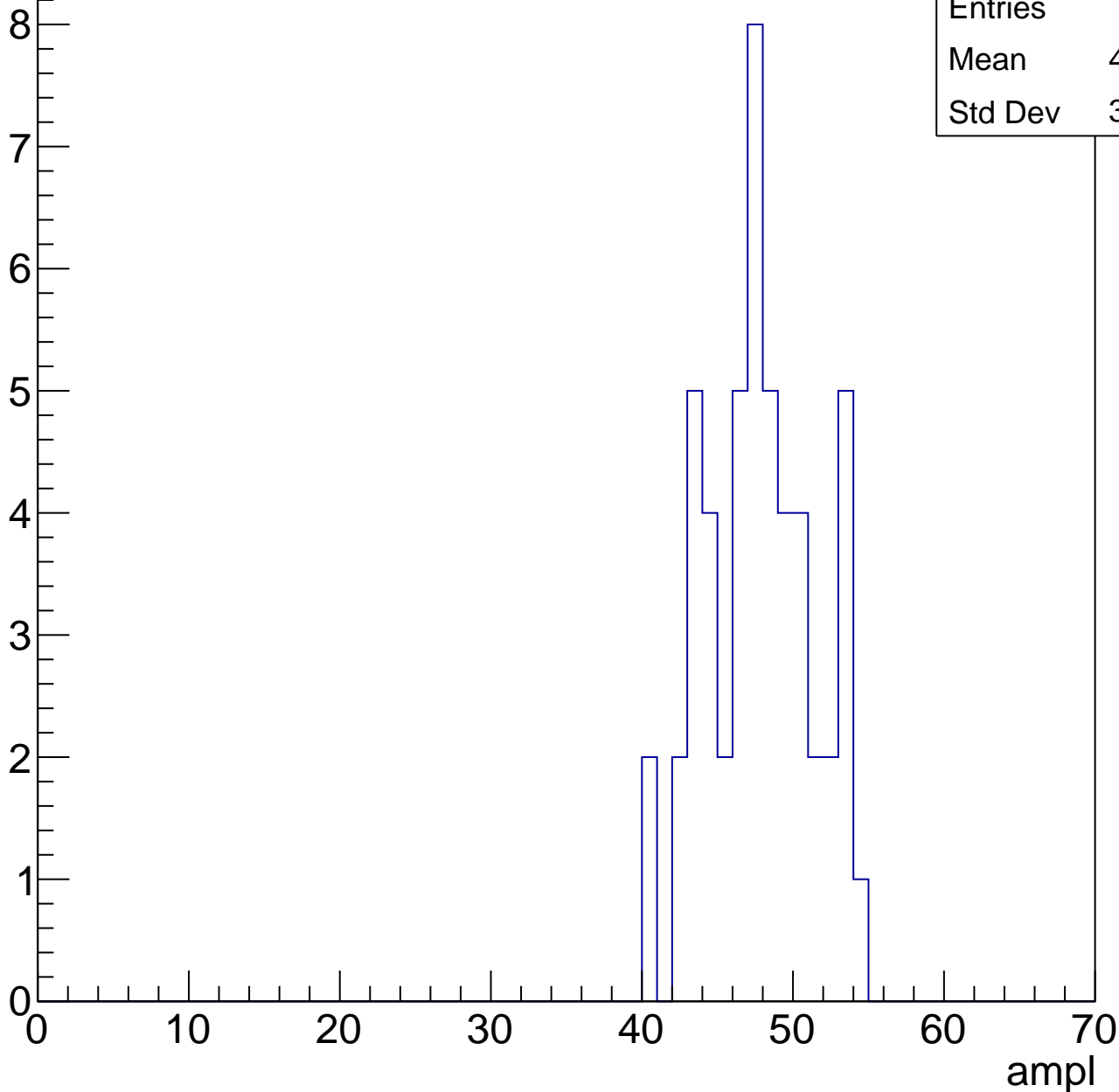


B1L101S, U15-ch92, adc3

calib_packv5_042523_0143.root, FC#0, port D2

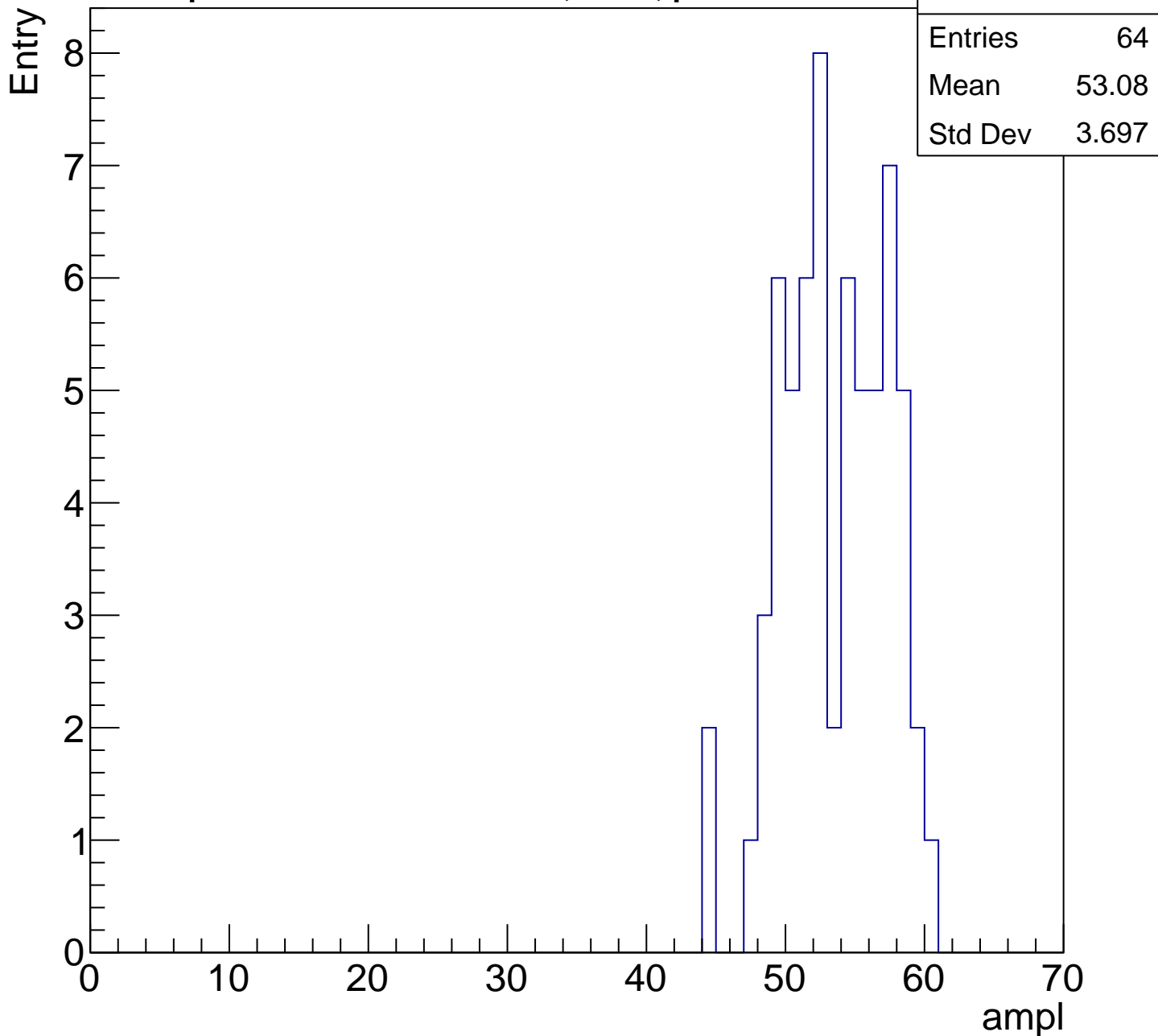
Entry

Entries	51
Mean	47.29
Std Dev	3.544



B1L101S, U15-ch92, adc4

calib_packv5_042523_0143.root, FC#0, port D2

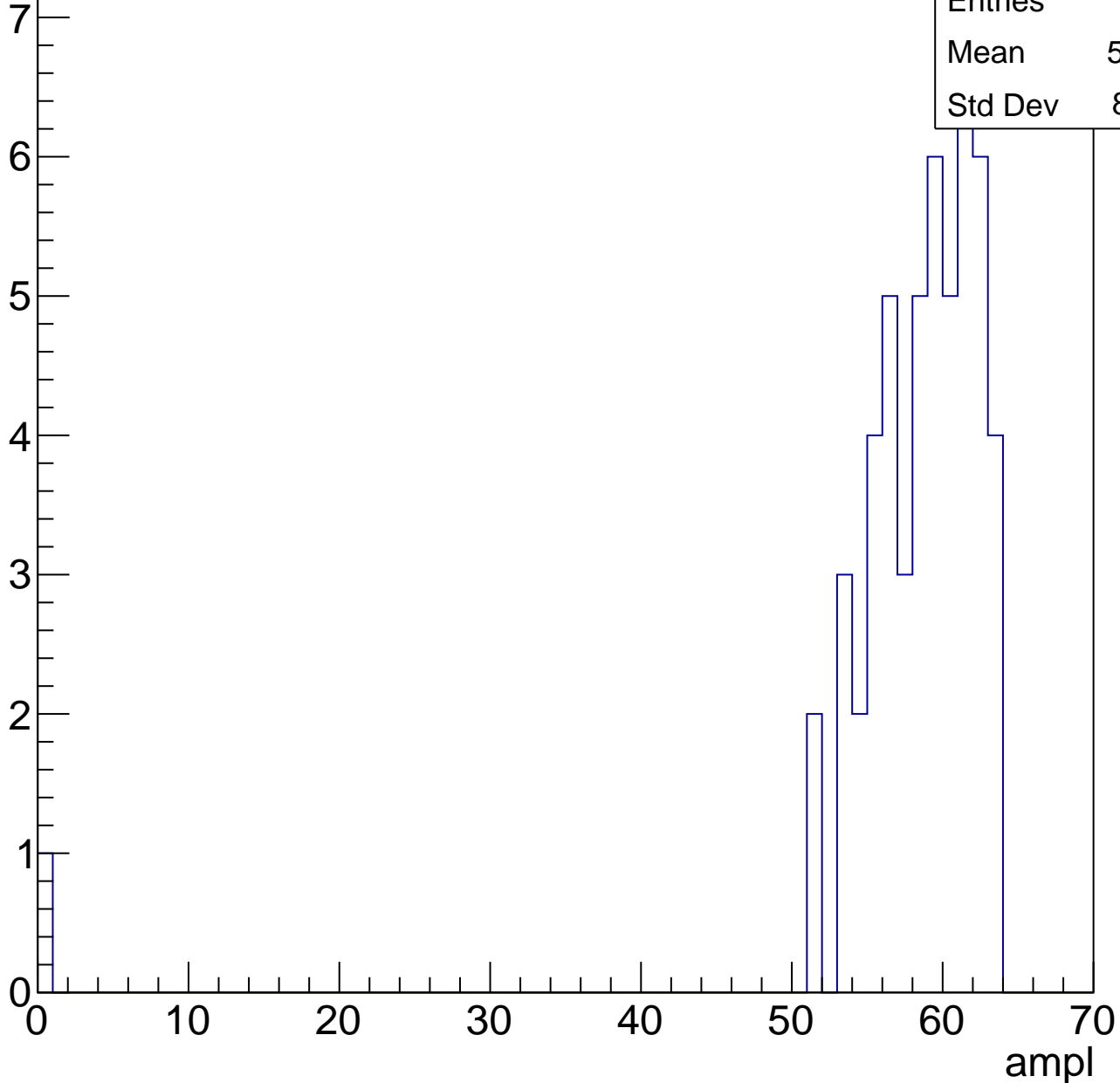


B1L101S, U15-ch92, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	57.26
Std Dev	8.561

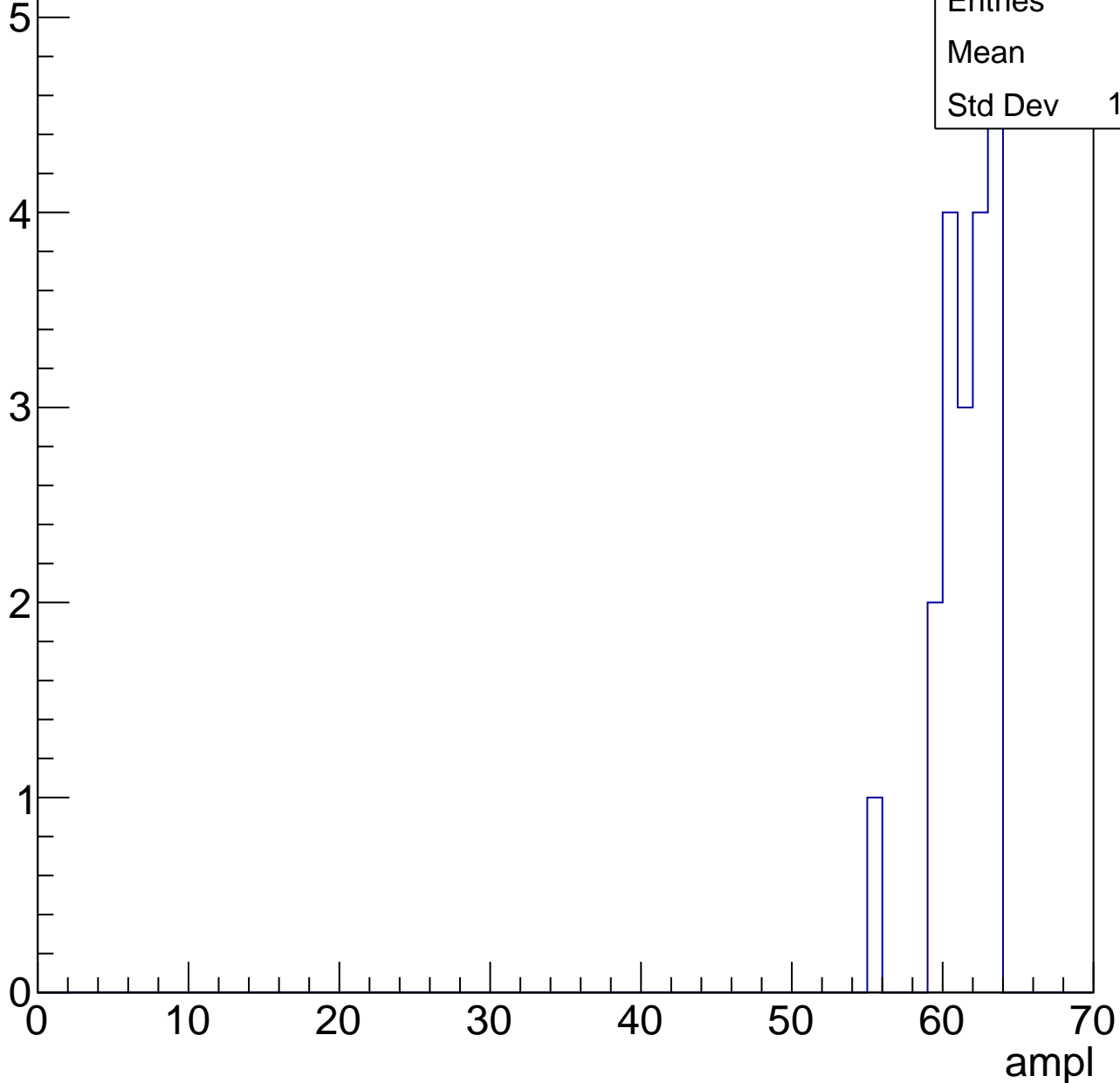


B1L101S, U15-ch92, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	19
Mean	61
Std Dev	1.947



B1L101S, U15-ch92, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch93, adc0

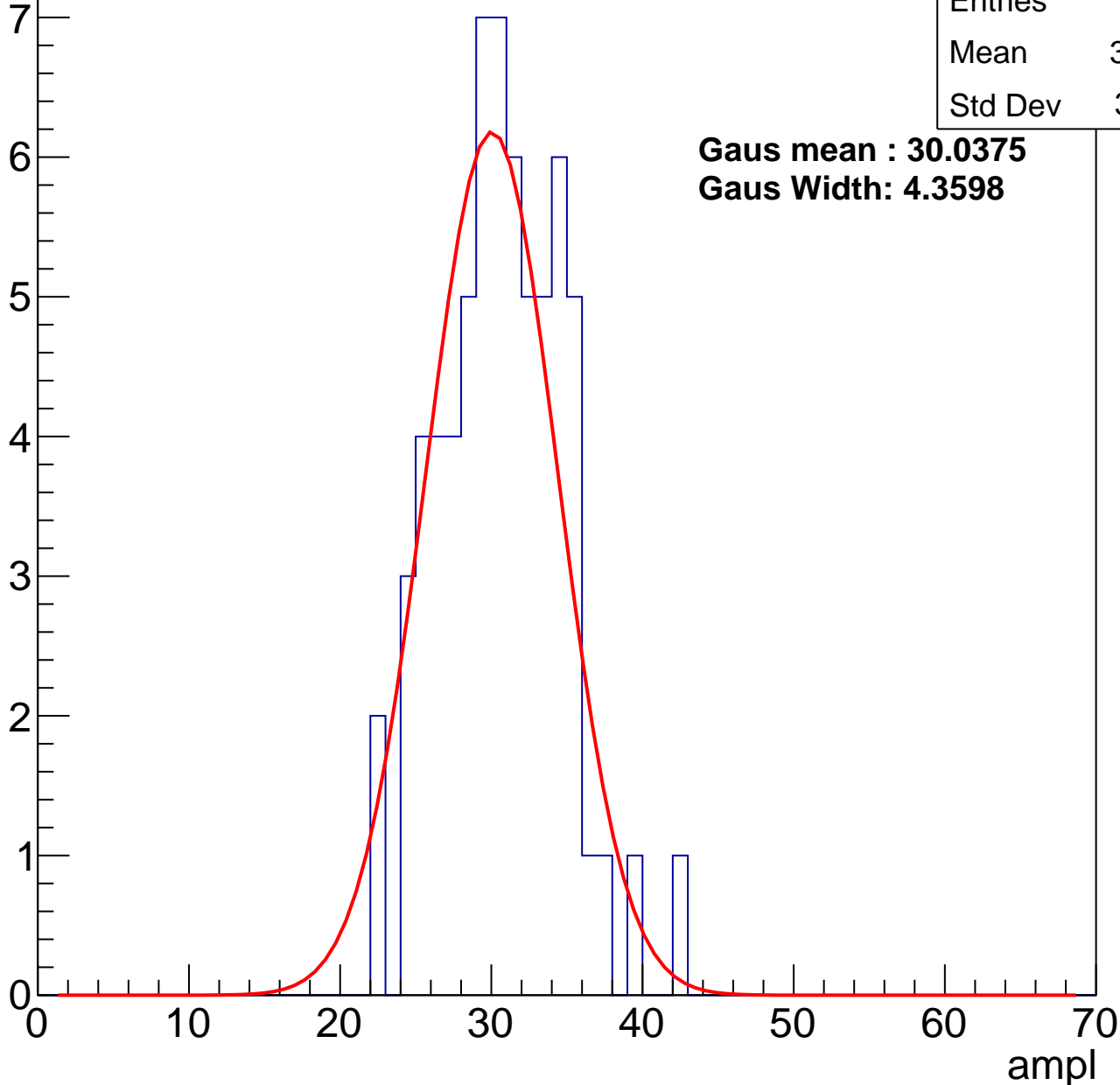
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.22
Std Dev	3.981

Gaus mean : 30.0375

Gaus Width: 4.3598



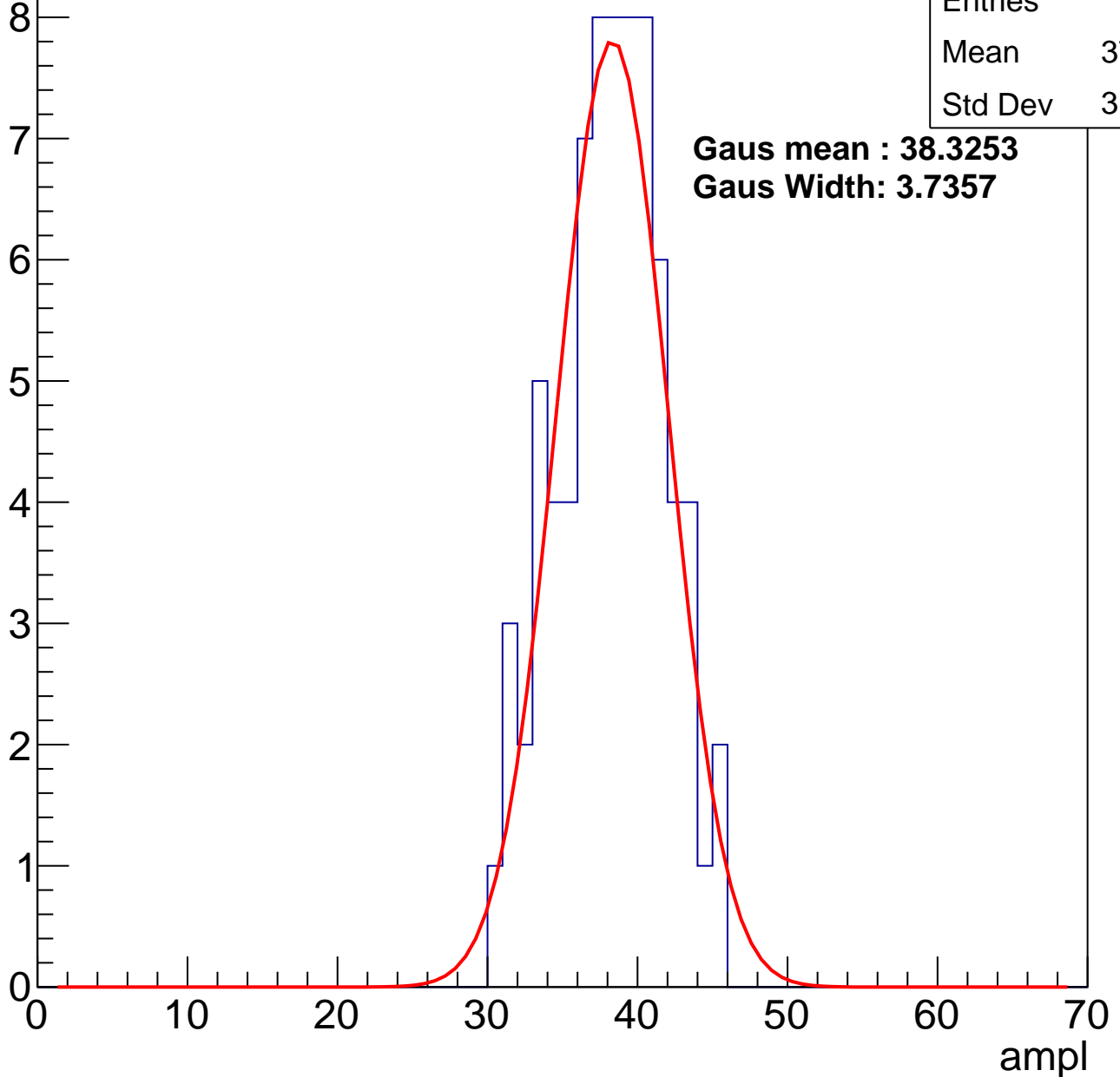
B1L101S, U15-ch93, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	37.76
Std Dev	3.525

Gaus mean : 38.3253
Gaus Width: 3.7357



B1L101S, U15-ch93, adc2

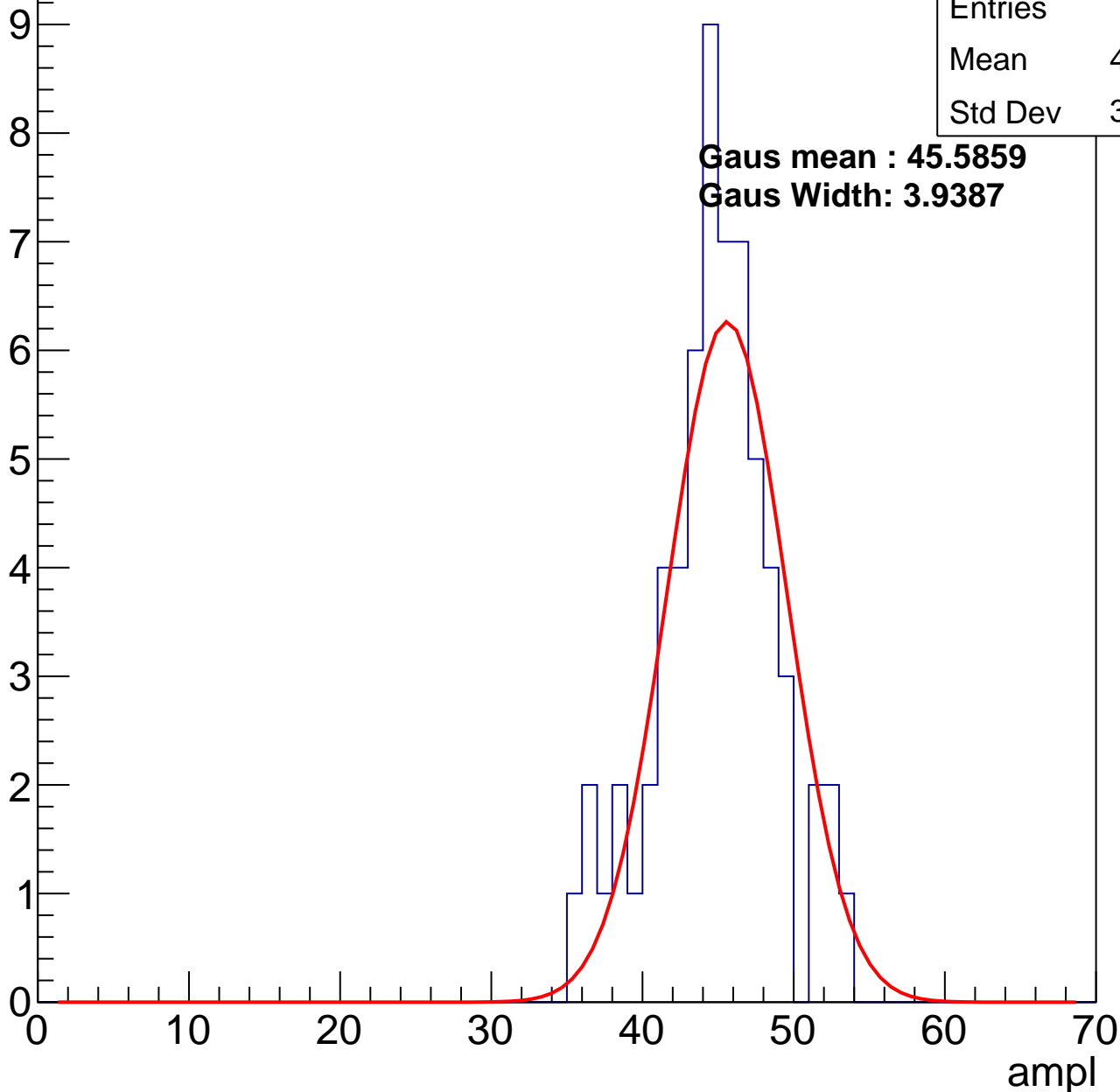
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	44.37
Std Dev	3.909

Gaus mean : 45.5859

Gaus Width: 3.9387

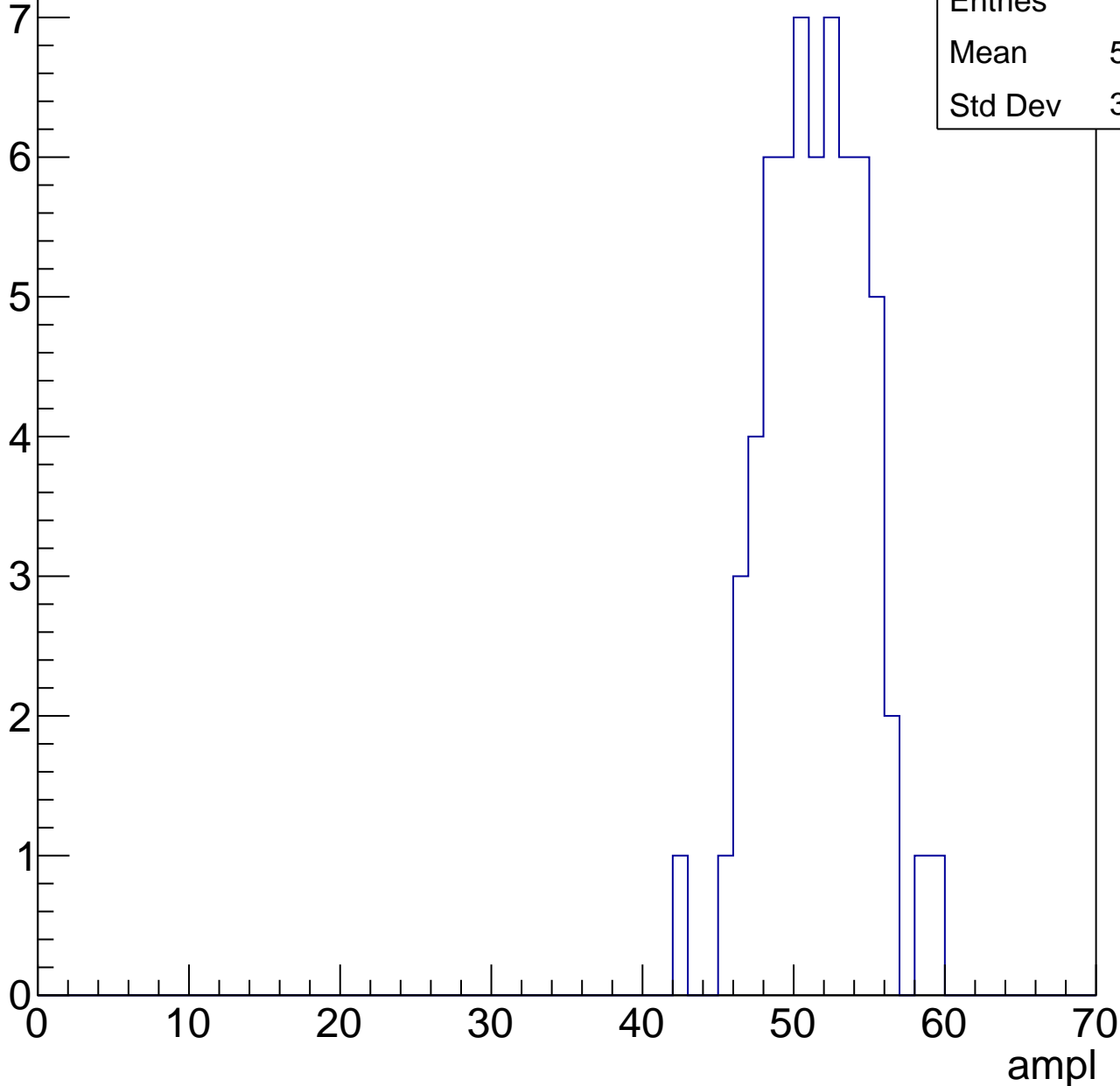


B1L101S, U15-ch93, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

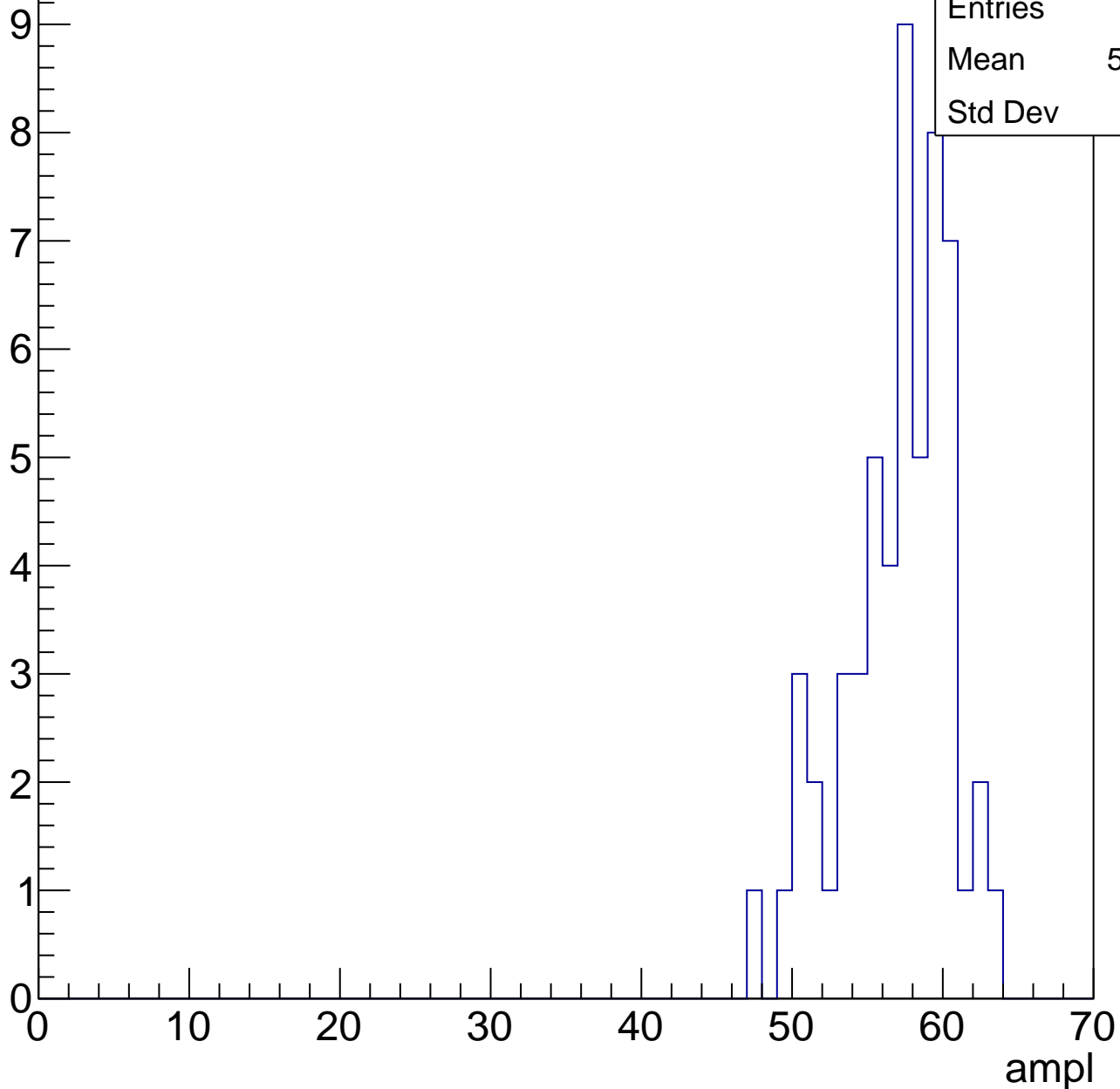
Entries	62
Mean	50.98
Std Dev	3.285



B1L101S, U15-ch93, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

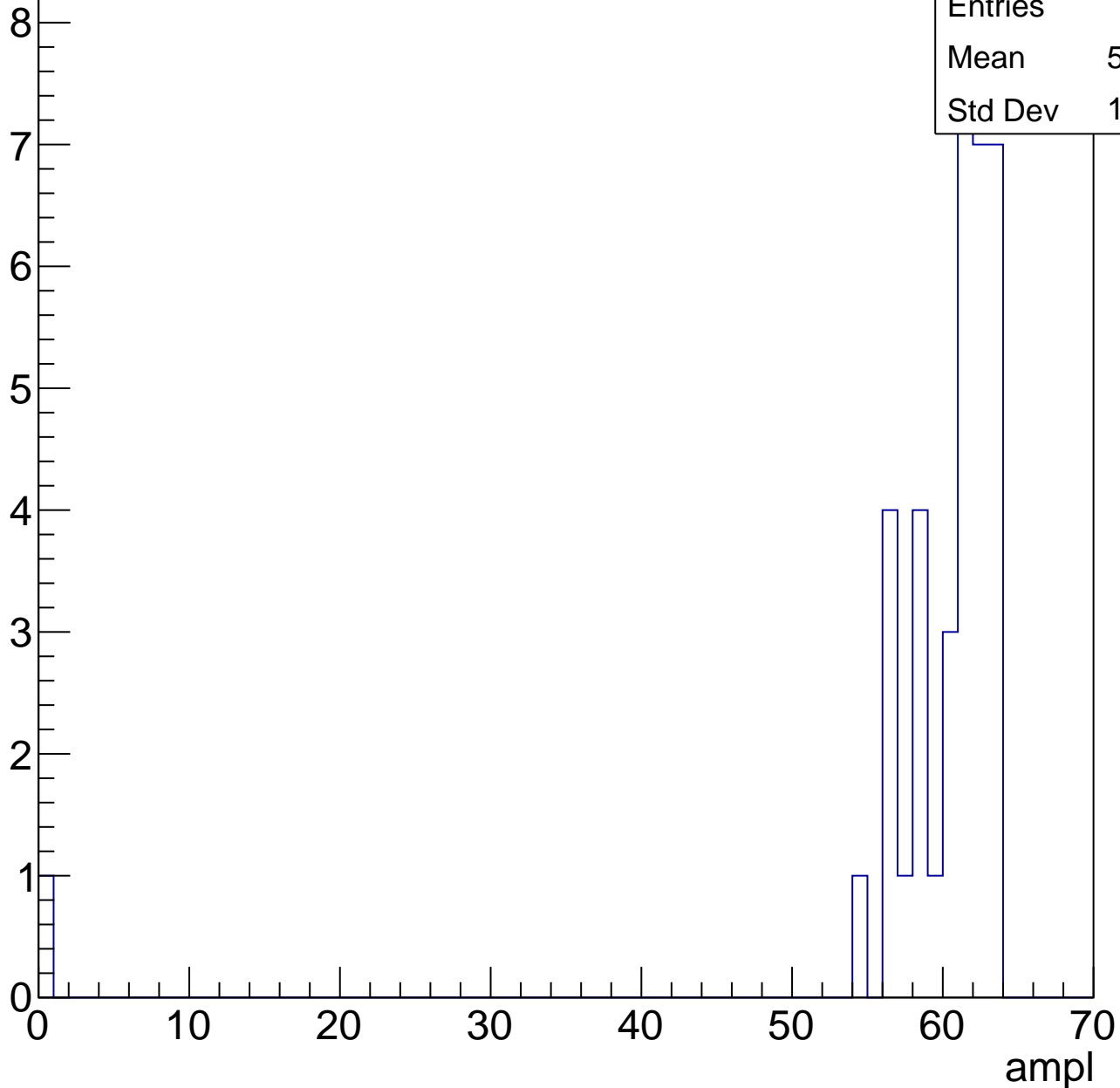


B1L101S, U15-ch93, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	58.62
Std Dev	10.07



B1L101S, U15-ch93, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch93, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L101S, U15-ch94, adc0

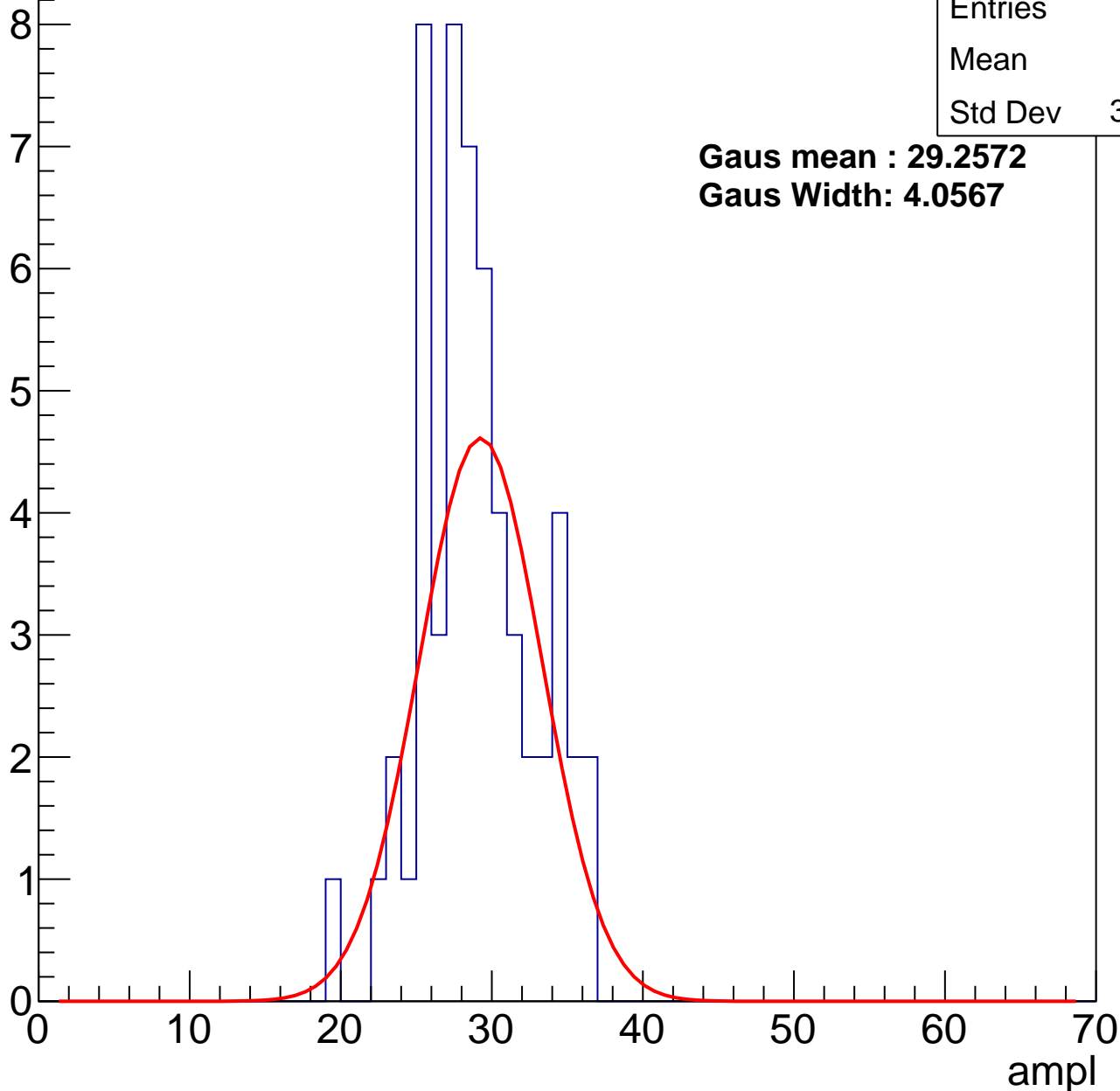
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.5
Std Dev	3.689

Gaus mean : 29.2572

Gaus Width: 4.0567



B1L101S, U15-ch94, adc1

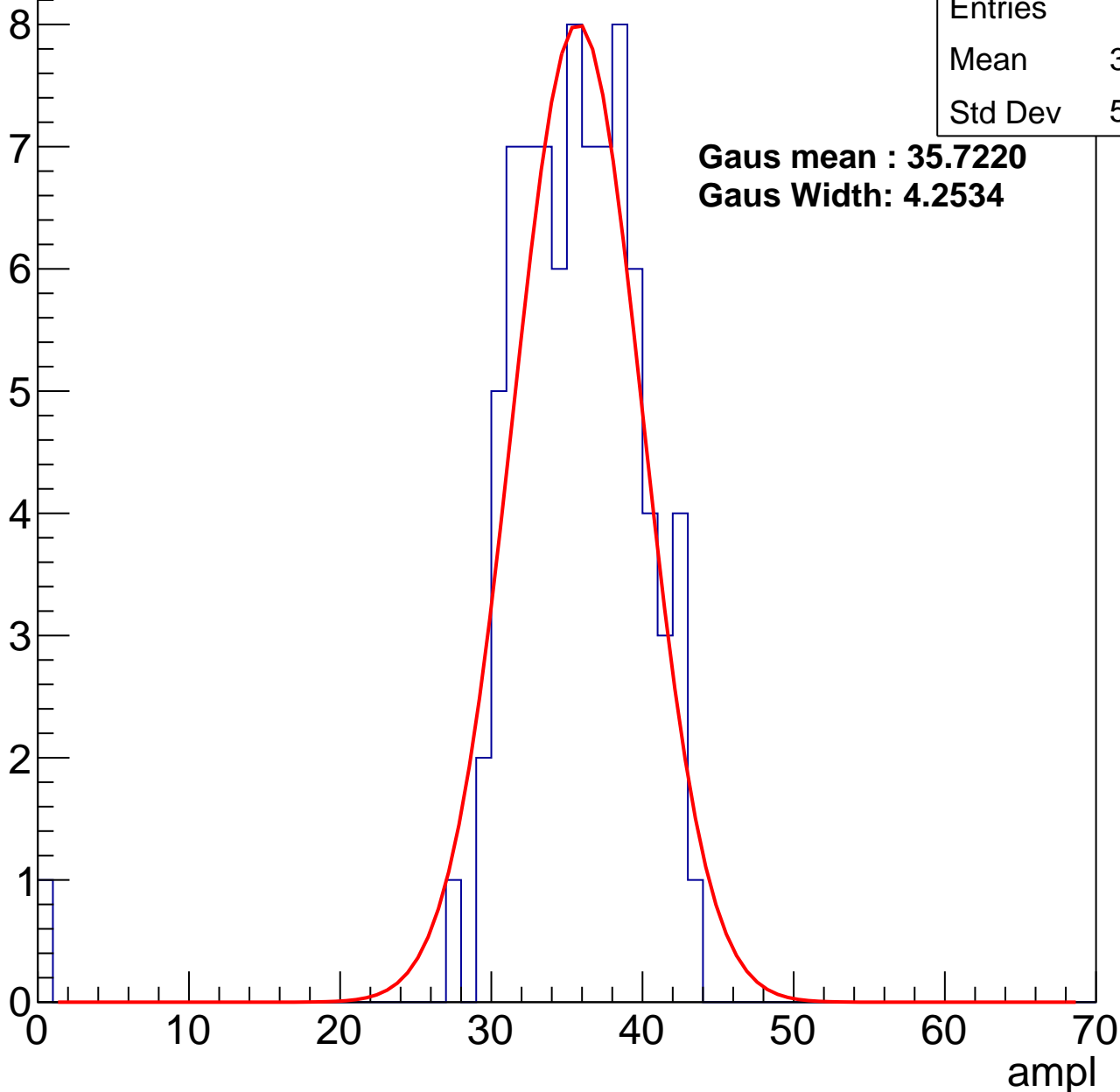
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	34.93
Std Dev	5.307

Gaus mean : 35.7220

Gaus Width: 4.2534



B1L101S, U15-ch94, adc2

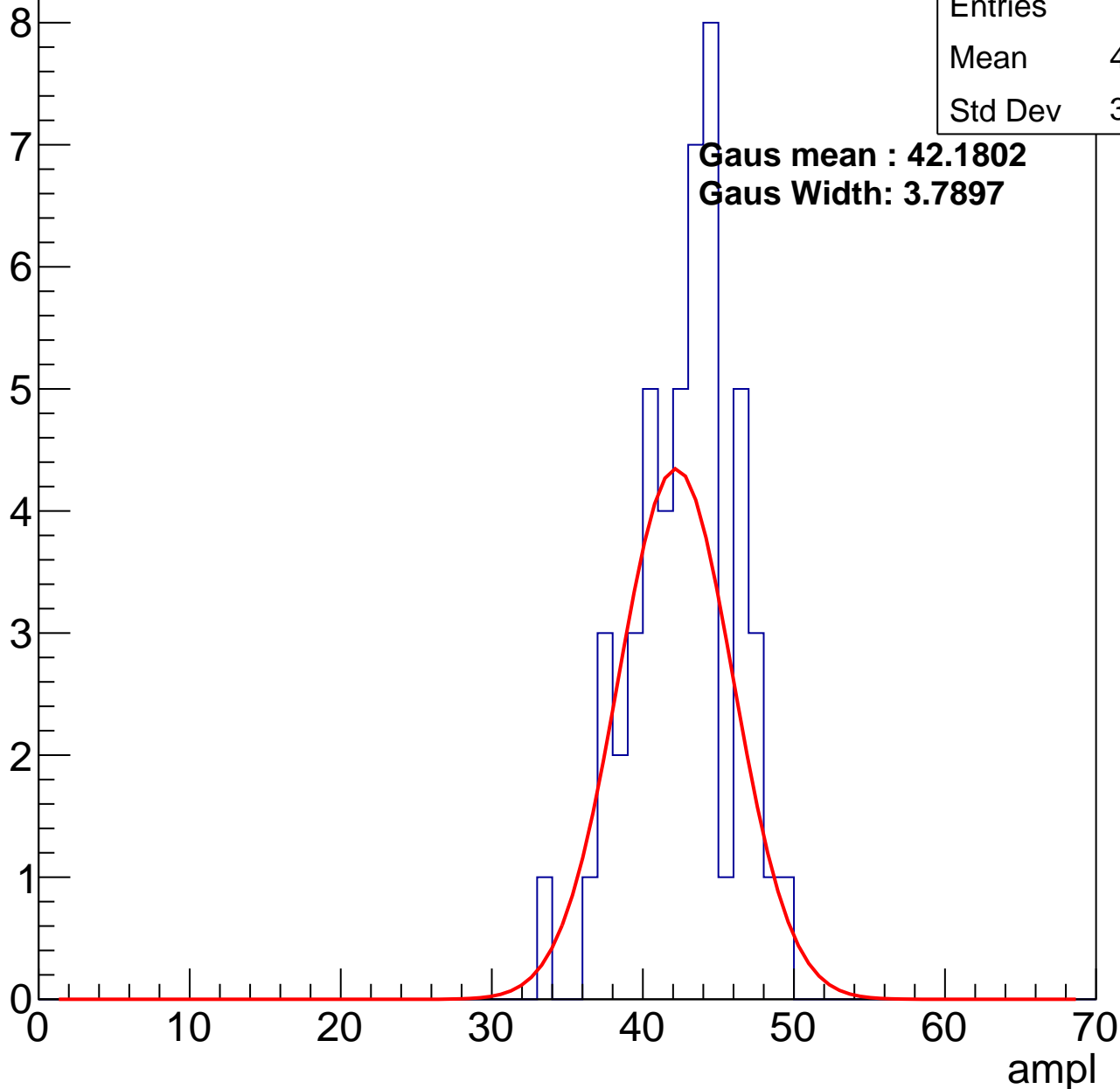
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	42.26
Std Dev	3.346

Gaus mean : 42.1802

Gaus Width: 3.7897

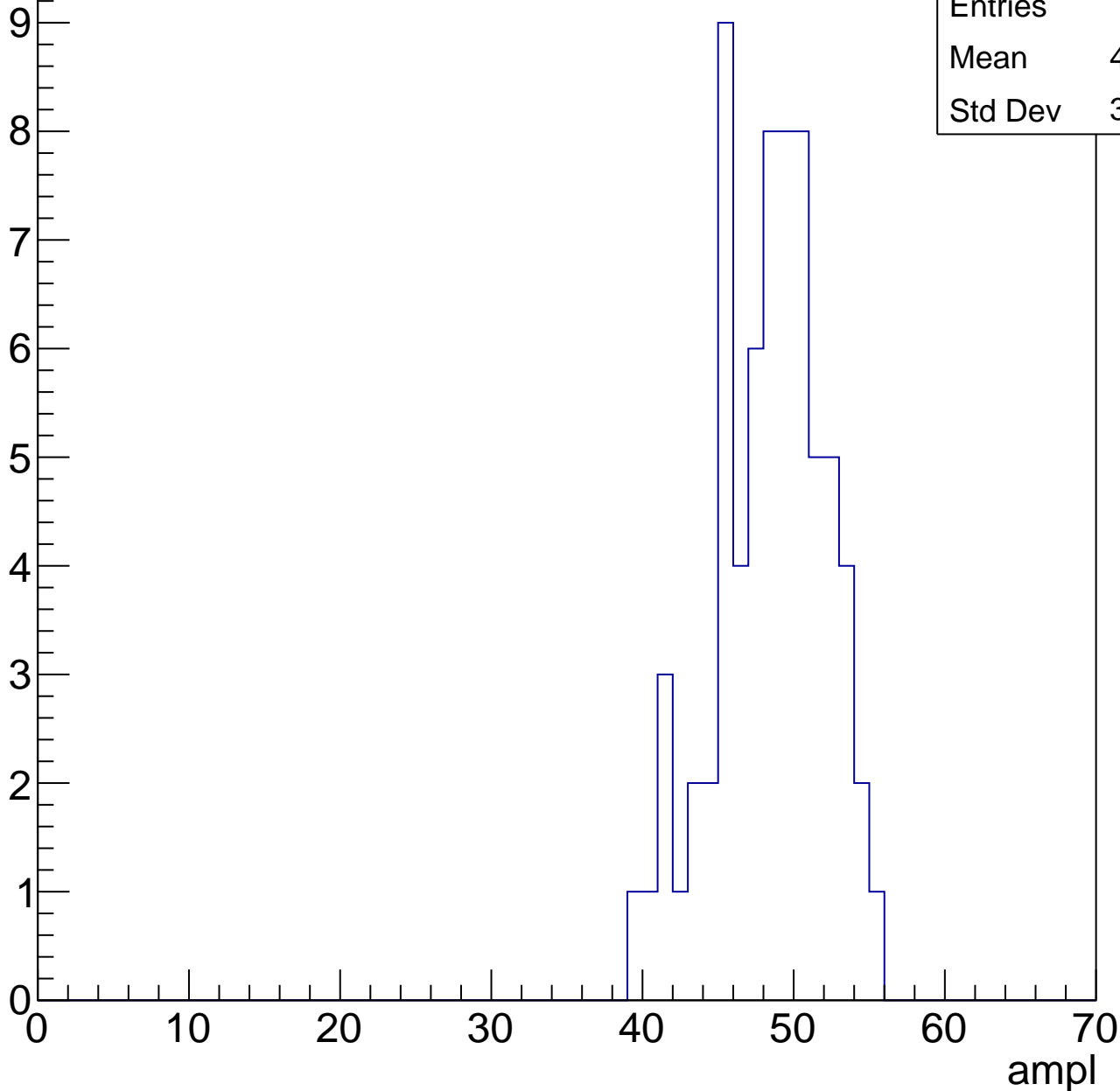


B1L101S, U15-ch94, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	47.93
Std Dev	3.583

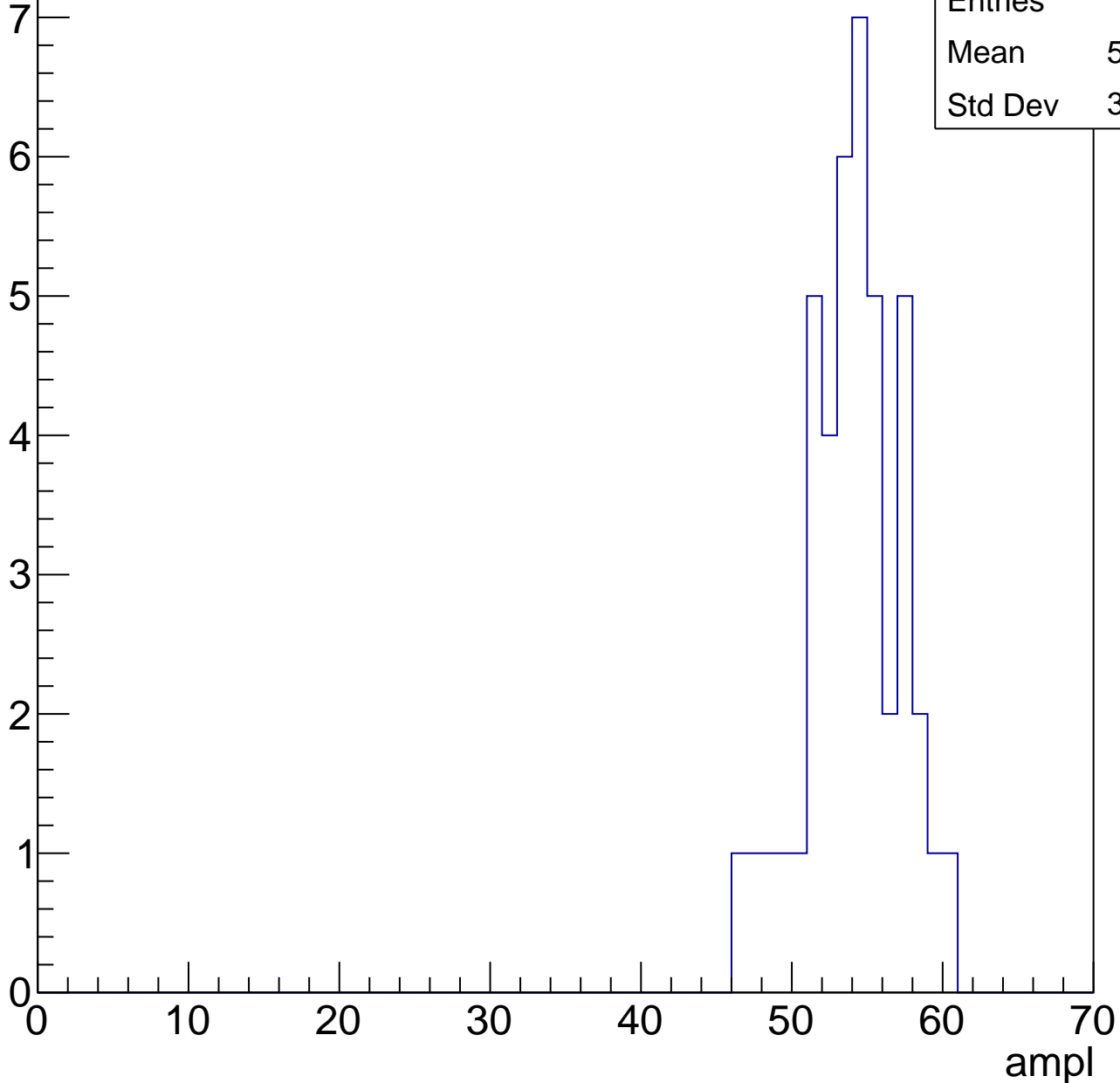


B1L101S, U15-ch94, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	53.63
Std Dev	3.058



B1L101S, U15-ch94, adc5

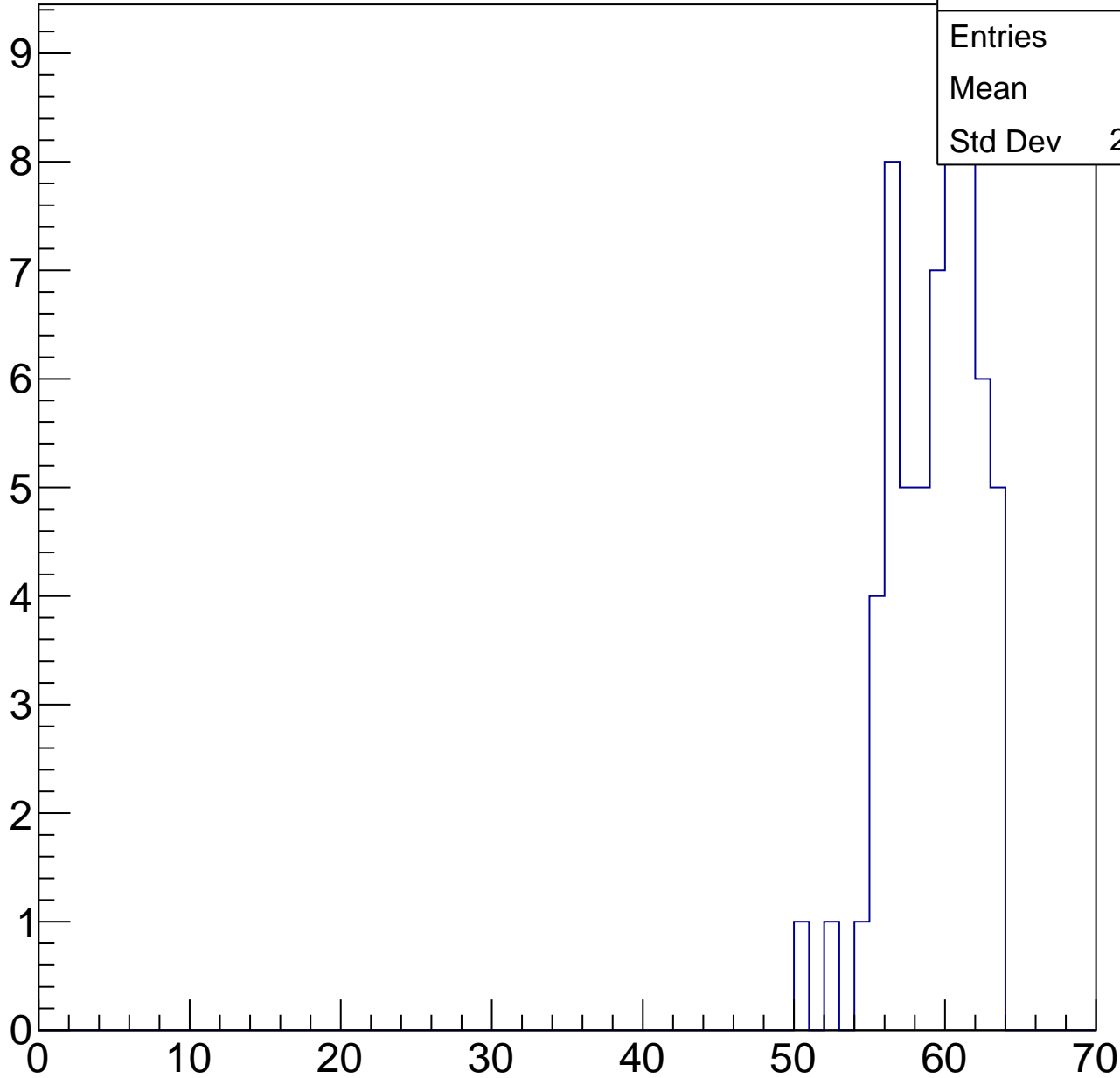
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	60
Mean	58.8
Std Dev	2.862

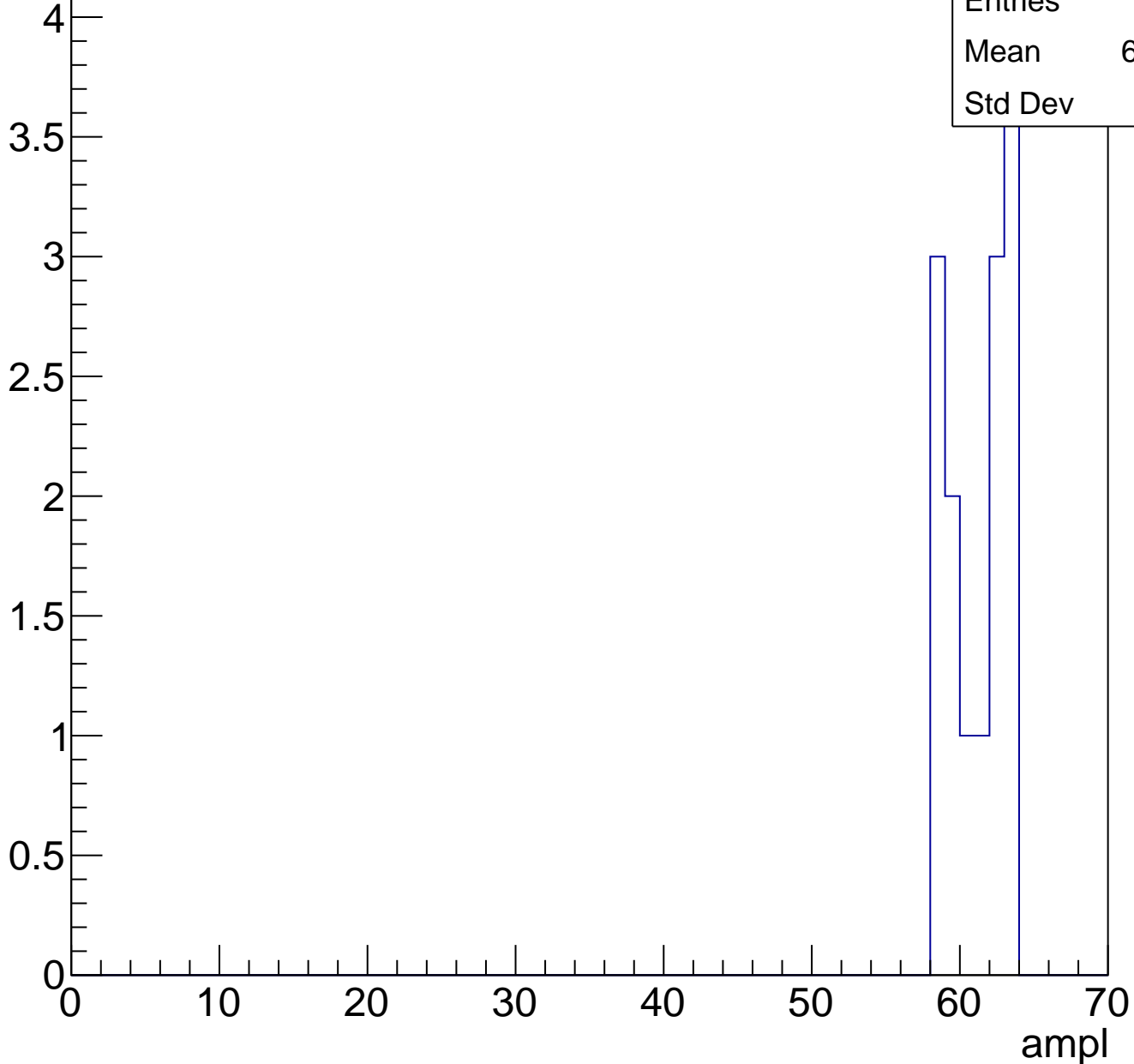
ampl



B1L101S, U15-ch94, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	14
Mean	60.79
Std Dev	1.97

B1L101S, U15-ch94, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch95, adc0

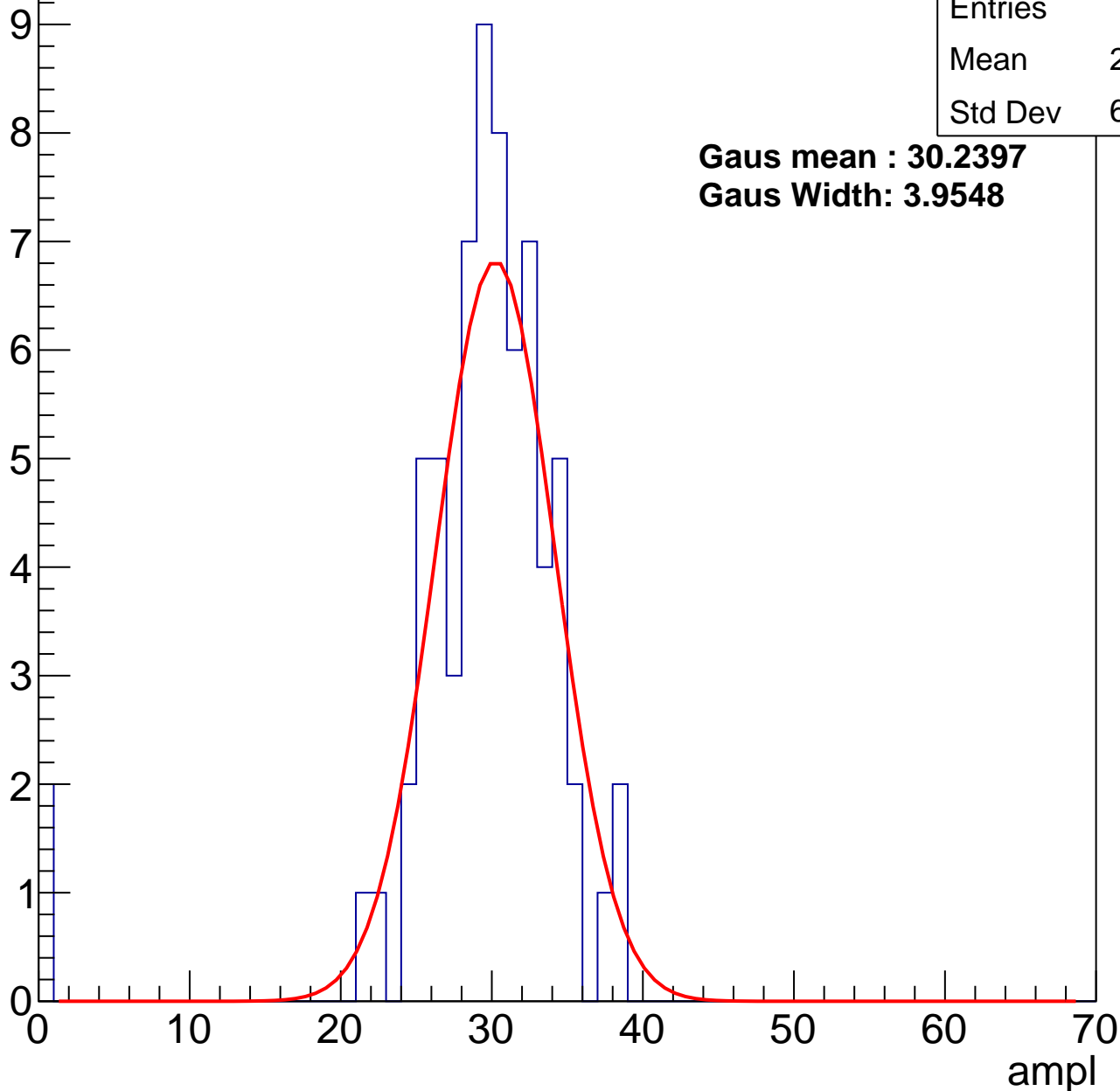
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	28.84
Std Dev	6.058

Gaus mean : 30.2397

Gaus Width: 3.9548



B1L101S, U15-ch95, adc1

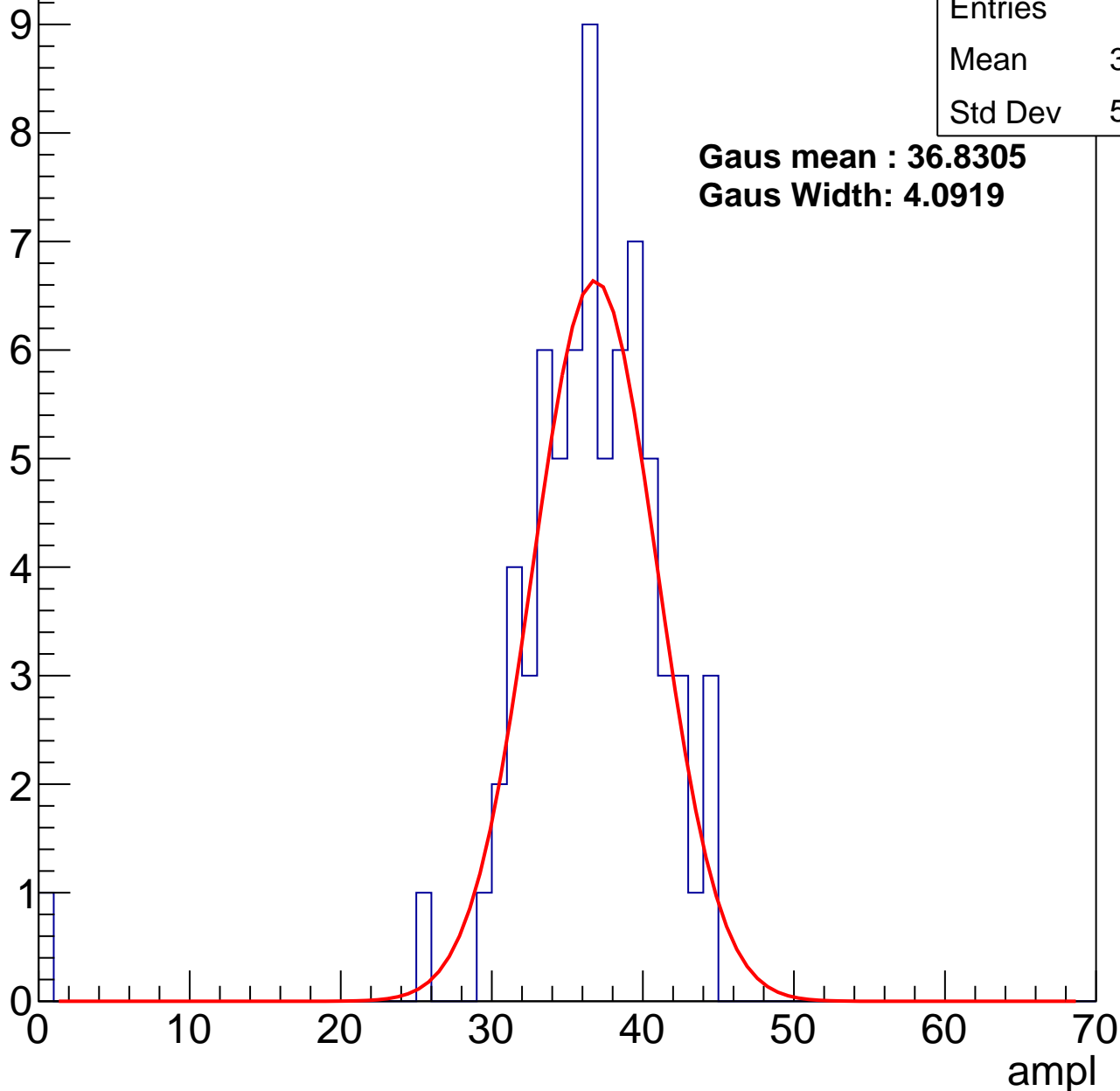
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	35.86
Std Dev	5.766

Gaus mean : 36.8305

Gaus Width: 4.0919



B1L101S, U15-ch95, adc2

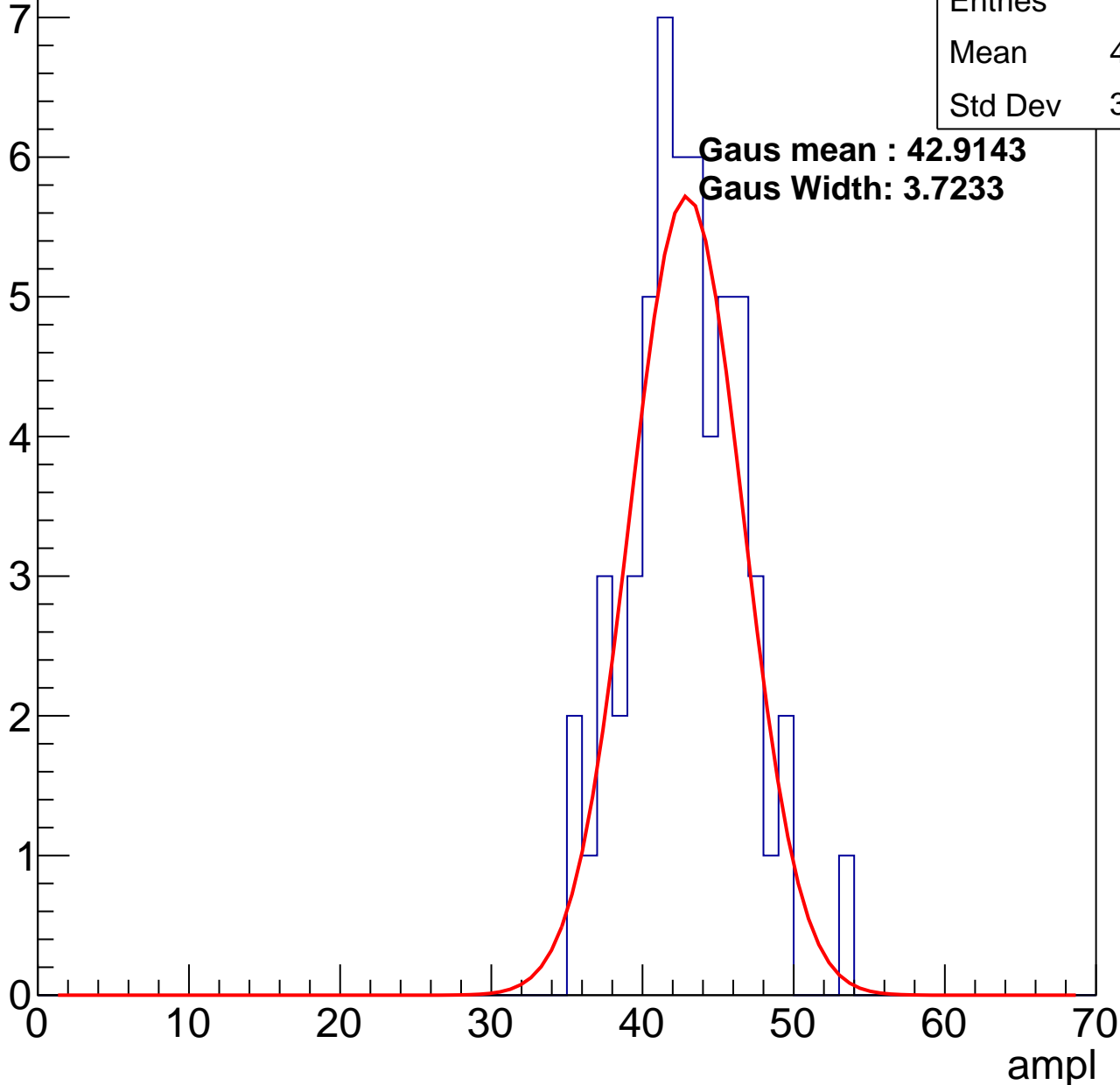
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	42.46
Std Dev	3.684

Gaus mean : 42.9143

Gaus Width: 3.7233

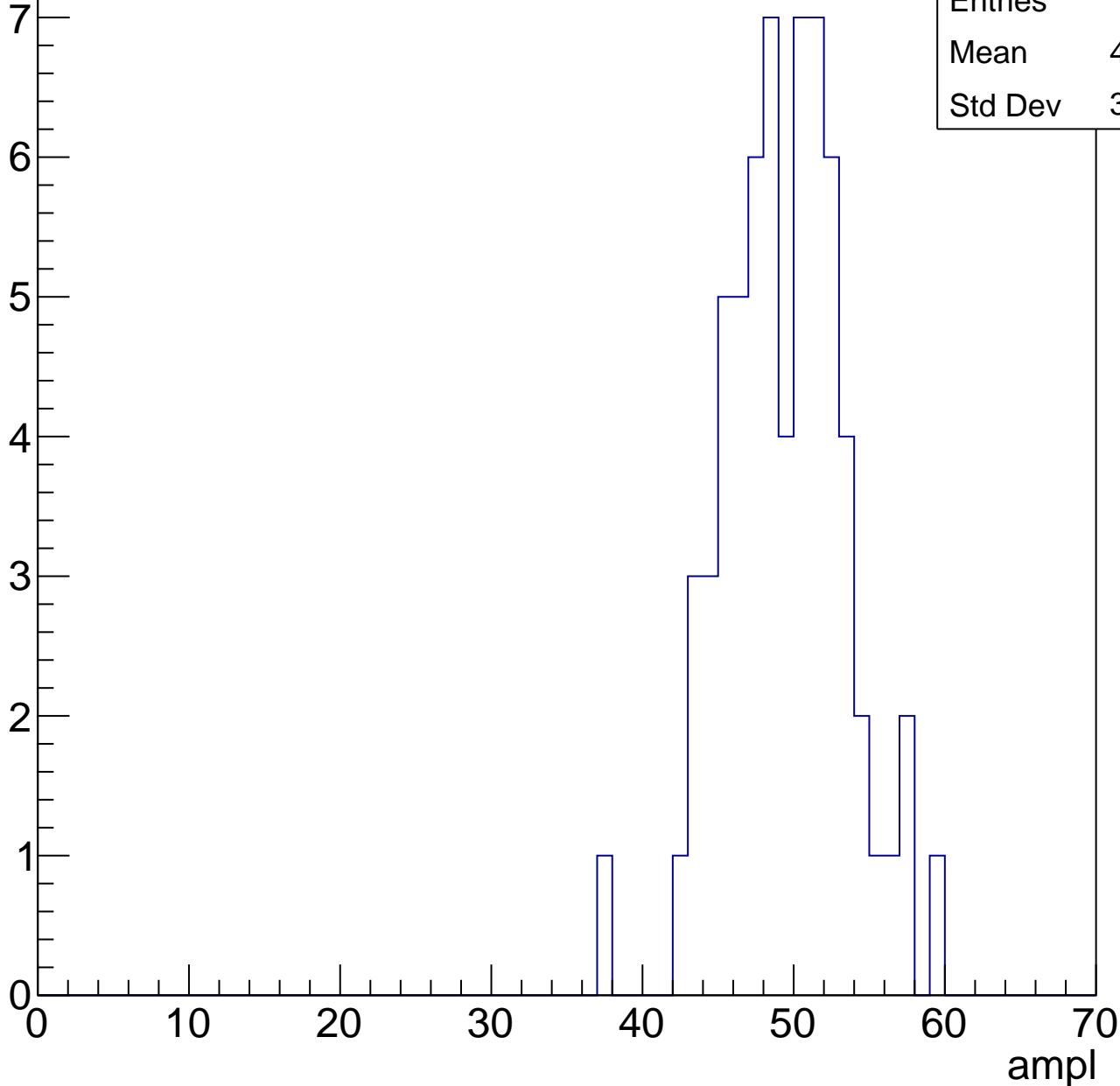


B1L101S, U15-ch95, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	48.97
Std Dev	3.985

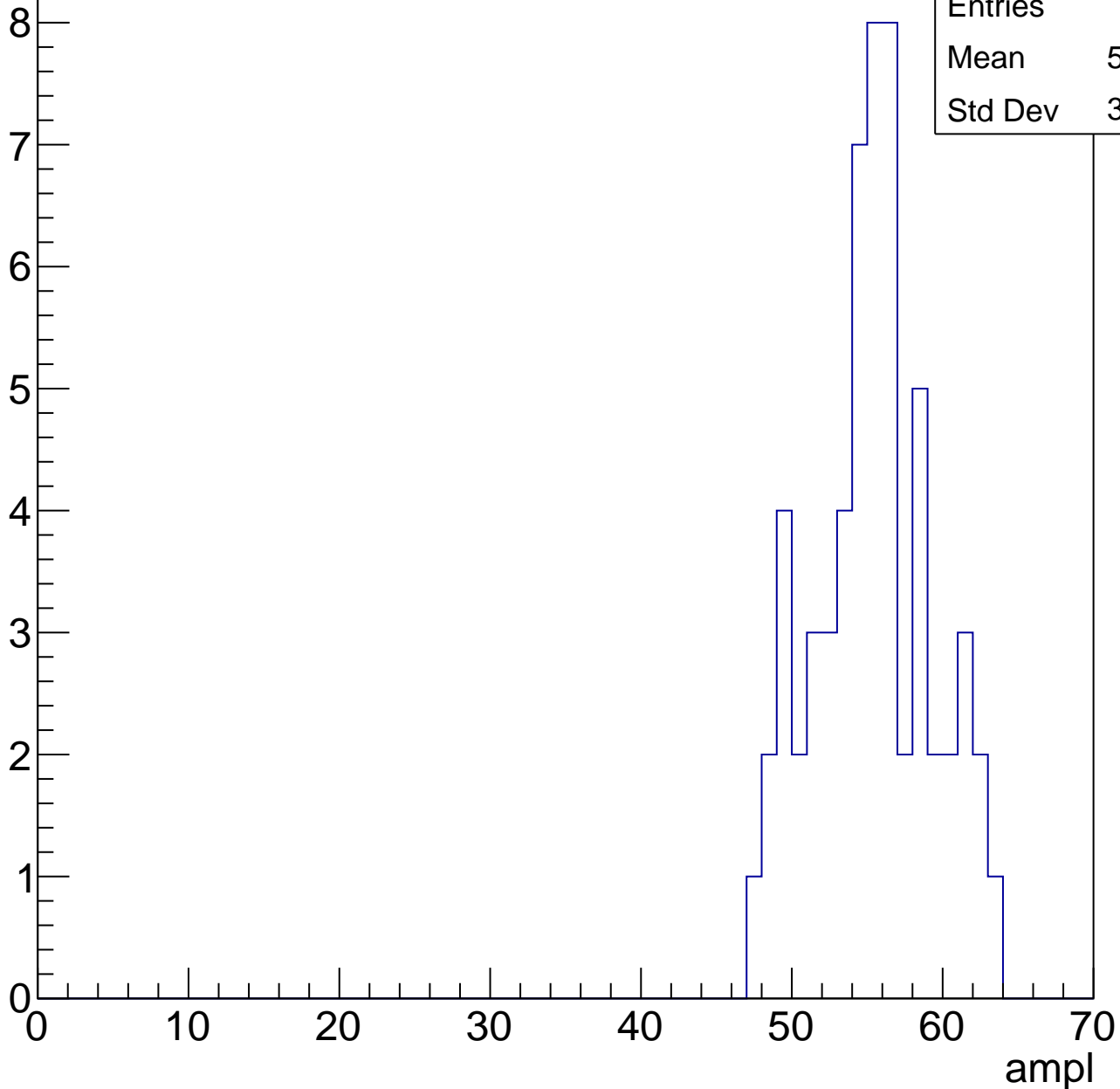


B1L101S, U15-ch95, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	54.88
Std Dev	3.832

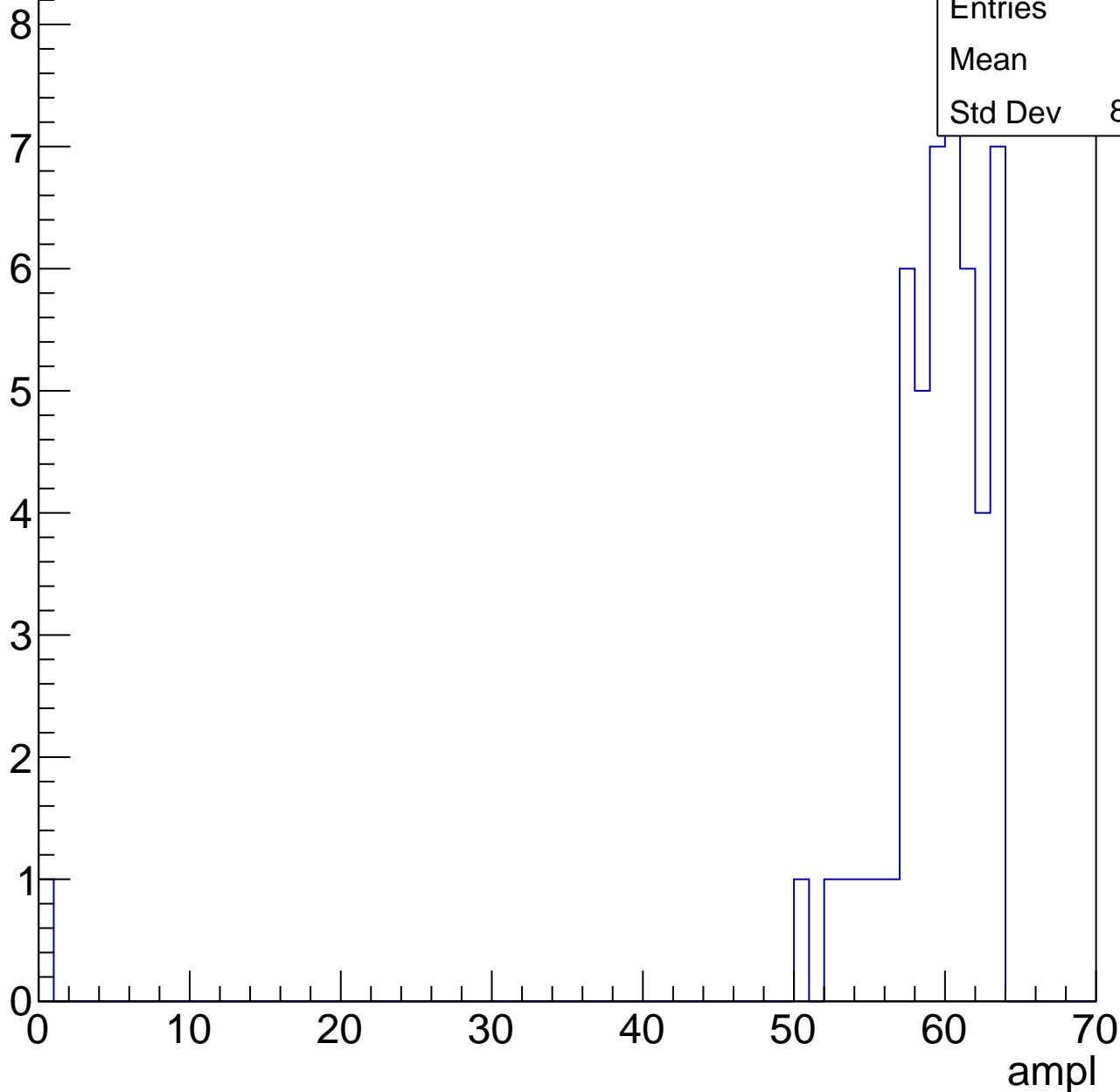


B1L101S, U15-ch95, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

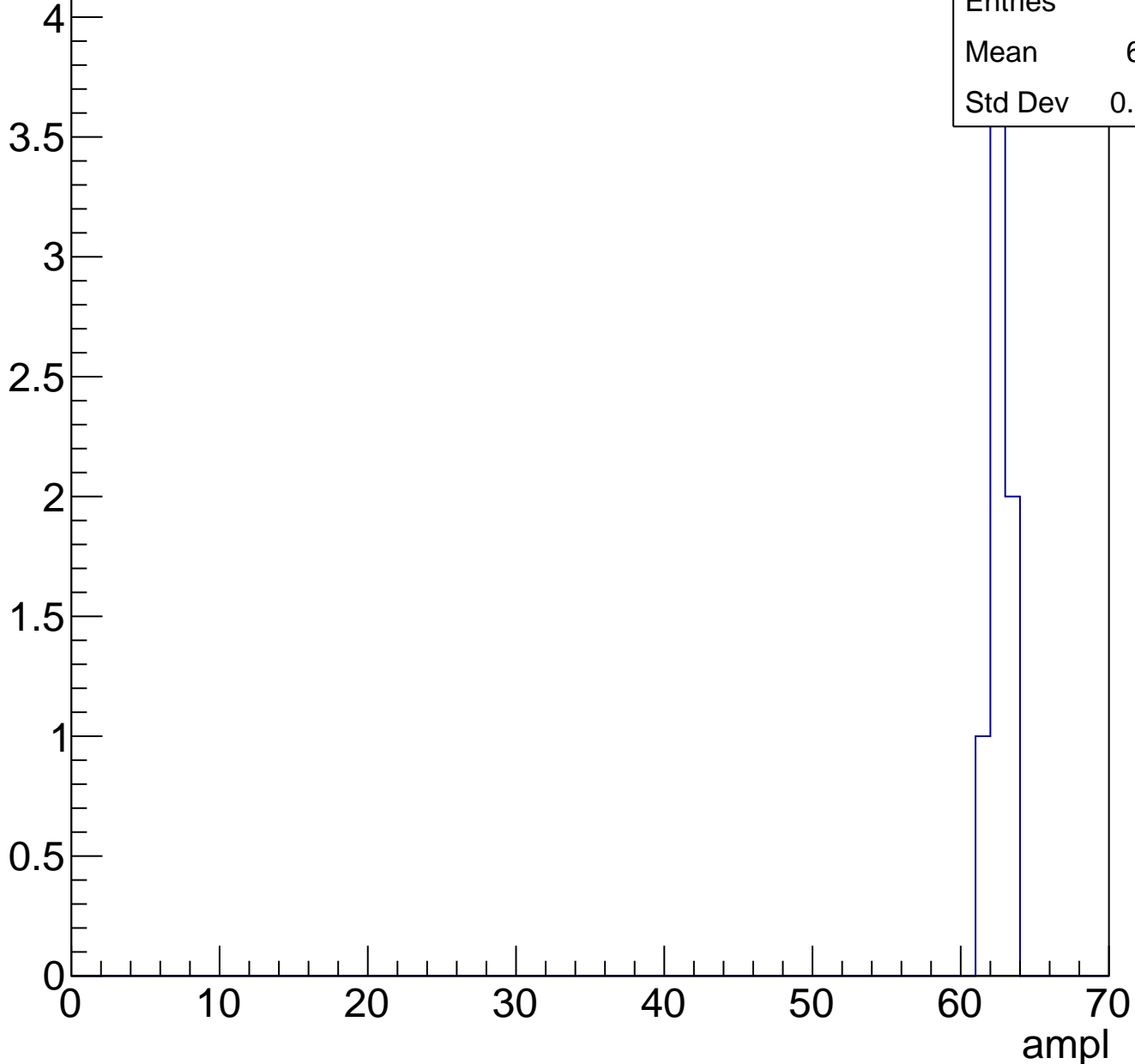
Entries	50
Mean	58
Std Dev	8.782



B1L101S, U15-ch95, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch95, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch96, adc0

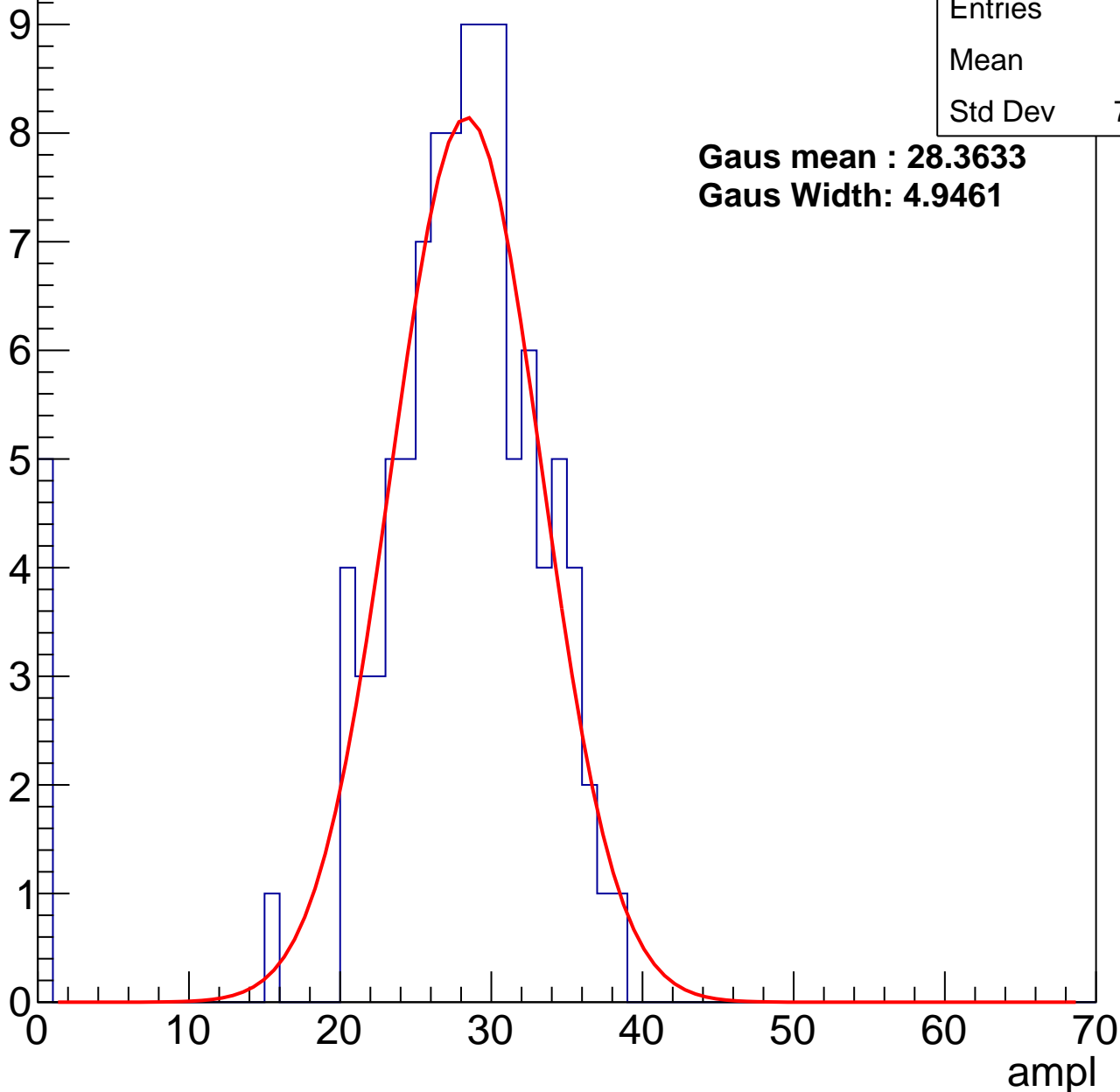
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	104
Mean	26.7
Std Dev	7.406

Gaus mean : 28.3633

Gaus Width: 4.9461



B1L101S, U15-ch96, adc1

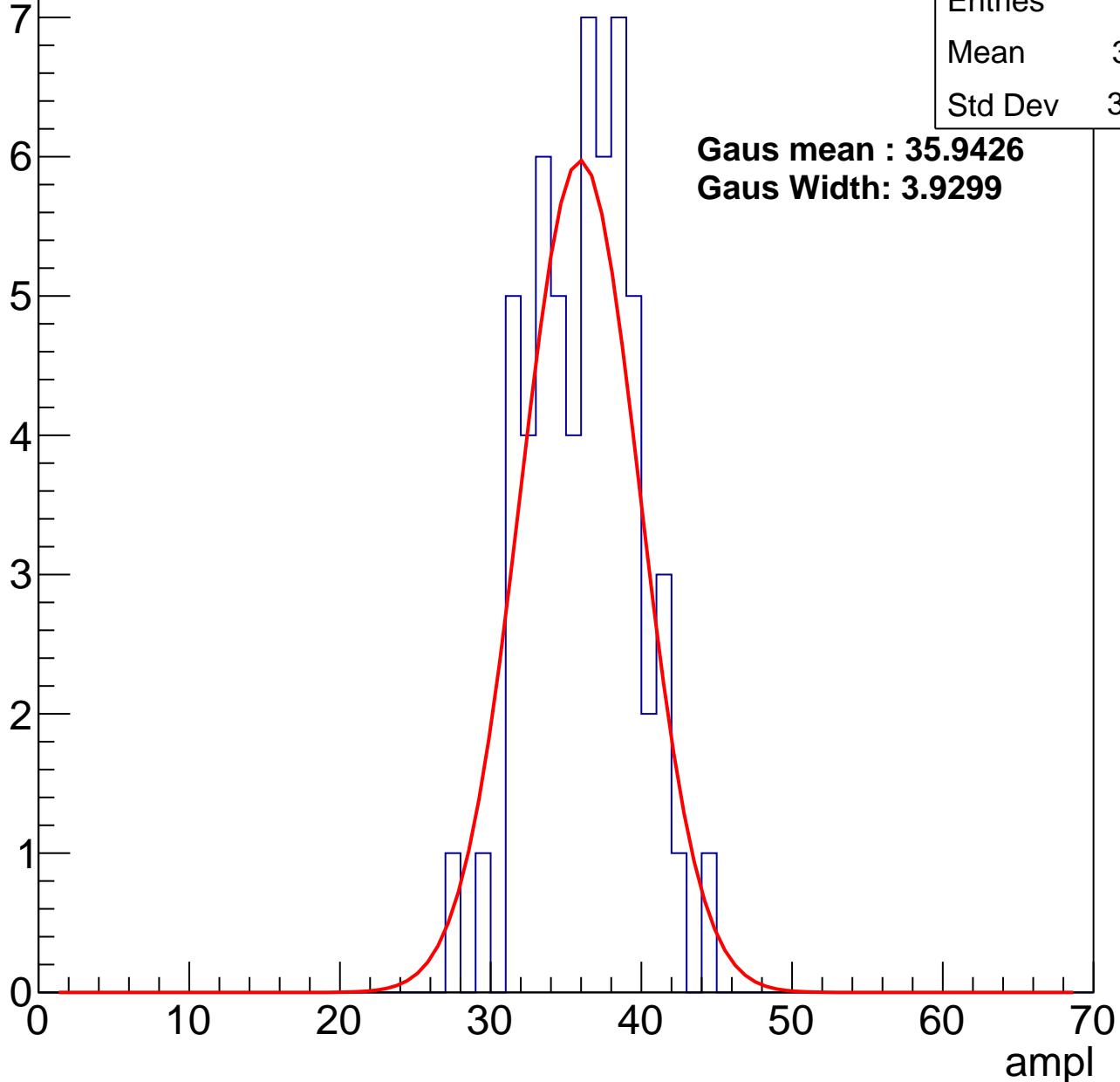
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	35.71
Std Dev	3.419

Gaus mean : 35.9426

Gaus Width: 3.9299



B1L101S, U15-ch96, adc2

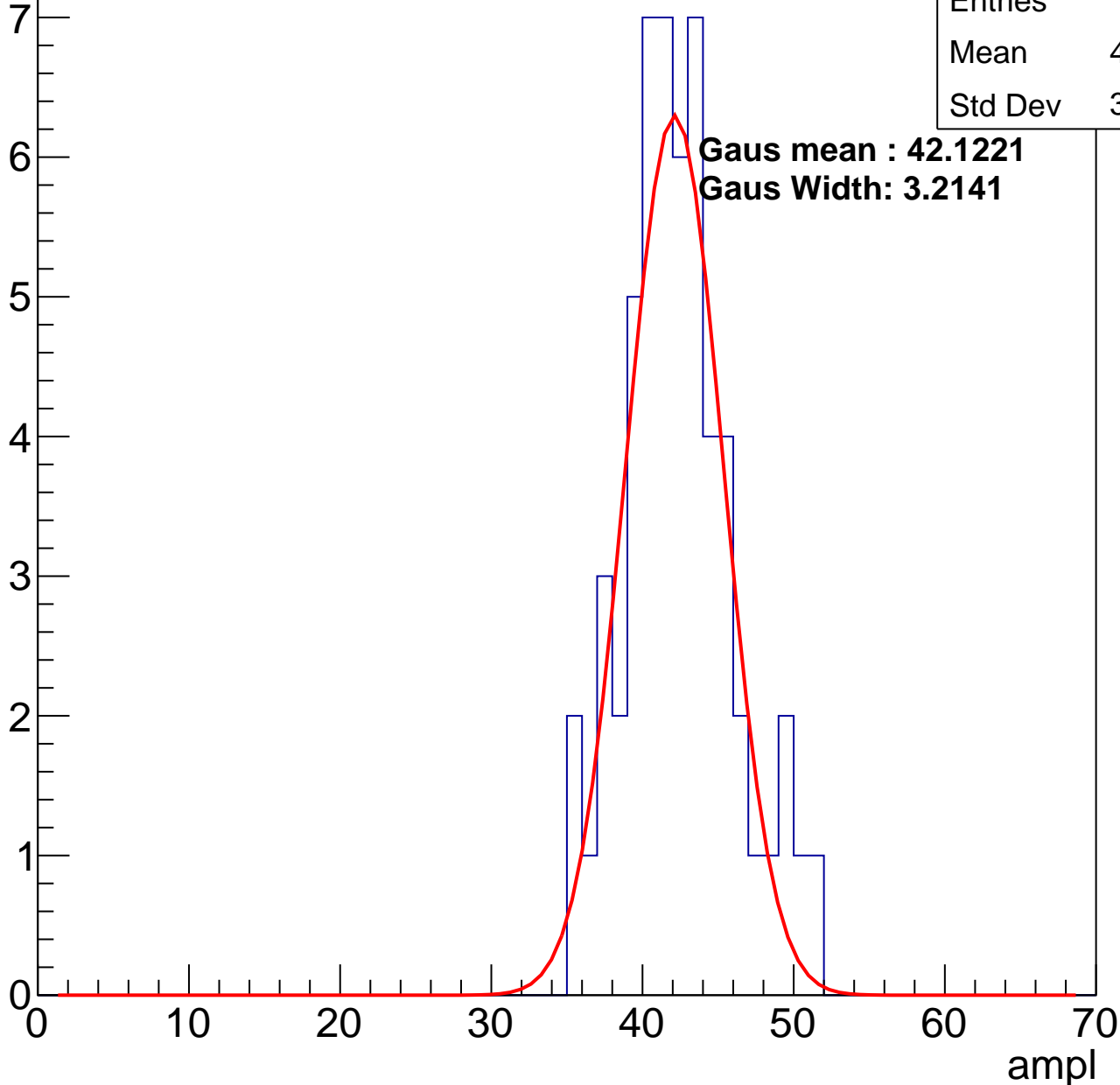
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.96
Std Dev	3.576

Gaus mean : 42.1221

Gaus Width: 3.2141

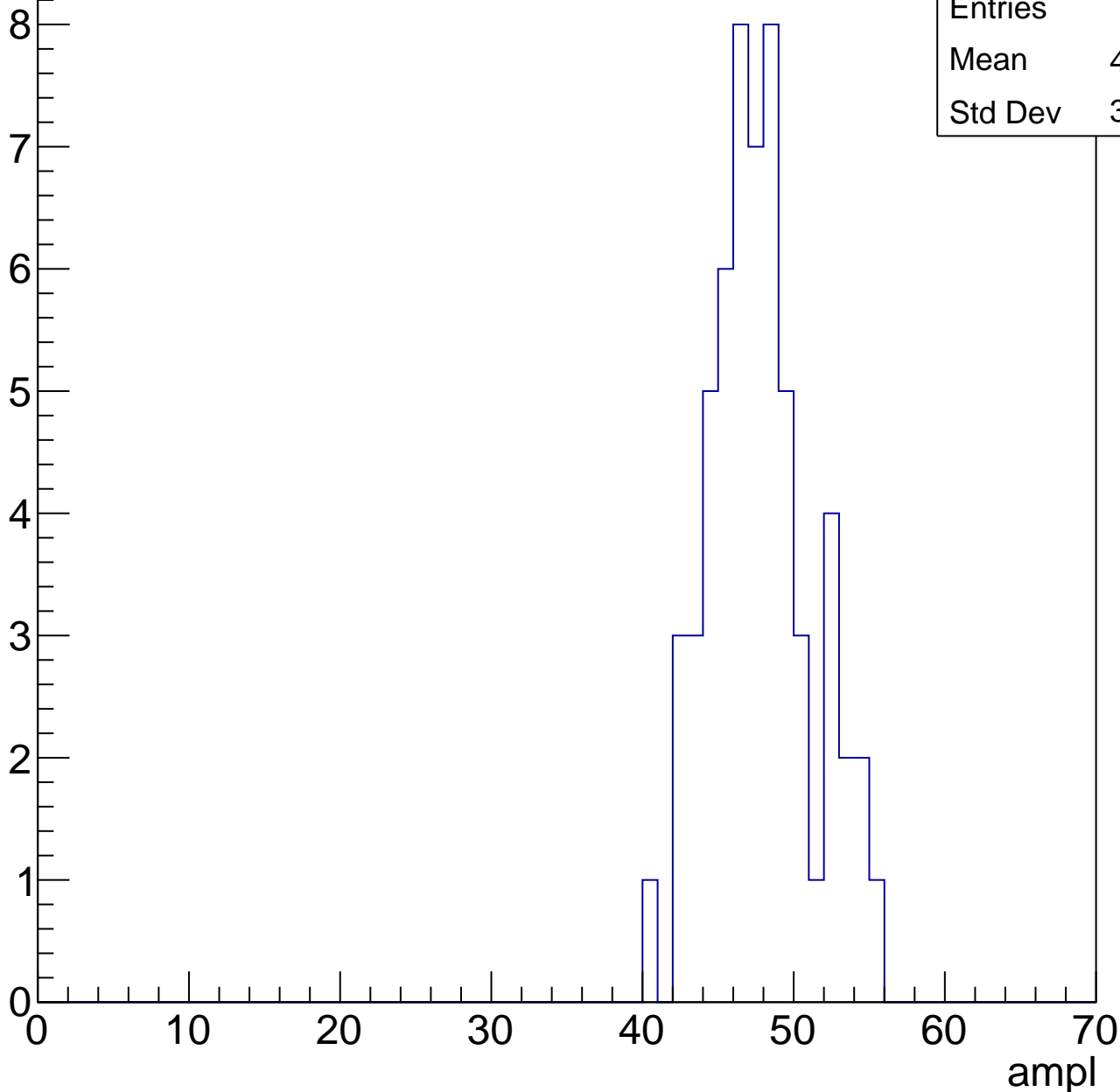


B1L101S, U15-ch96, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	47.27
Std Dev	3.318

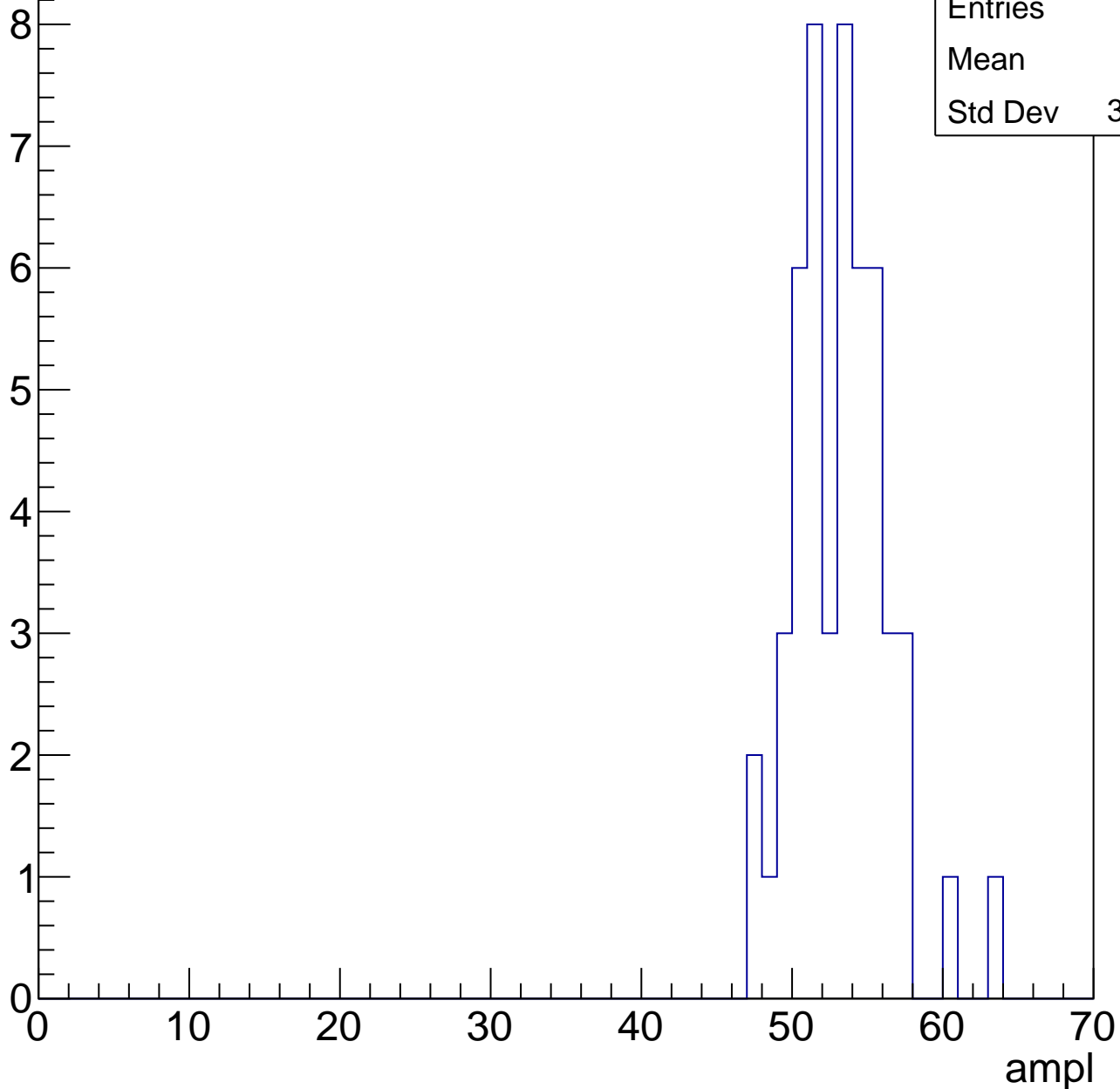


B1L101S, U15-ch96, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

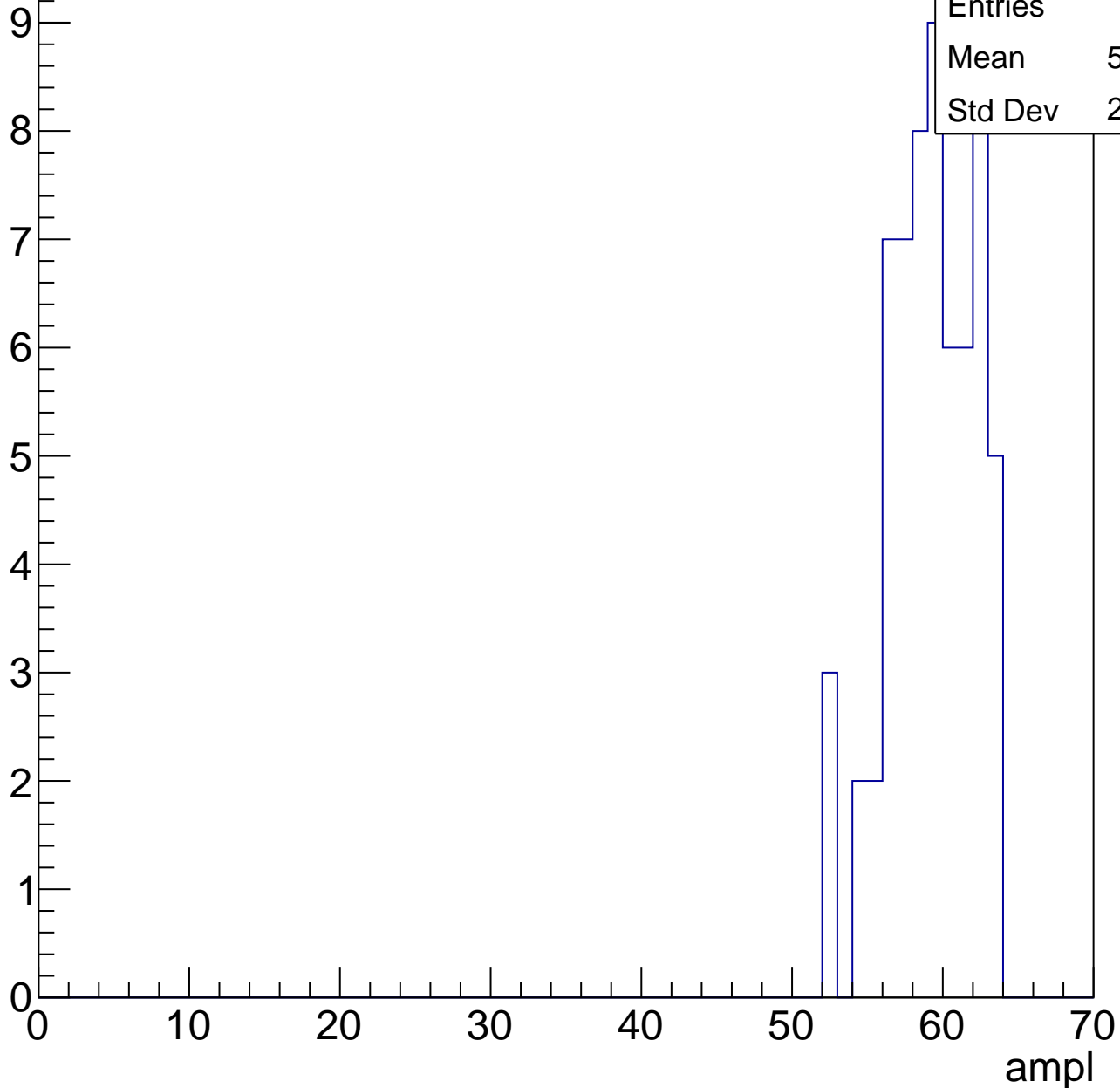
Entries	51
Mean	52.8
Std Dev	3.087



B1L101S, U15-ch96, adc5

calib_packv5_042523_0143.root, FC#0, port D2

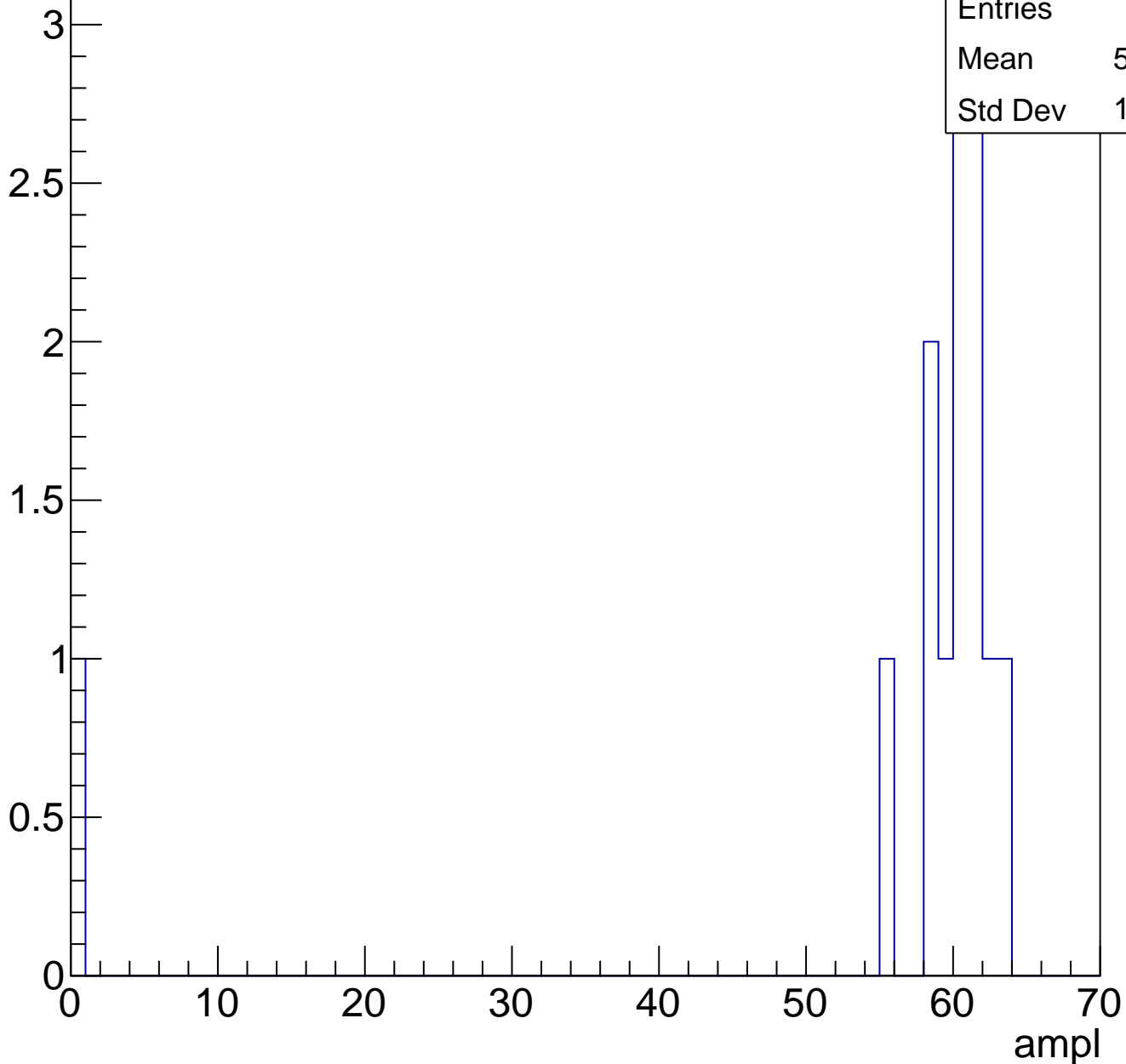
Entry



B1L101S, U15-ch96, adc6

calib_packv5_042523_0143.root, FC#0, port D2

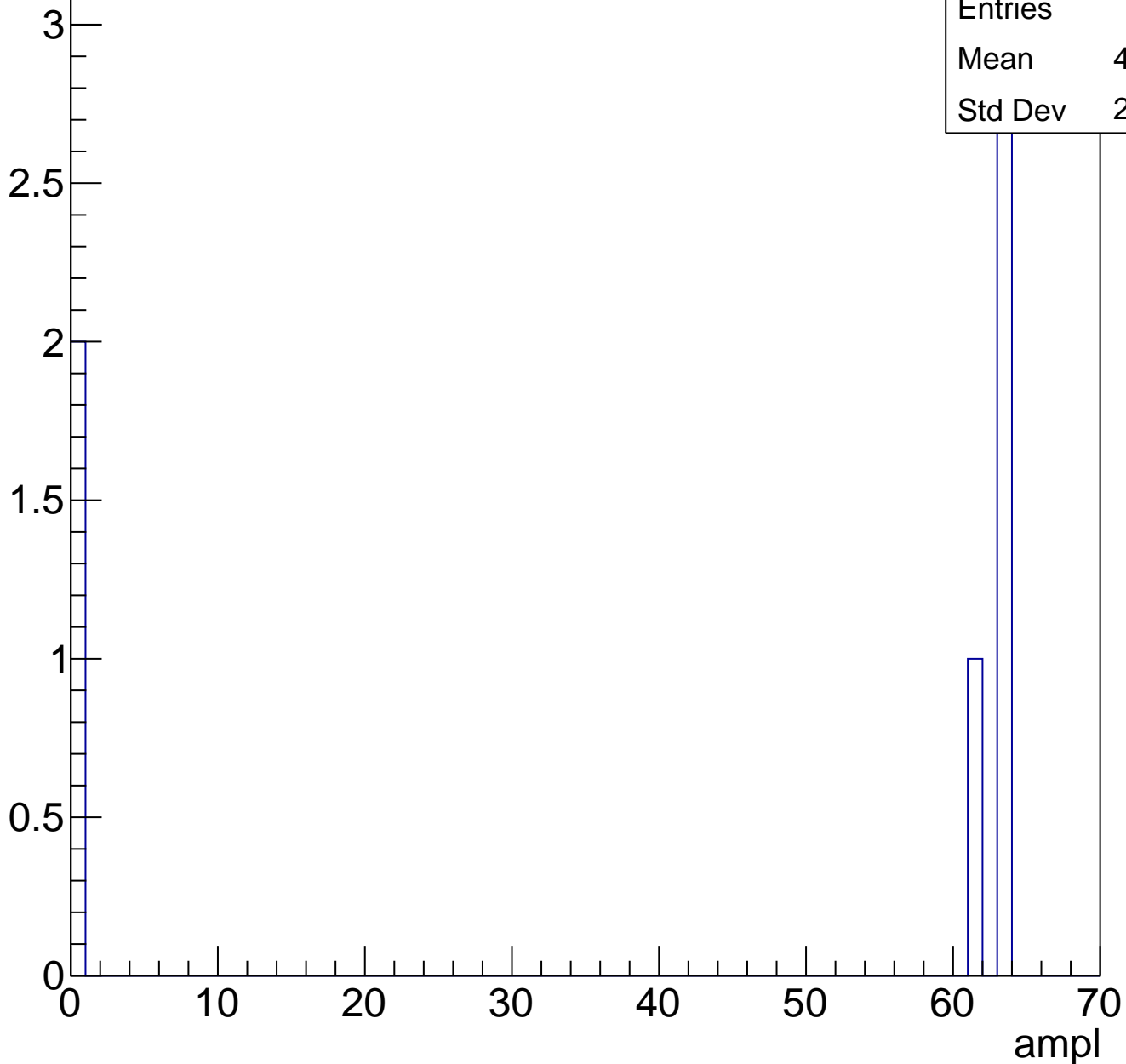
Entry



B1L101S, U15-ch96, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch97, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	76
Mean	28.68
Std Dev	3.968

Gaus mean : 29.2684

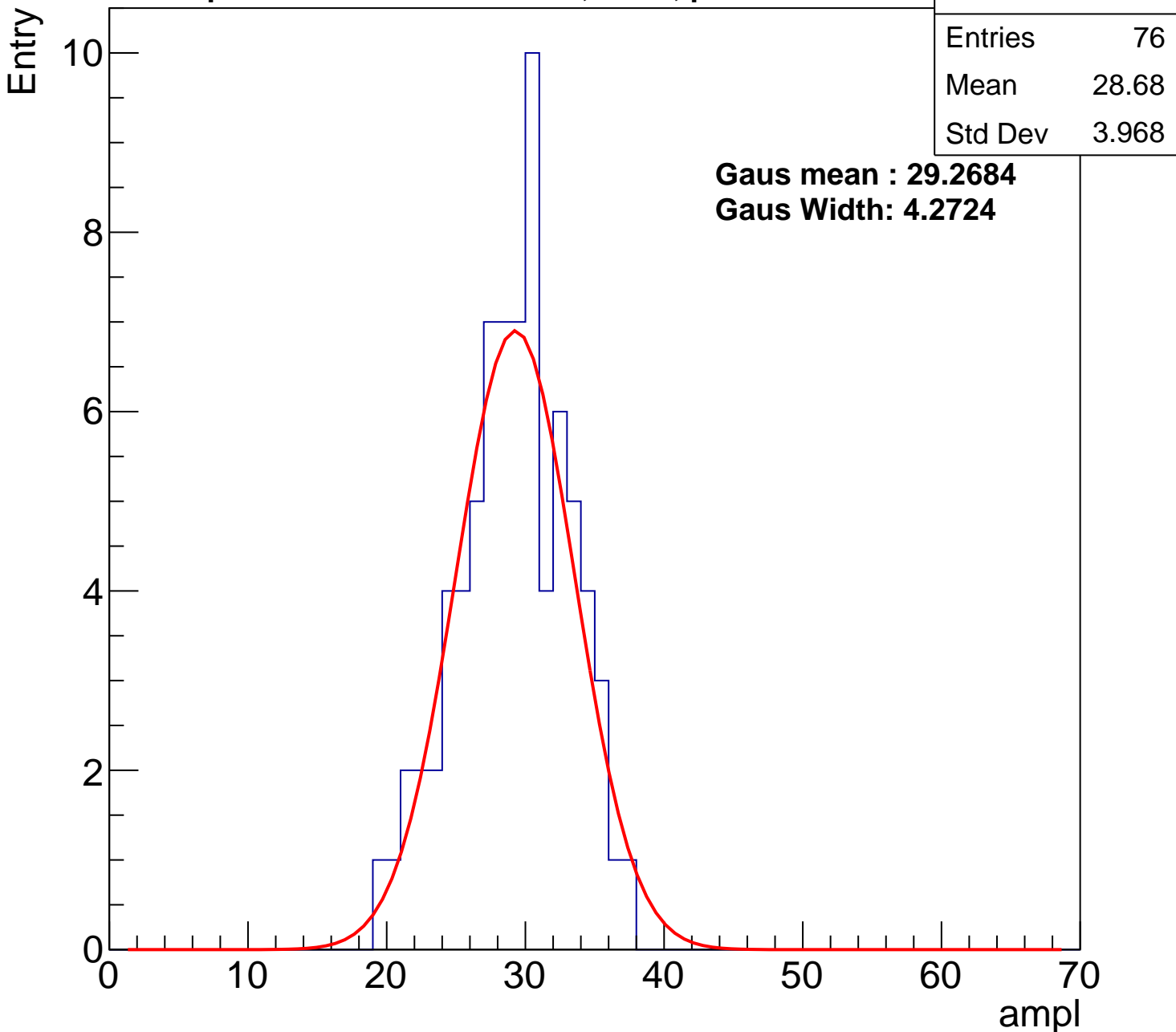
Gaus Width: 4.2724

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch97, adc1

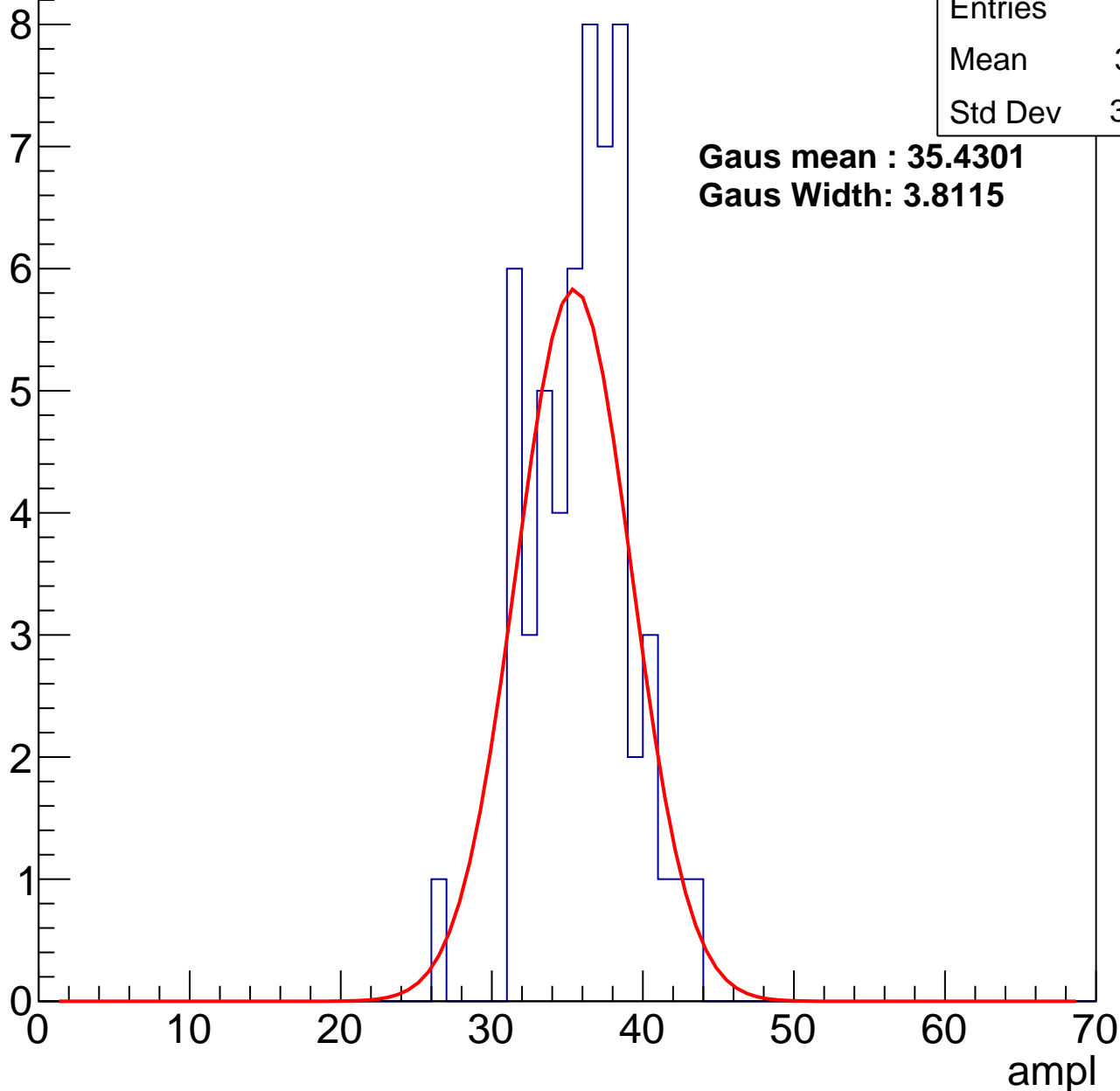
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	35.61
Std Dev	3.194

Gaus mean : 35.4301

Gaus Width: 3.8115



B1L101S, U15-ch97, adc2

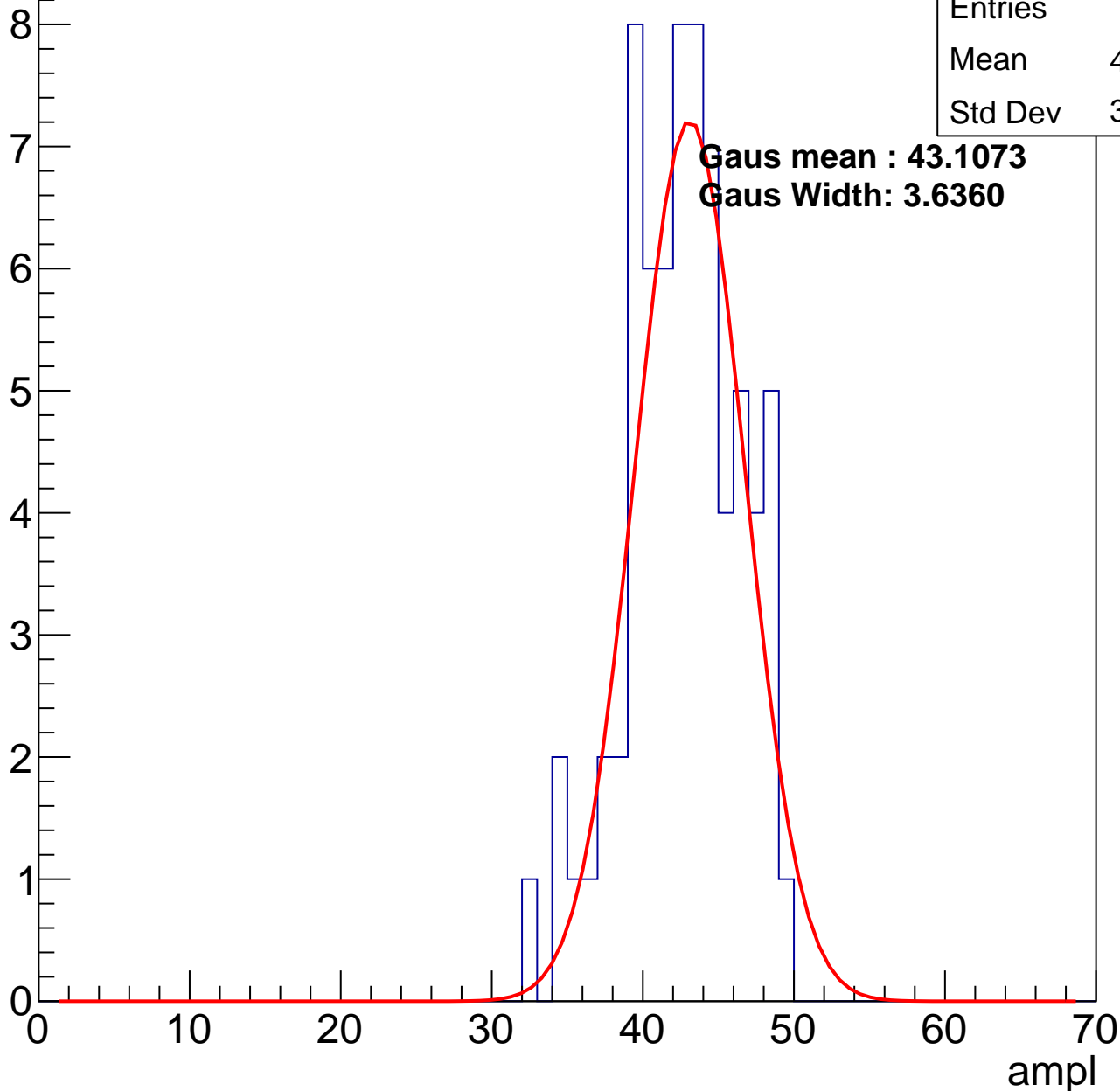
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	71
Mean	42.17
Std Dev	3.708

Gaus mean : 43.1073

Gaus Width: 3.6360

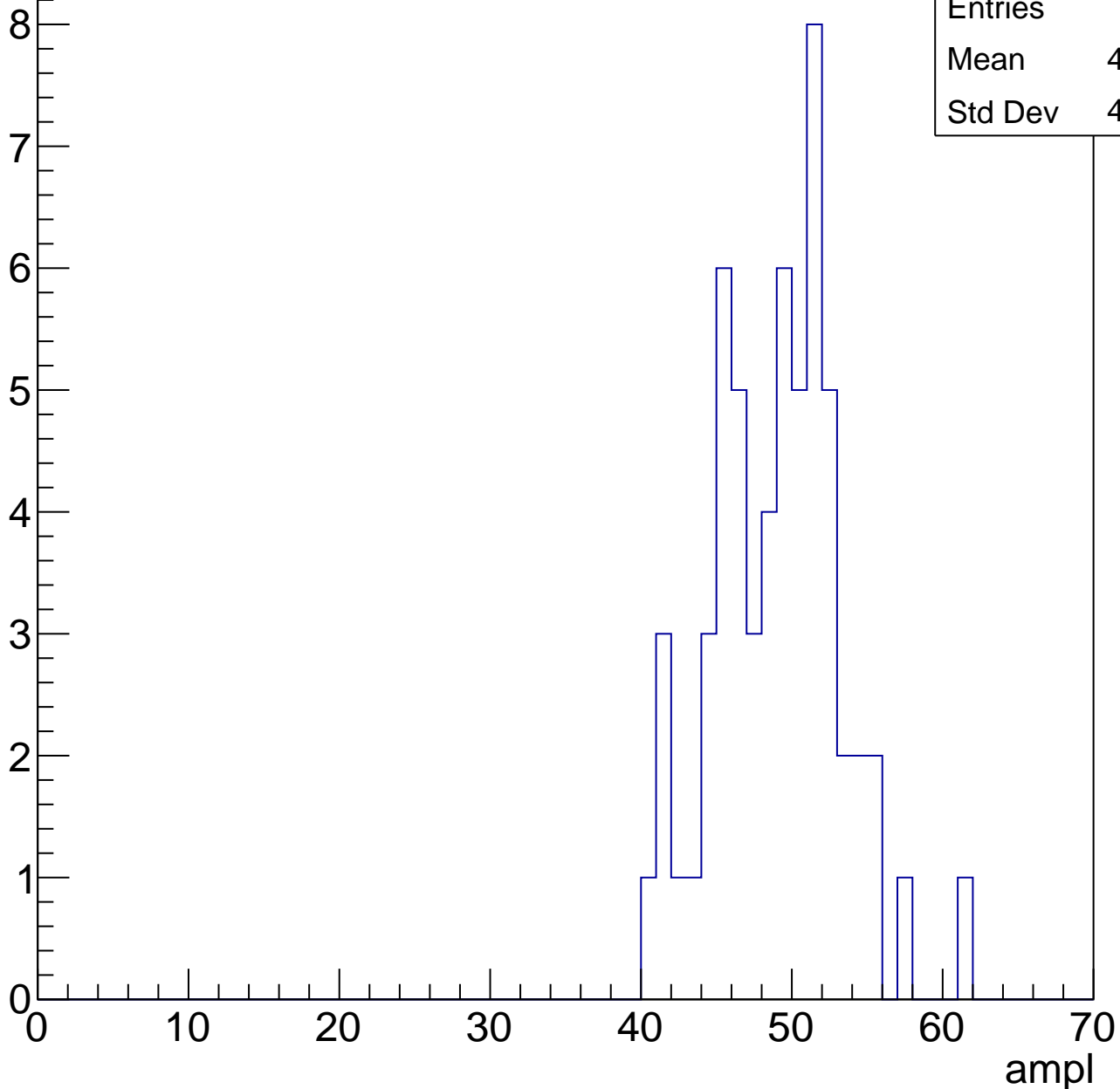


B1L101S, U15-ch97, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	48.59
Std Dev	4.167

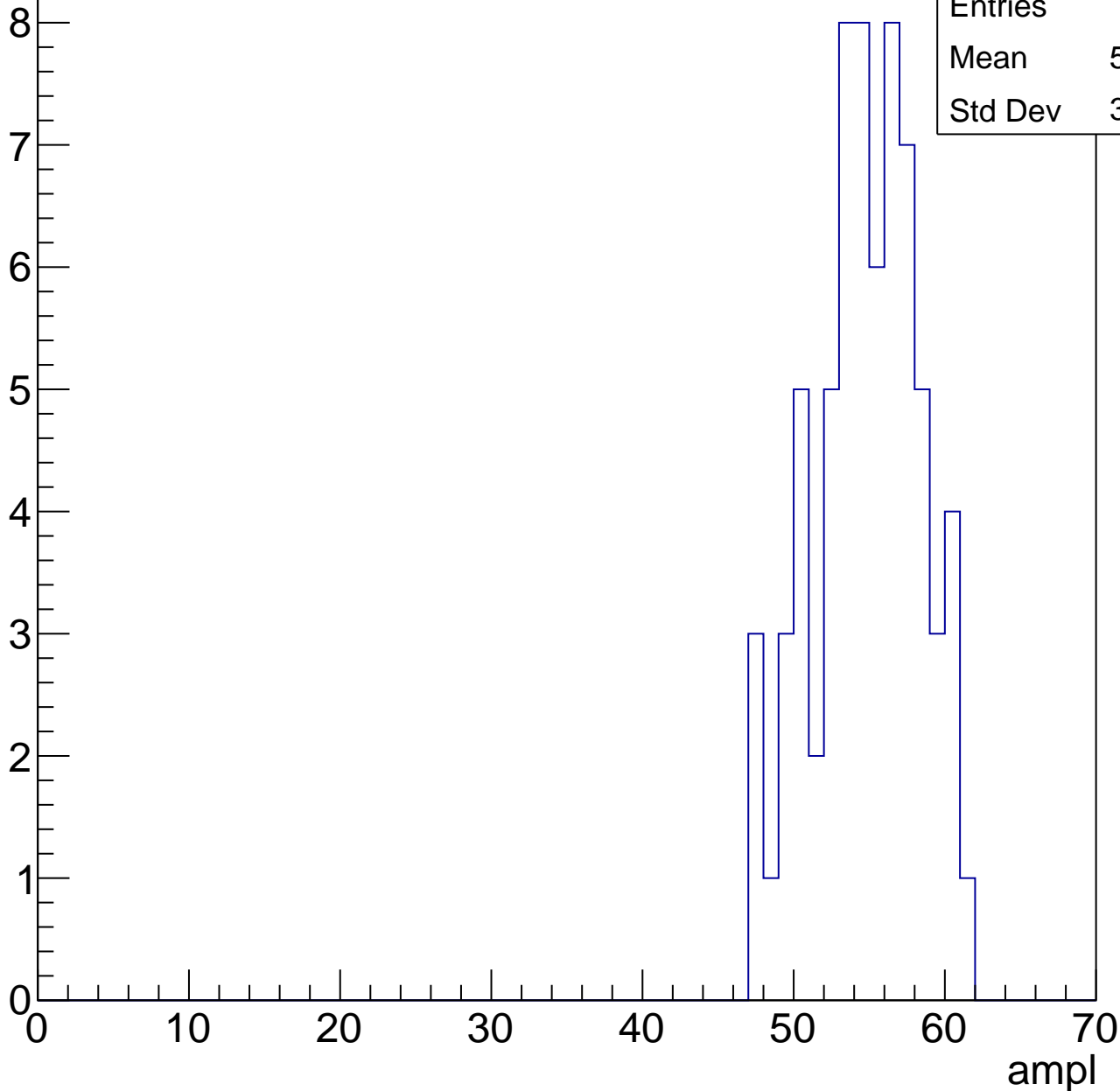


B1L101S, U15-ch97, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

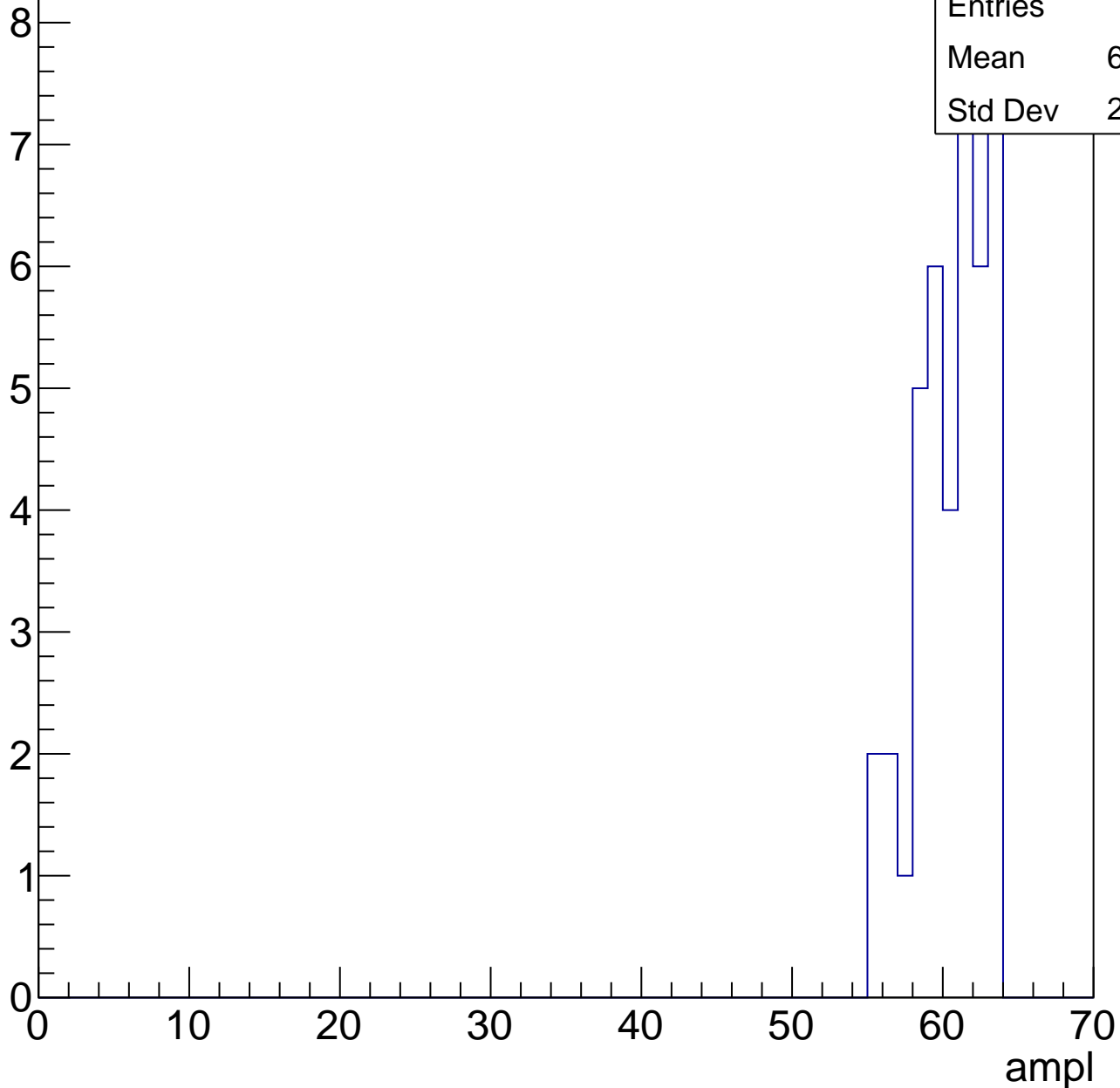
Entries	69
Mean	54.33
Std Dev	3.459



B1L101S, U15-ch97, adc5

calib_packv5_042523_0143.root, FC#0, port D2

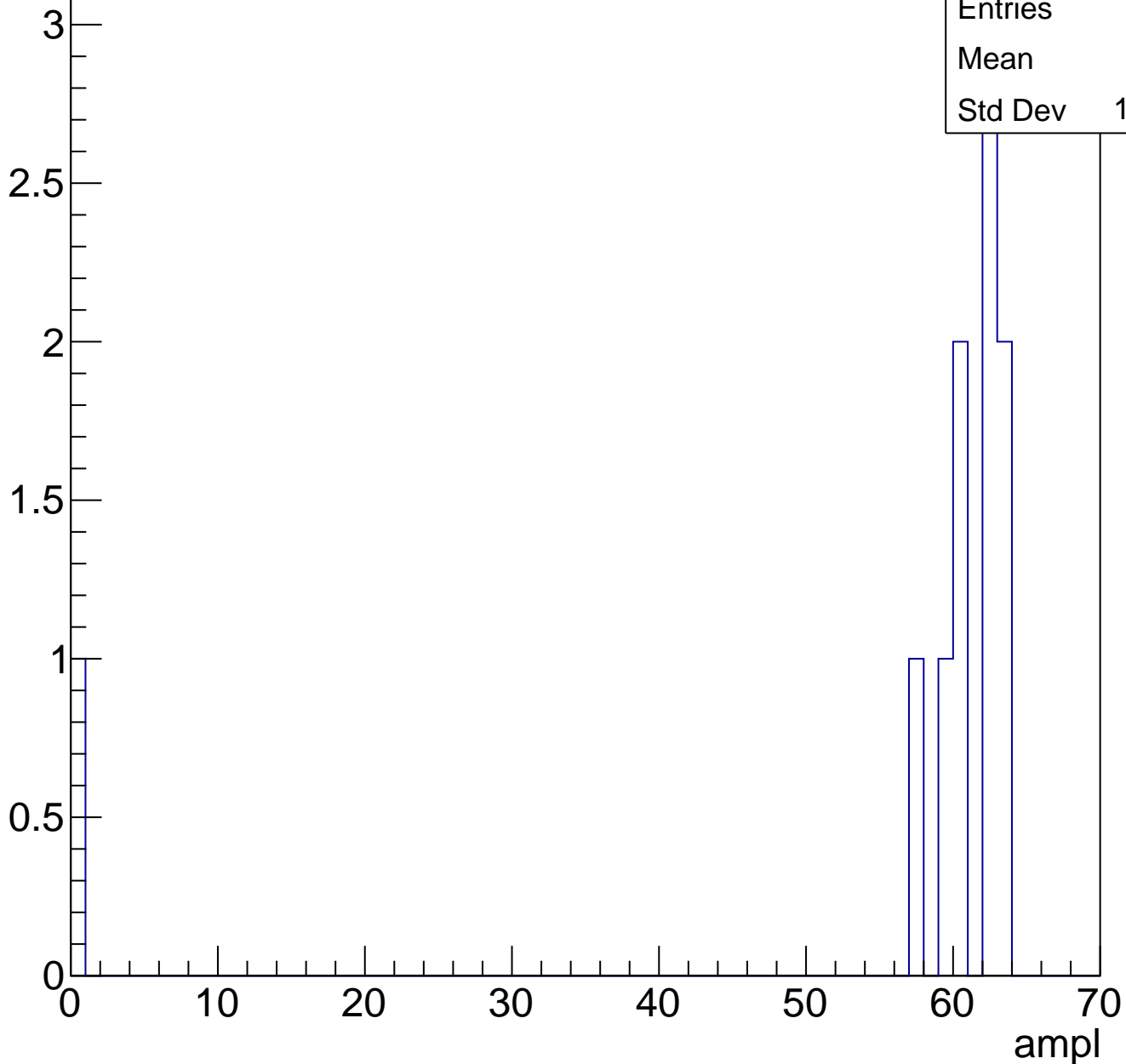
Entry



B1L101S, U15-ch97, adc6

calib_packv5_042523_0143.root, FC#0, port D2

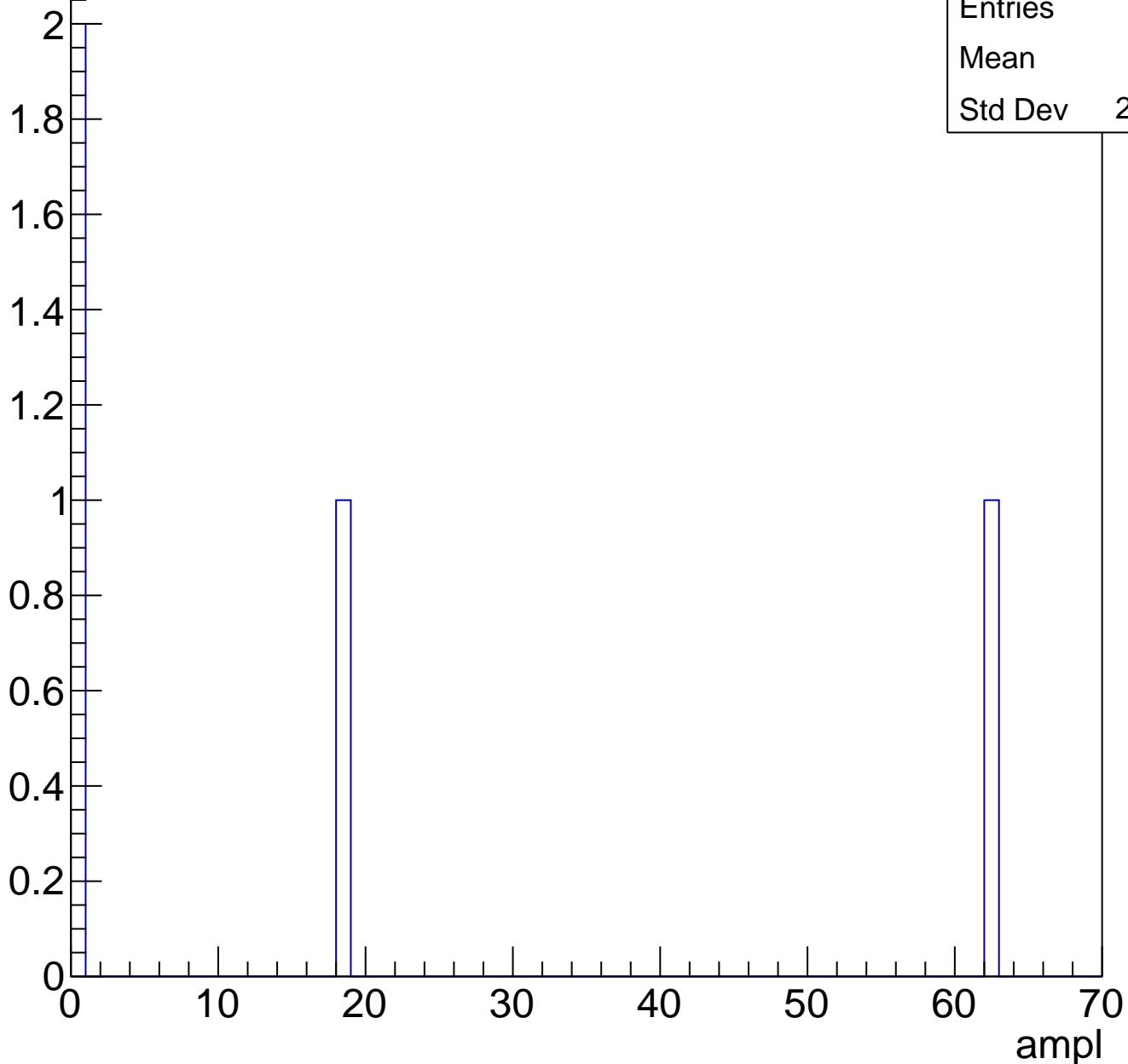
Entry



B1L101S, U15-ch97, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	20
Std Dev	25.34

B1L101S, U15-ch98, adc0

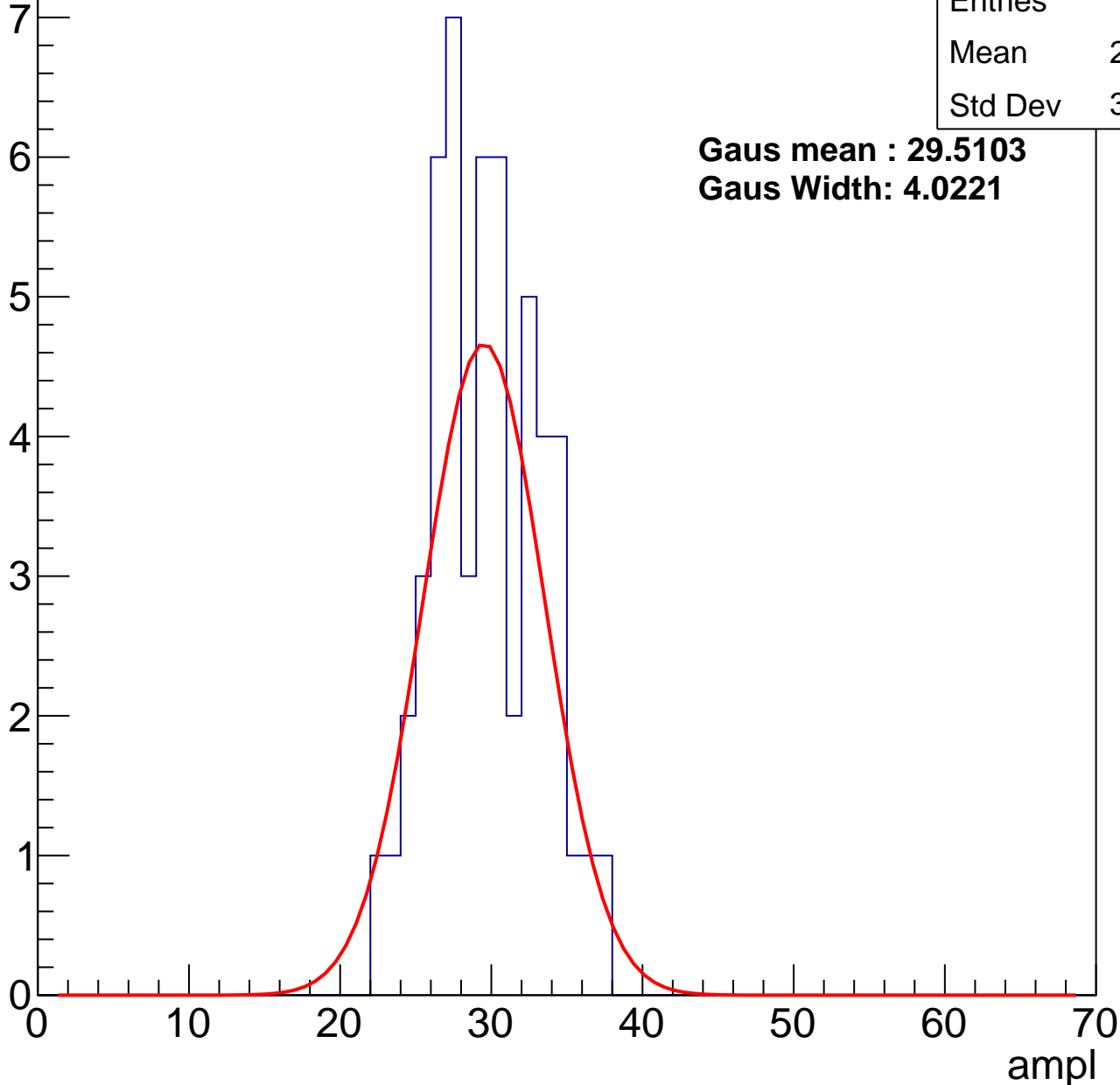
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	29.23
Std Dev	3.468

Gaus mean : 29.5103

Gaus Width: 4.0221



B1L101S, U15-ch98, adc1

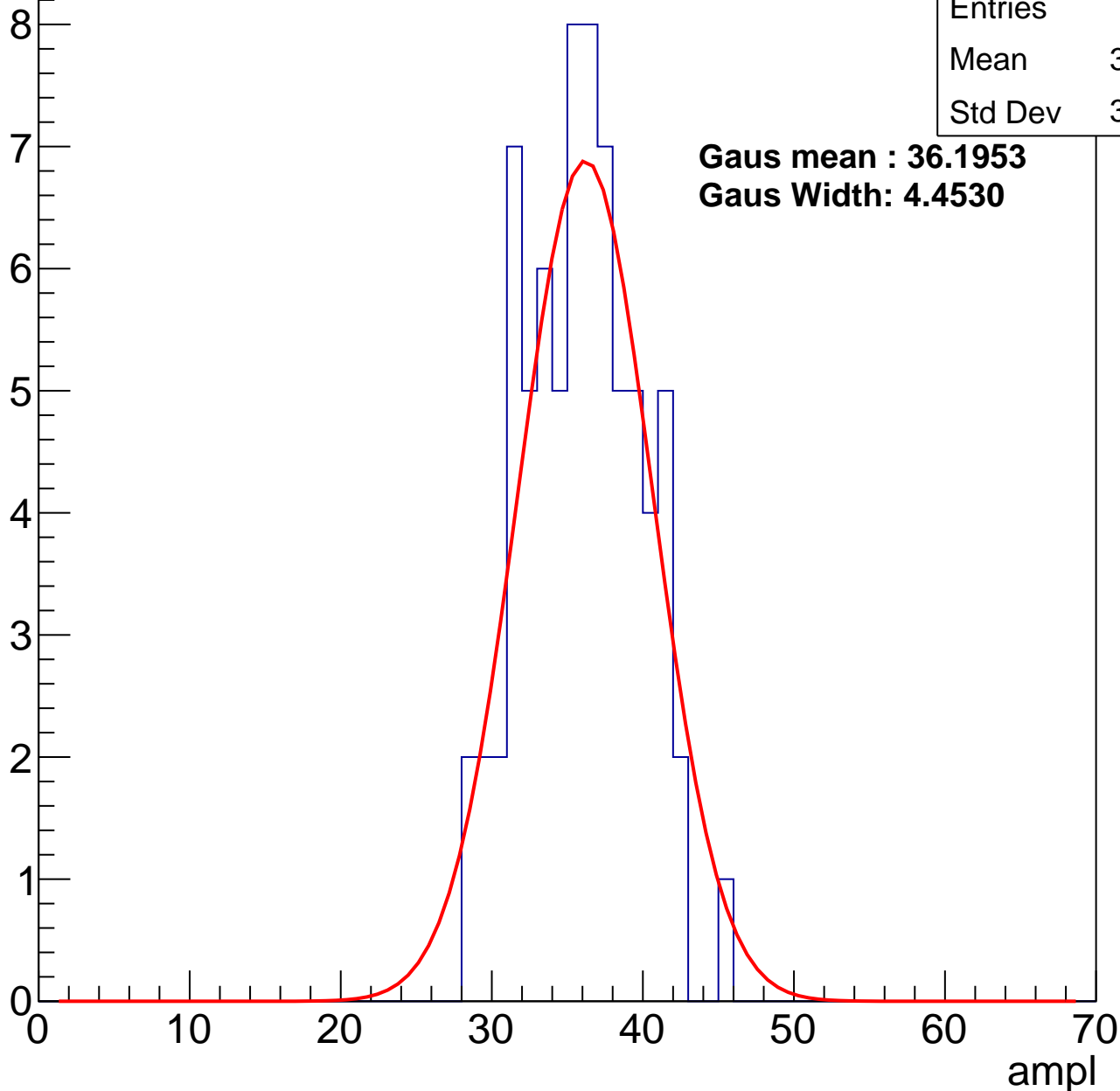
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.47
Std Dev	3.728

Gaus mean : 36.1953

Gaus Width: 4.4530



B1L101S, U15-ch98, adc2

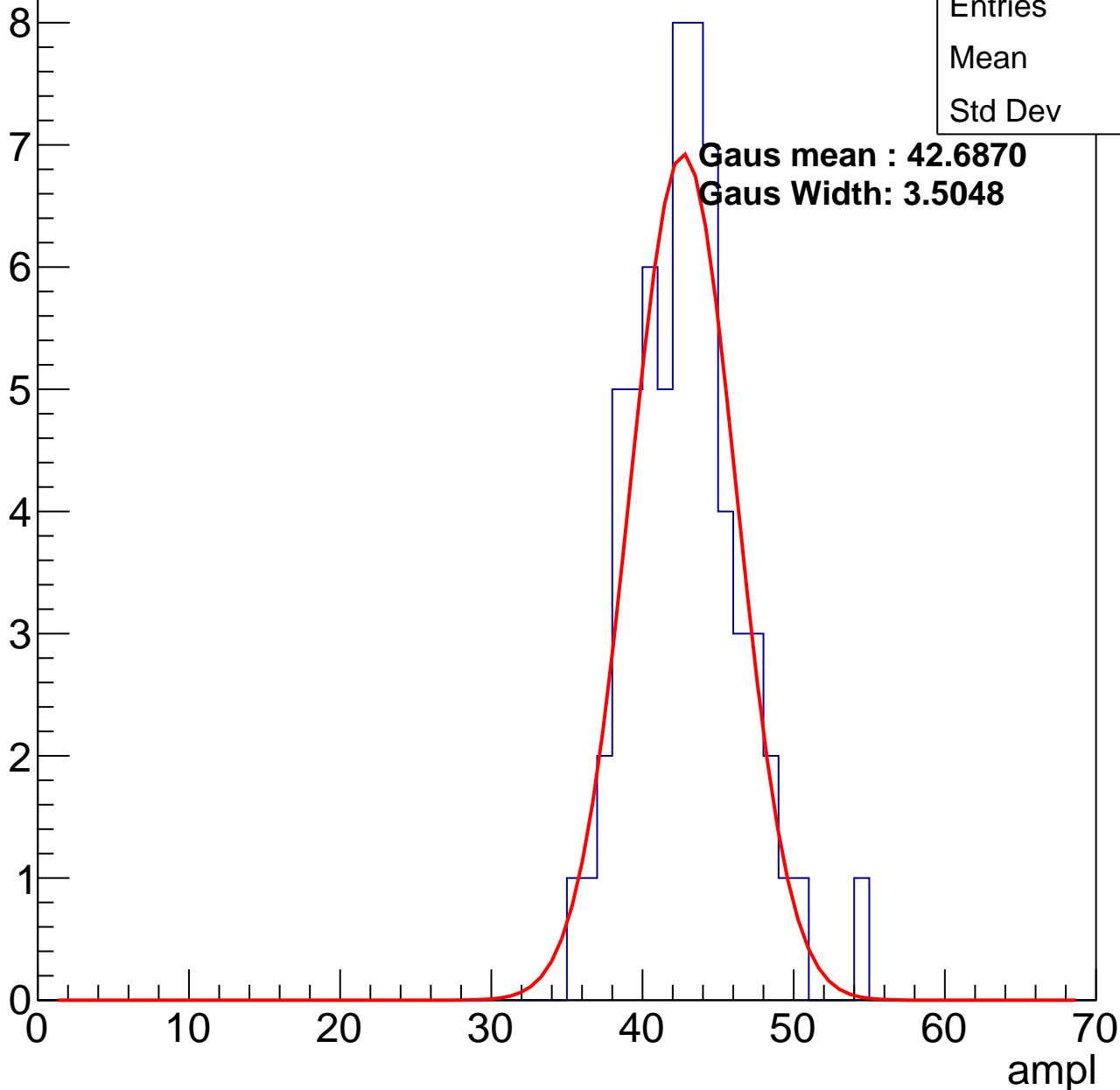
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	42.4
Std Dev	3.57

Gaus mean : 42.6870

Gaus Width: 3.5048

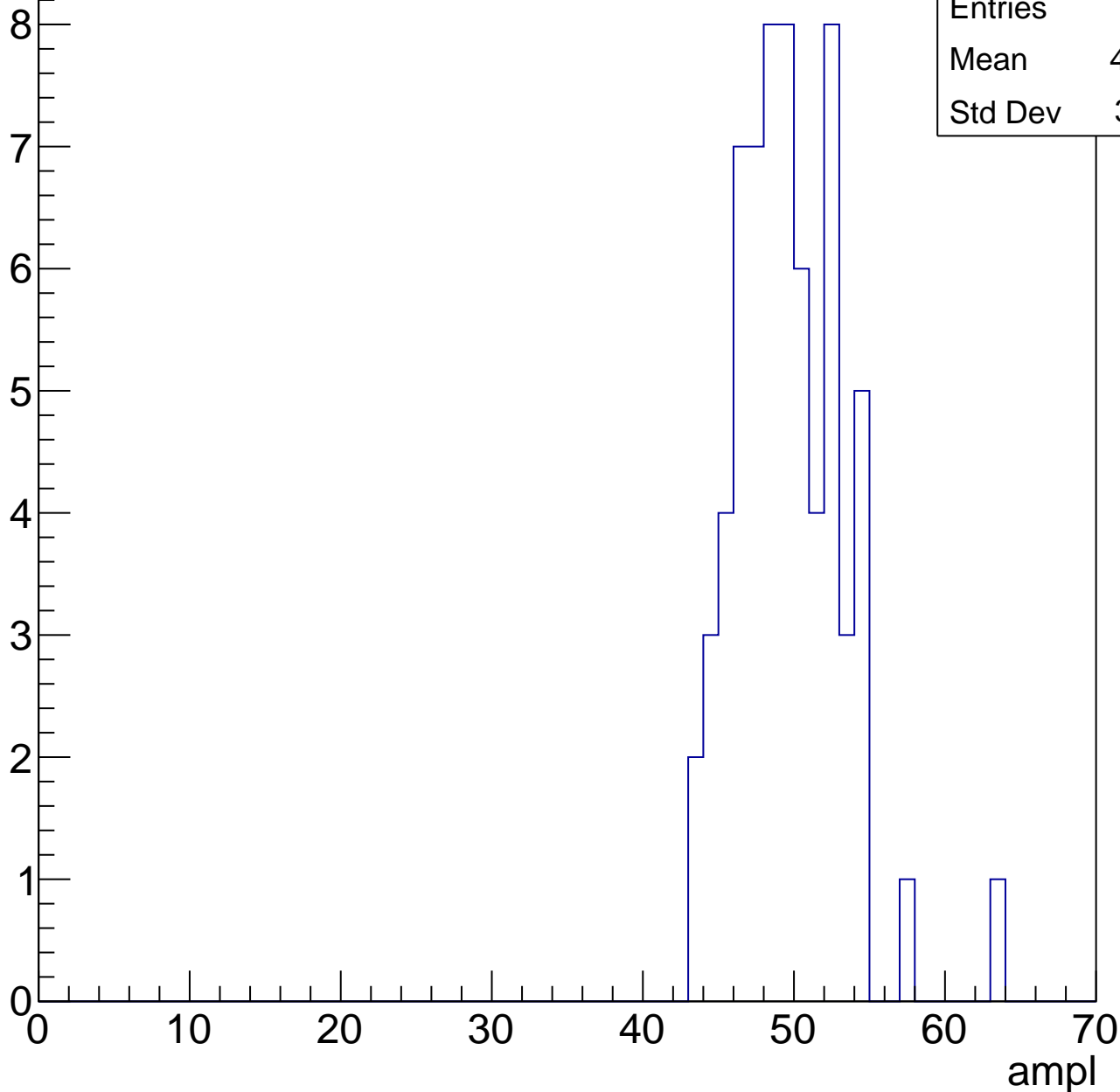


B1L101S, U15-ch98, adc3

calib_packv5_042523_0143.root, FC#0, port D2

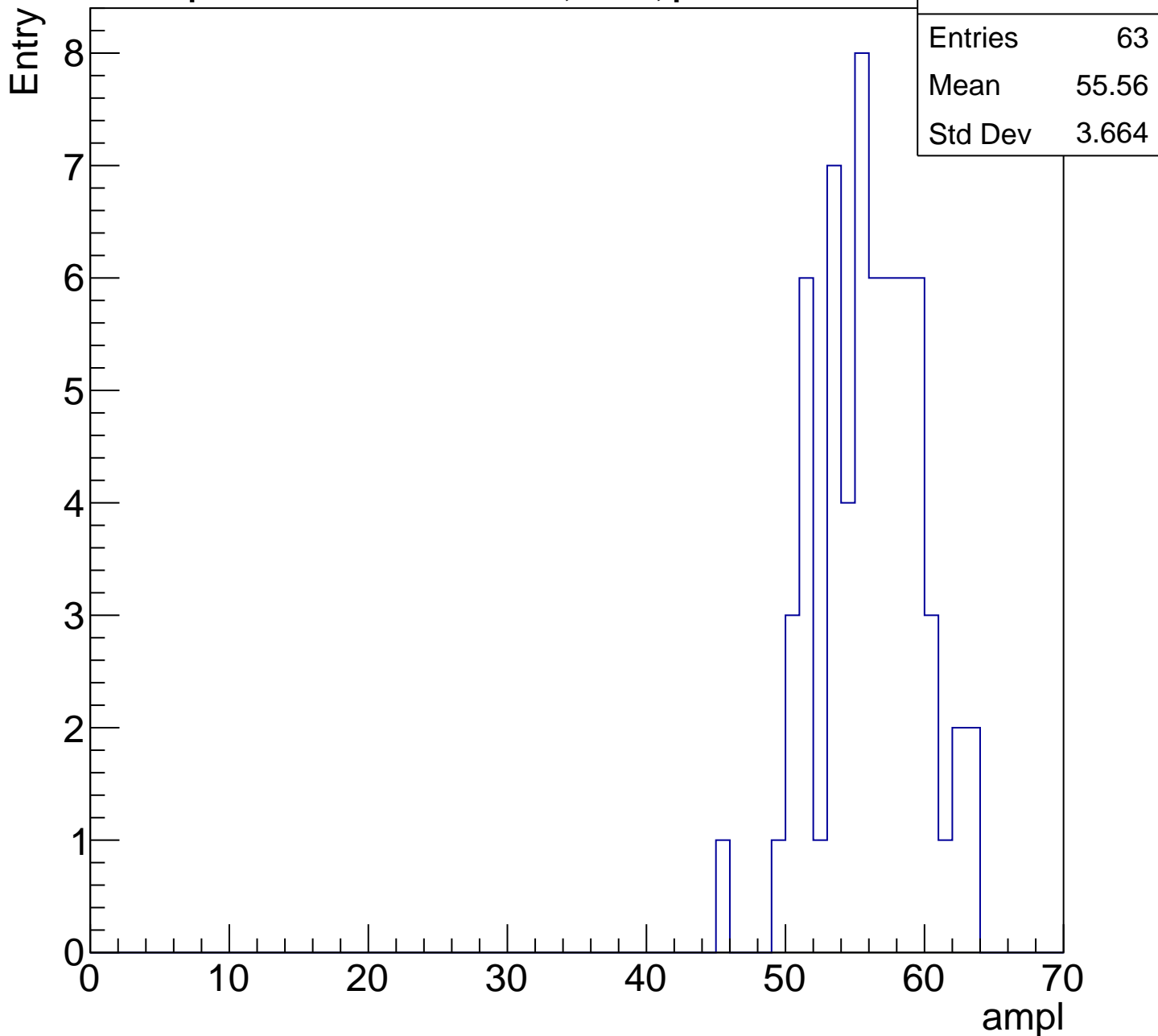
Entry

Entries	67
Mean	49.16
Std Dev	3.531



B1L101S, U15-ch98, adc4

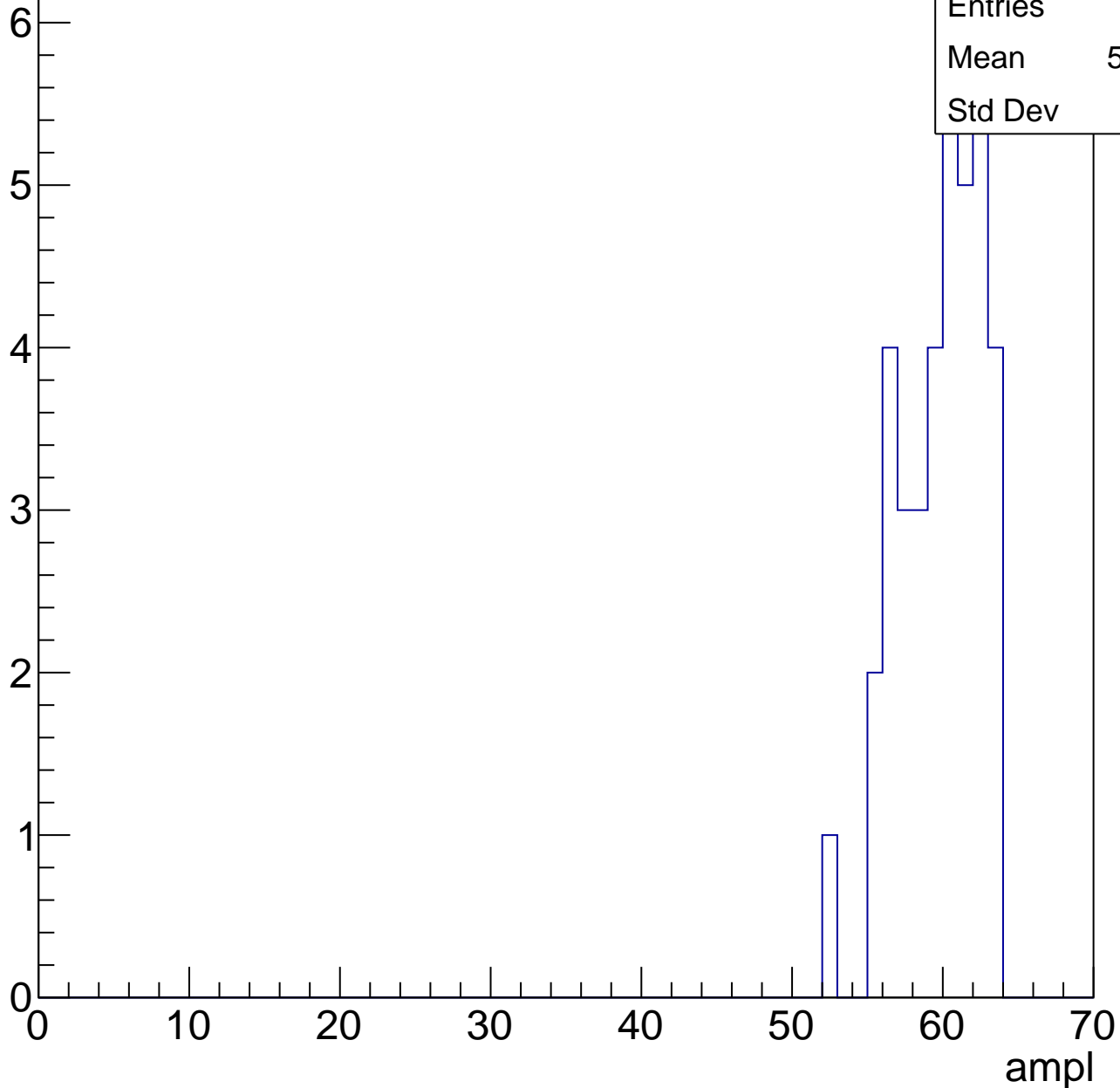
calib_packv5_042523_0143.root, FC#0, port D2



B1L101S, U15-ch98, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

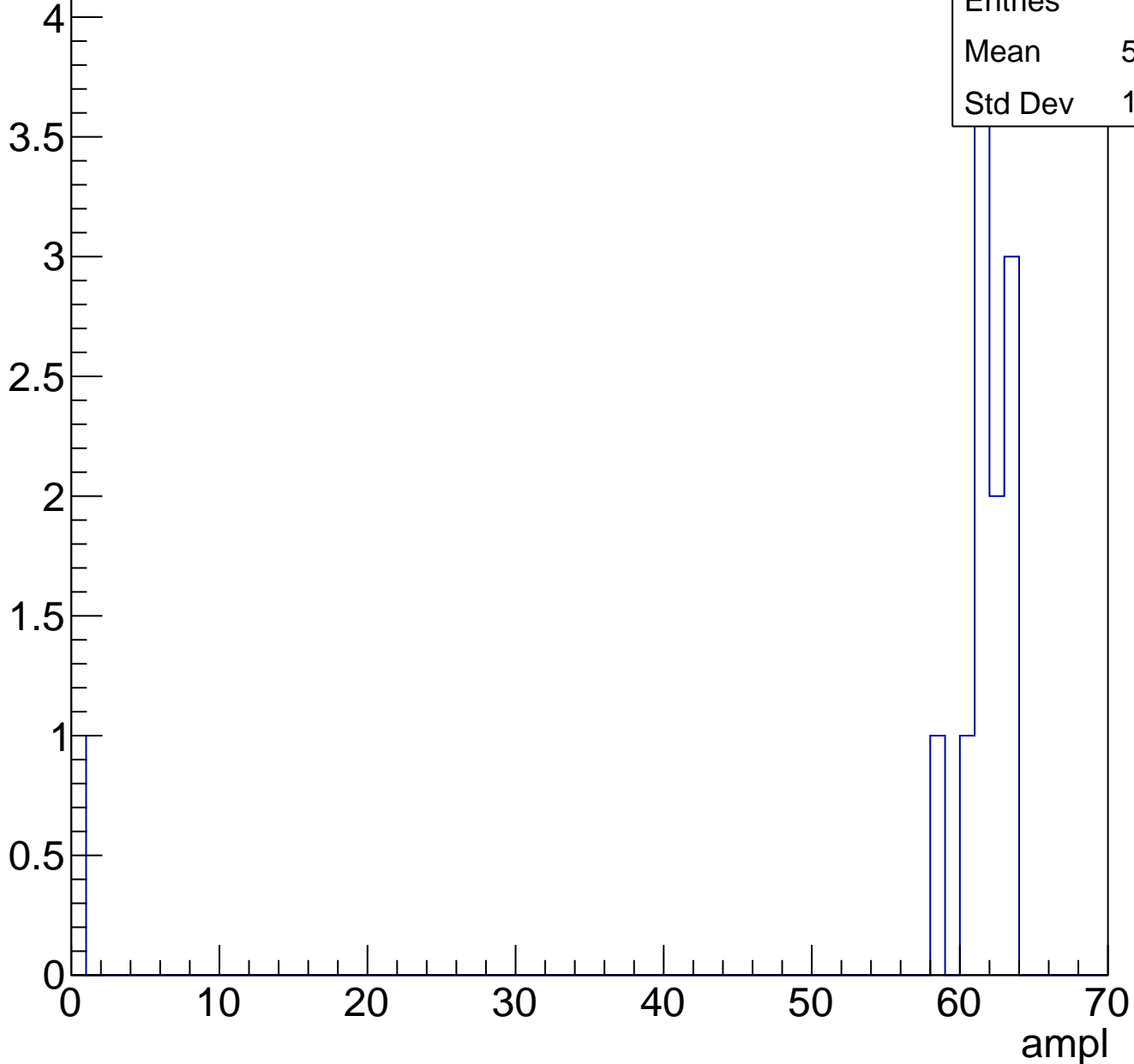


Entries	38
Mean	59.37
Std Dev	2.67

B1L101S, U15-ch98, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	12
Mean	56.25
Std Dev	17.02

B1L101S, U15-ch98, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch99, adc0

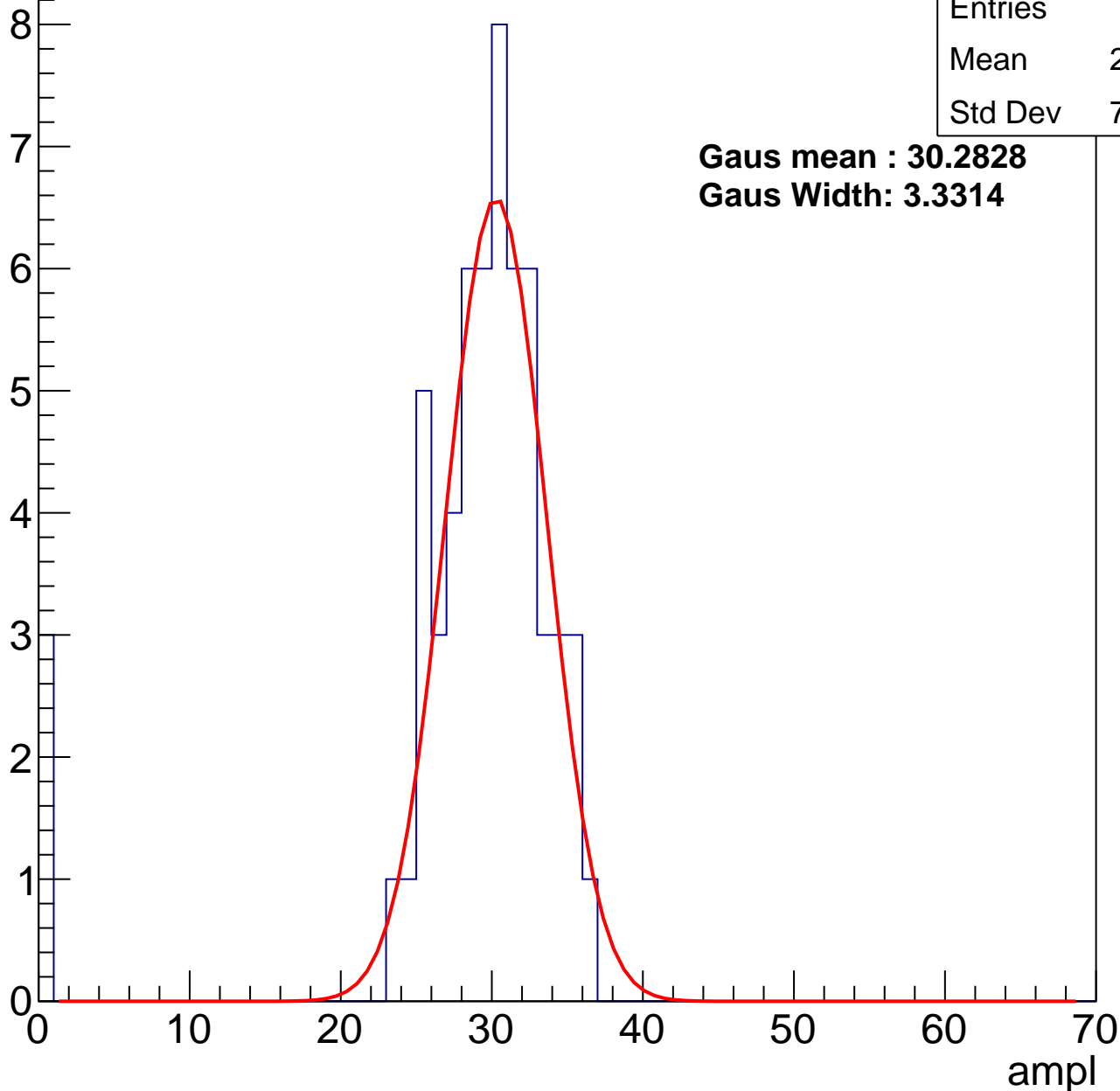
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	28.14
Std Dev	7.172

Gaus mean : 30.2828

Gaus Width: 3.3314



B1L101S, U15-ch99, adc1

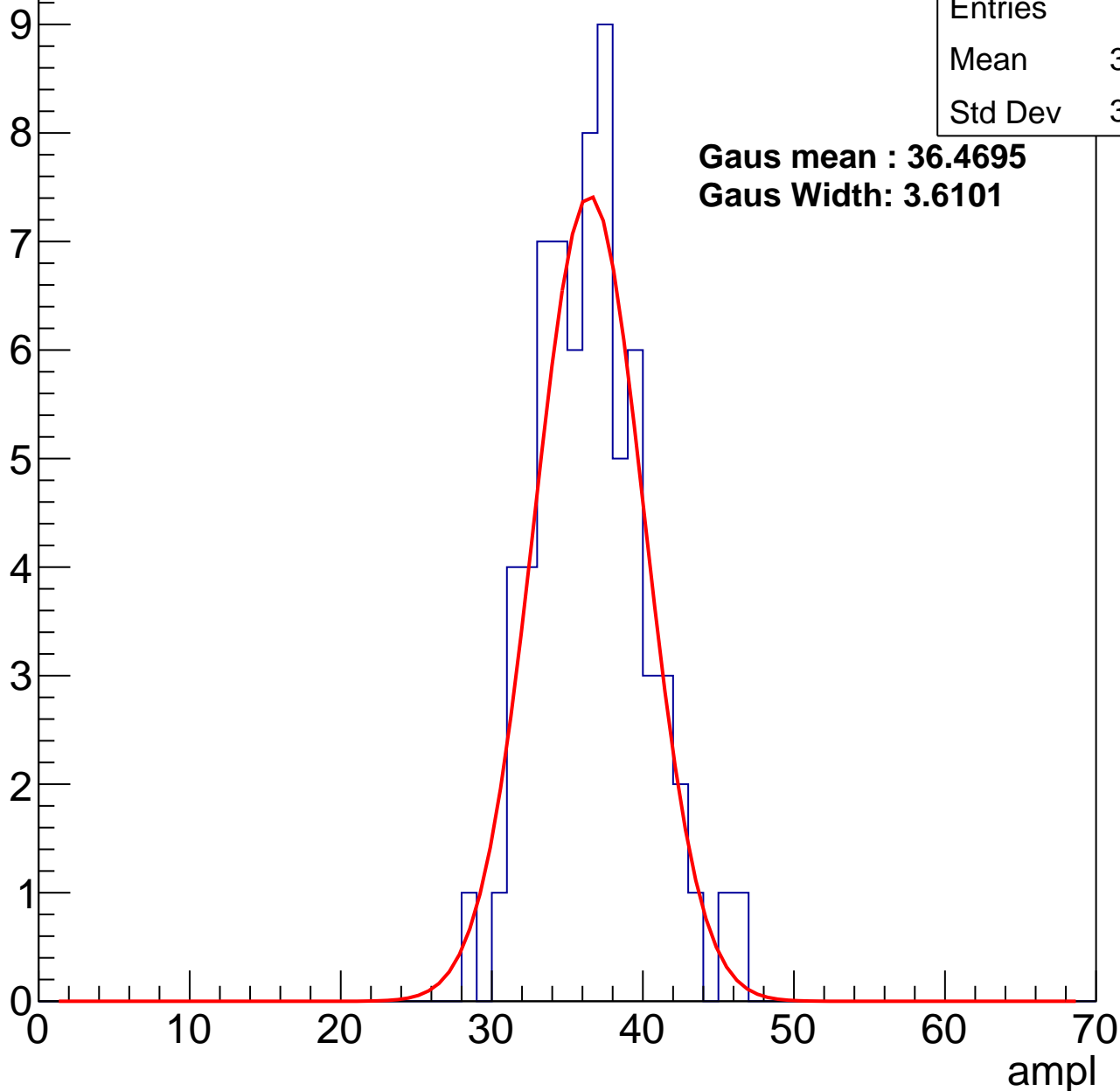
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	36.16
Std Dev	3.545

Gaus mean : 36.4695

Gaus Width: 3.6101



B1L101S, U15-ch99, adc2

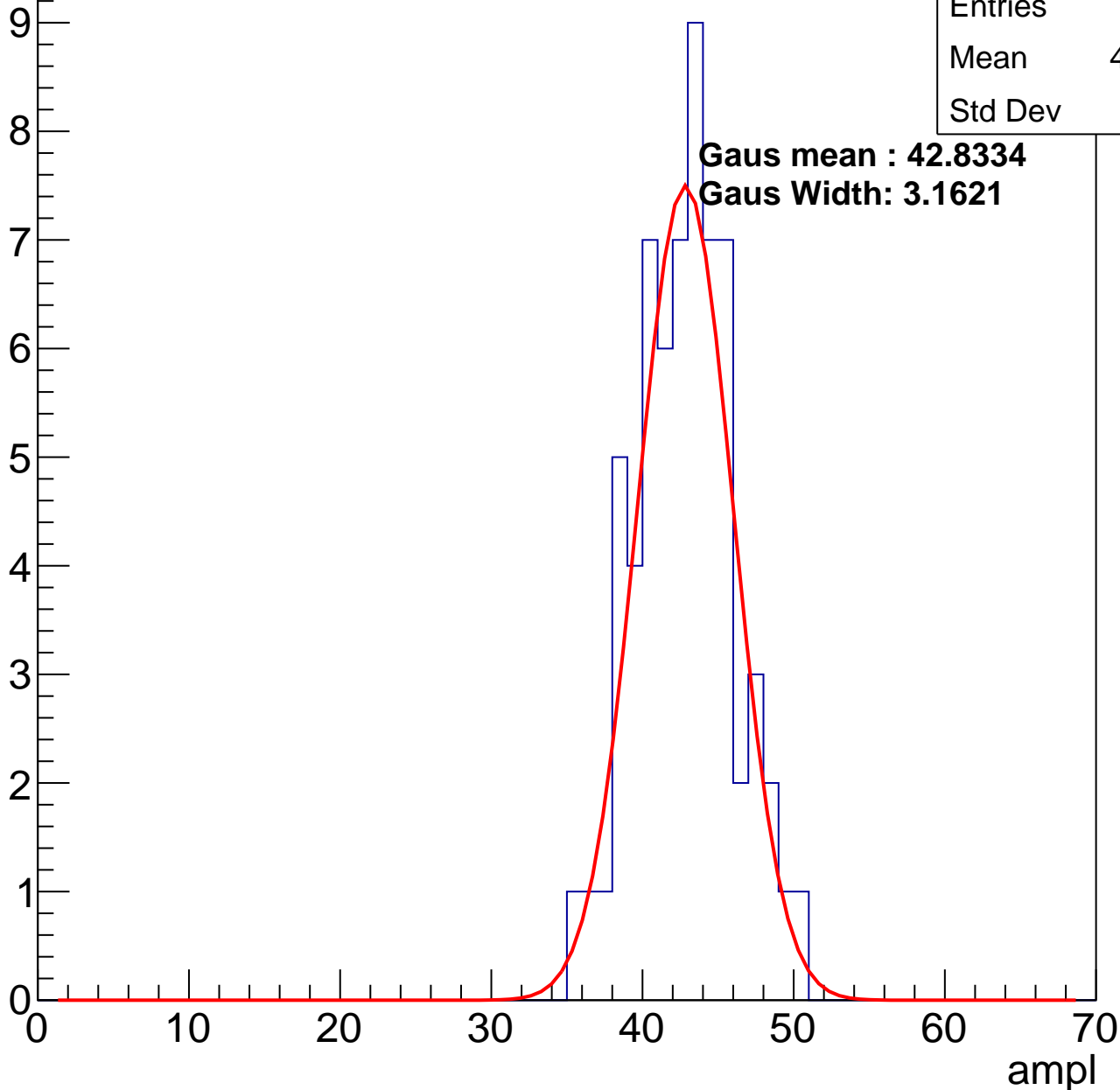
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	42.38
Std Dev	3.17

Gaus mean : 42.8334

Gaus Width: 3.1621

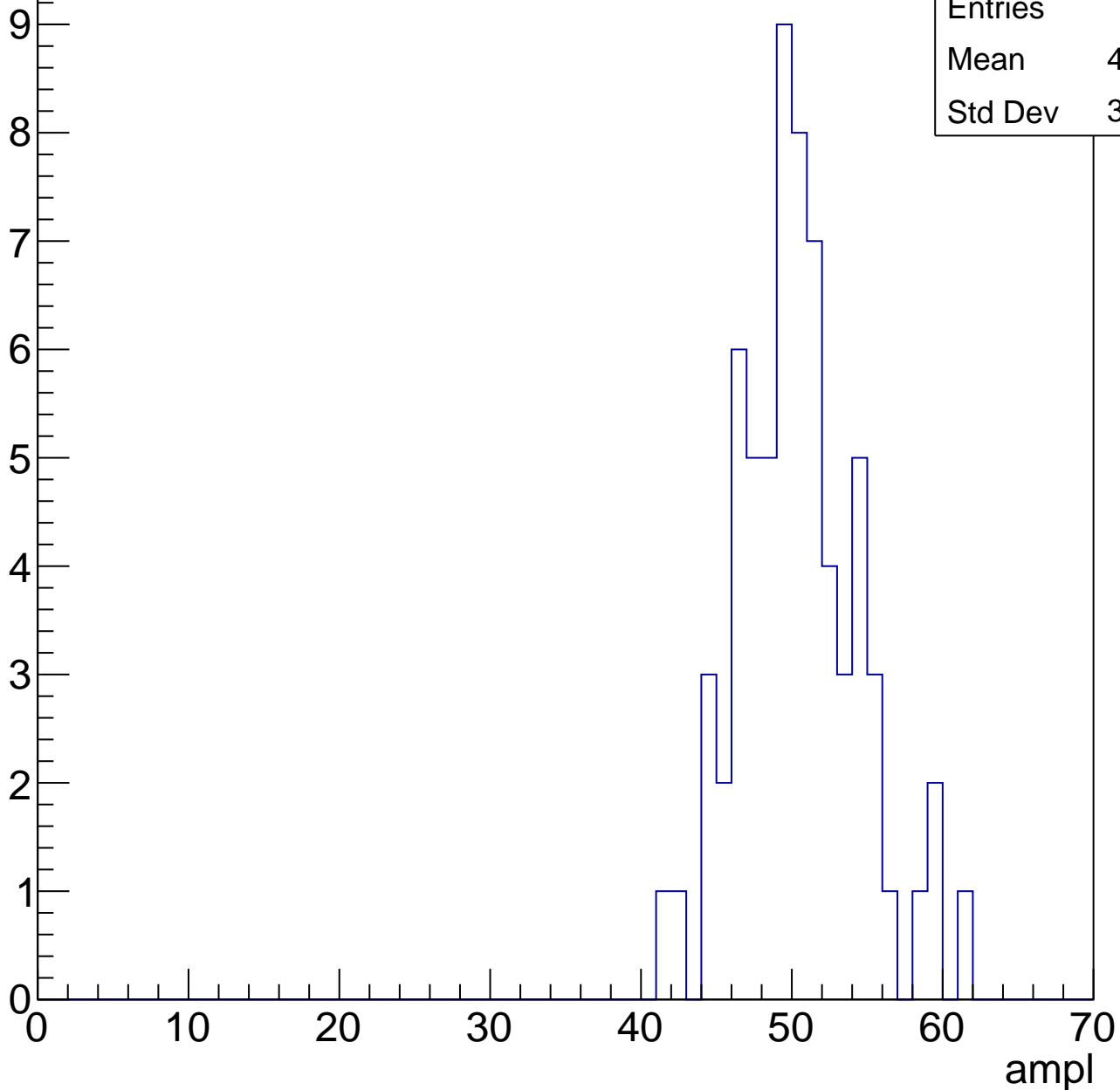


B1L101S, U15-ch99, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	49.99
Std Dev	3.979

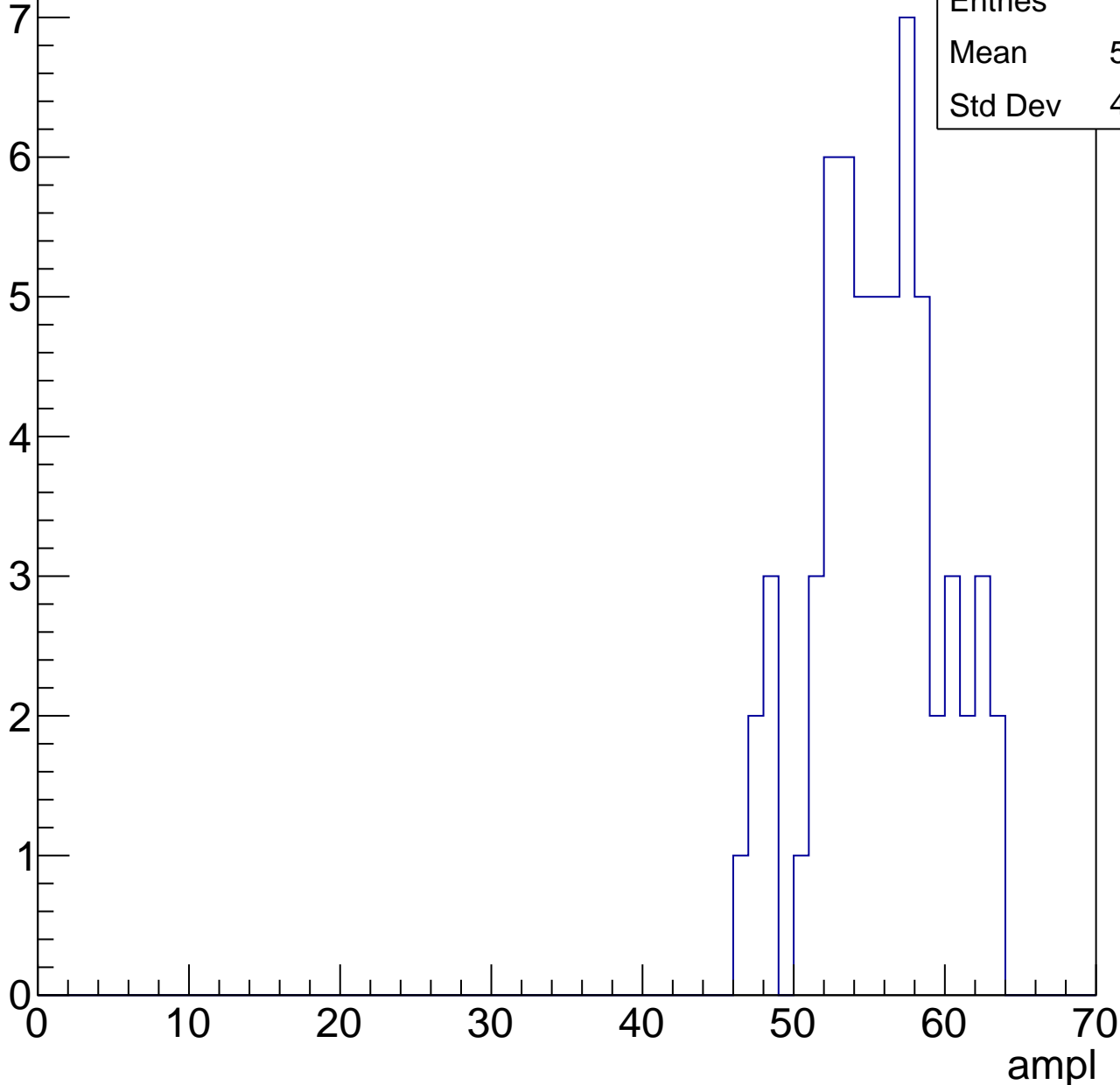


B1L101S, U15-ch99, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	55.13
Std Dev	4.135

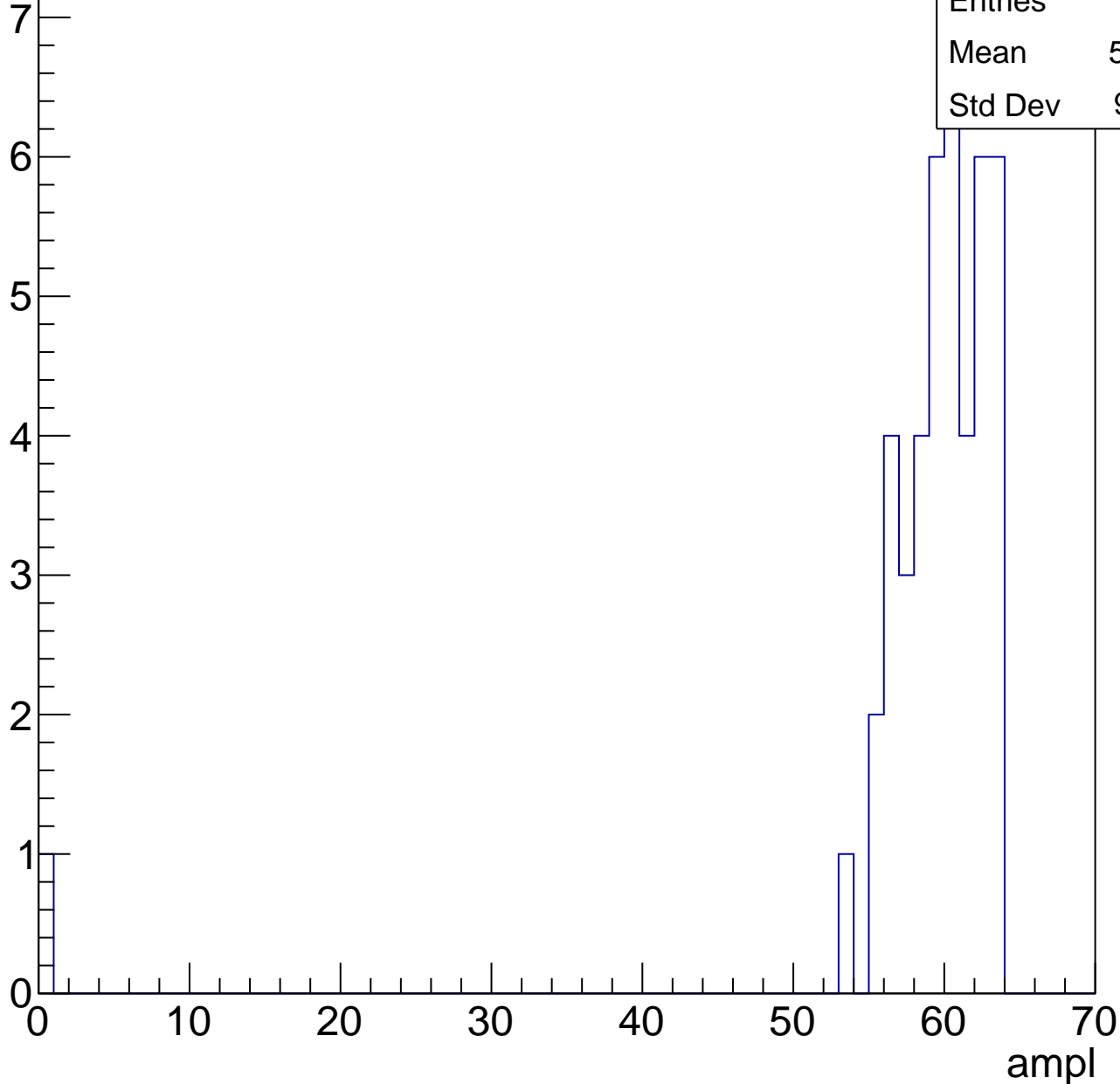


B1L101S, U15-ch99, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

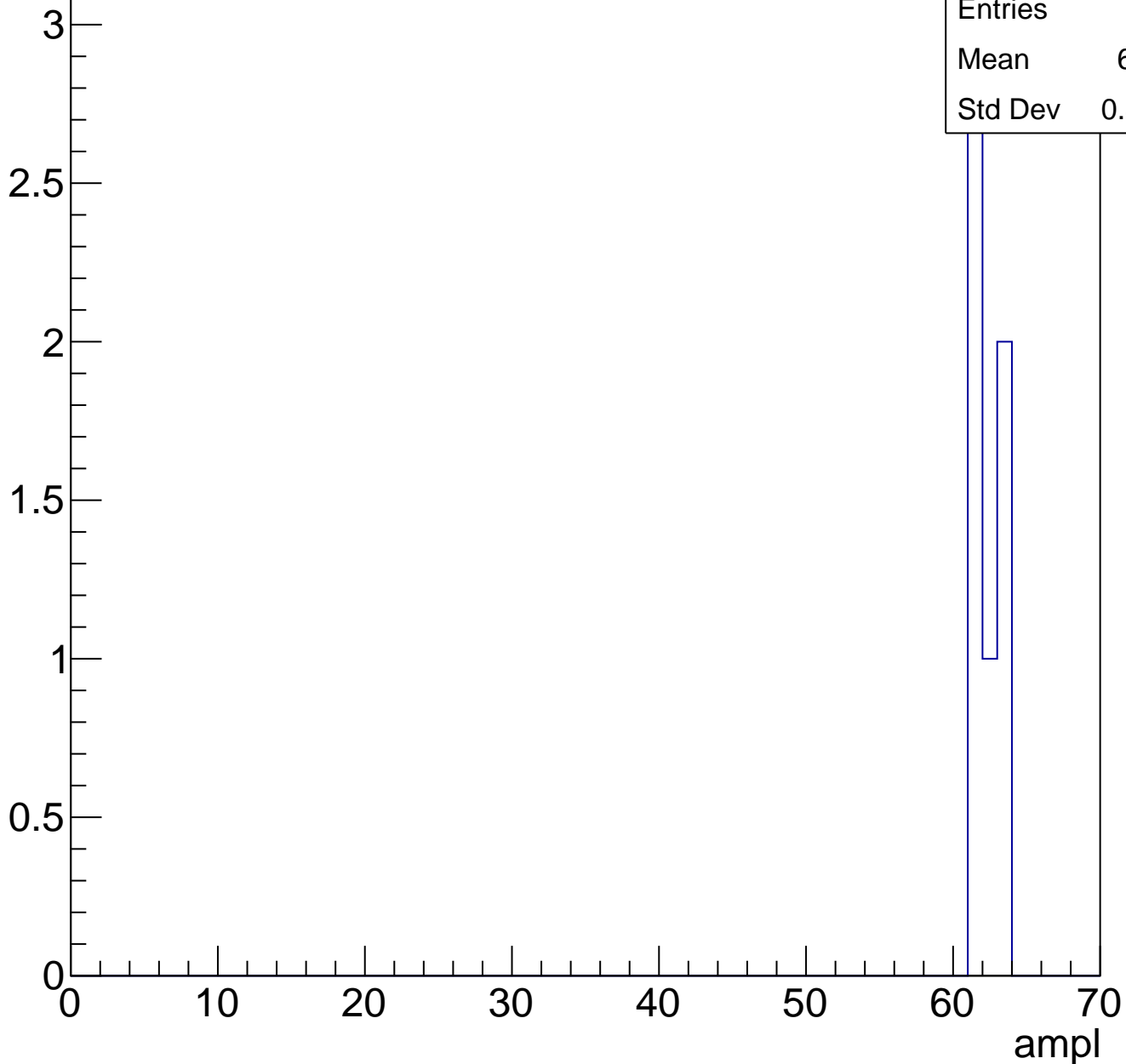
Entries	44
Mean	58.14
Std Dev	9.221



B1L101S, U15-ch99, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch99, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch100, adc0

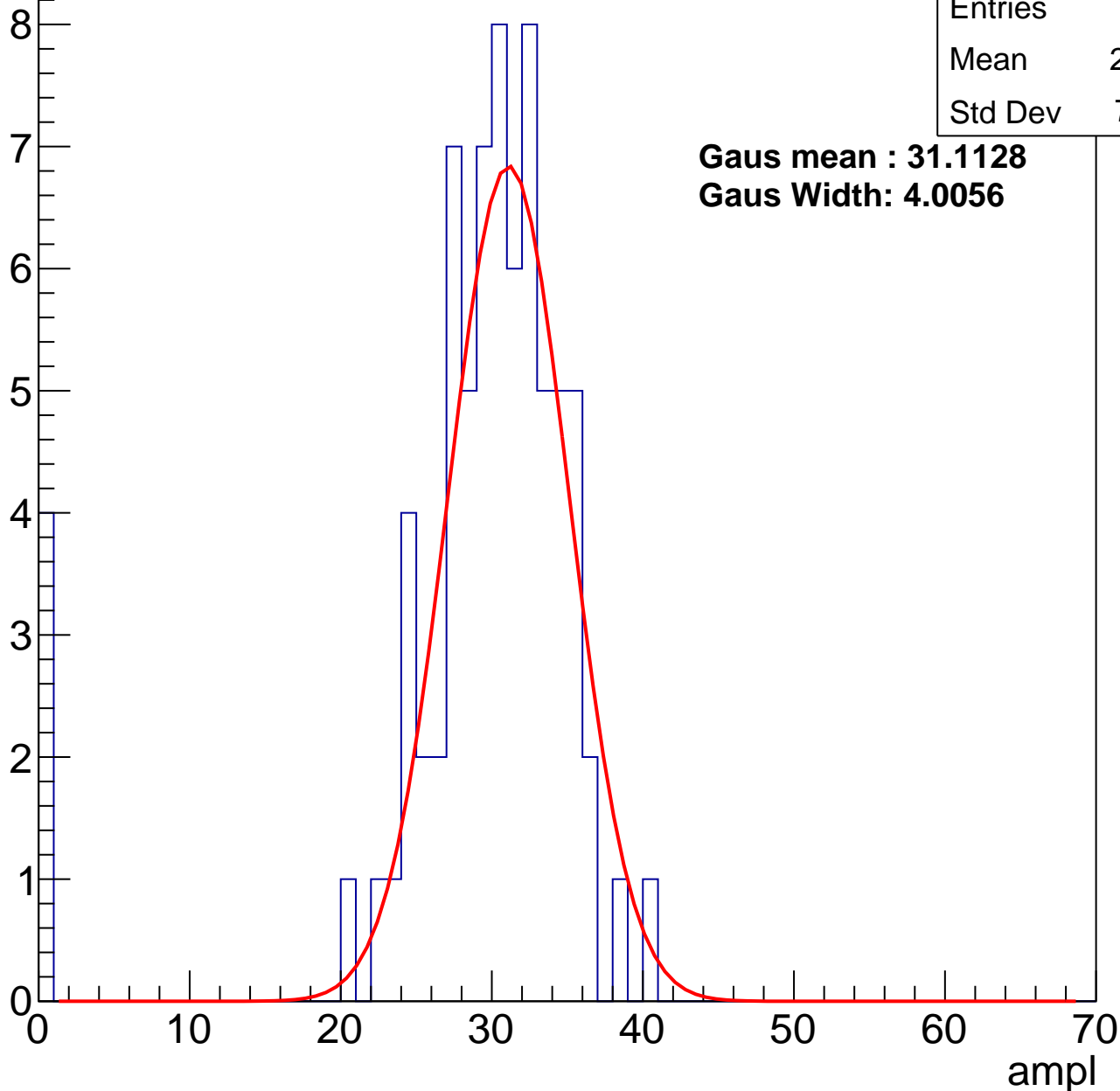
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	28.49
Std Dev	7.731

Gaus mean : 31.1128

Gaus Width: 4.0056



B1L101S, U15-ch100, adc1

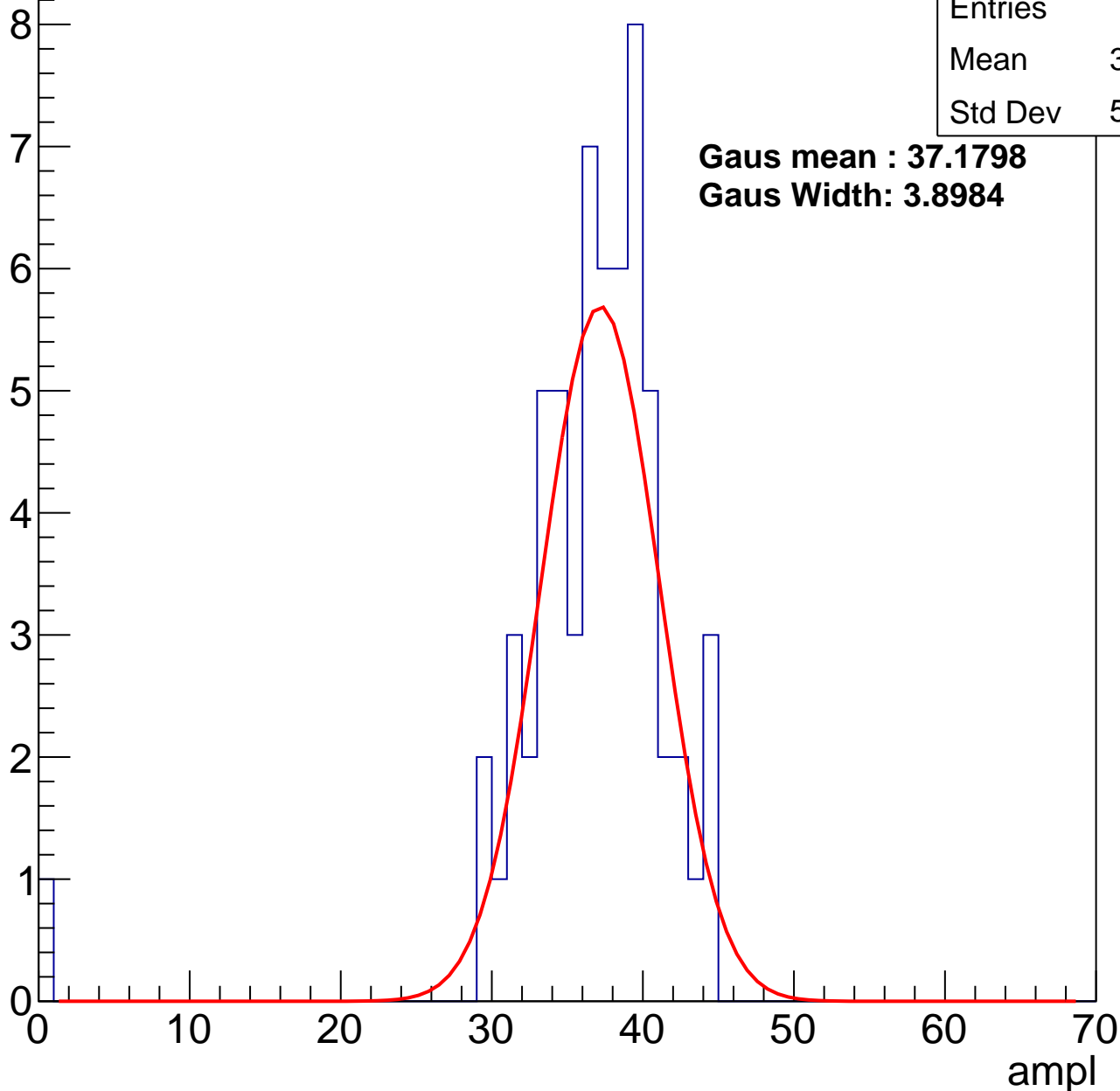
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	36.13
Std Dev	5.887

Gaus mean : 37.1798

Gaus Width: 3.8984



B1L101S, U15-ch100, adc2

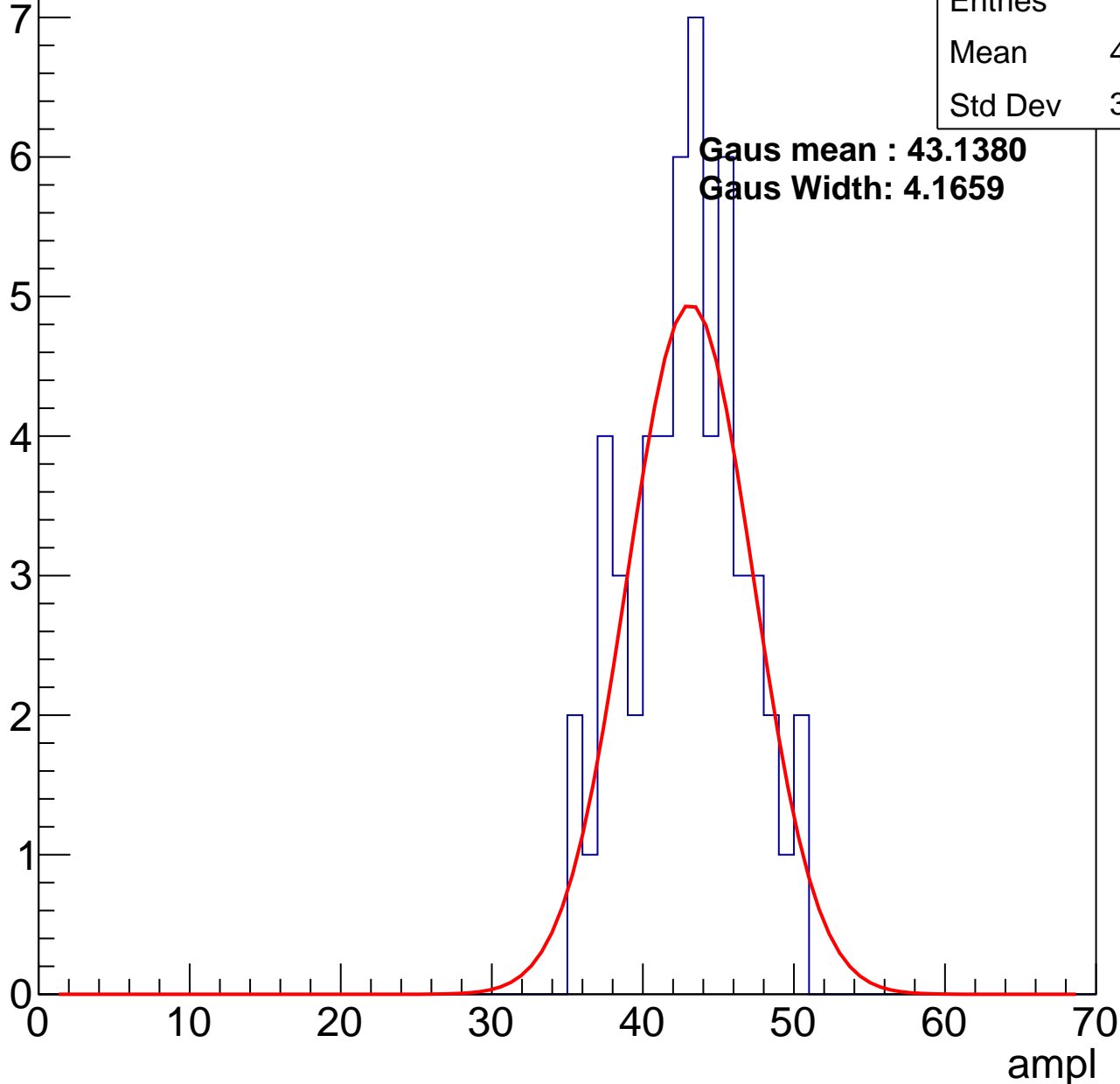
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	42.46
Std Dev	3.745

Gaus mean : 43.1380

Gaus Width: 4.1659

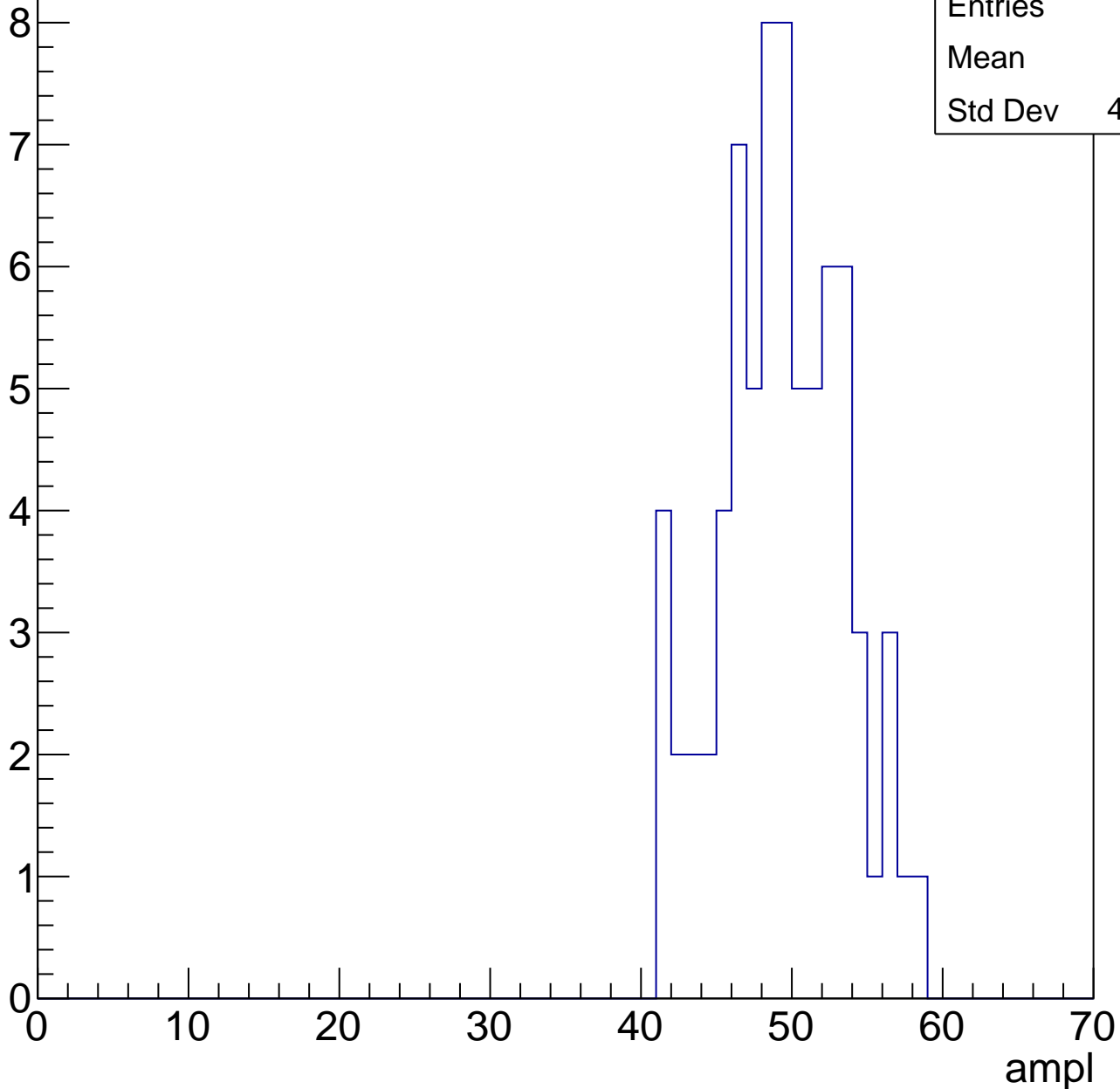


B1L101S, U15-ch100, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	48.9
Std Dev	4.068

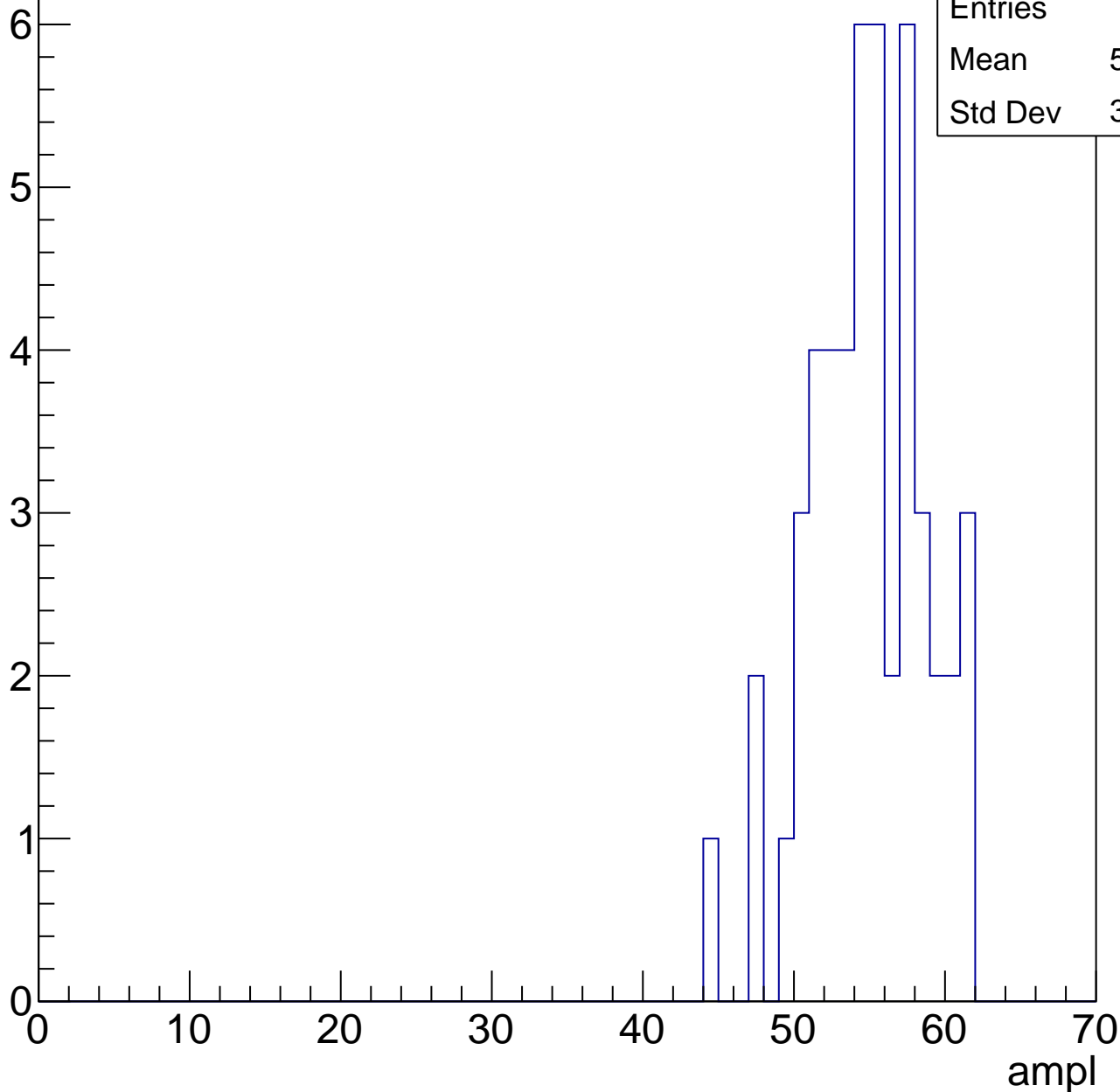


B1L101S, U15-ch100, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	54.37
Std Dev	3.794

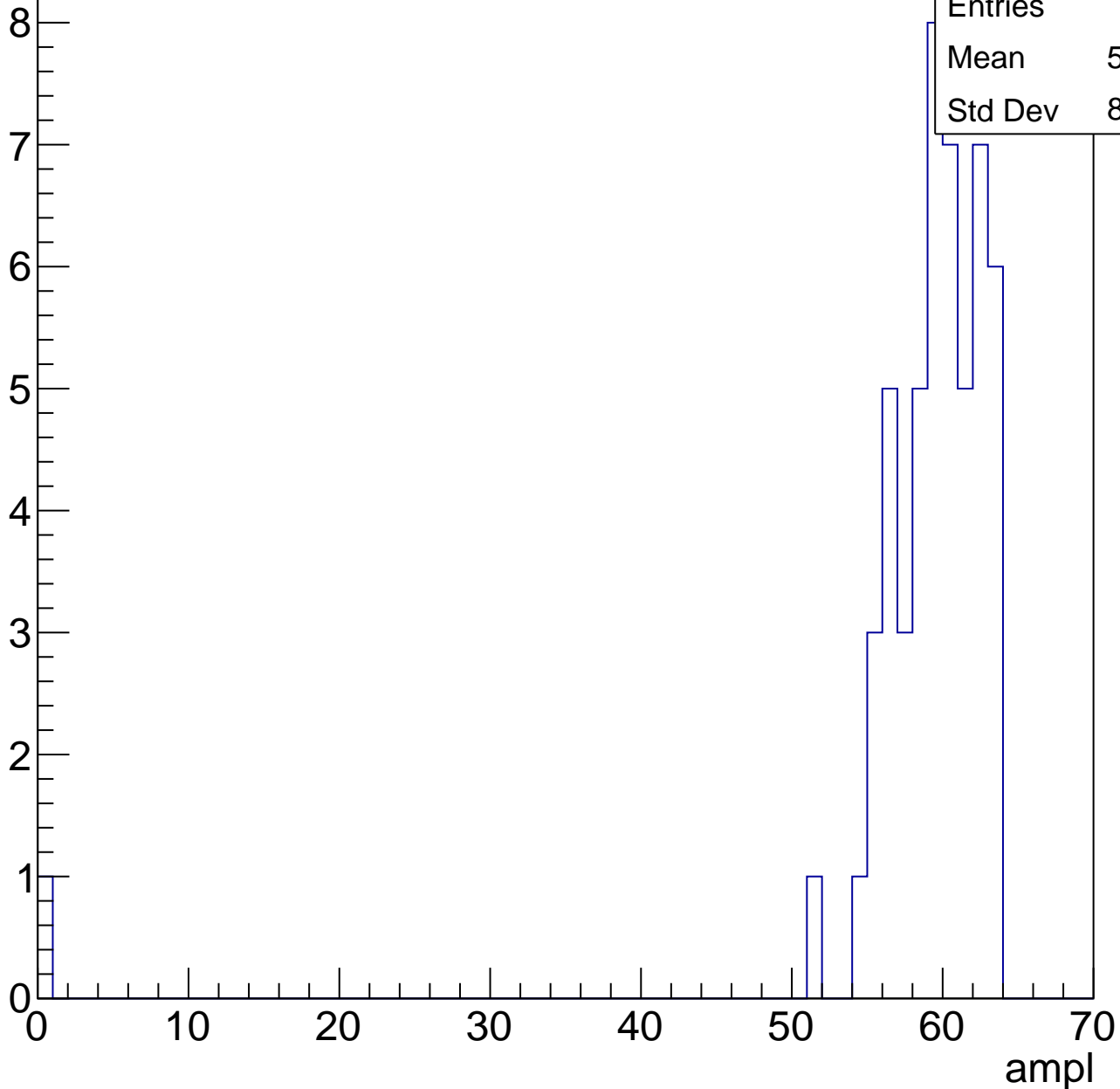


B1L101S, U15-ch100, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

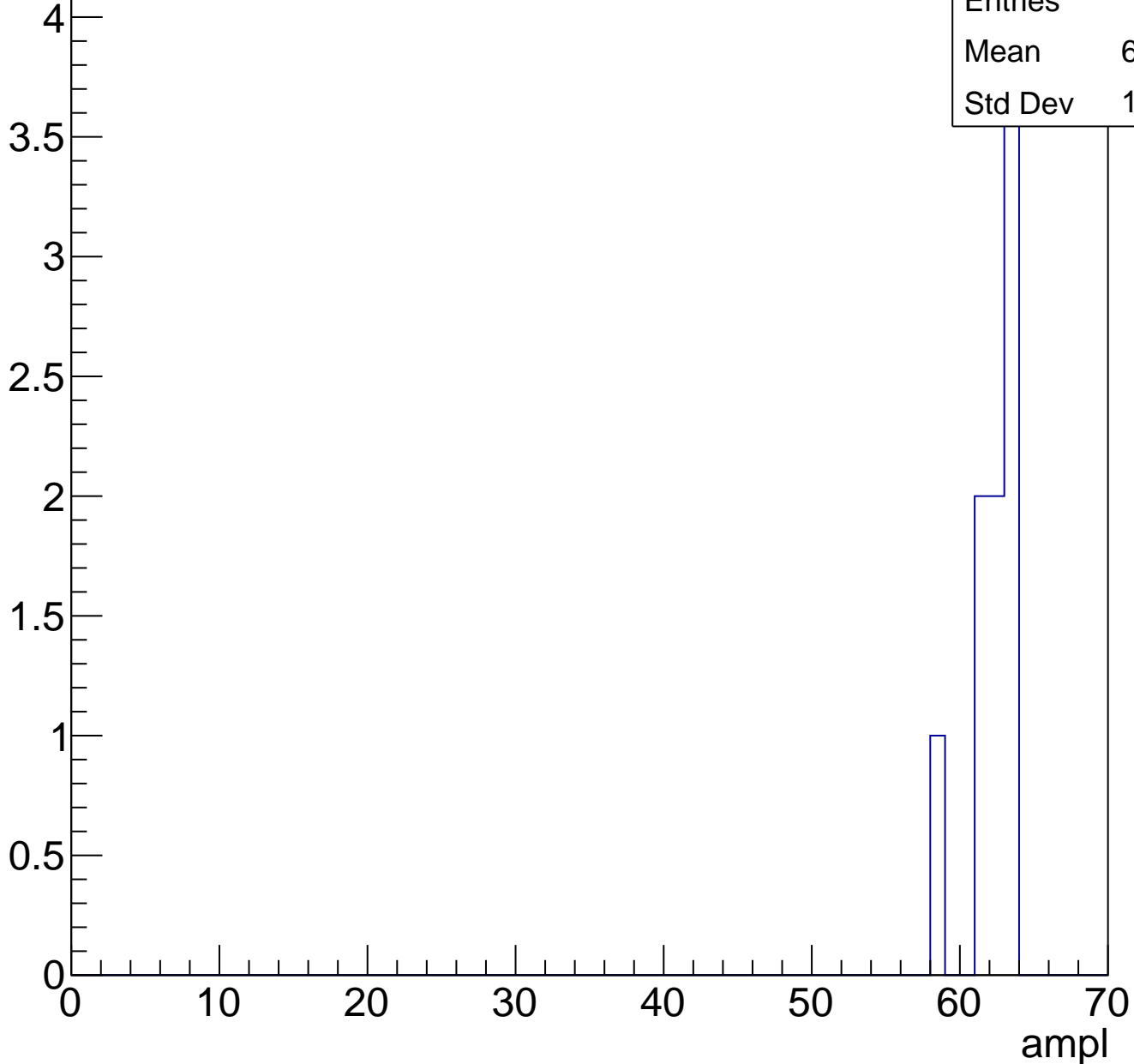
Entries	52
Mean	58.08
Std Dev	8.573



B1L101S, U15-ch100, adc6

calib_packv5_042523_0143.root, FC#0, port D2

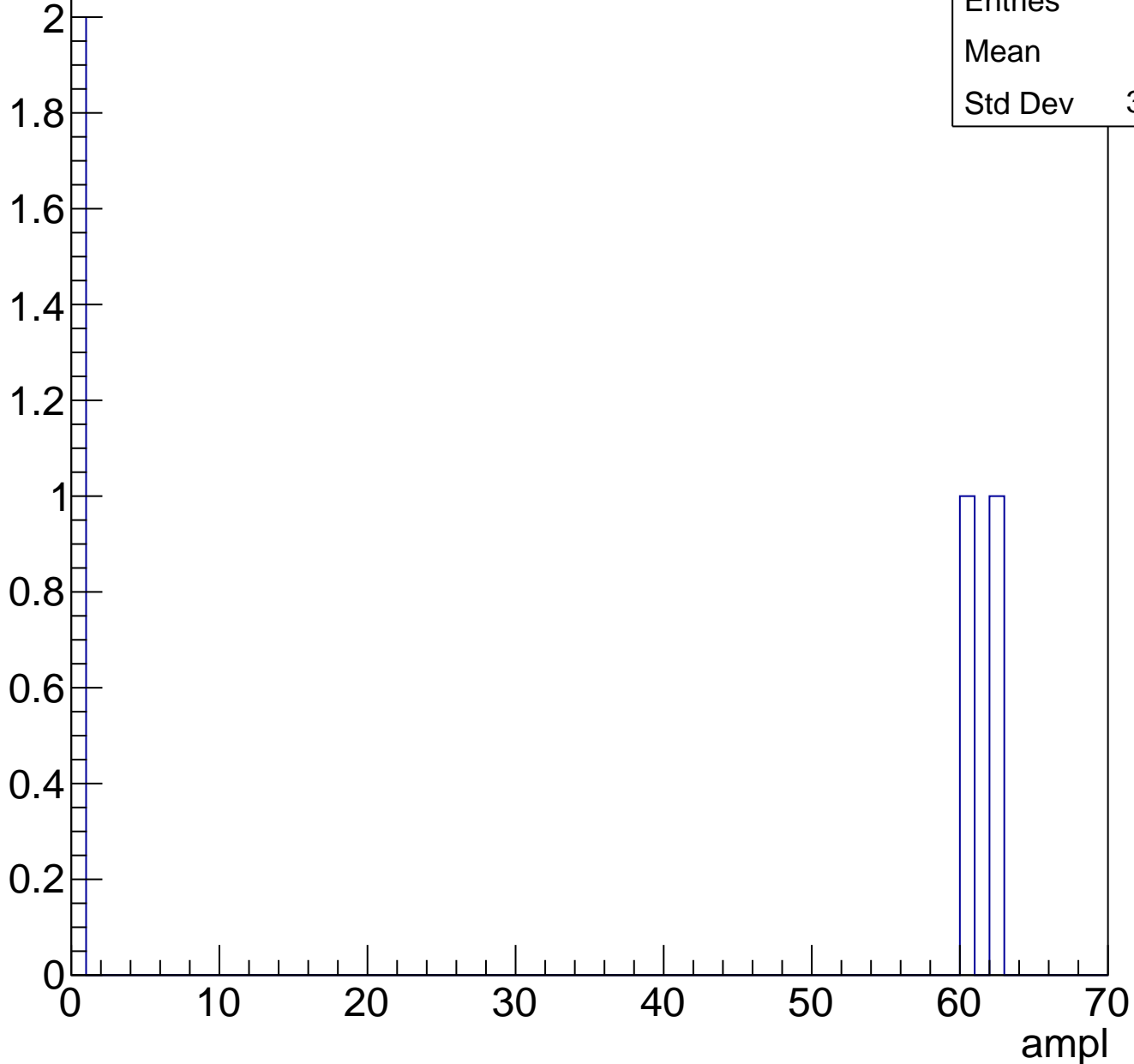
Entry



B1L101S, U15-ch100, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch101, adc0

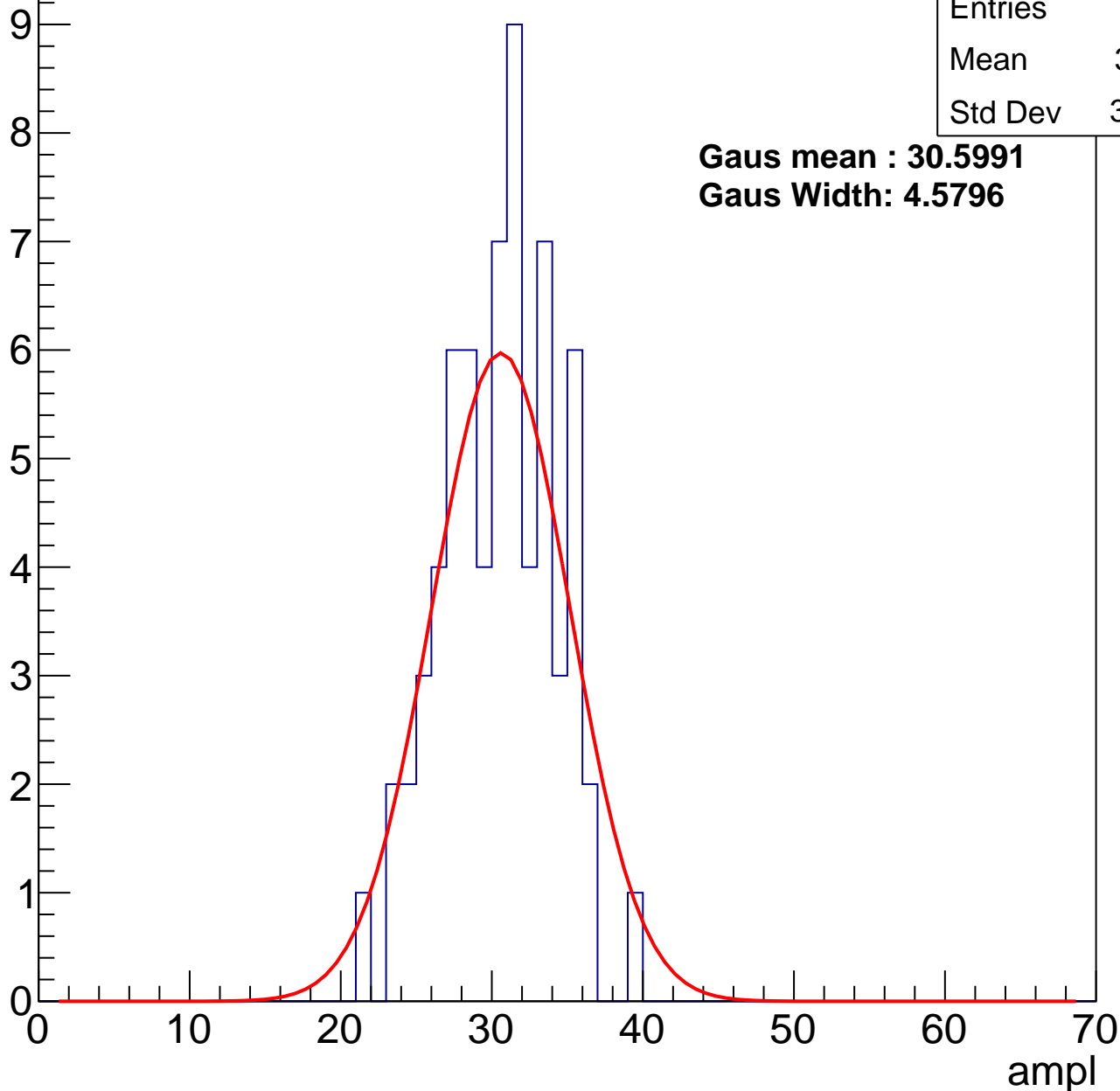
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	30.01
Std Dev	3.687

Gaus mean : 30.5991

Gaus Width: 4.5796



B1L101S, U15-ch101, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	69
Mean	36.7
Std Dev	3.381

Gaus mean : 37.3863

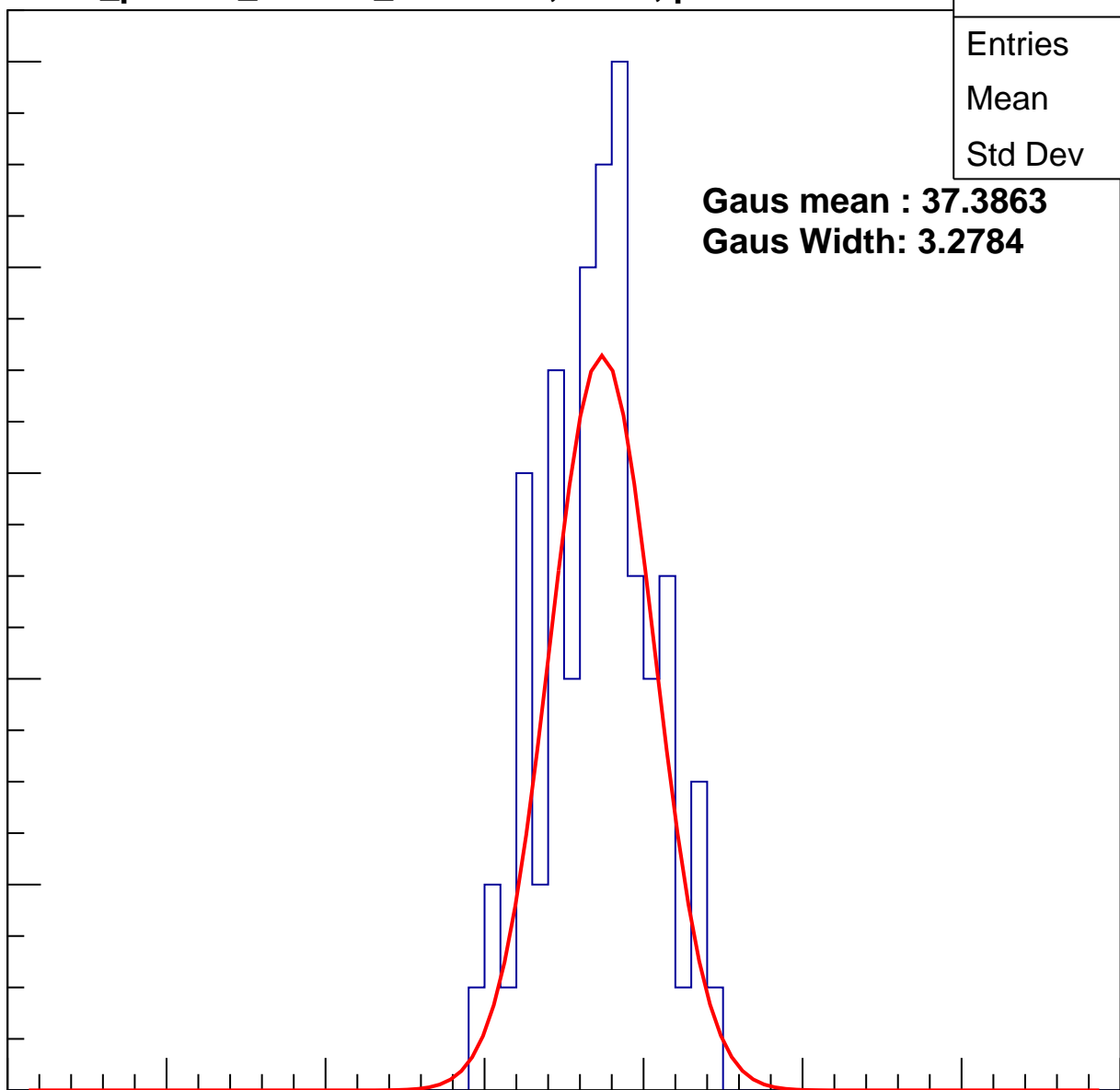
Gaus Width: 3.2784

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch101, adc2

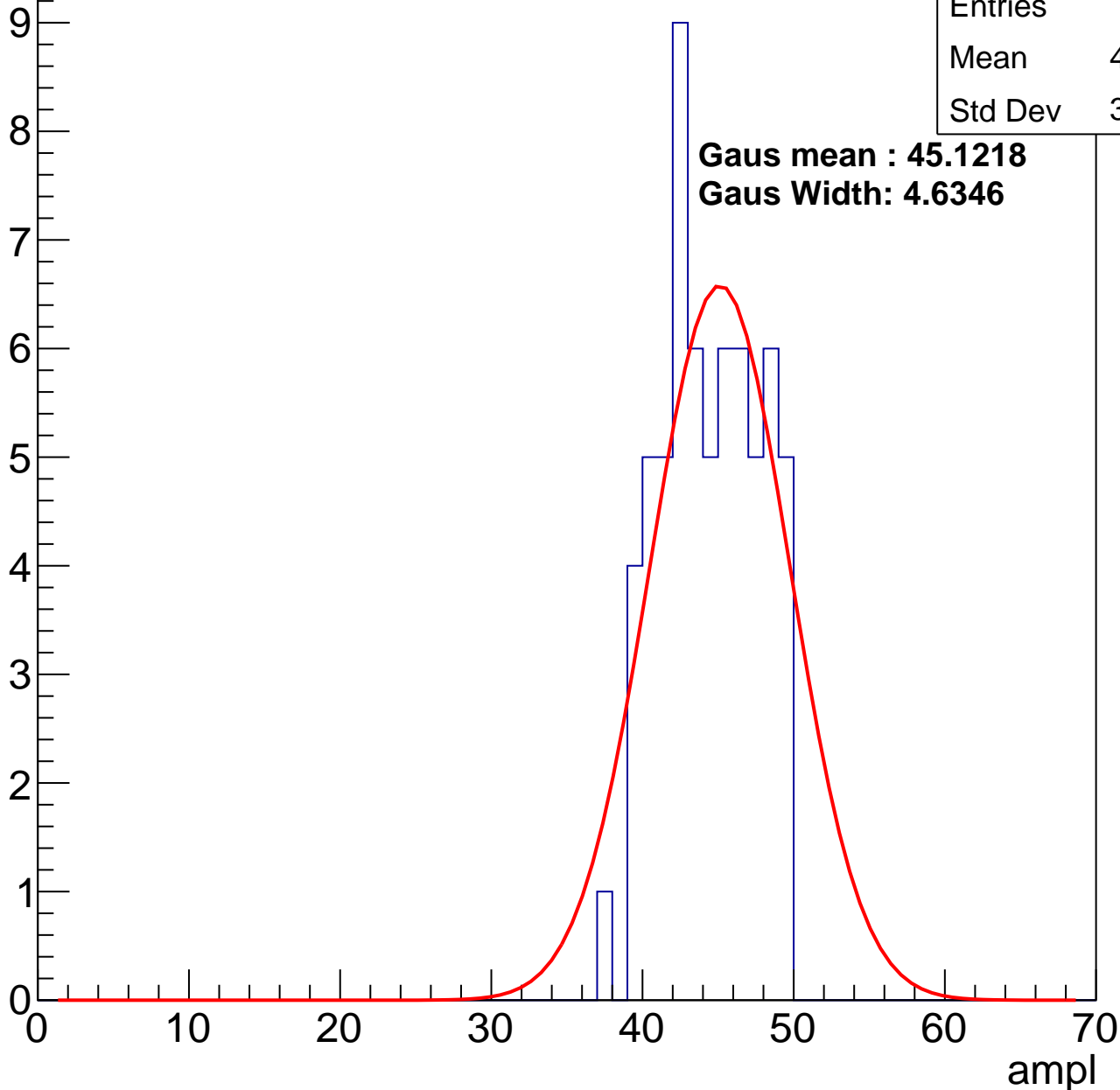
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	43.94
Std Dev	3.116

Gaus mean : 45.1218

Gaus Width: 4.6346

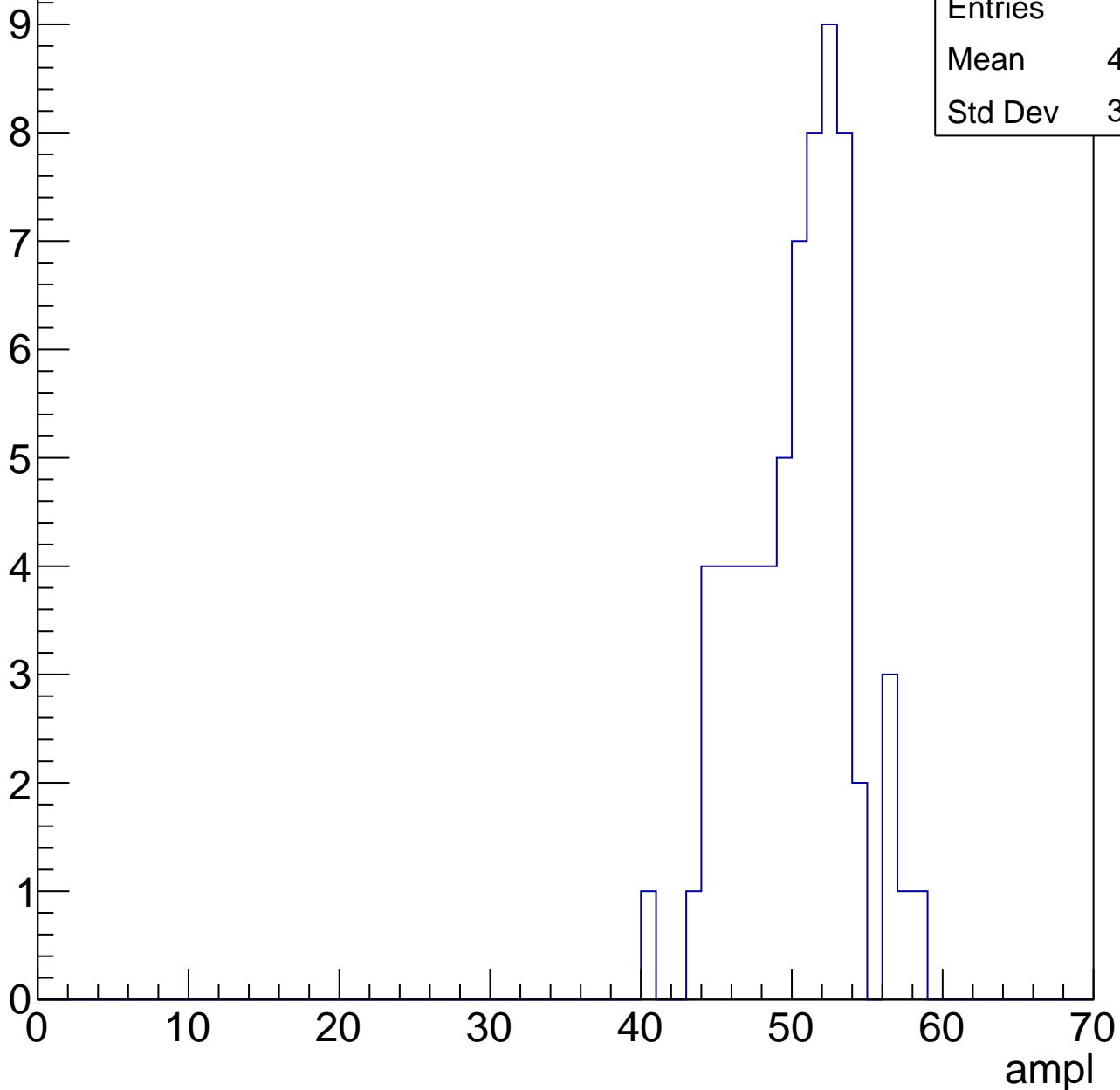


B1L101S, U15-ch101, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	49.83
Std Dev	3.654

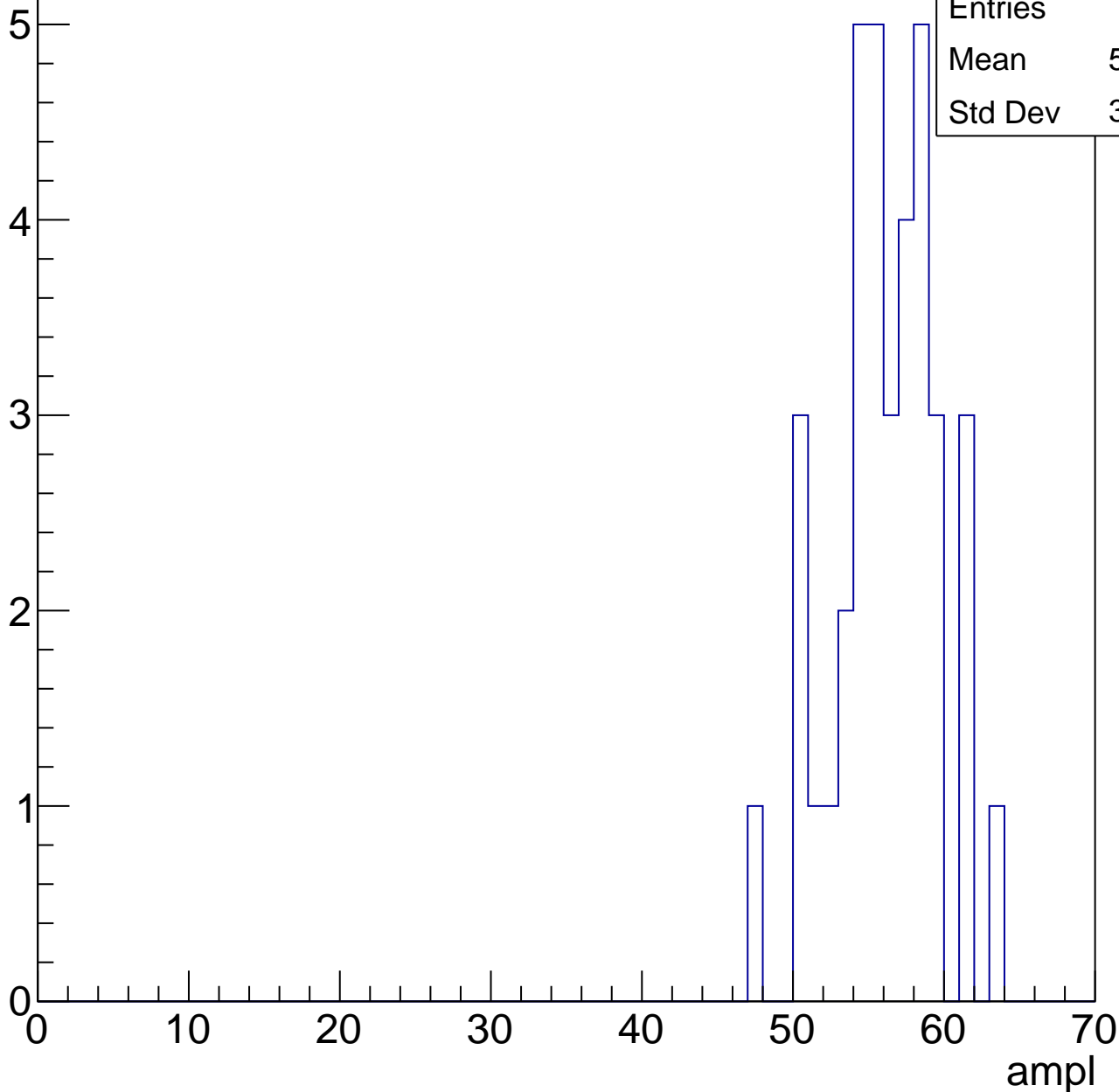


B1L101S, U15-ch101, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	37
Mean	55.68
Std Dev	3.465

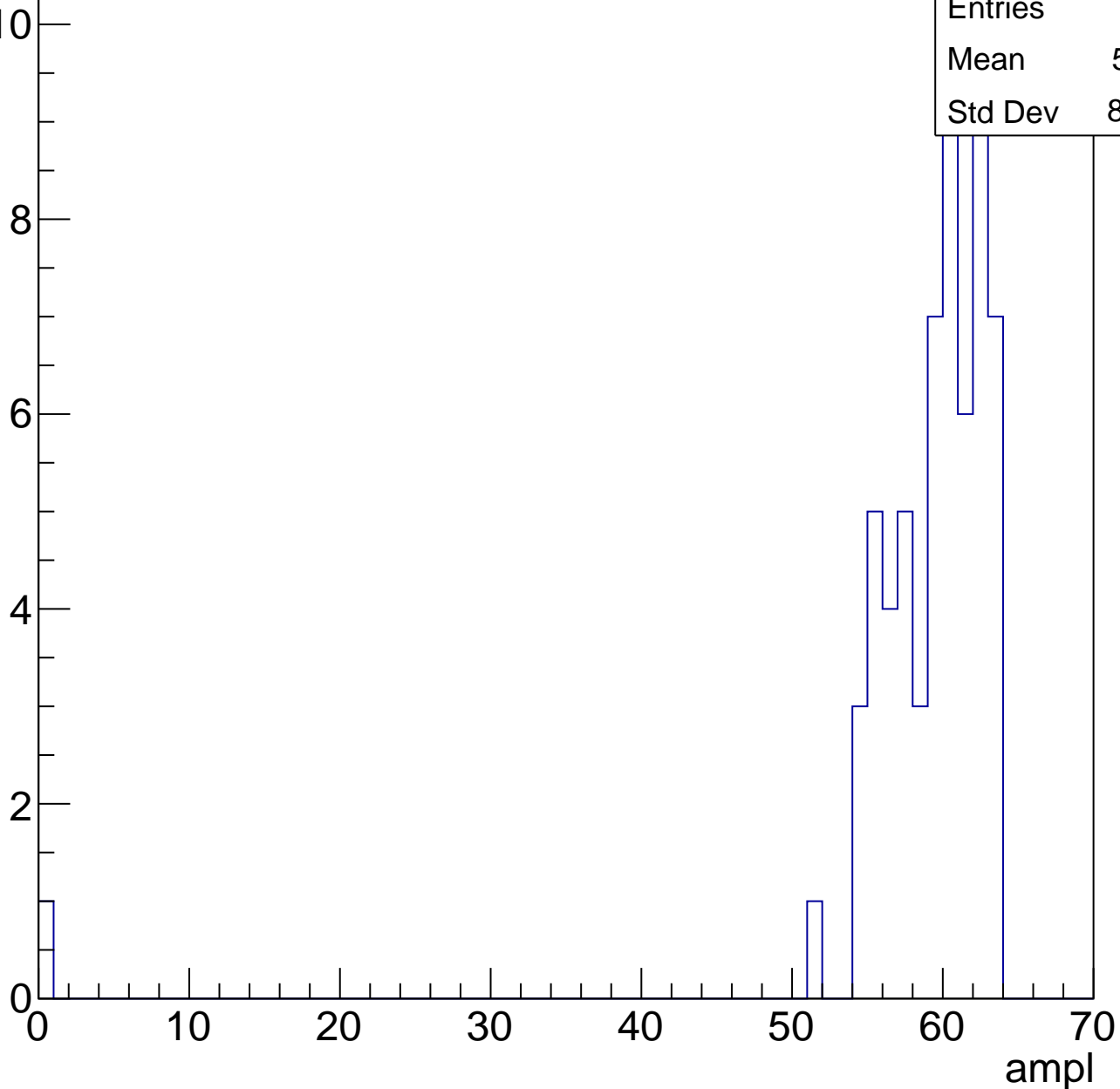


B1L101S, U15-ch101, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	58.21
Std Dev	8.053



B1L101S, U15-ch101, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries	4
Mean	60.5
Std Dev	1.803

B1L101S, U15-ch101, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch102, adc0

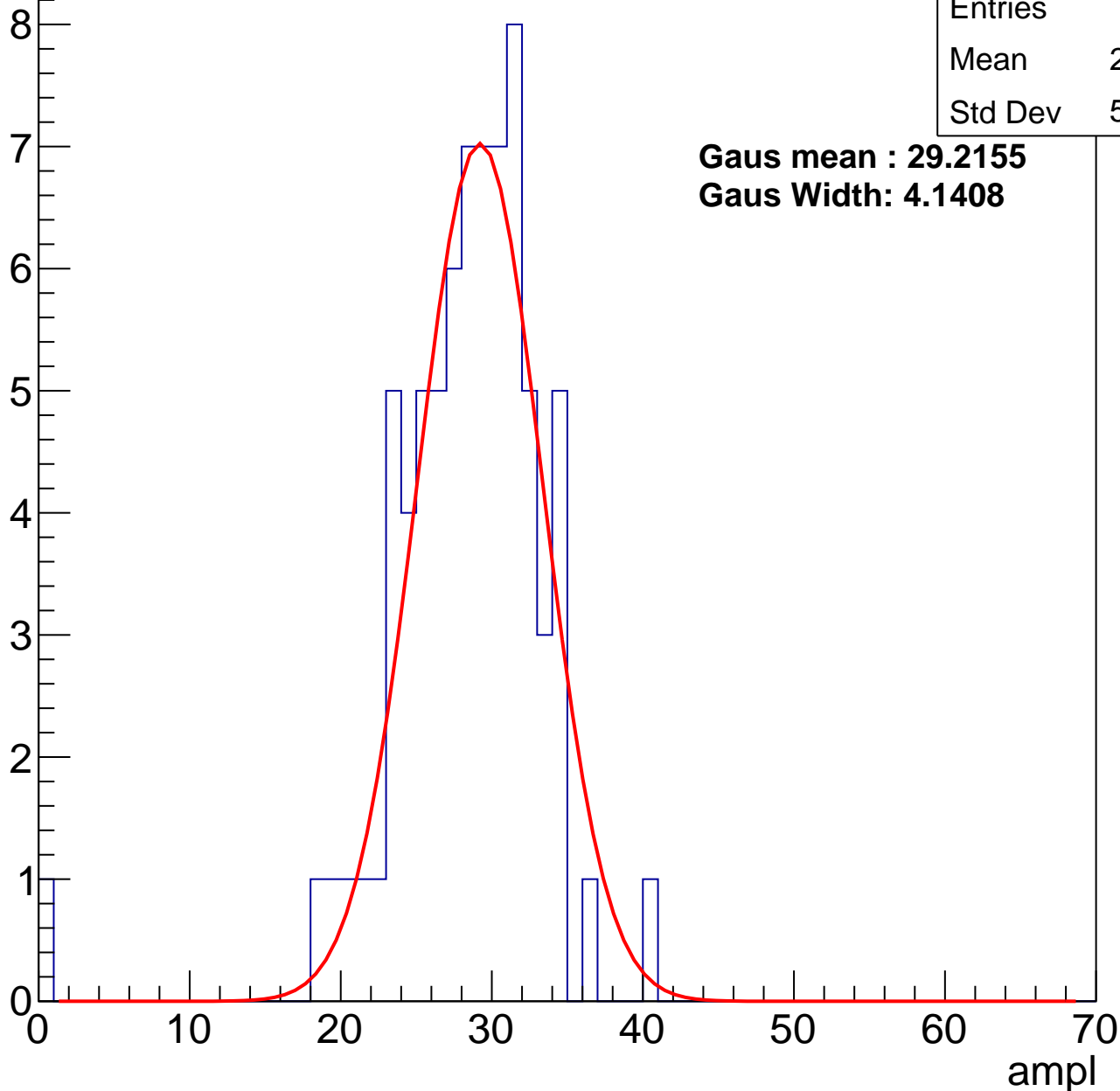
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	27.87
Std Dev	5.193

Gaus mean : 29.2155

Gaus Width: 4.1408



B1L101S, U15-ch102, adc1

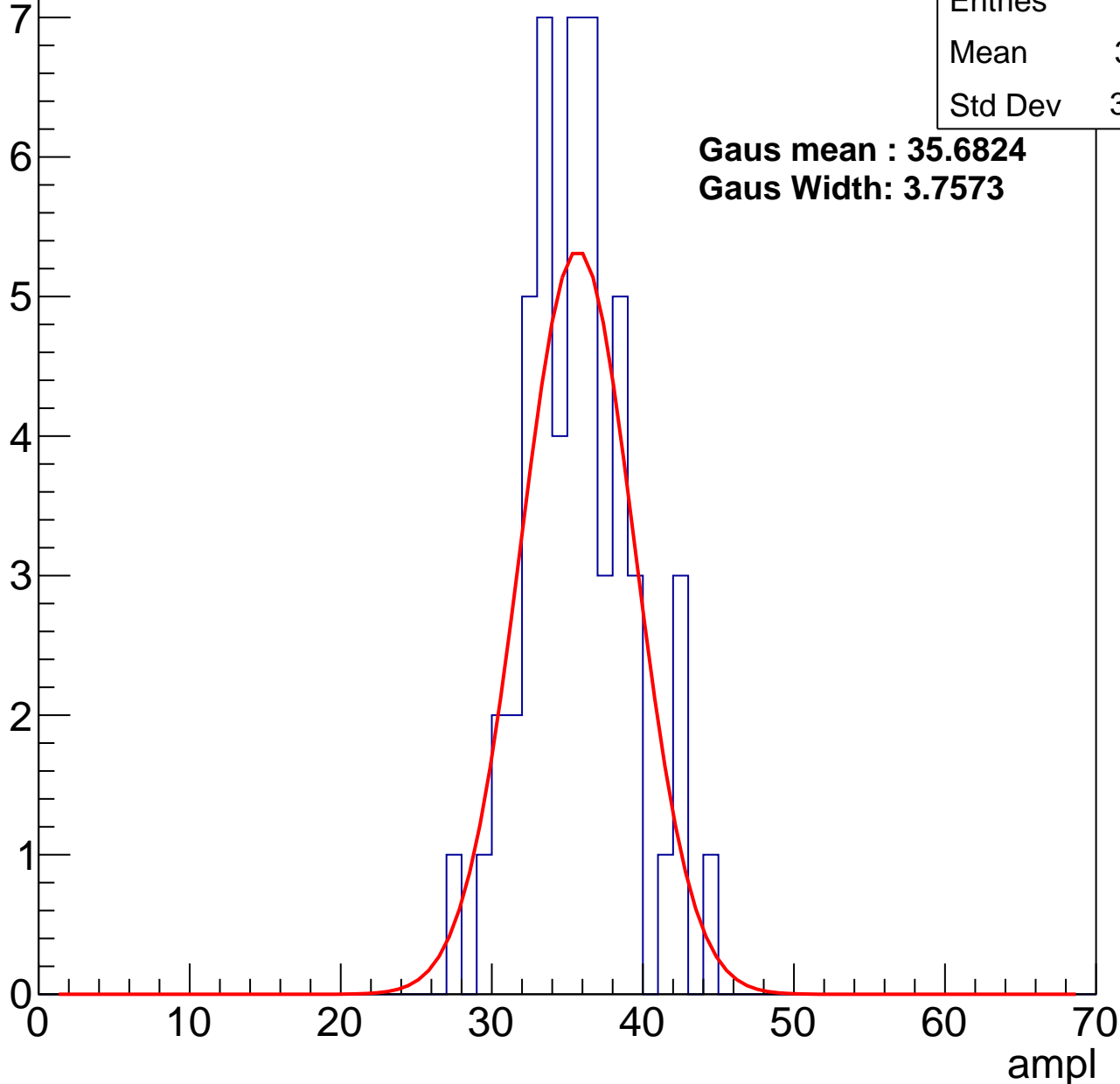
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	35.21
Std Dev	3.488

Gaus mean : 35.6824

Gaus Width: 3.7573



B1L101S, U15-ch102, adc2

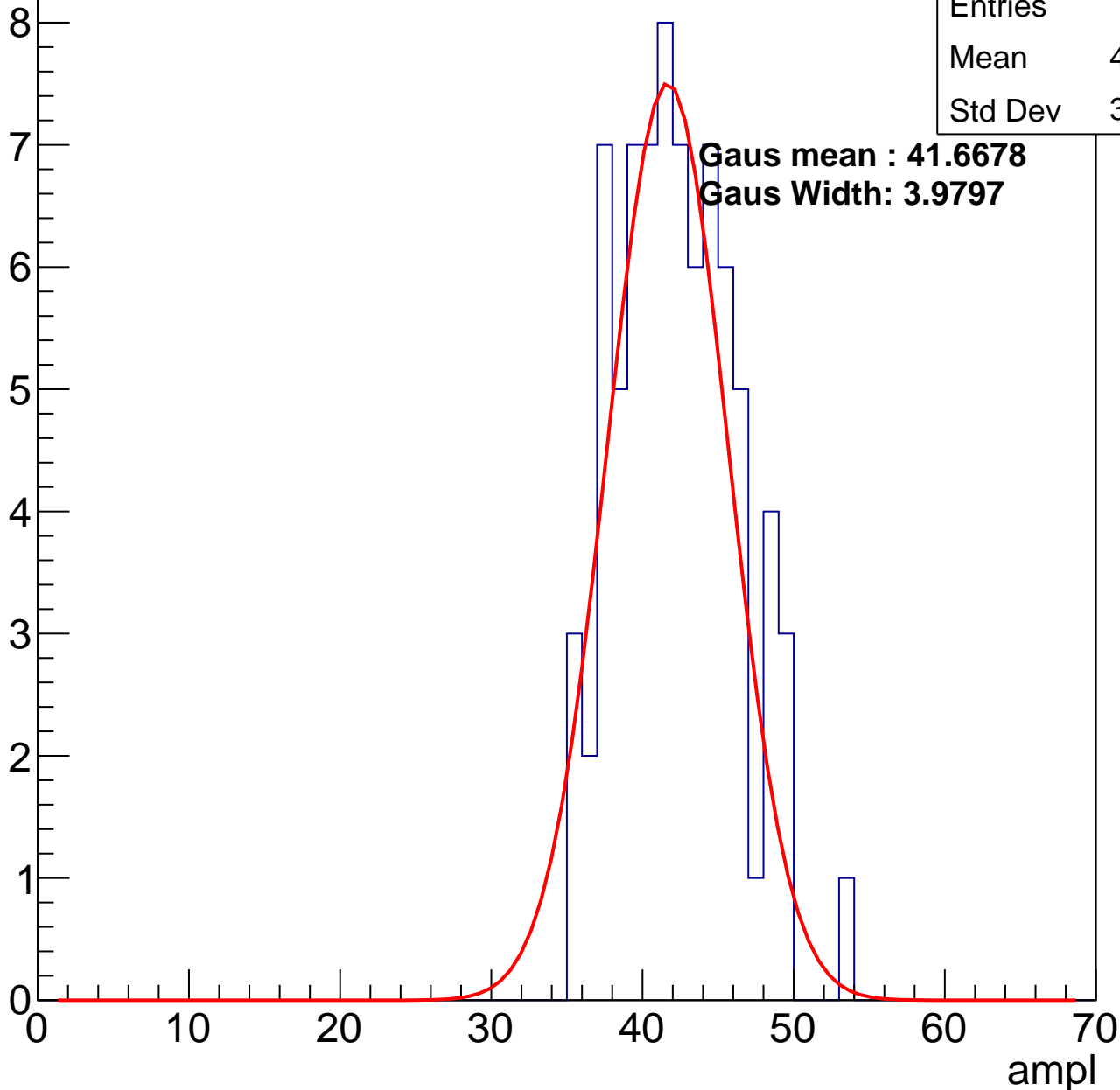
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	79
Mean	41.85
Std Dev	3.859

Gaus mean : 41.6678

Gaus Width: 3.9797

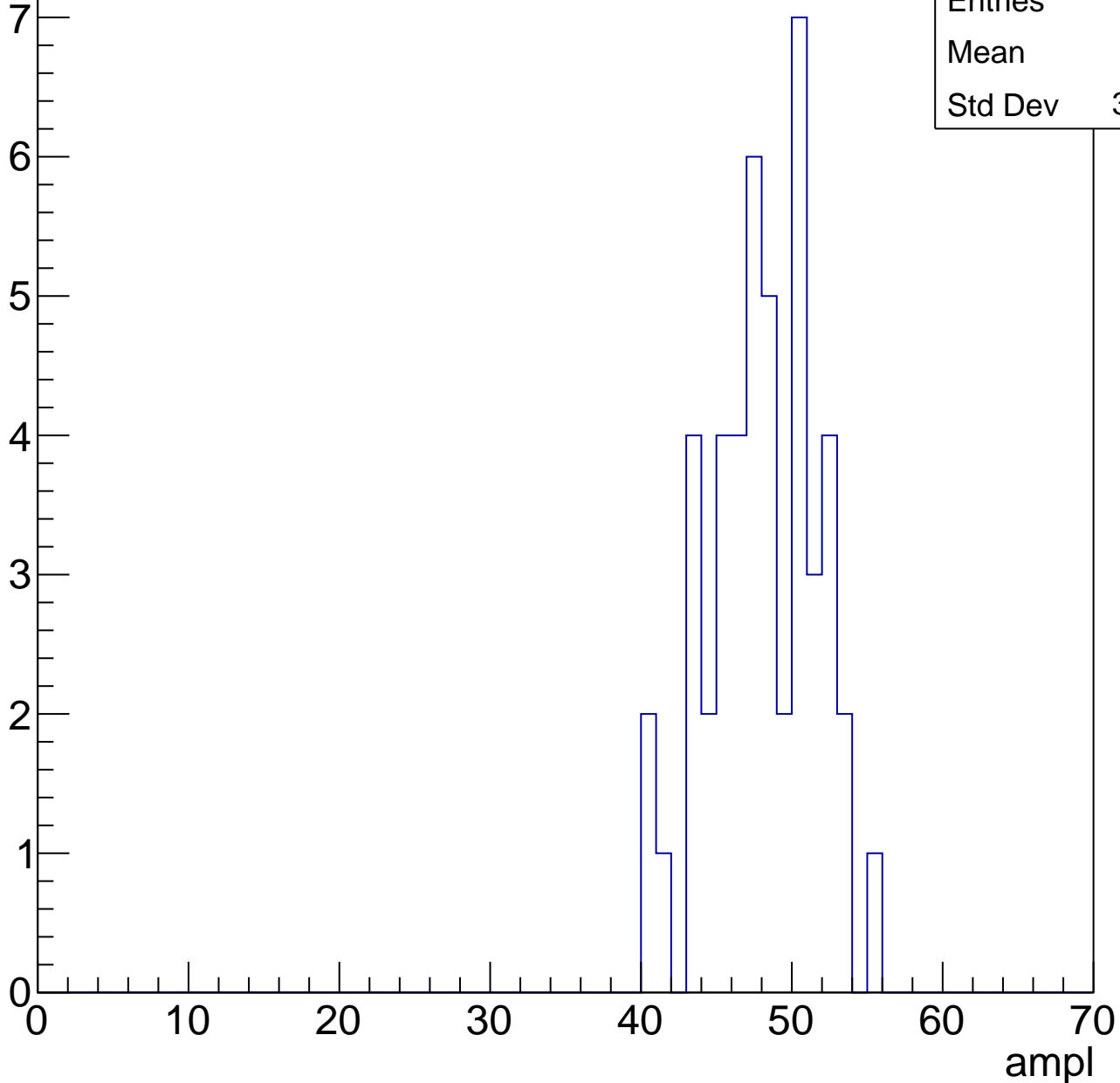


B1L101S, U15-ch102, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	47.6
Std Dev	3.511

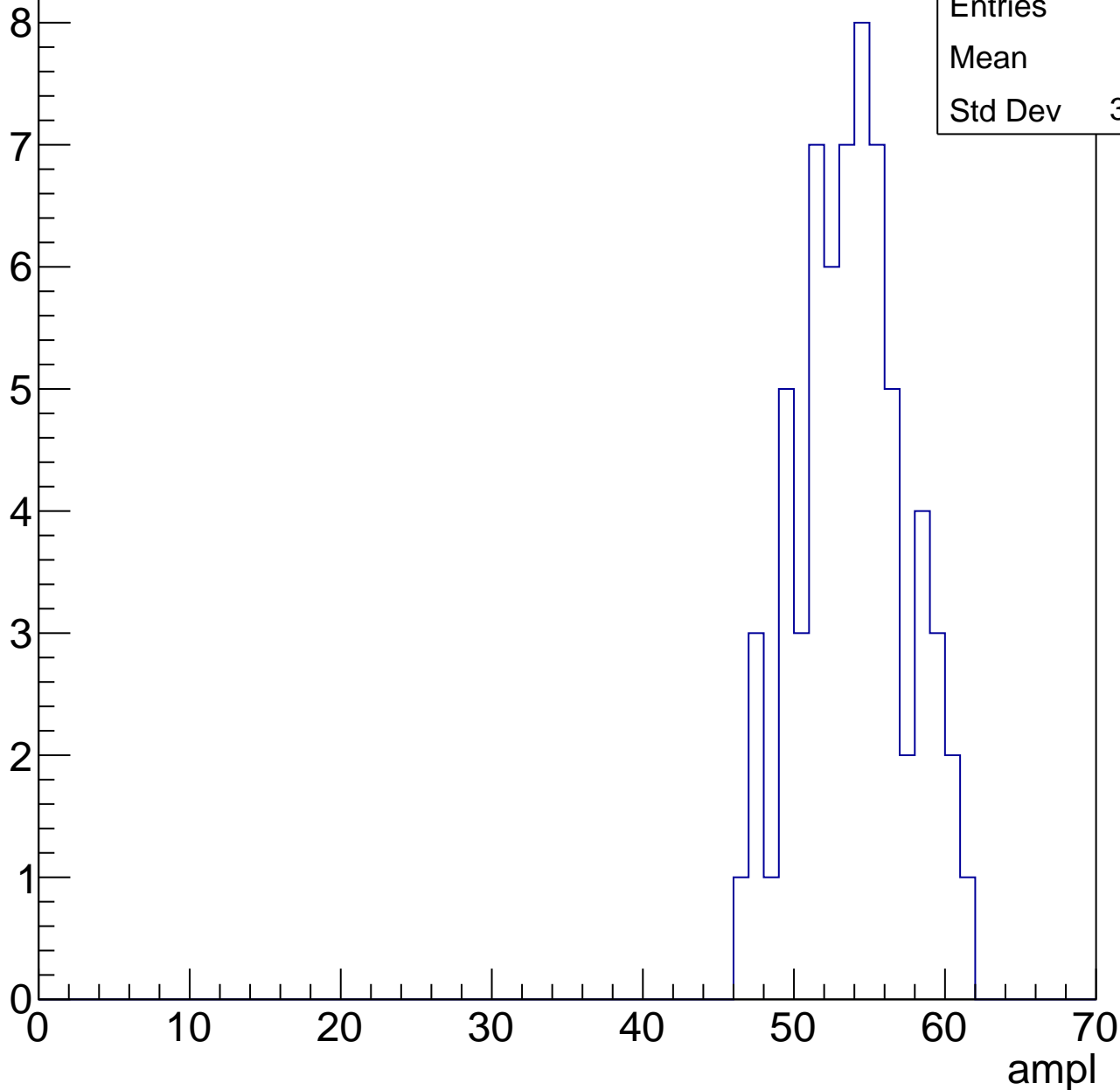


B1L101S, U15-ch102, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

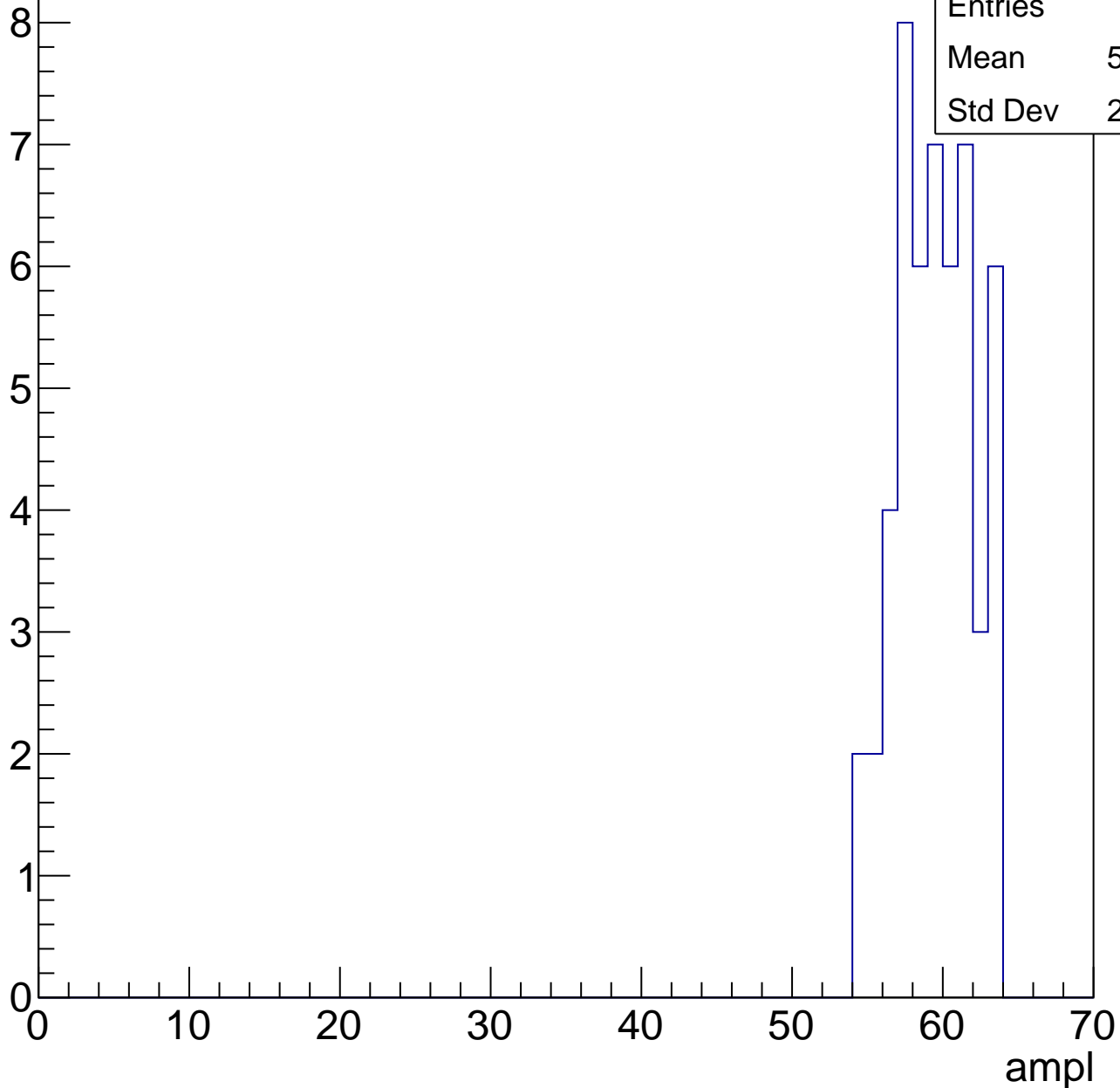
Entries	65
Mean	53.4
Std Dev	3.499



B1L101S, U15-ch102, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

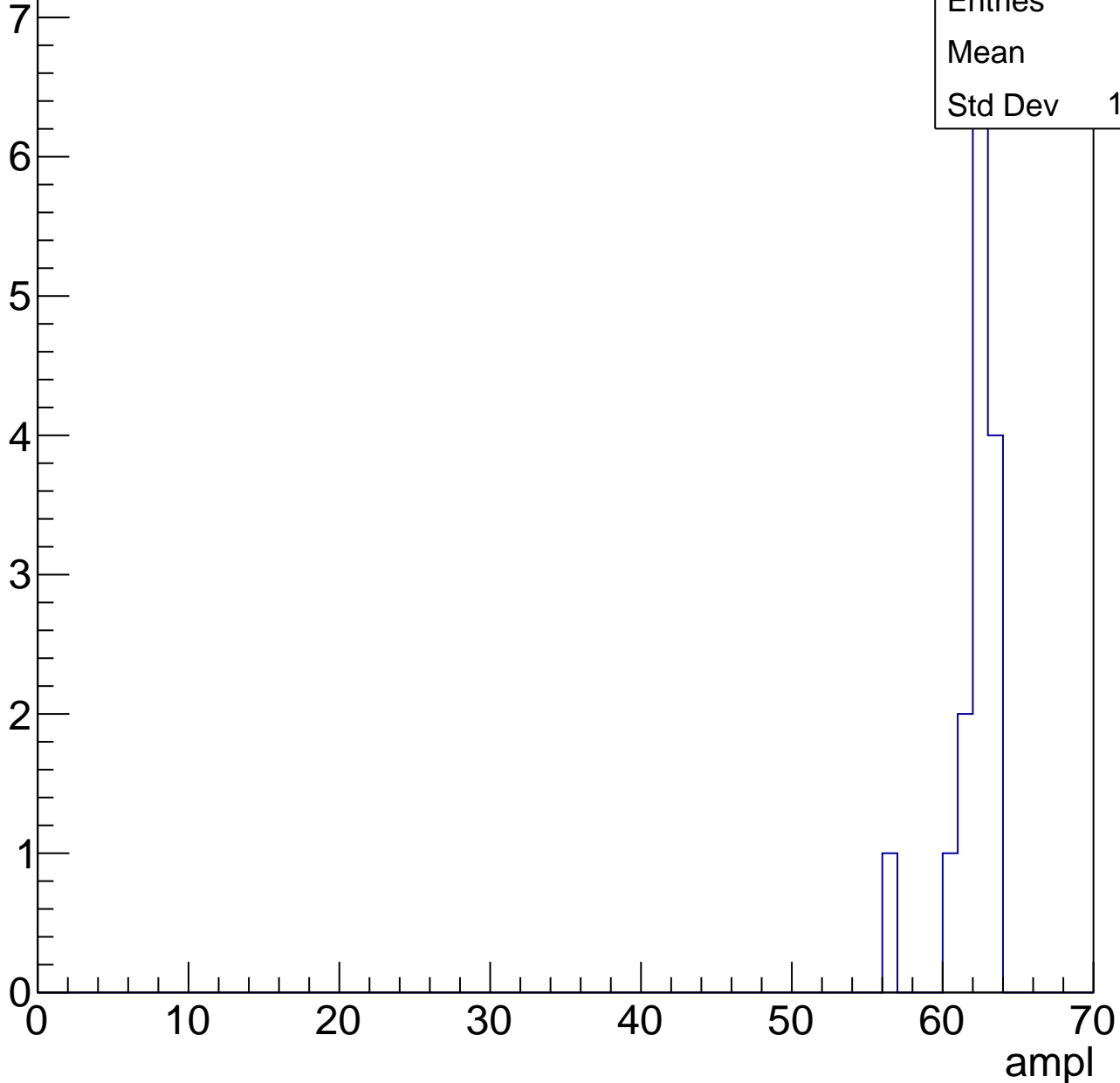


B1L101S, U15-ch102, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

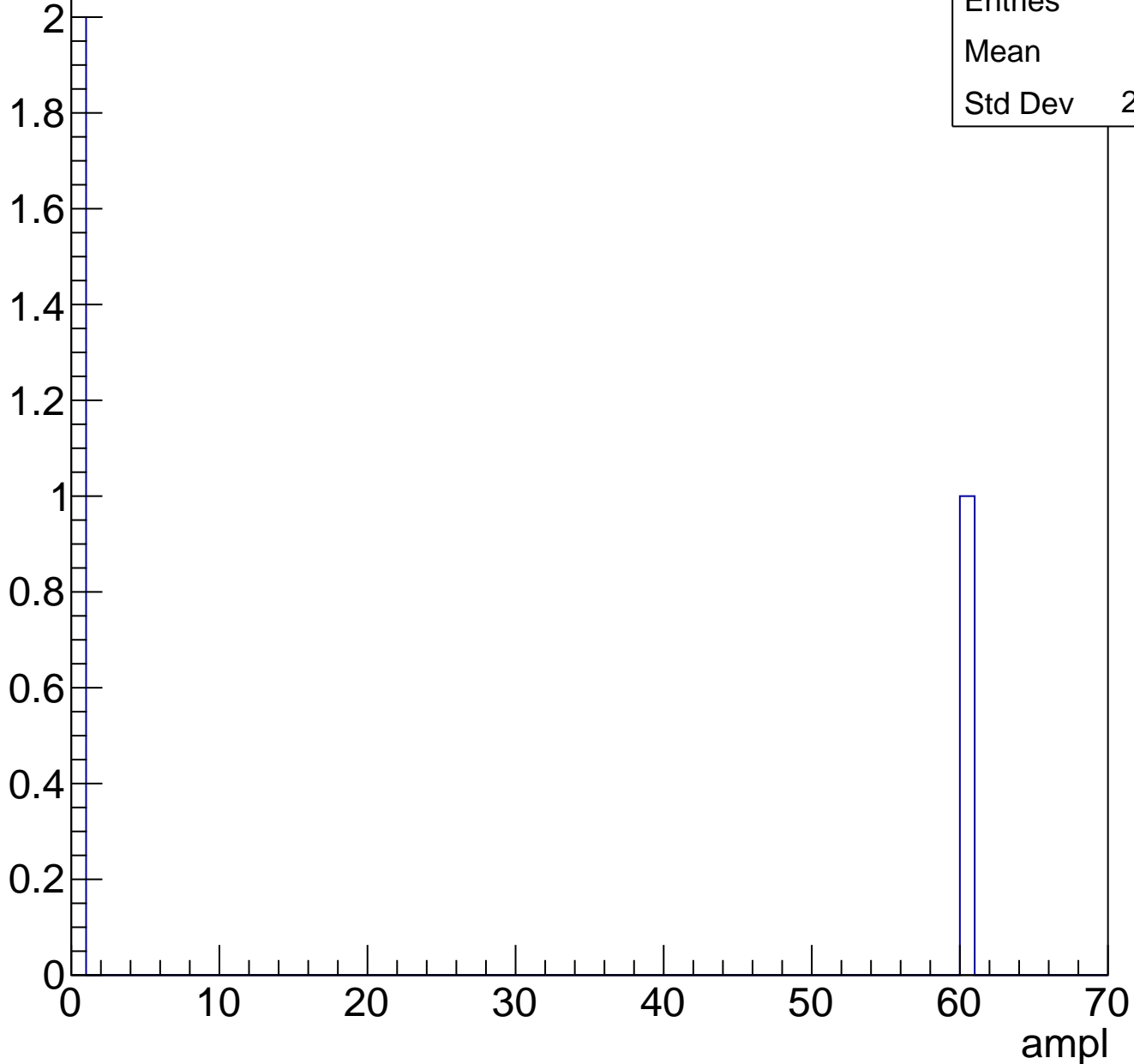
Entries	15
Mean	61.6
Std Dev	1.705



B1L101S, U15-ch102, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch103, adc0

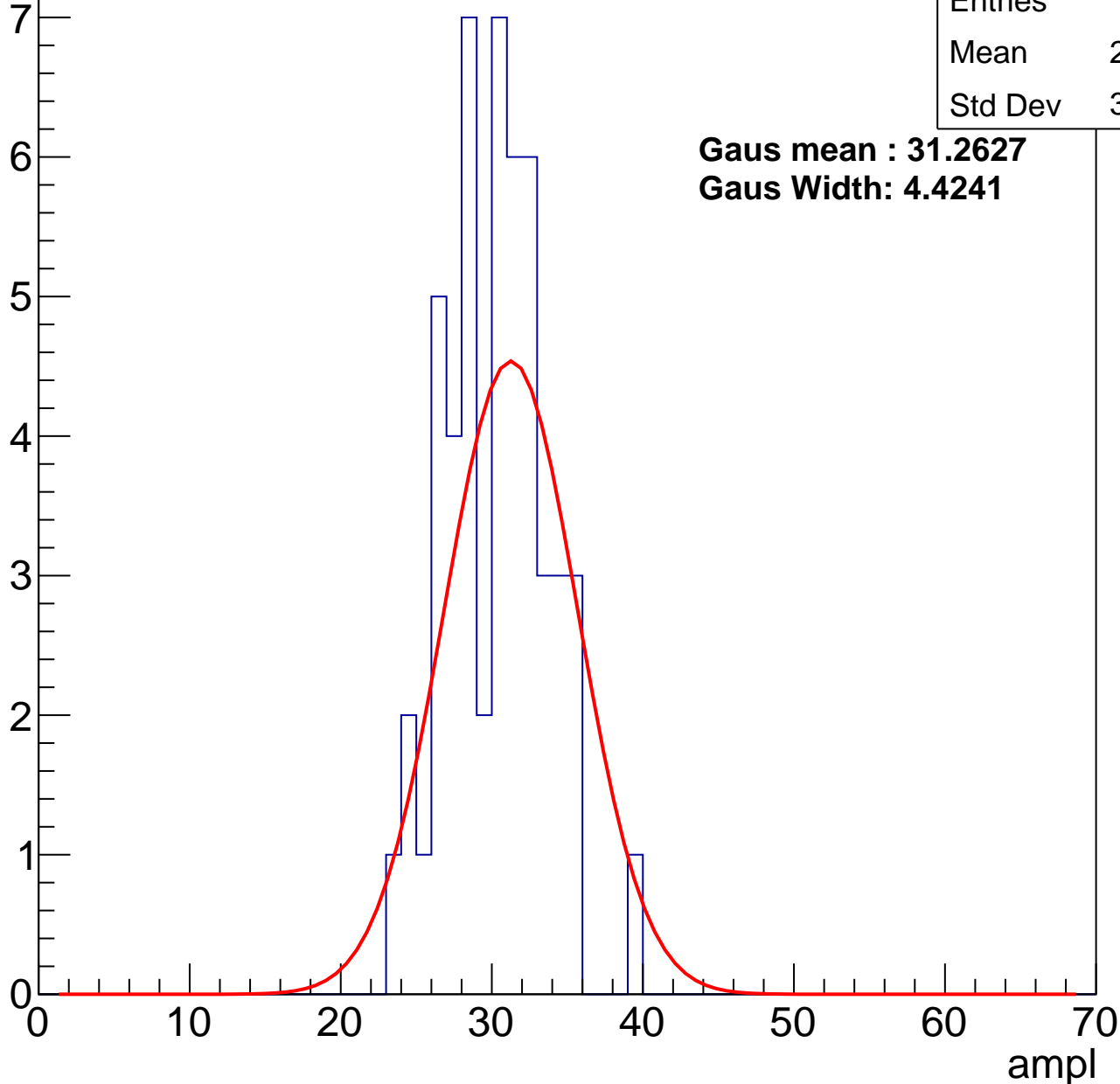
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	29.82
Std Dev	3.288

Gaus mean : 31.2627

Gaus Width: 4.4241



B1L101S, U15-ch103, adc1

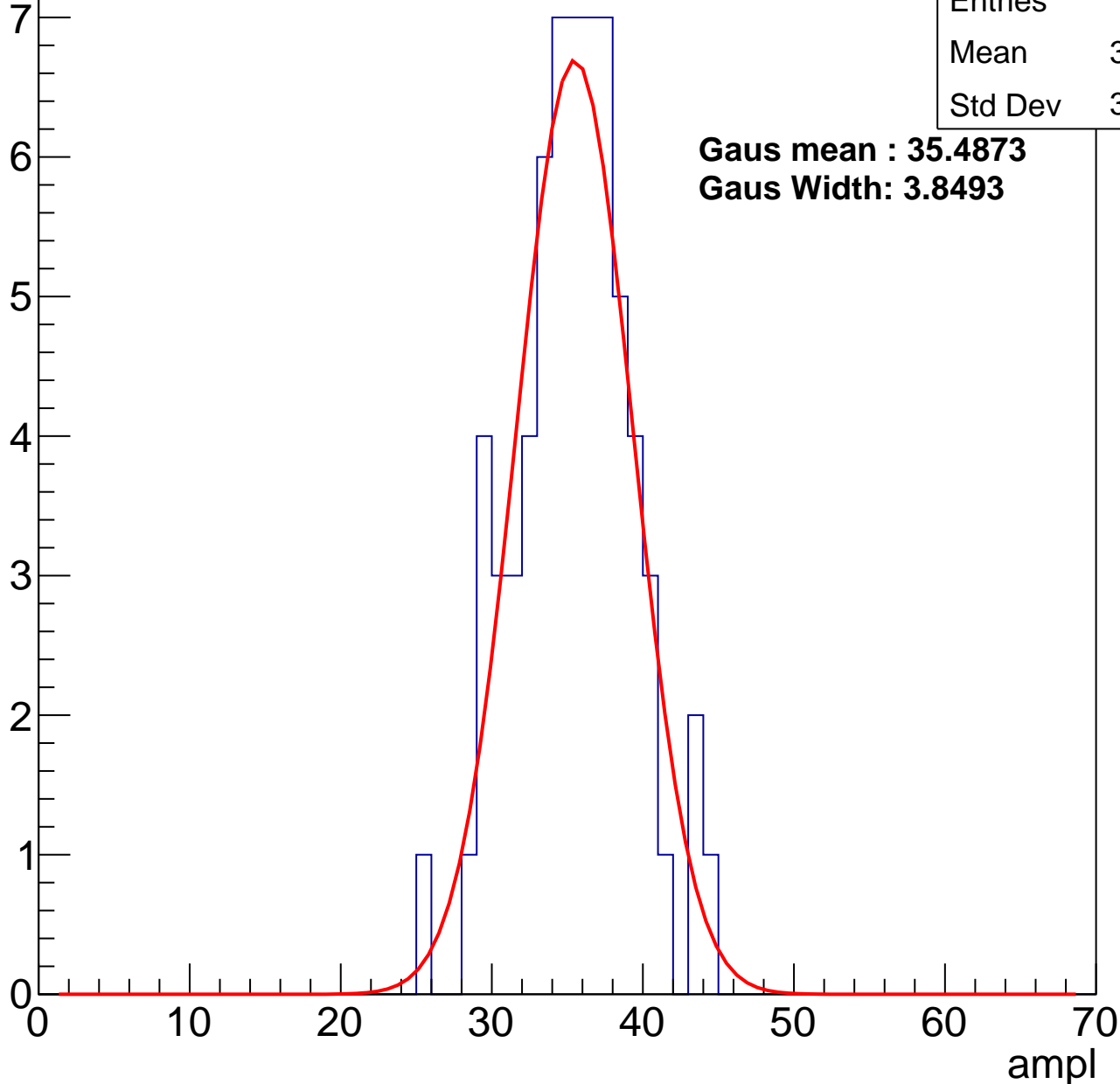
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.98
Std Dev	3.788

Gaus mean : 35.4873

Gaus Width: 3.8493



B1L101S, U15-ch103, adc2

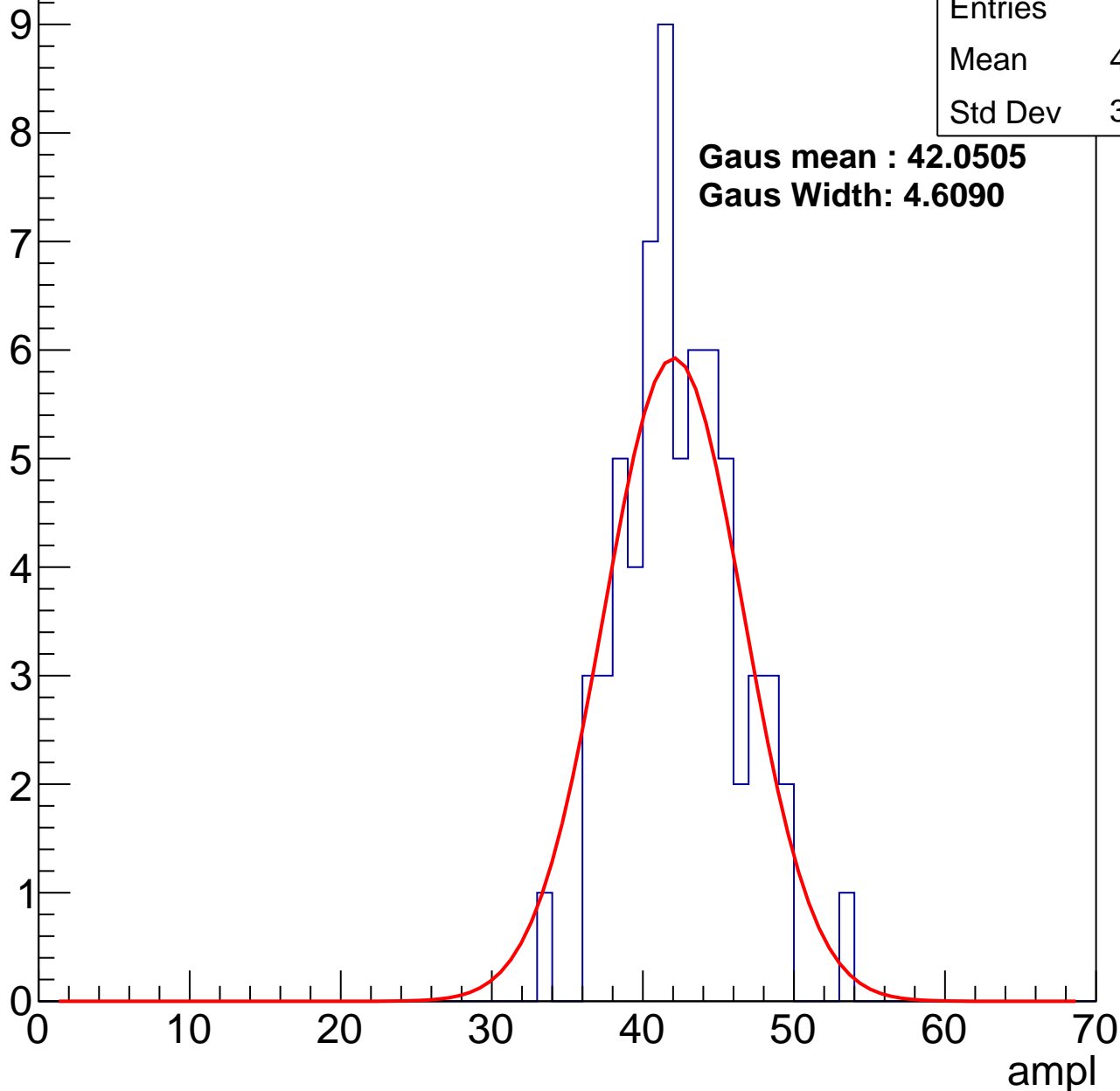
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	42.03
Std Dev	3.774

Gaus mean : 42.0505

Gaus Width: 4.6090

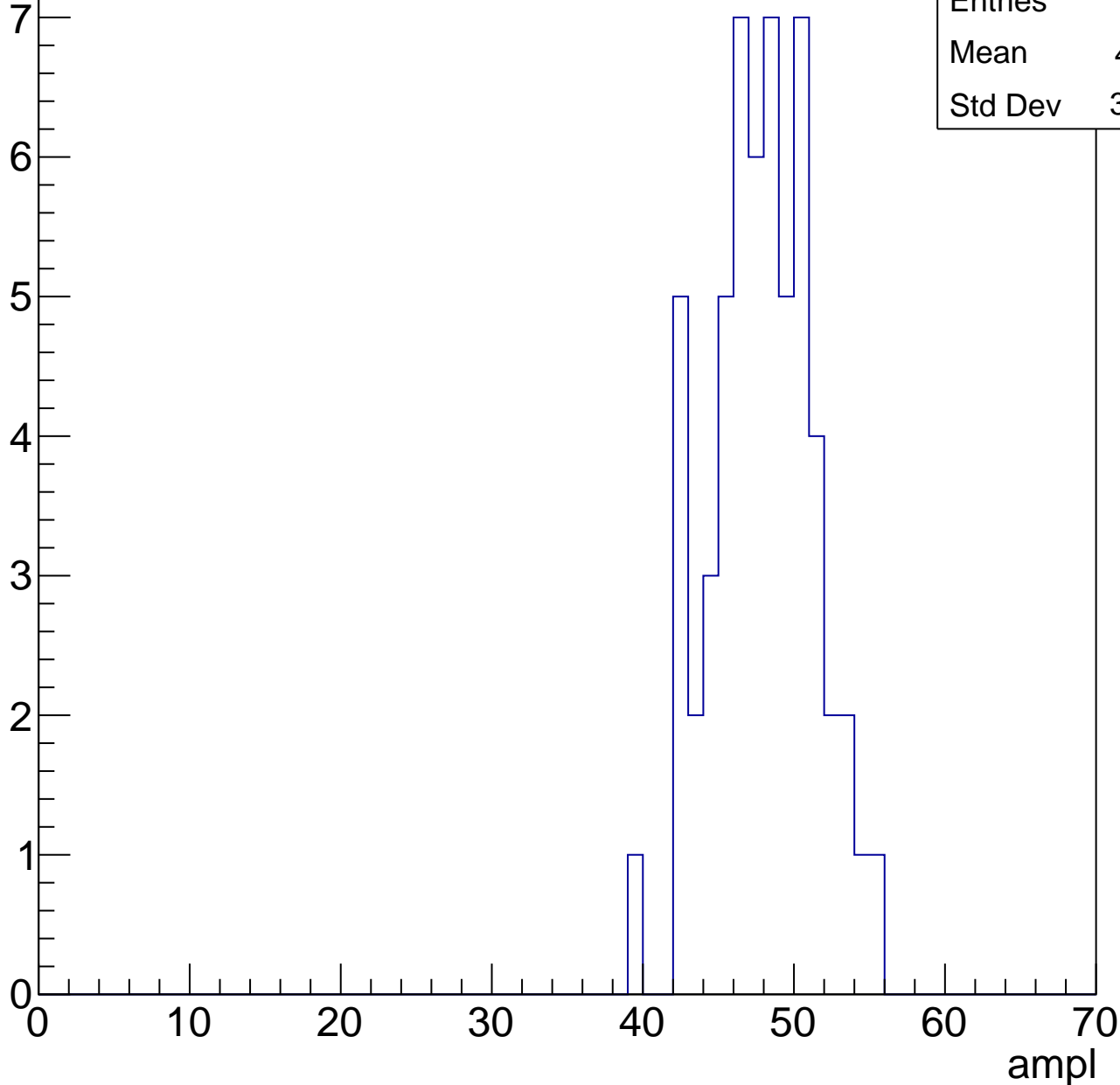


B1L101S, U15-ch103, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	47.41
Std Dev	3.358

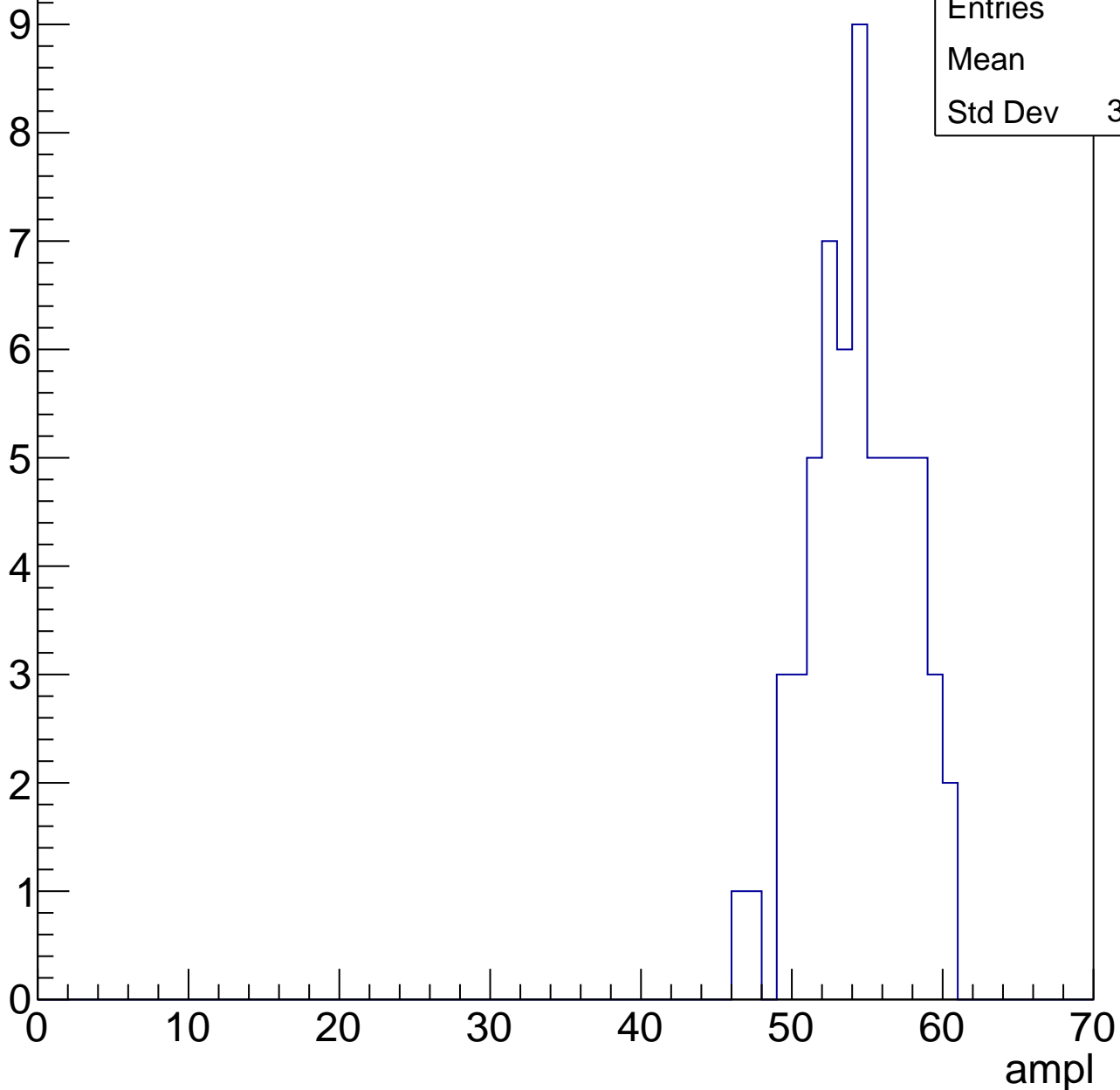


B1L101S, U15-ch103, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	54
Std Dev	3.194

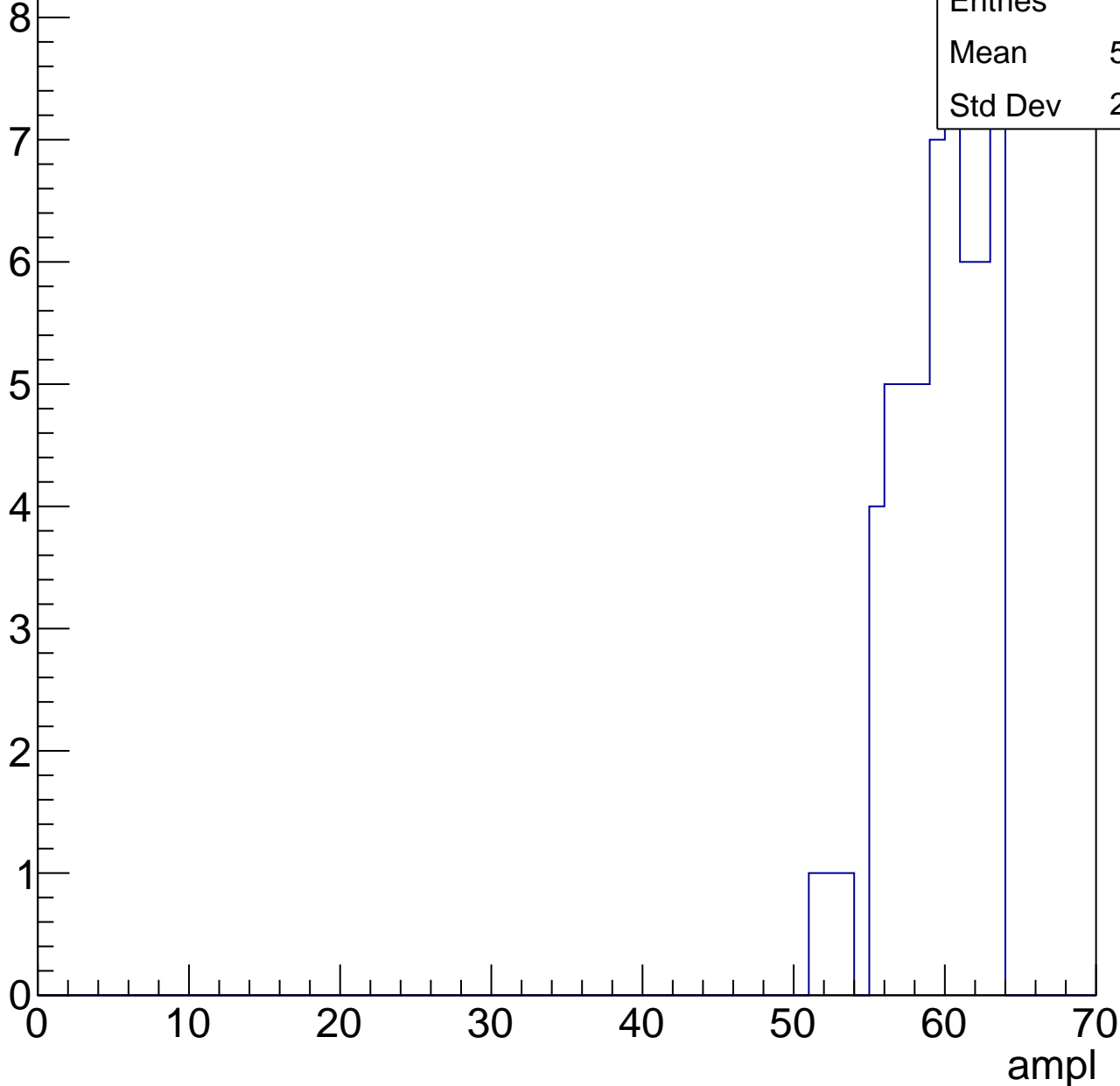


B1L101S, U15-ch103, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

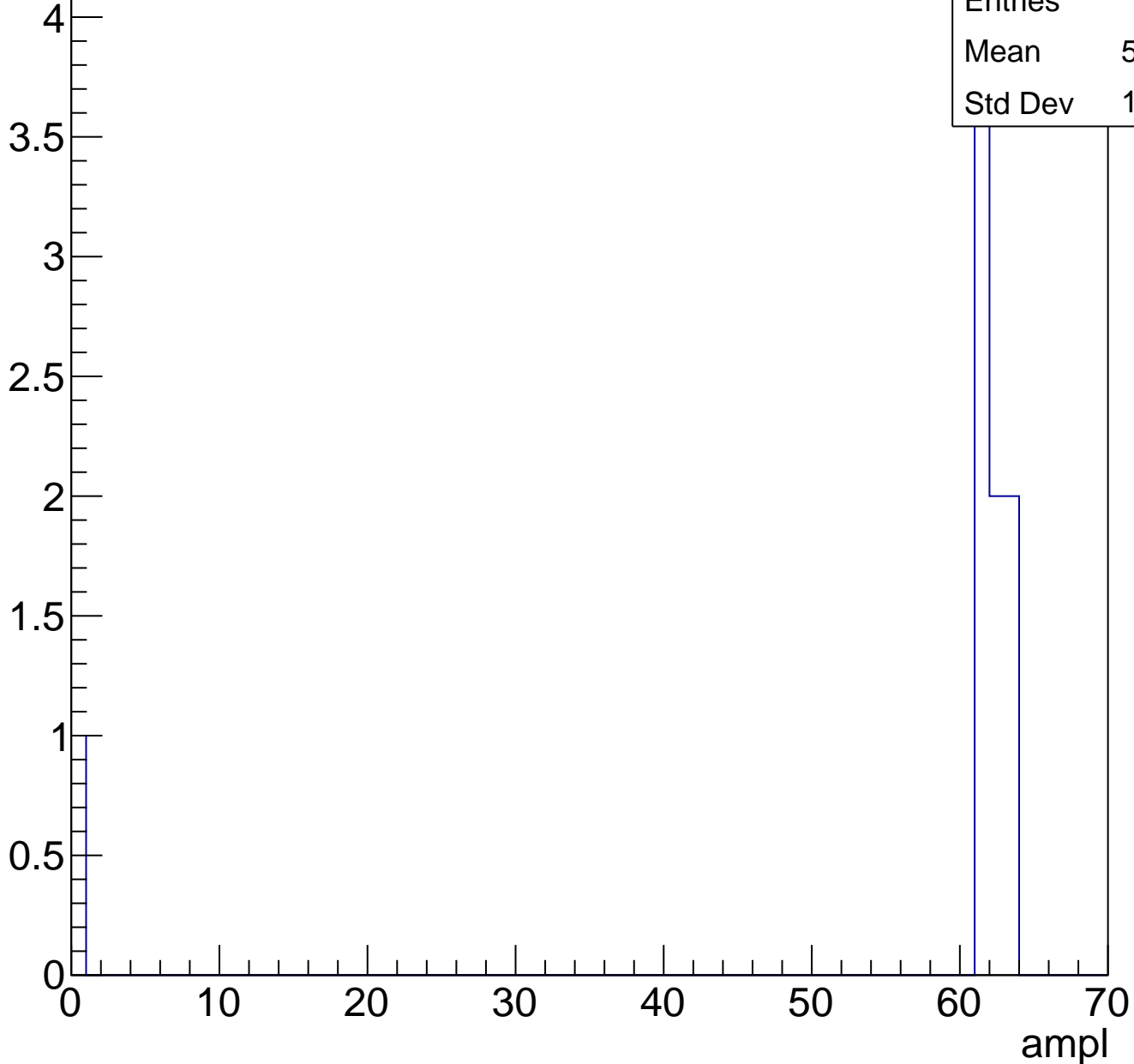
Entries	57
Mean	59.05
Std Dev	2.952



B1L101S, U15-ch103, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch103, adc7

calib_packv5_042523_0143.root, FC#0, port D2

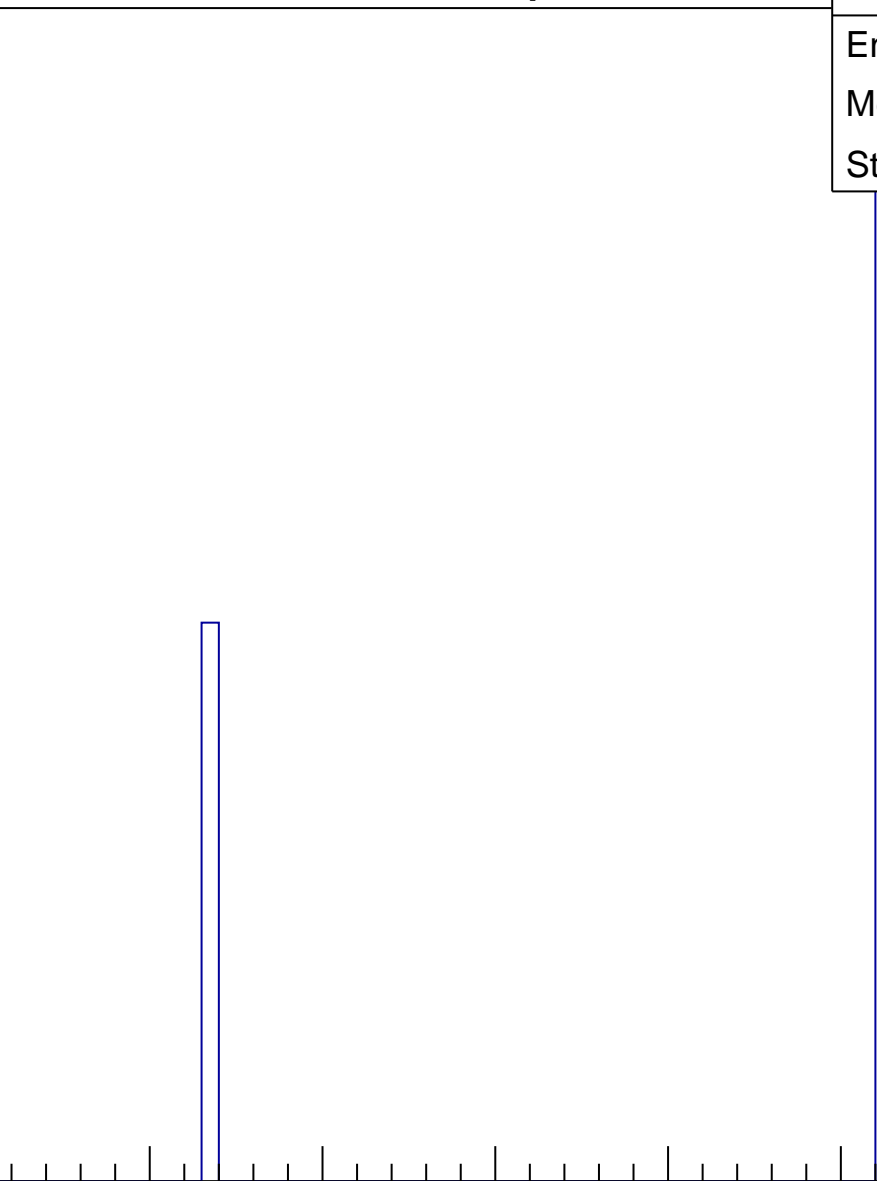
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	49
Std Dev	18.38

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch104, adc0

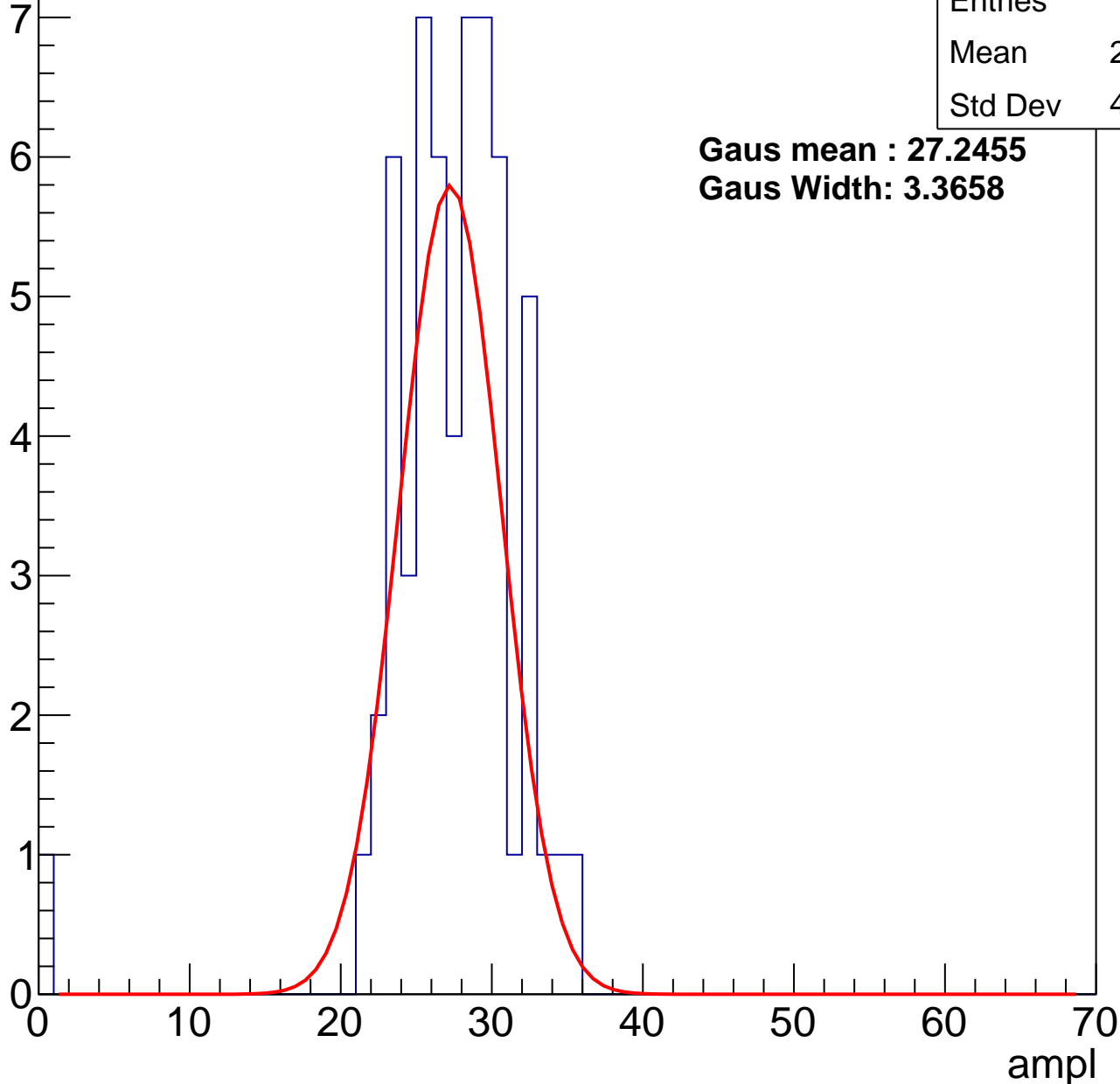
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	26.88
Std Dev	4.784

Gaus mean : 27.2455

Gaus Width: 3.3658



B1L101S, U15-ch104, adc1

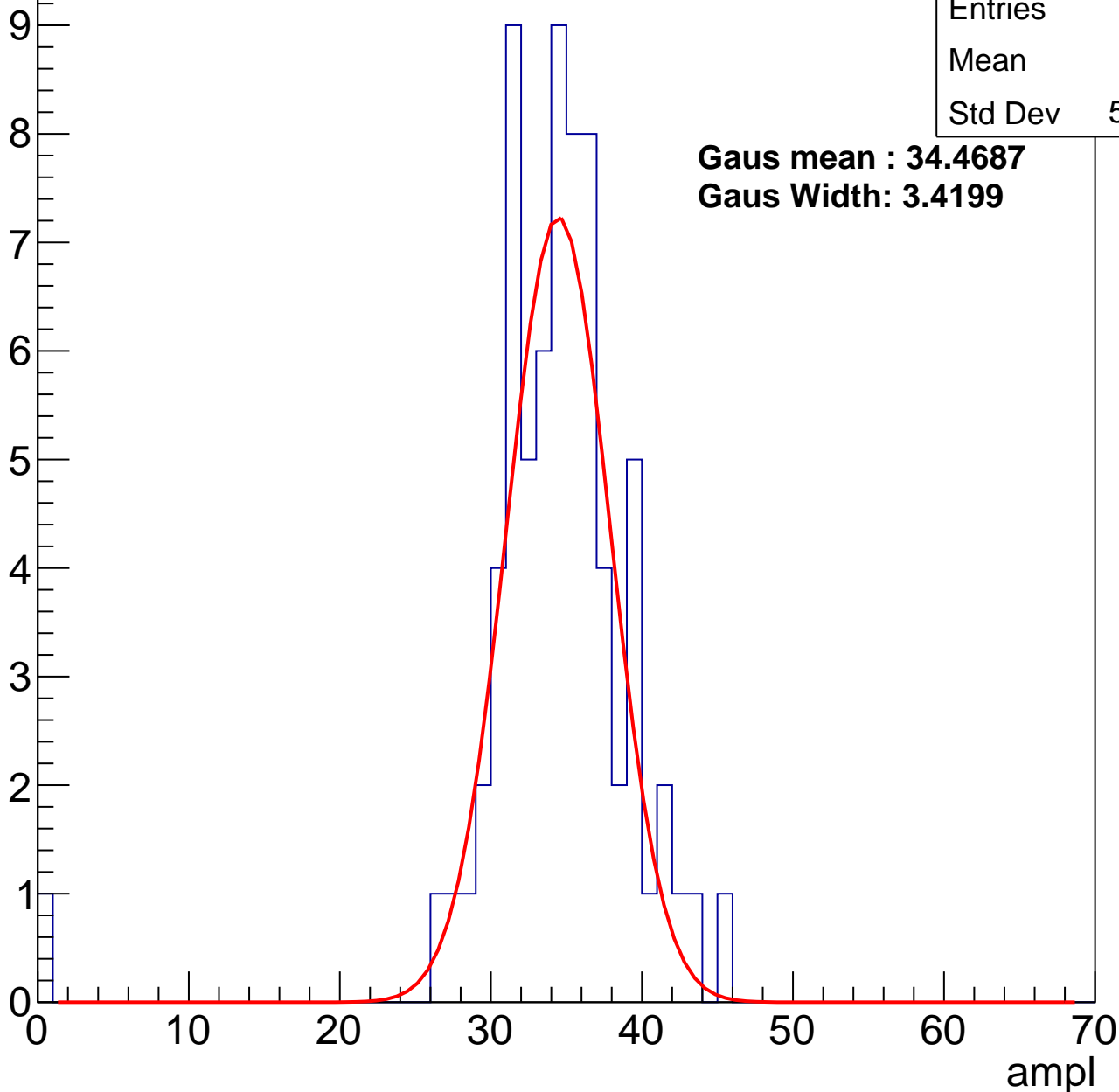
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	33.9
Std Dev	5.475

Gaus mean : 34.4687

Gaus Width: 3.4199



B1L101S, U15-ch104, adc2

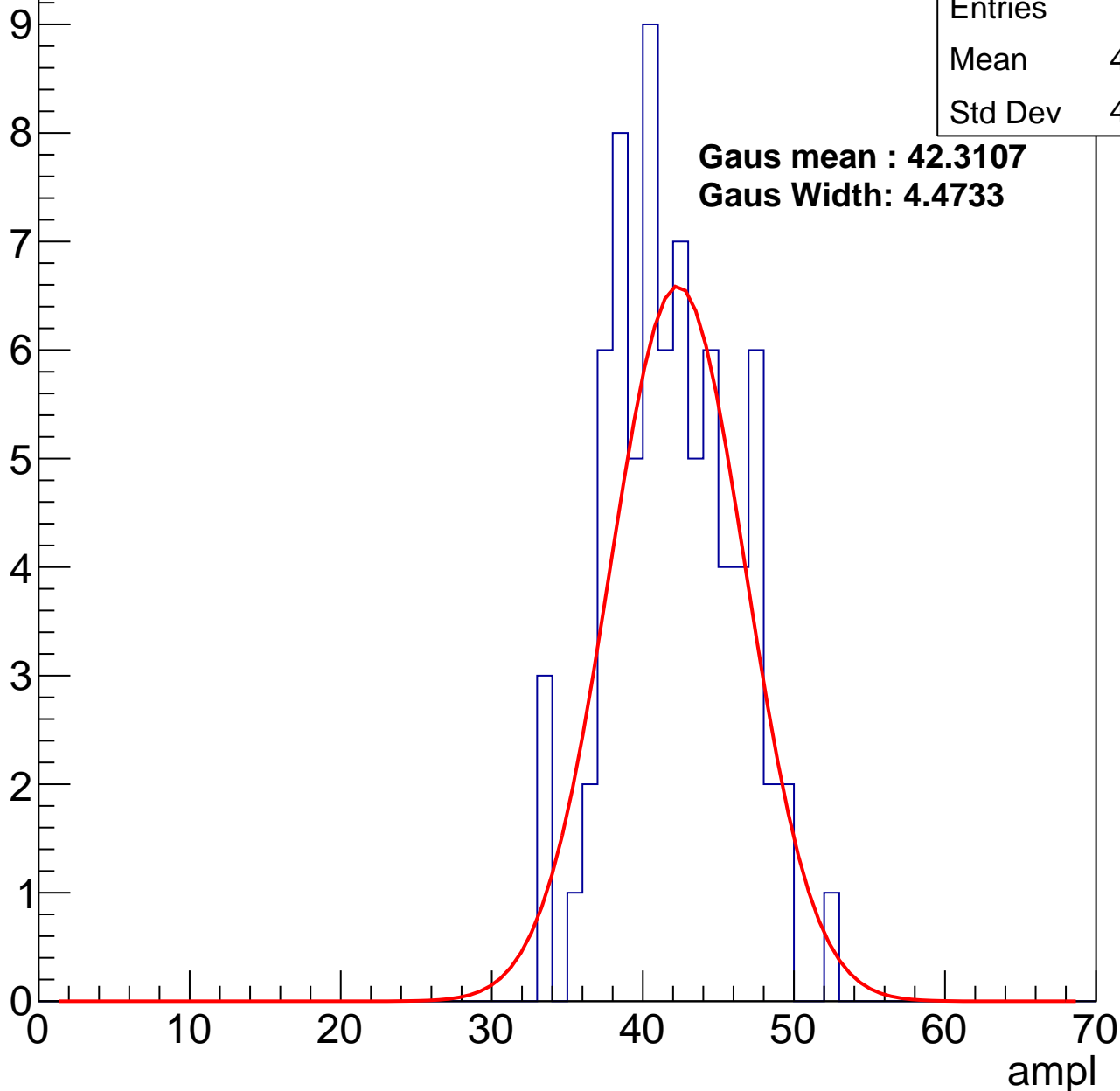
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	41.53
Std Dev	4.047

Gaus mean : 42.3107

Gaus Width: 4.4733

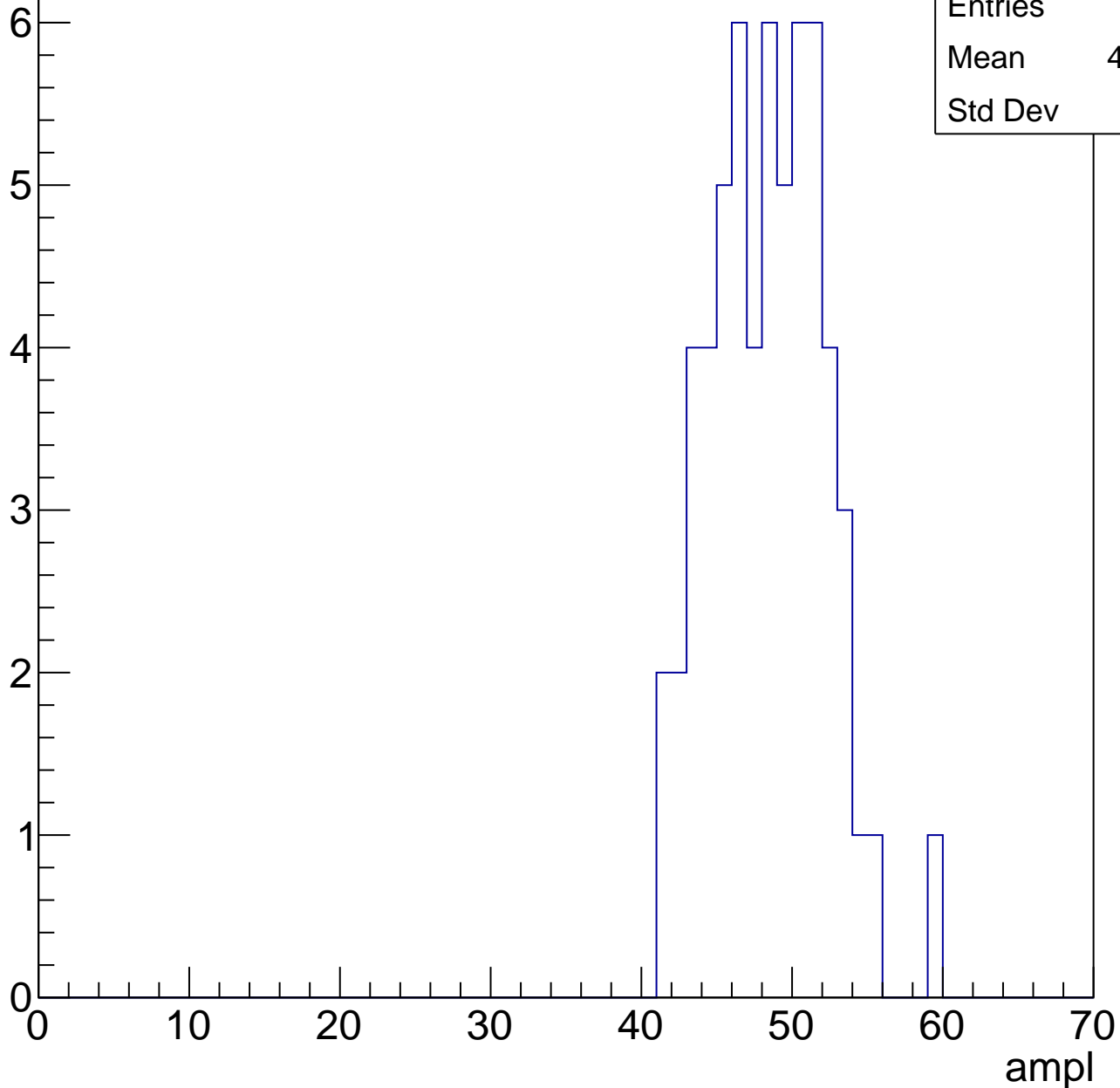


B1L101S, U15-ch104, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	47.95
Std Dev	3.73

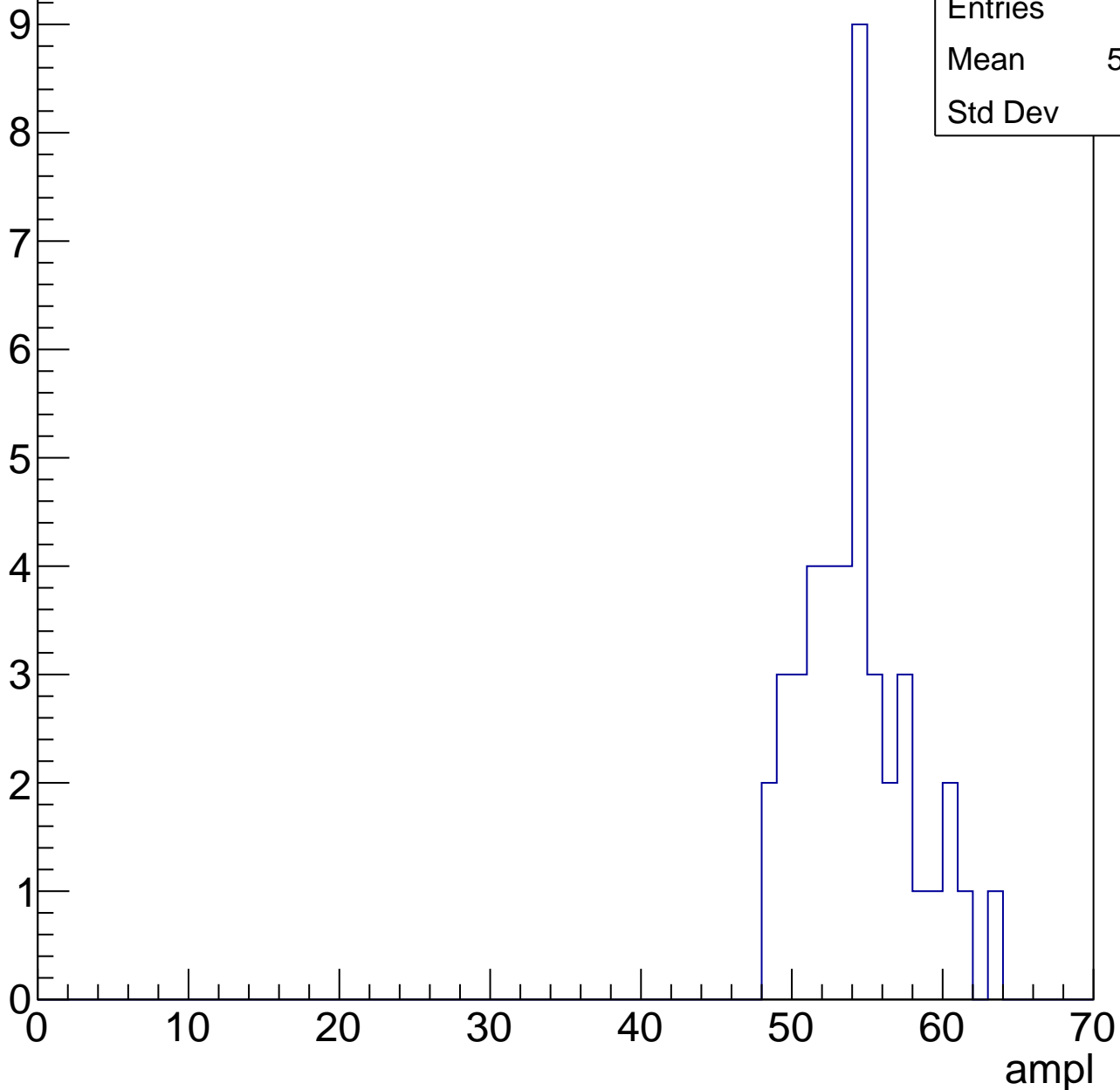


B1L101S, U15-ch104, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	43
Mean	53.77
Std Dev	3.51

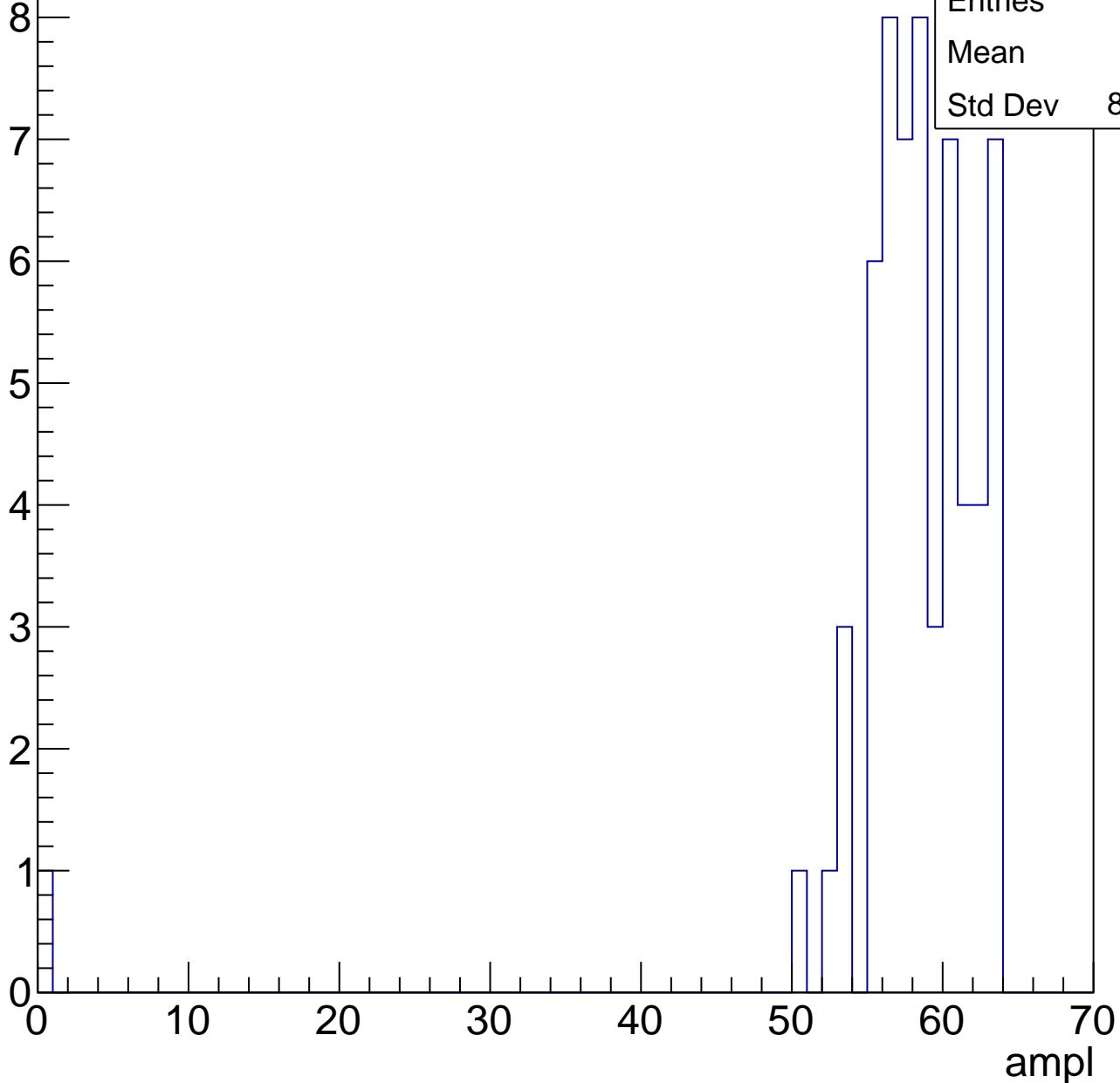


B1L101S, U15-ch104, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

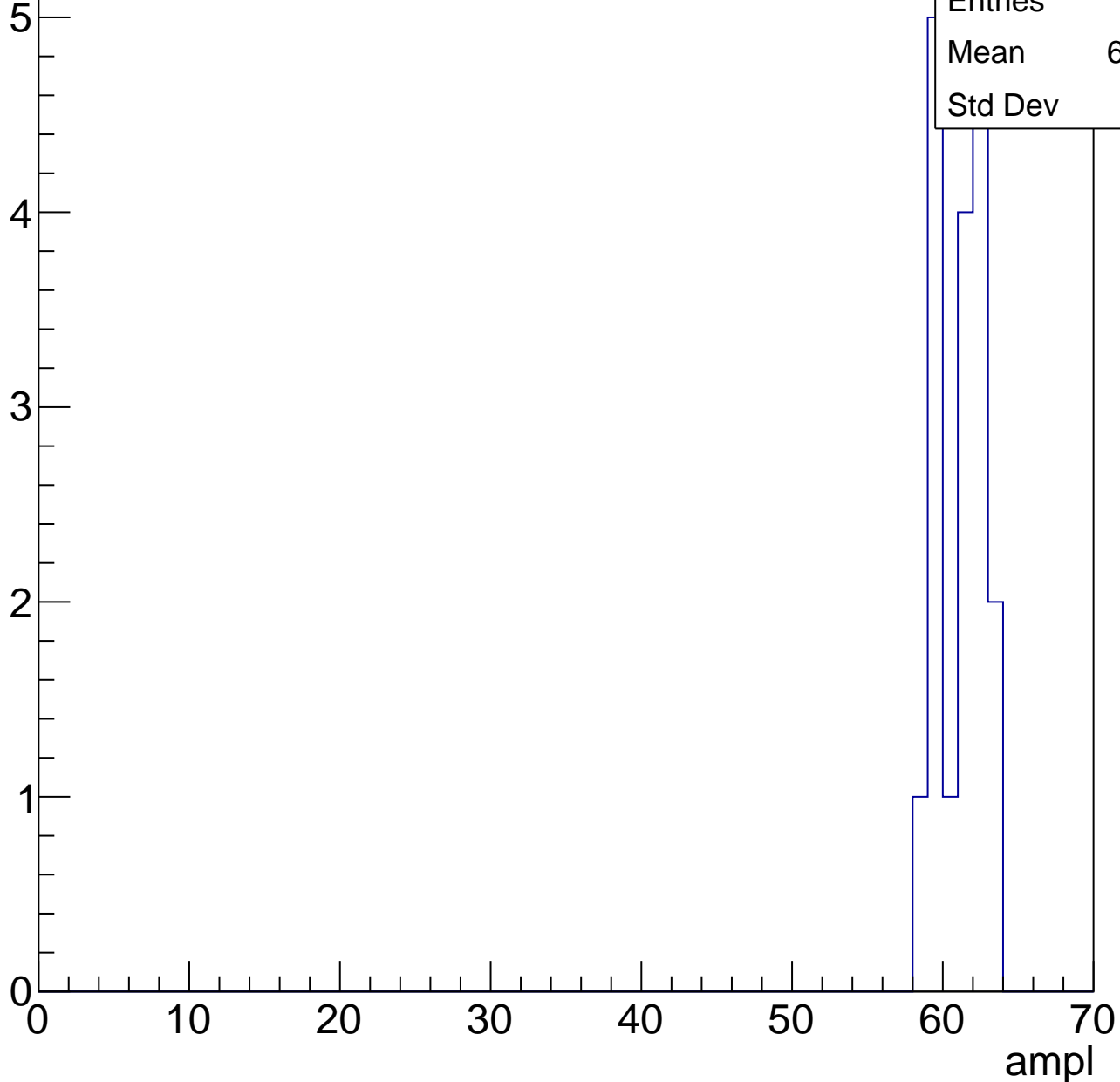
Entries	60
Mean	57.2
Std Dev	8.062



B1L101S, U15-ch104, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

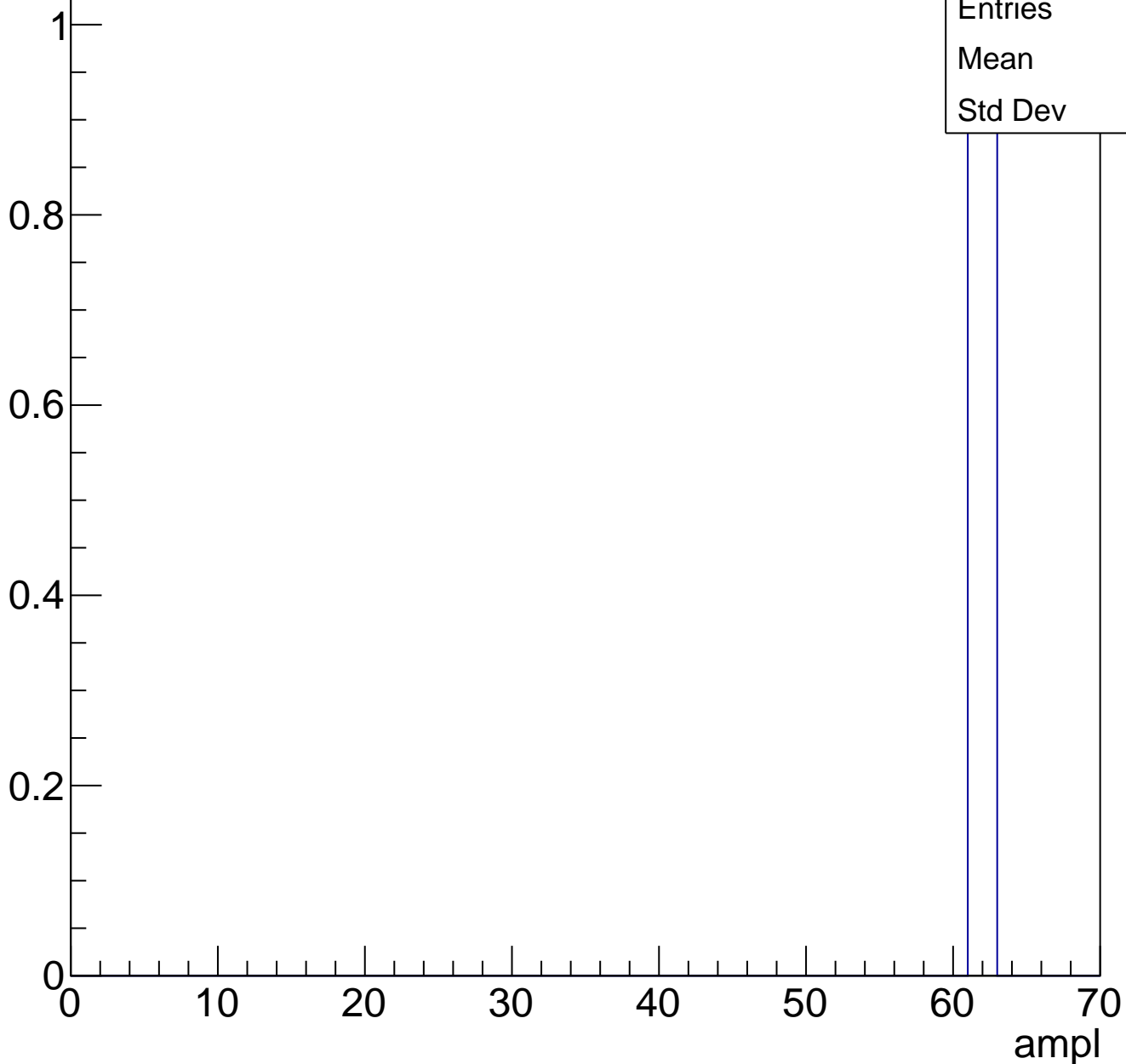


Entries	18
Mean	60.72
Std Dev	1.52

B1L101S, U15-ch104, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch105, adc0

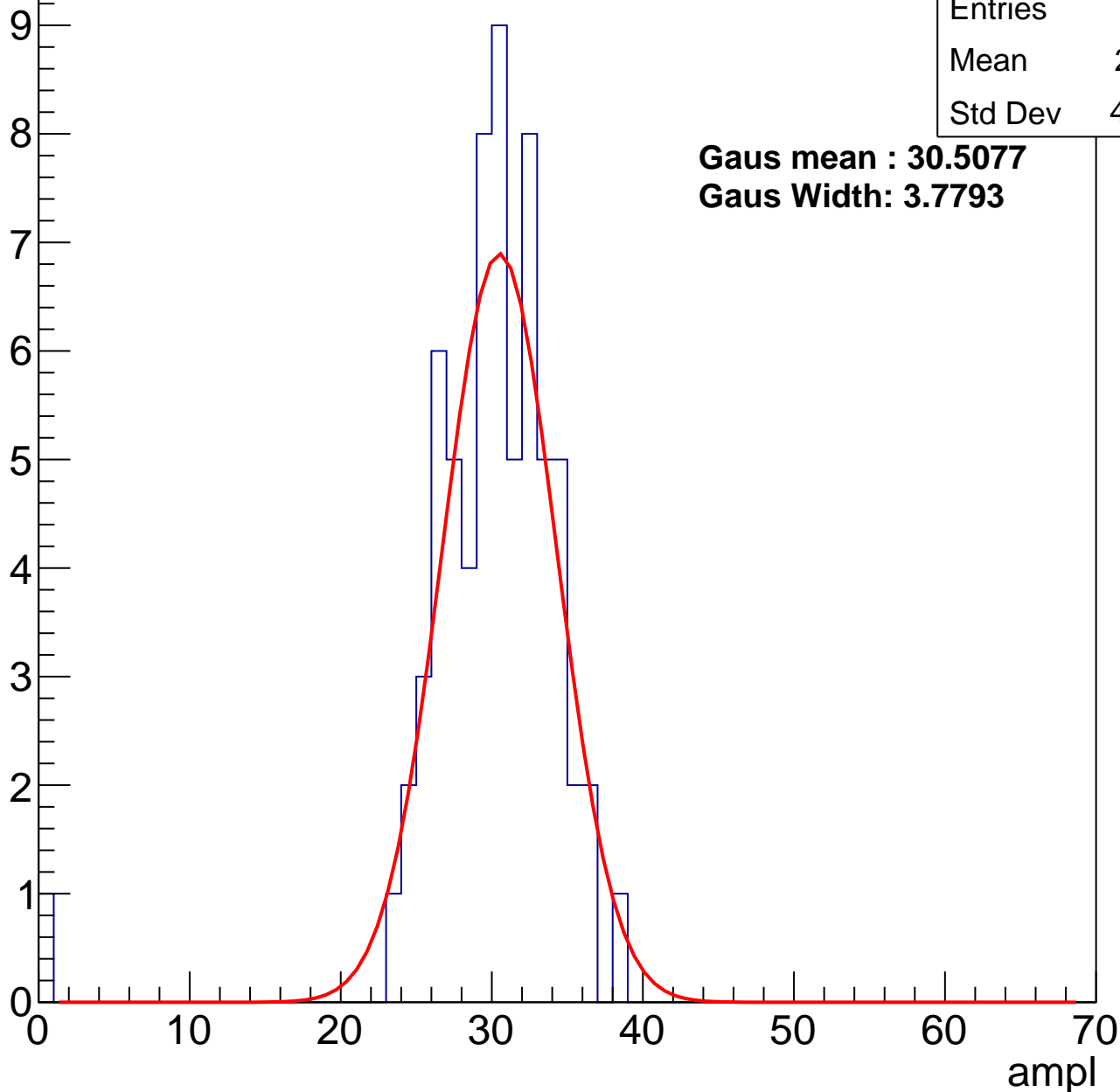
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	29.51
Std Dev	4.876

Gaus mean : 30.5077

Gaus Width: 3.7793



B1L101S, U15-ch105, adc1

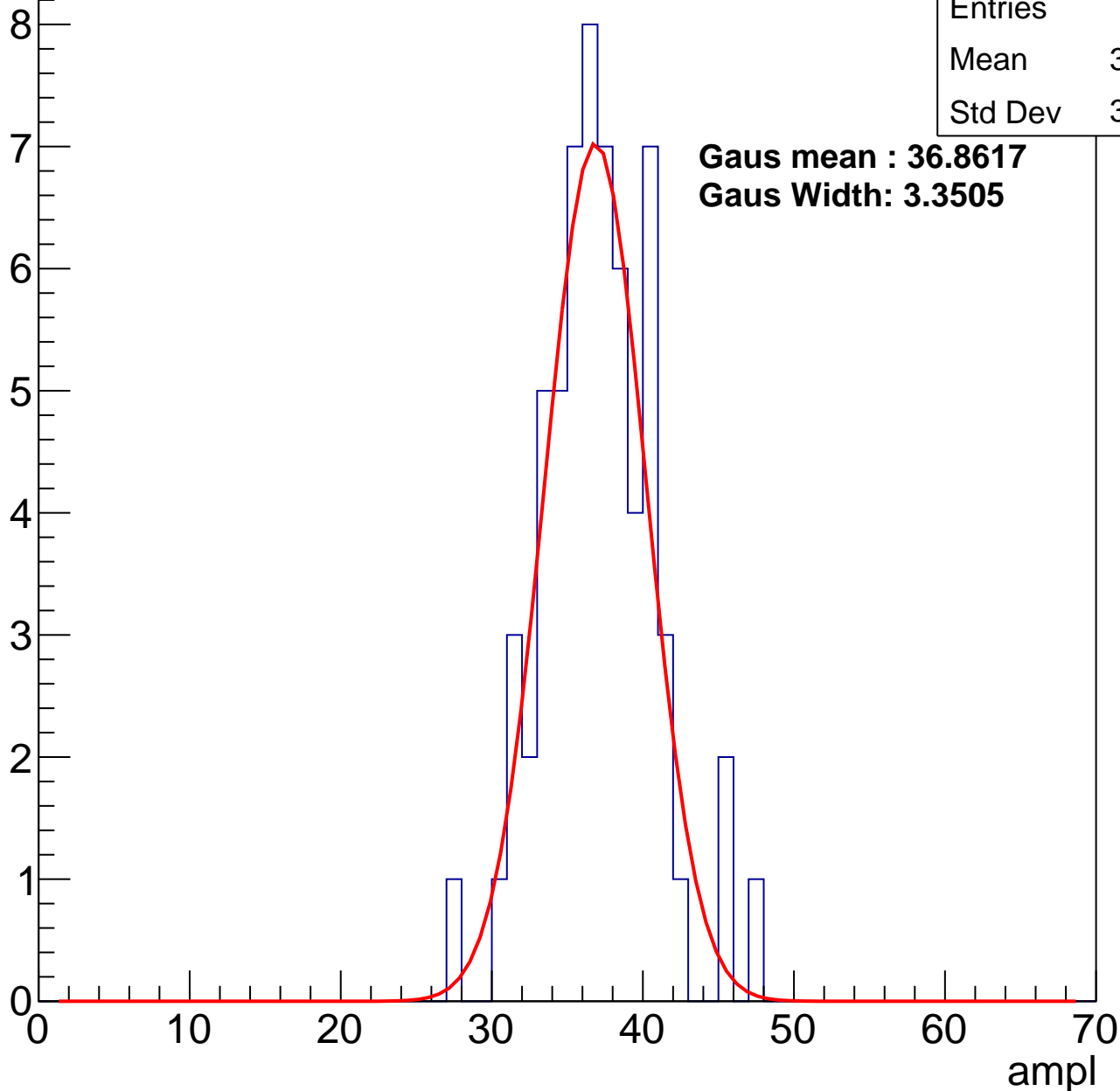
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	36.62
Std Dev	3.662

Gaus mean : 36.8617

Gaus Width: 3.3505



B1L101S, U15-ch105, adc2

calib_packv5_042523_0143.root, FC#0, port D2

Entries	70
Mean	42.99
Std Dev	3.45

Gaus mean : 43.3217

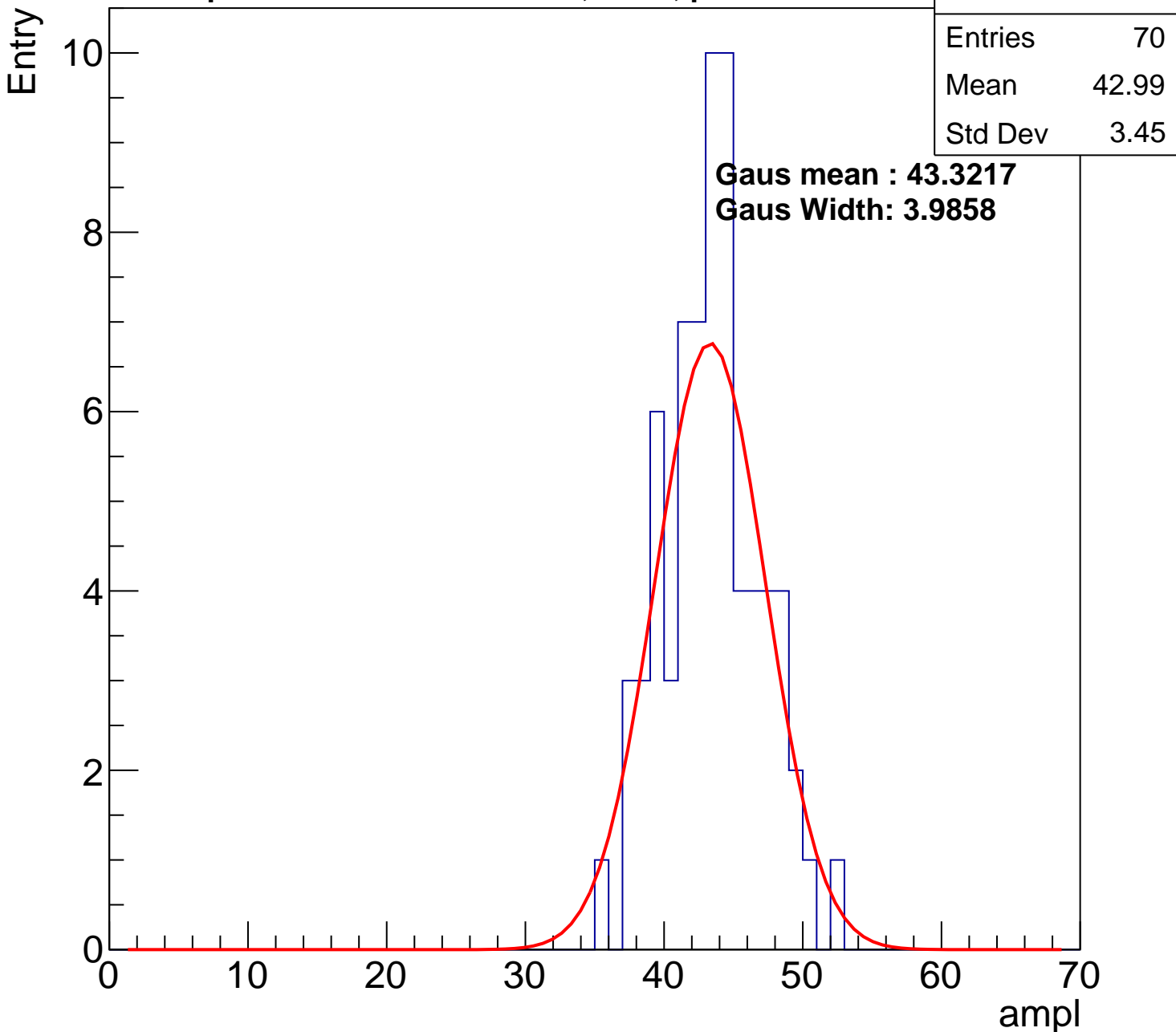
Gaus Width: 3.9858

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

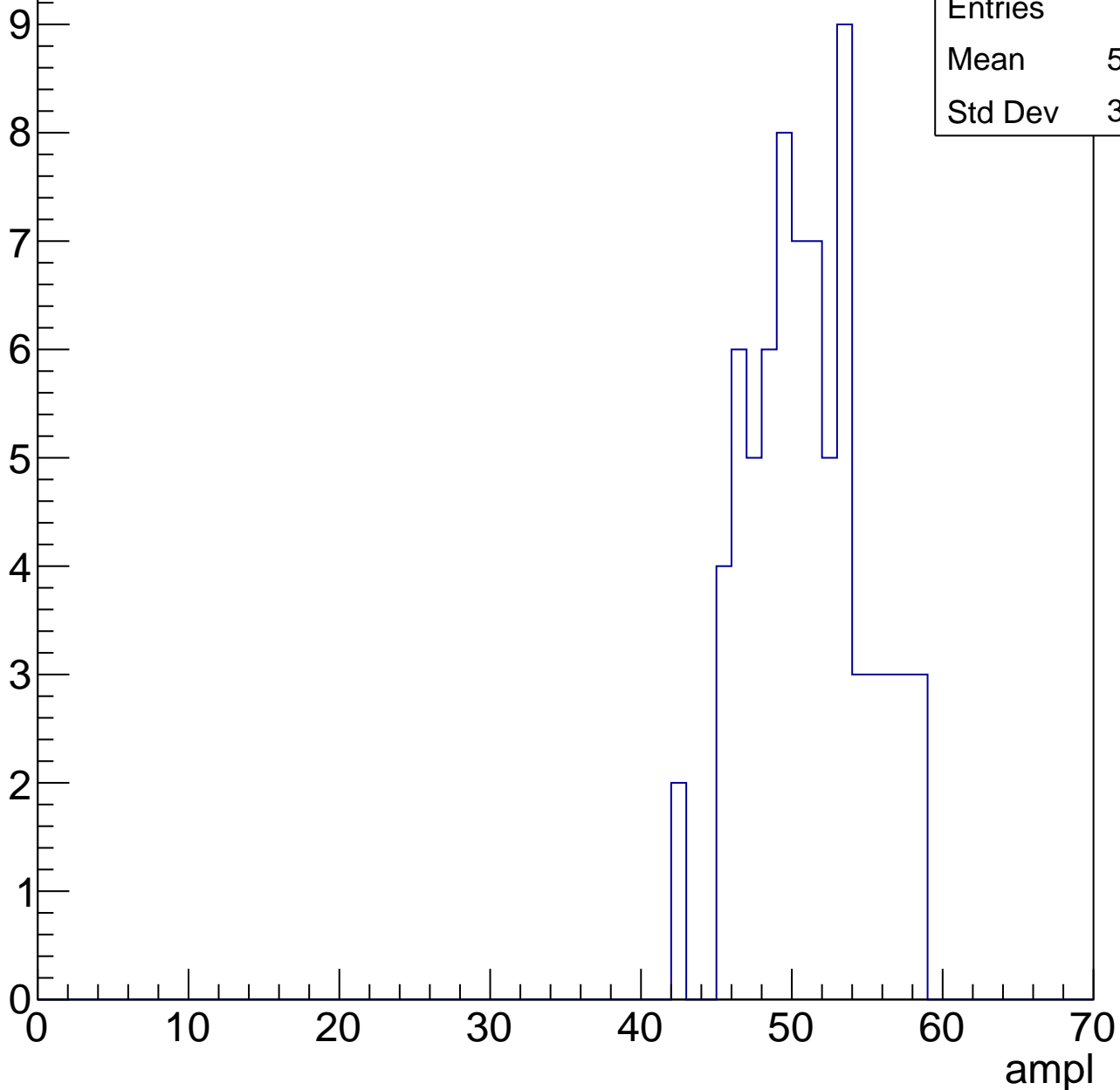


B1L101S, U15-ch105, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

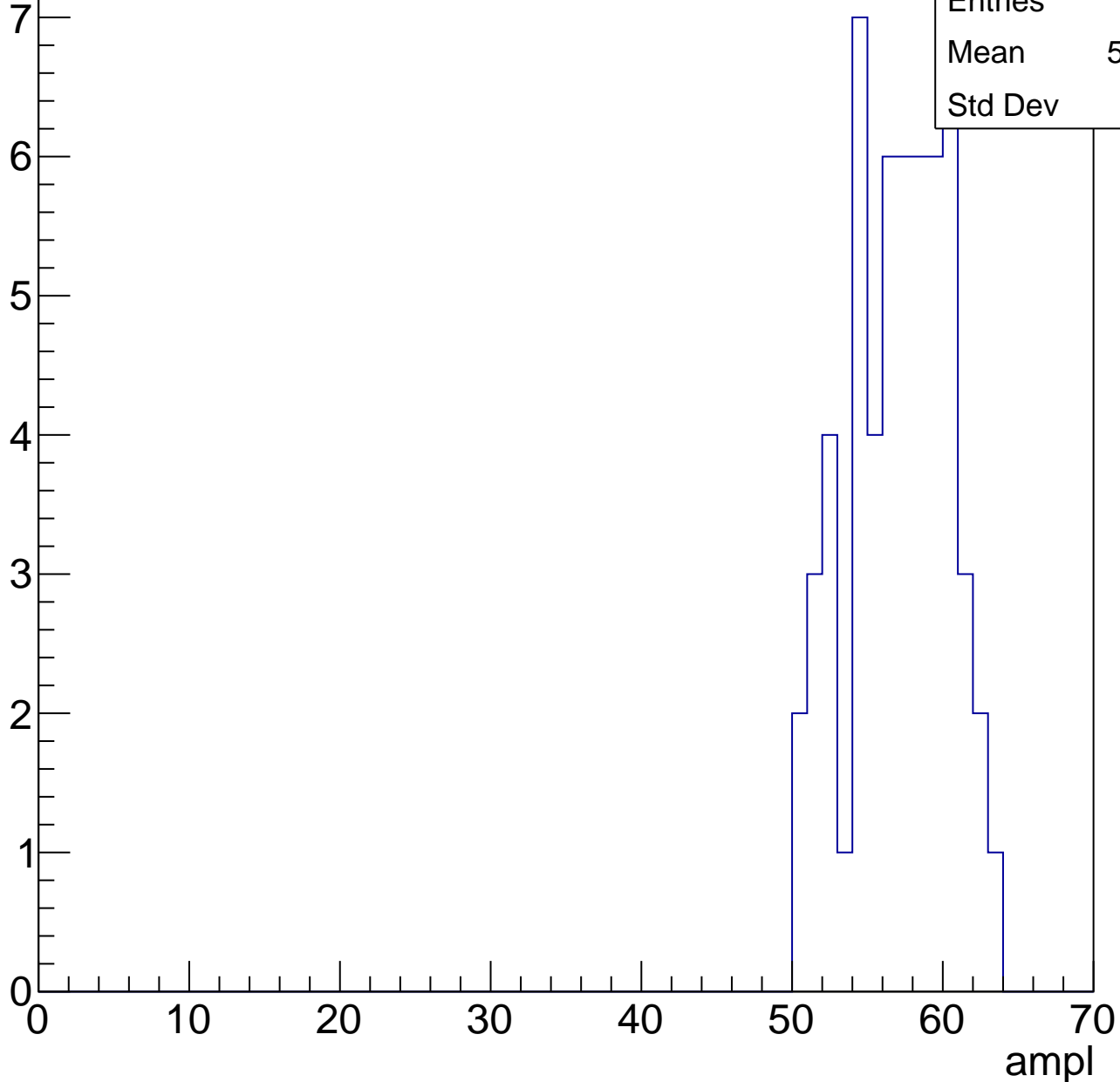
Entries	74
Mean	50.53
Std Dev	3.782



B1L101S, U15-ch105, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

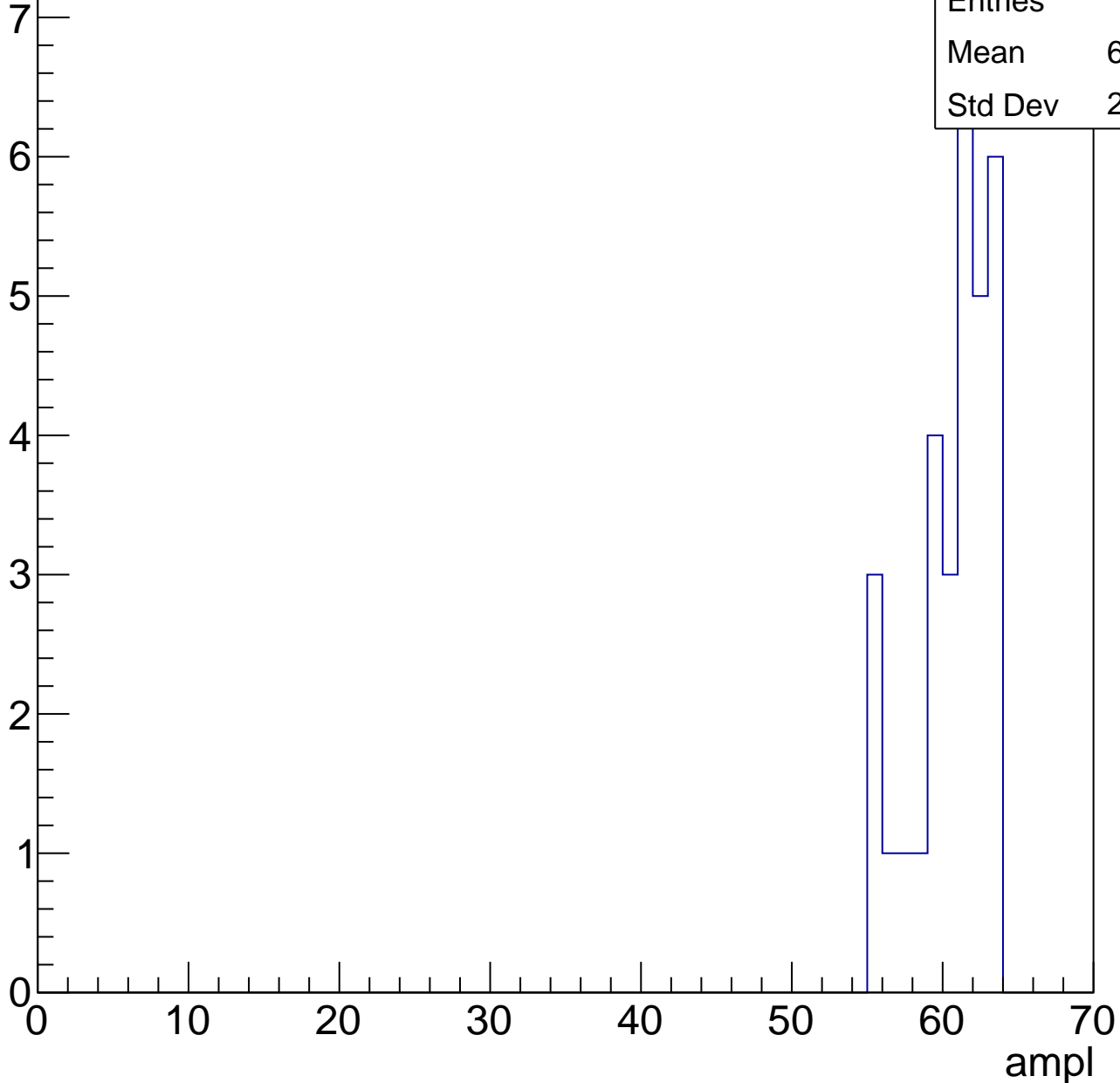


B1L101S, U15-ch105, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	31
Mean	60.23
Std Dev	2.459



B1L101S, U15-ch105, adc6

calib_packv5_042523_0143.root, FC#0, port D2

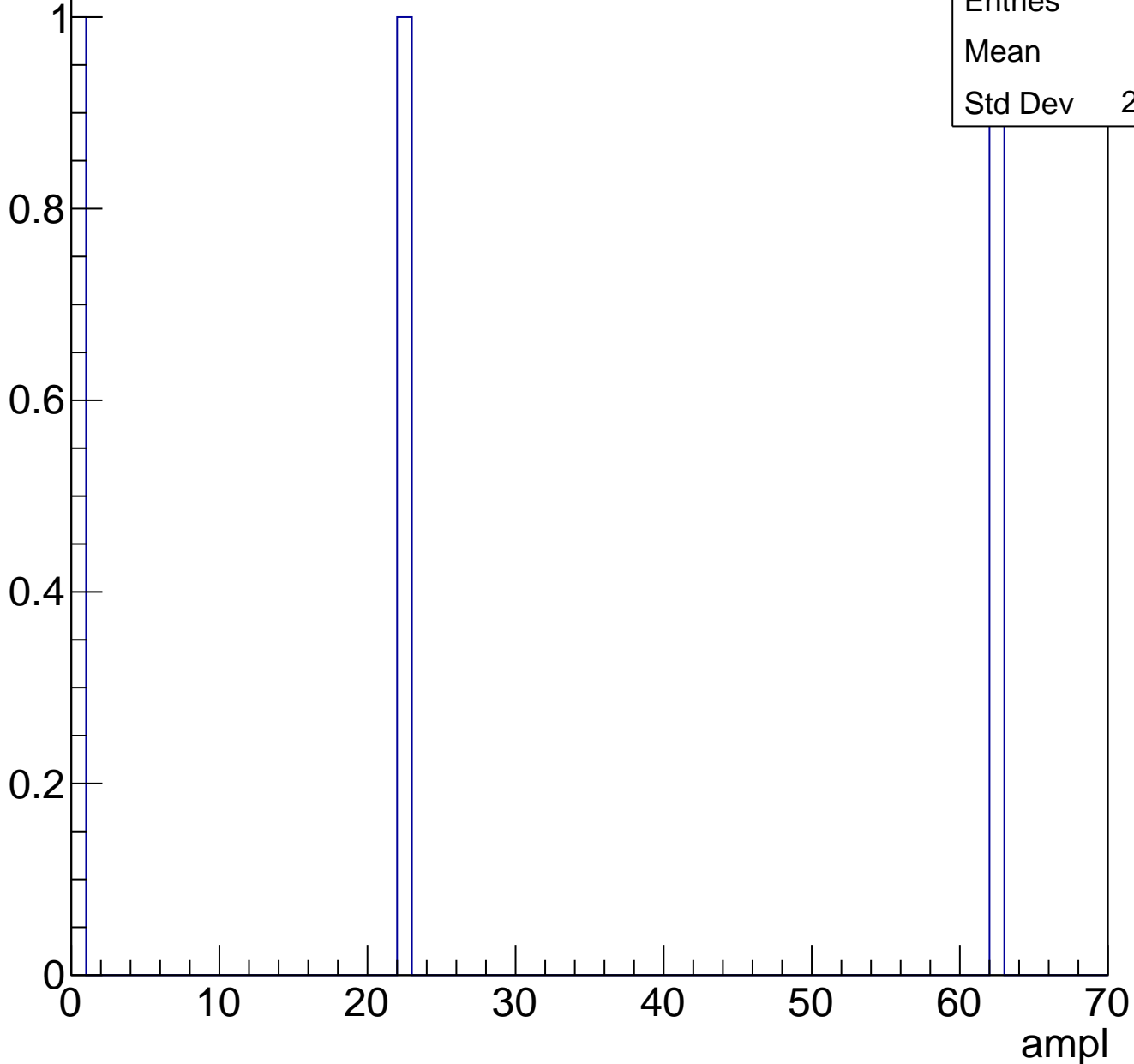
Entry



B1L101S, U15-ch105, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	3
Mean	28
Std Dev	25.66

B1L101S, U15-ch106, adc0

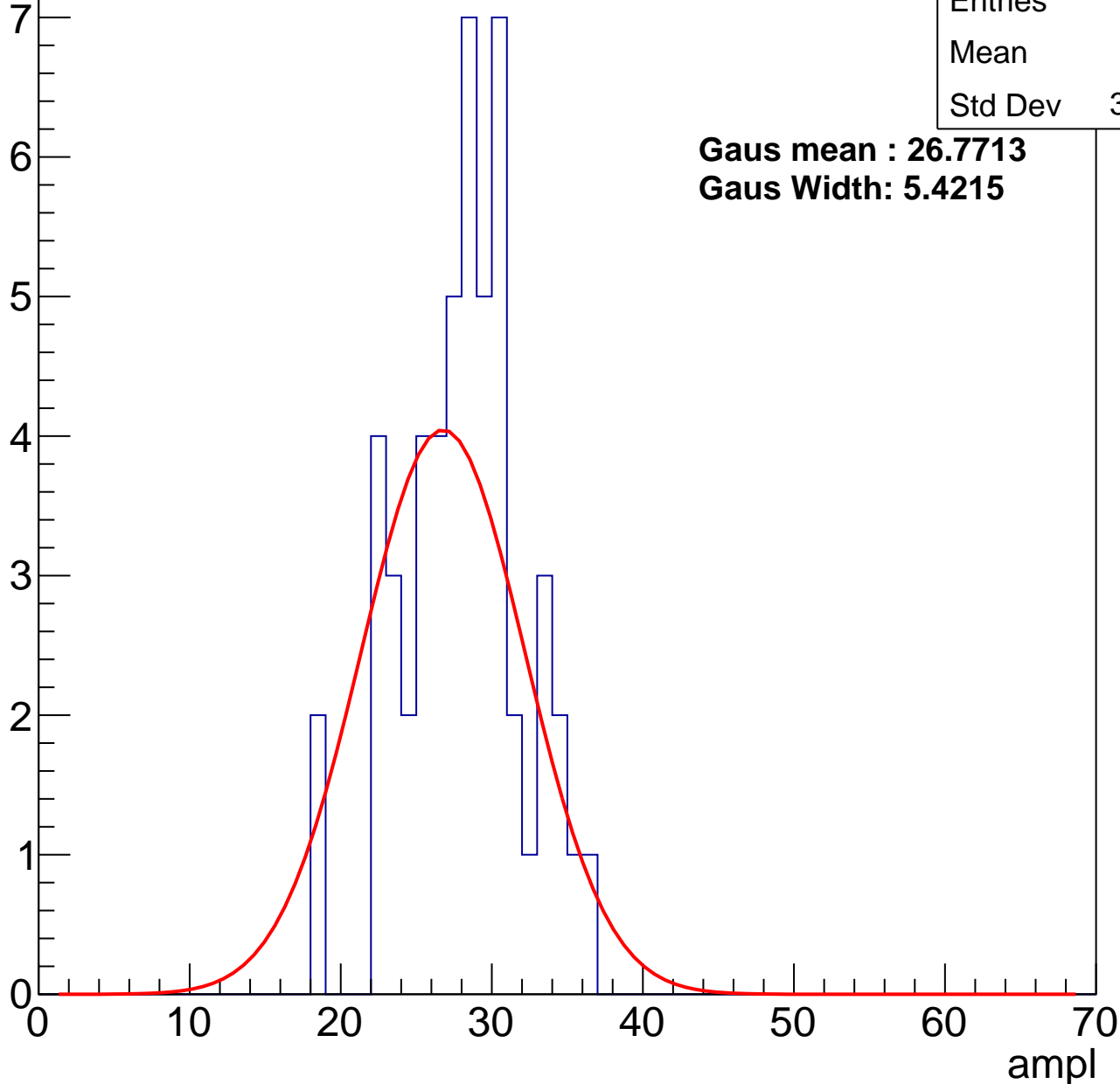
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	27.6
Std Dev	3.949

Gaus mean : 26.7713

Gaus Width: 5.4215



B1L101S, U15-ch106, adc1

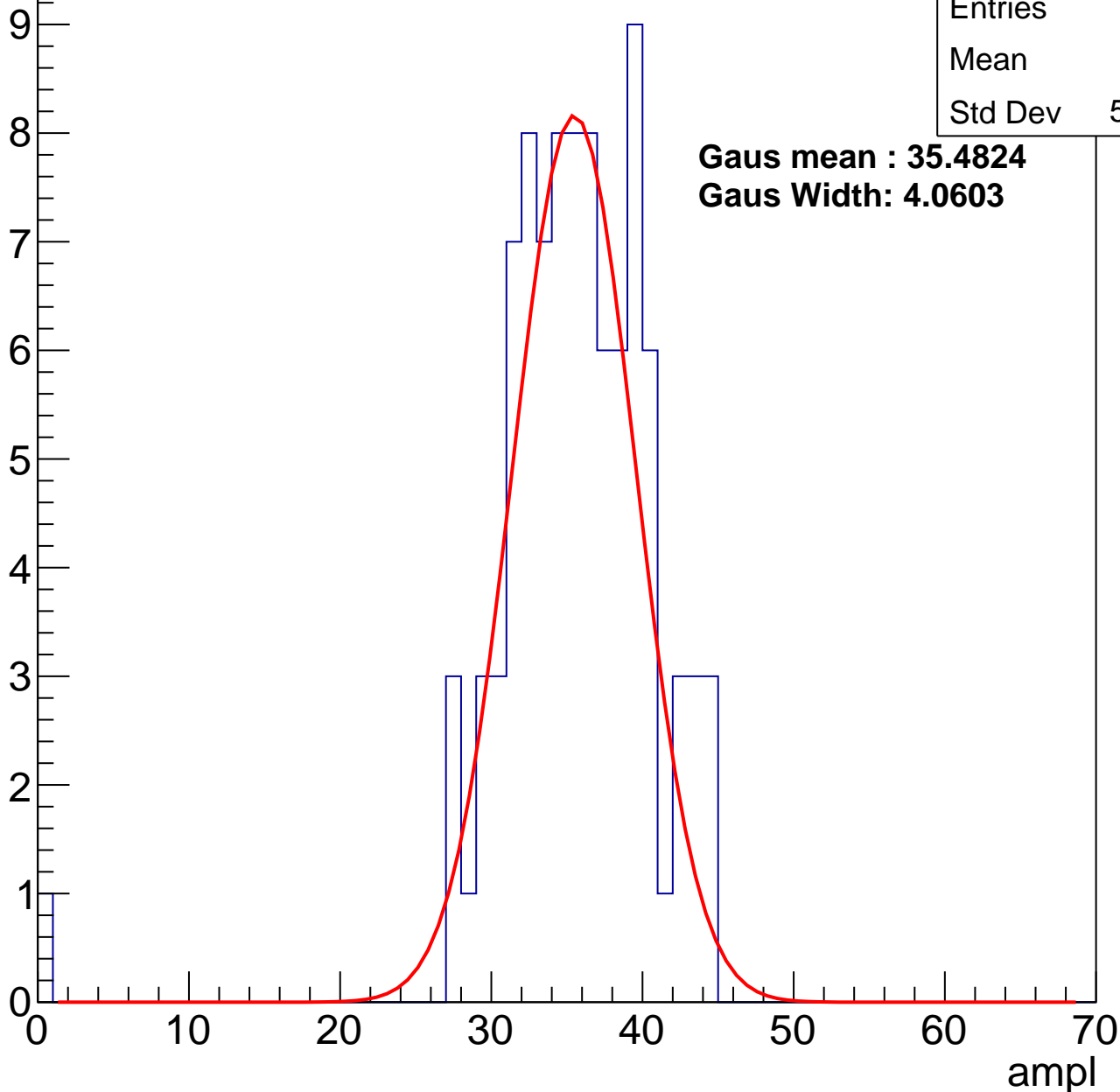
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	94
Mean	35.1
Std Dev	5.522

Gaus mean : 35.4824

Gaus Width: 4.0603



B1L101S, U15-ch106, adc2

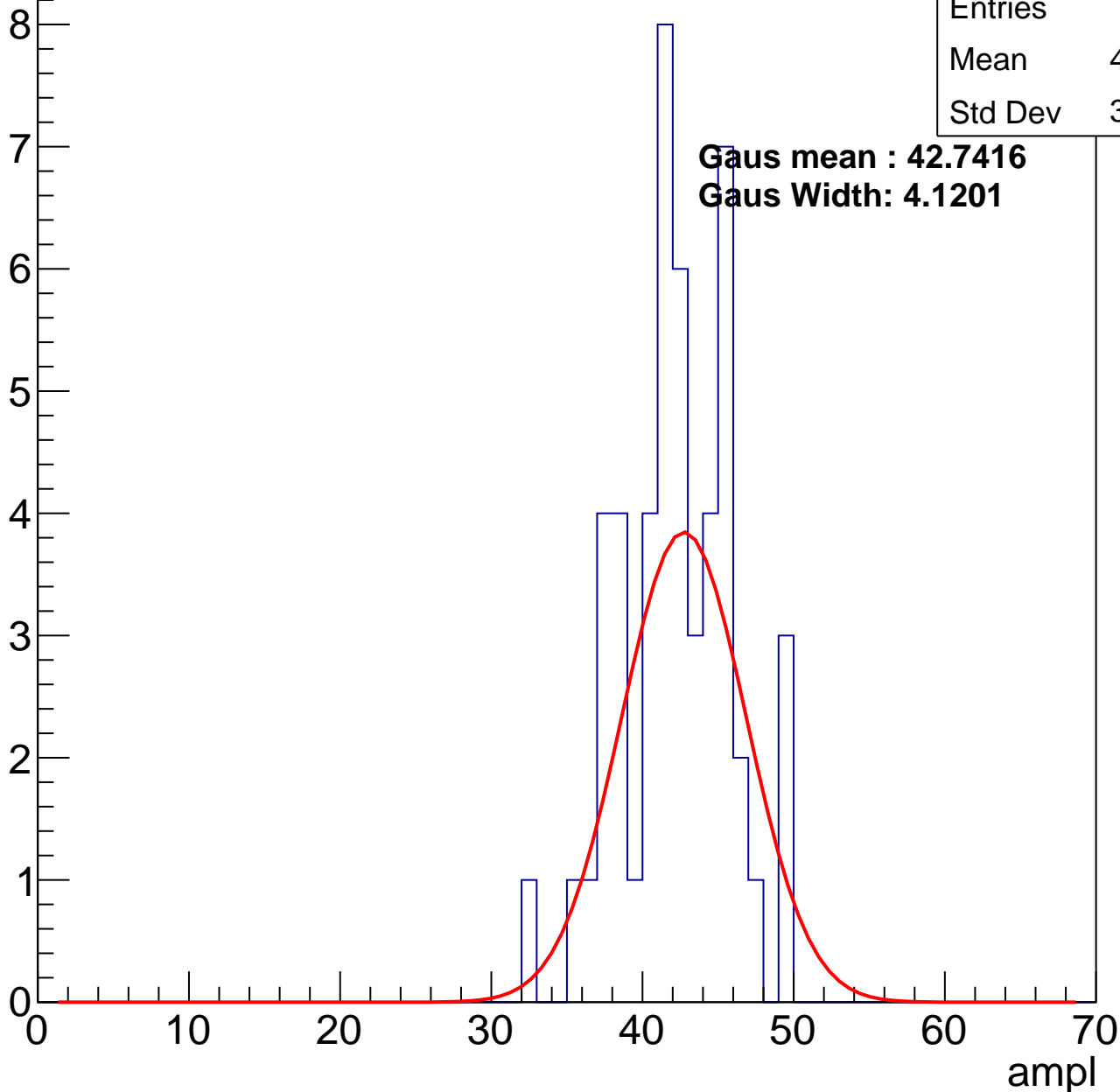
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	41.76
Std Dev	3.647

Gaus mean : 42.7416

Gaus Width: 4.1201

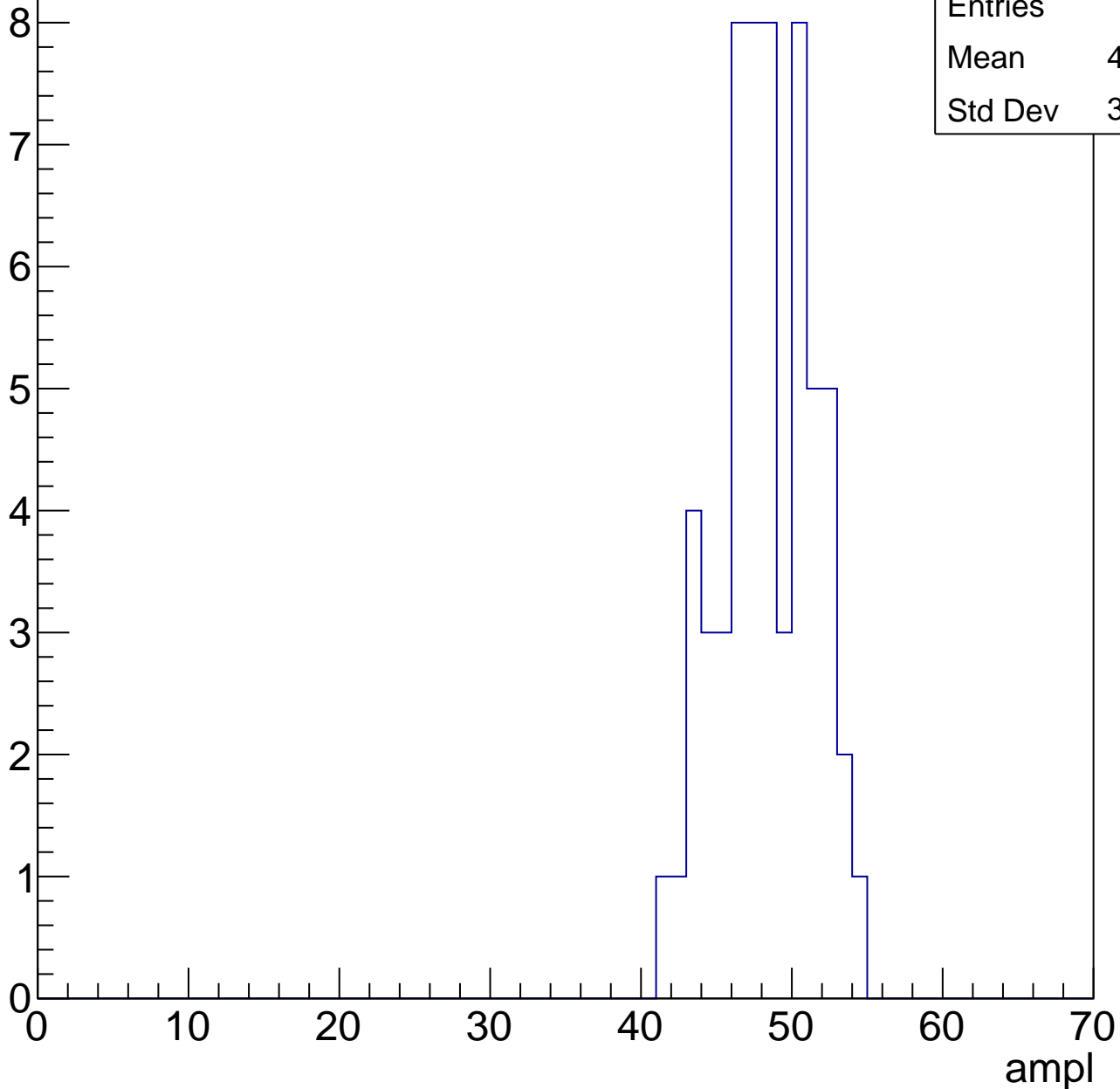


B1L101S, U15-ch106, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	47.87
Std Dev	3.014

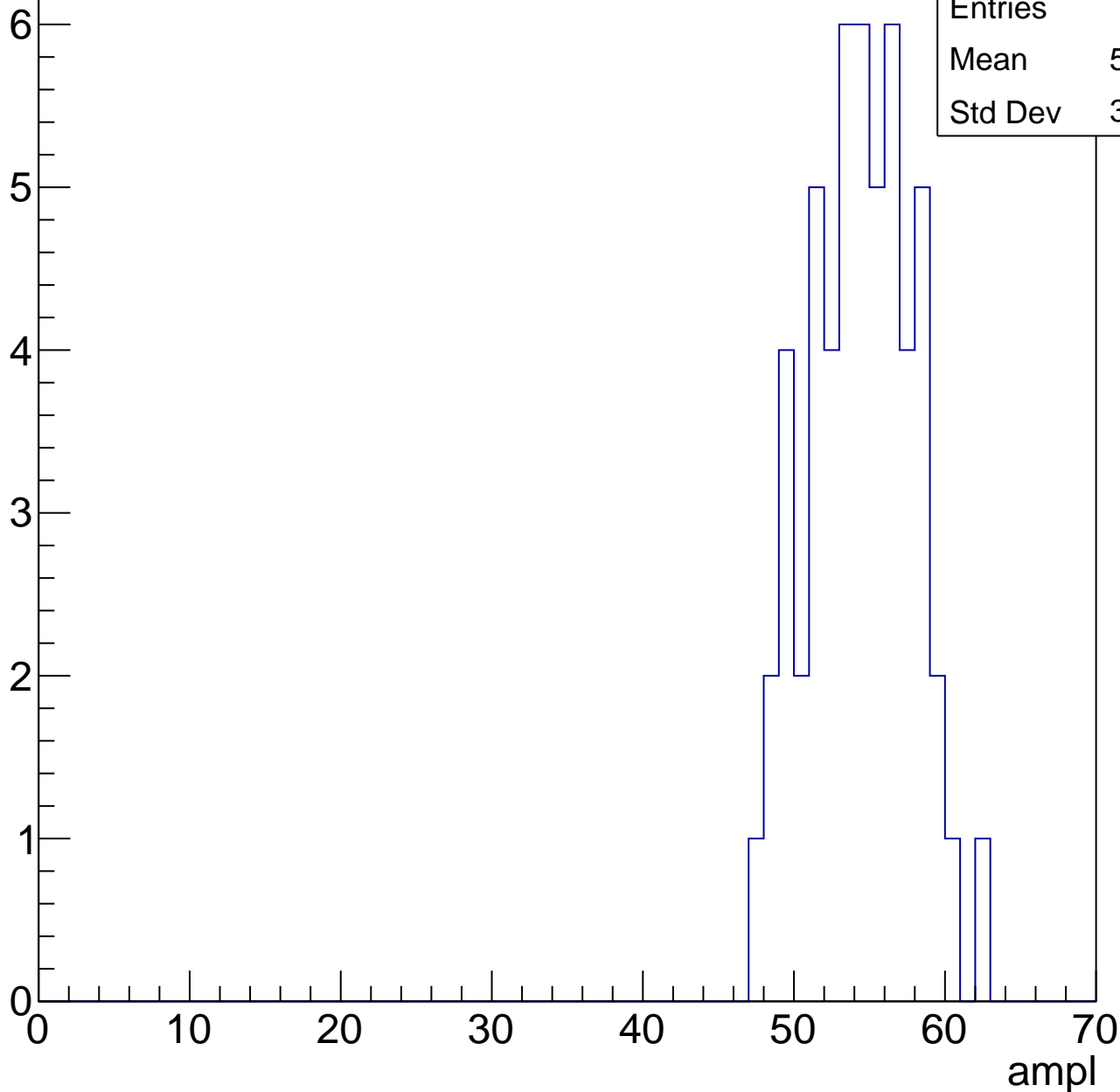


B1L101S, U15-ch106, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

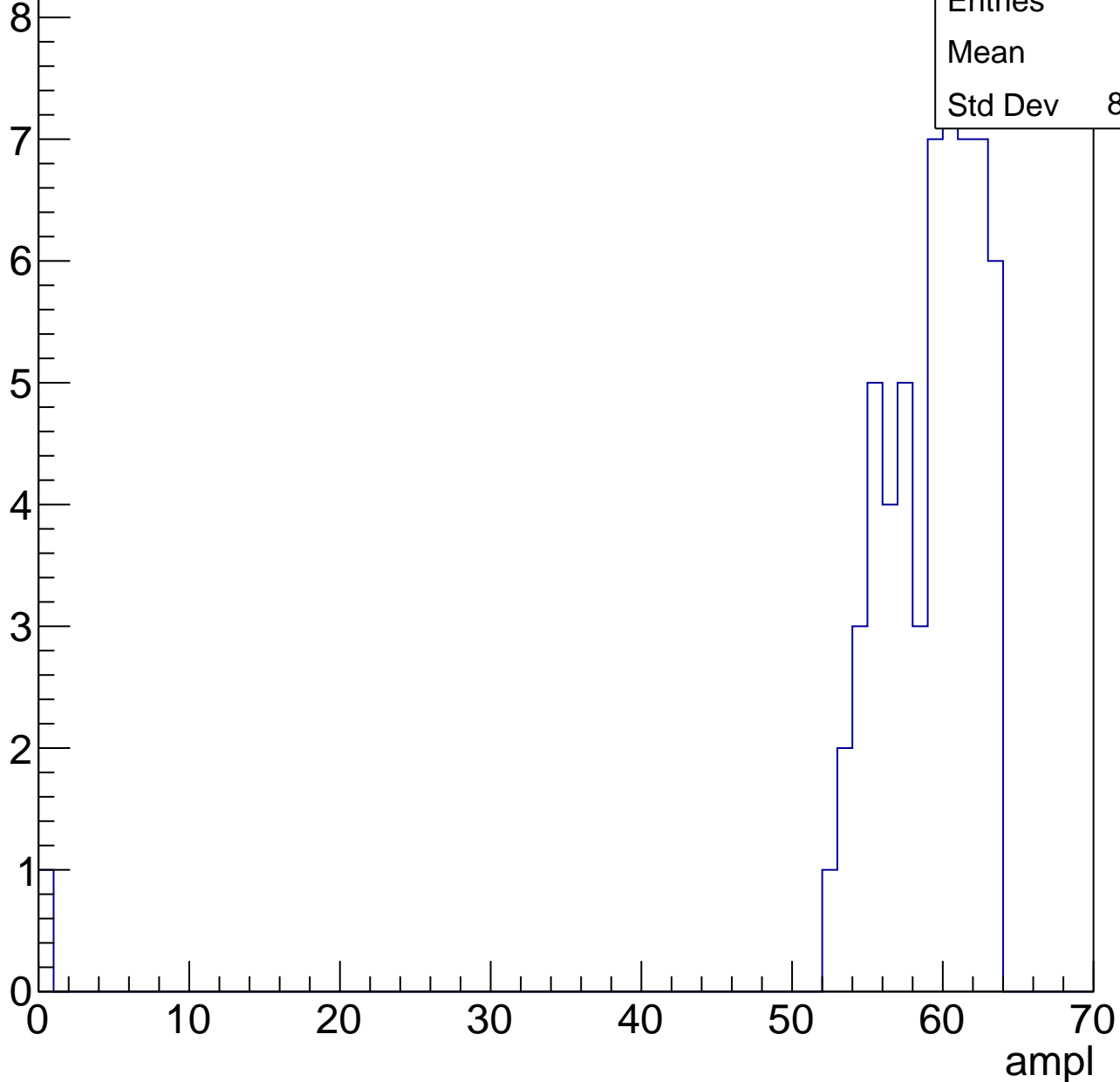
Entries	54
Mean	53.94
Std Dev	3.374



B1L101S, U15-ch106, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

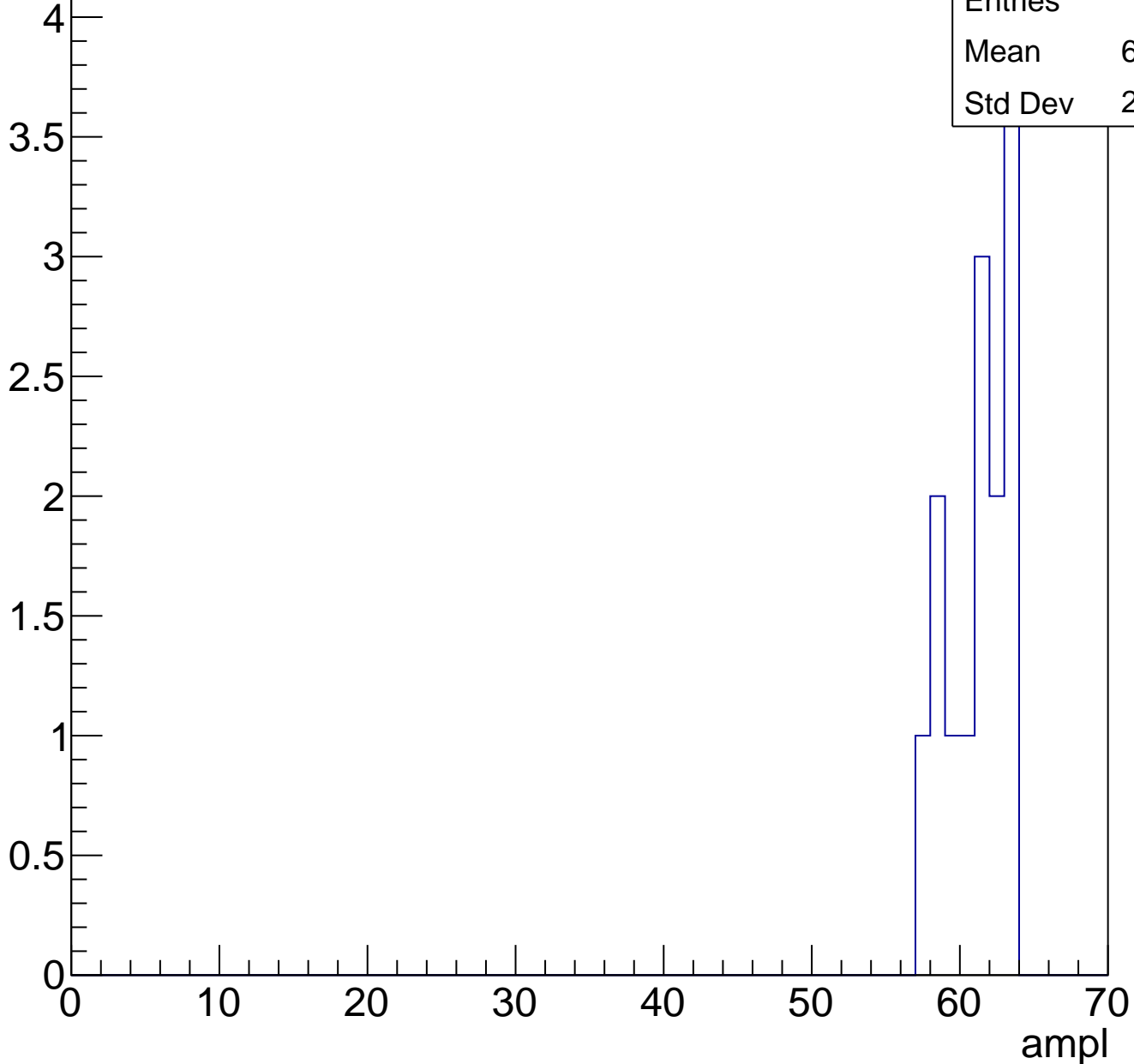


Entries	59
Mean	57.8
Std Dev	8.157

B1L101S, U15-ch106, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch106, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch107, adc0

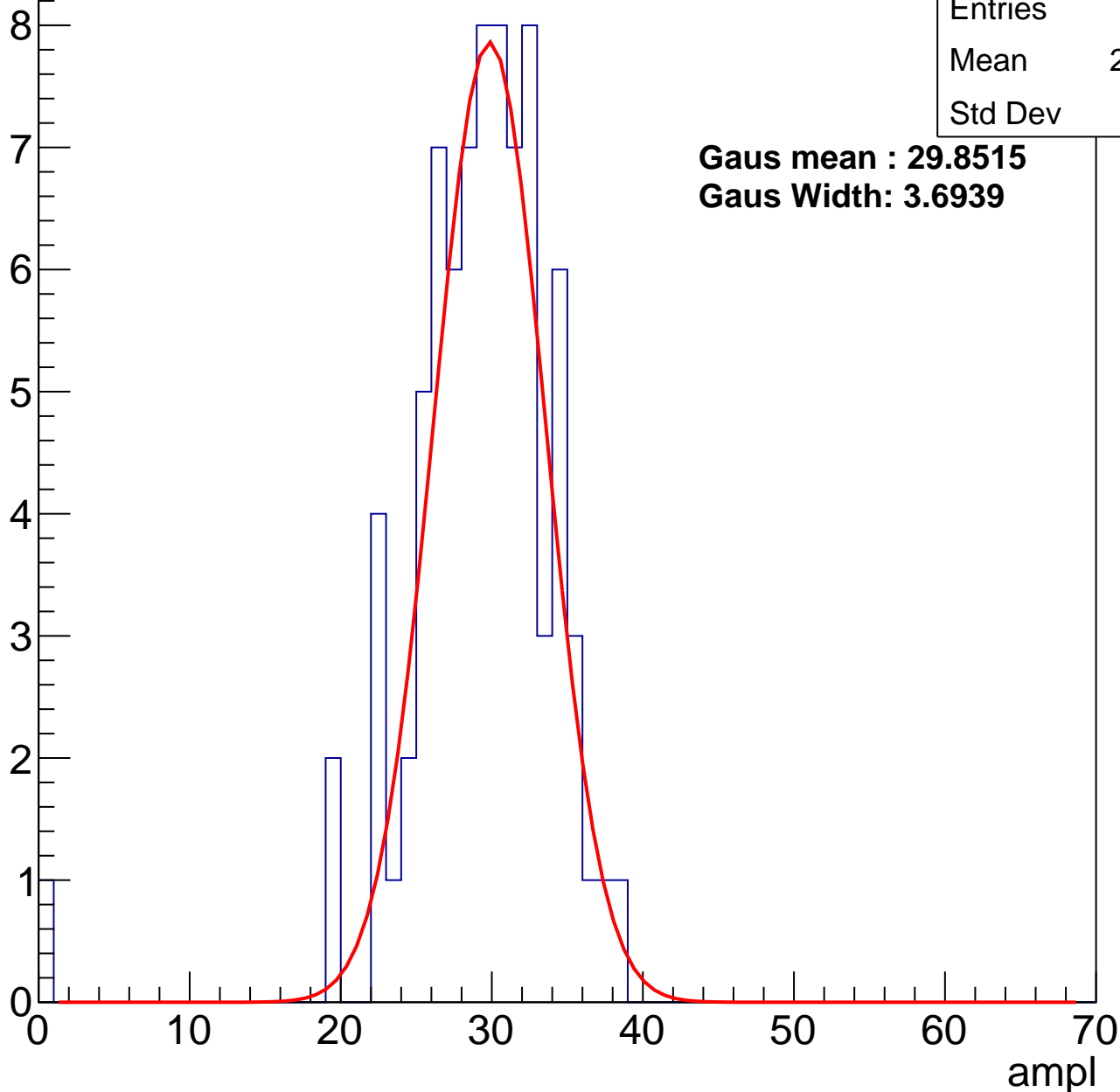
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	28.72
Std Dev	5.09

Gaus mean : 29.8515

Gaus Width: 3.6939



B1L101S, U15-ch107, adc1

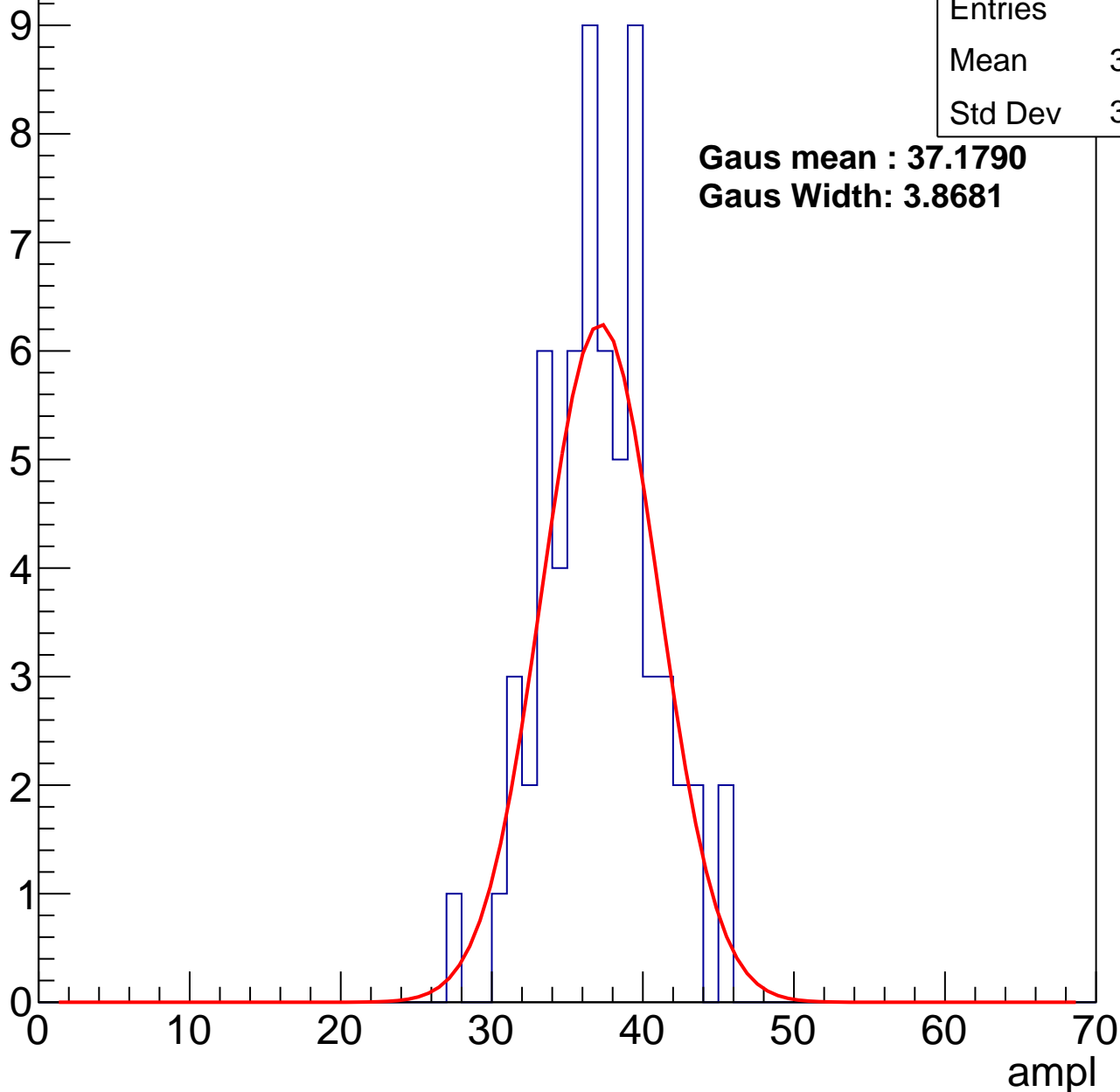
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	36.69
Std Dev	3.605

Gaus mean : 37.1790

Gaus Width: 3.8681



B1L101S, U15-ch107, adc2

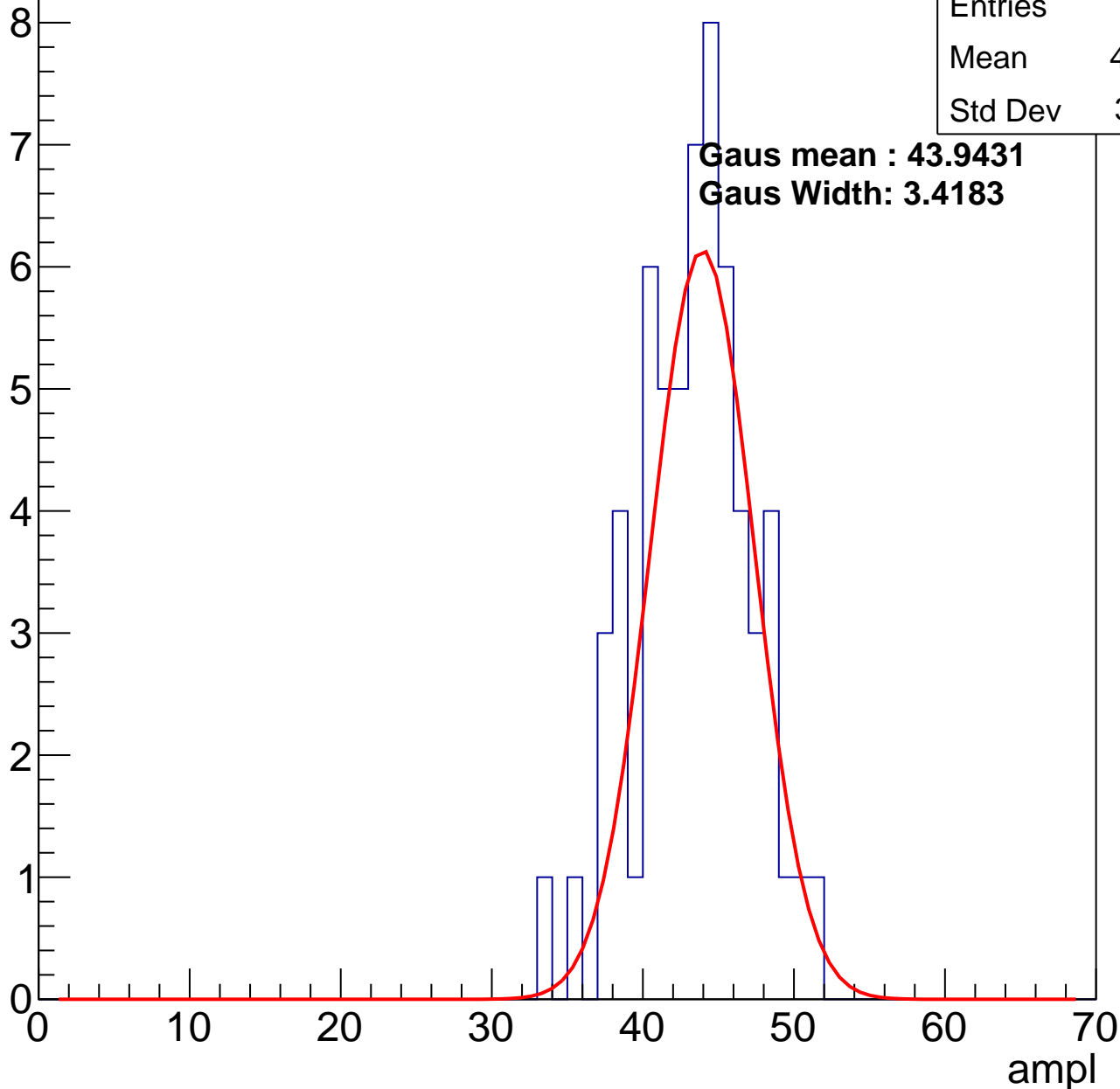
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	42.87
Std Dev	3.691

Gaus mean : 43.9431

Gaus Width: 3.4183

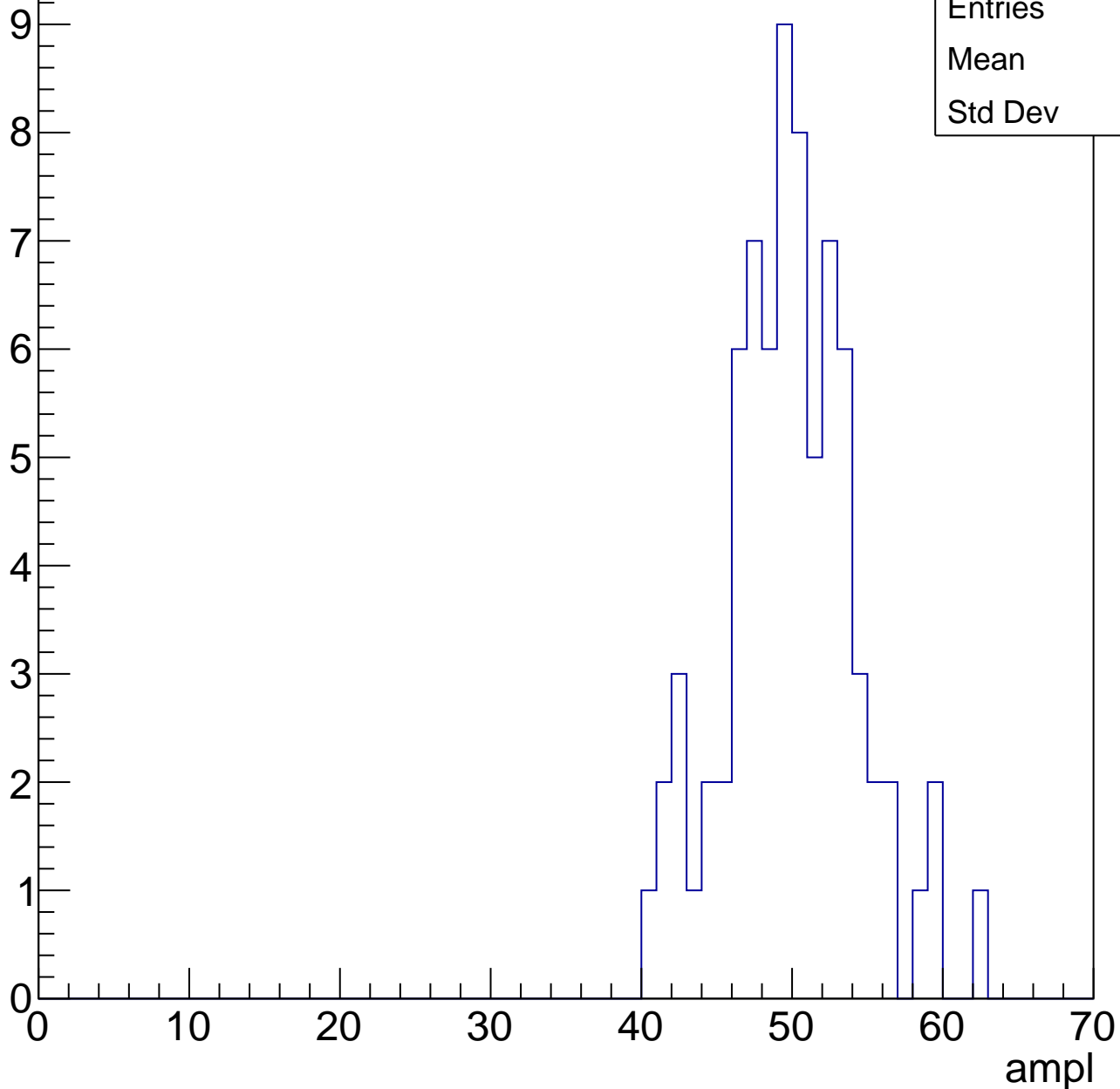


B1L101S, U15-ch107, adc3

calib_packv5_042523_0143.root, FC#0, port D2

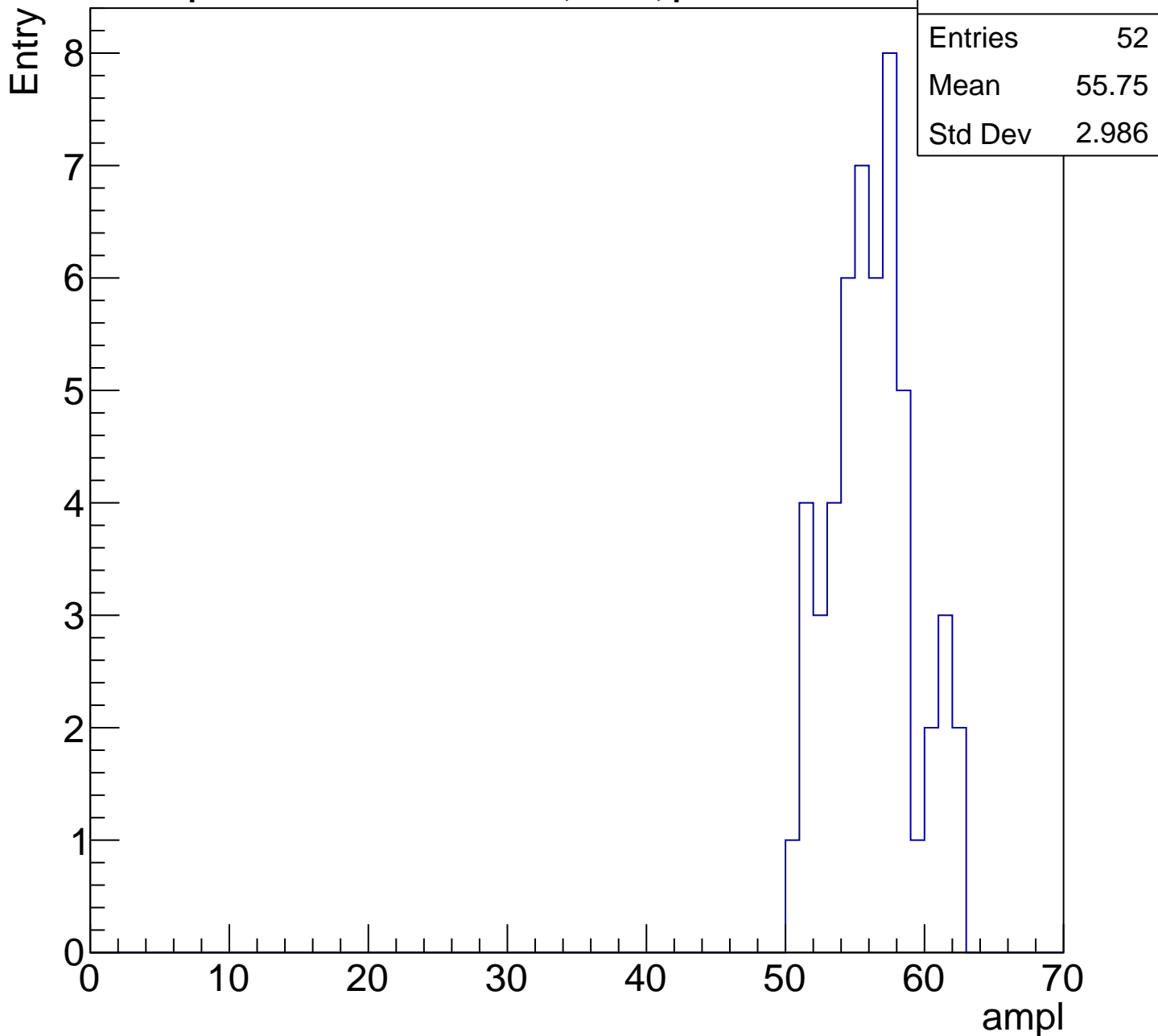
Entry

Entries	76
Mean	49.5
Std Dev	4.33



B1L101S, U15-ch107, adc4

calib_packv5_042523_0143.root, FC#0, port D2

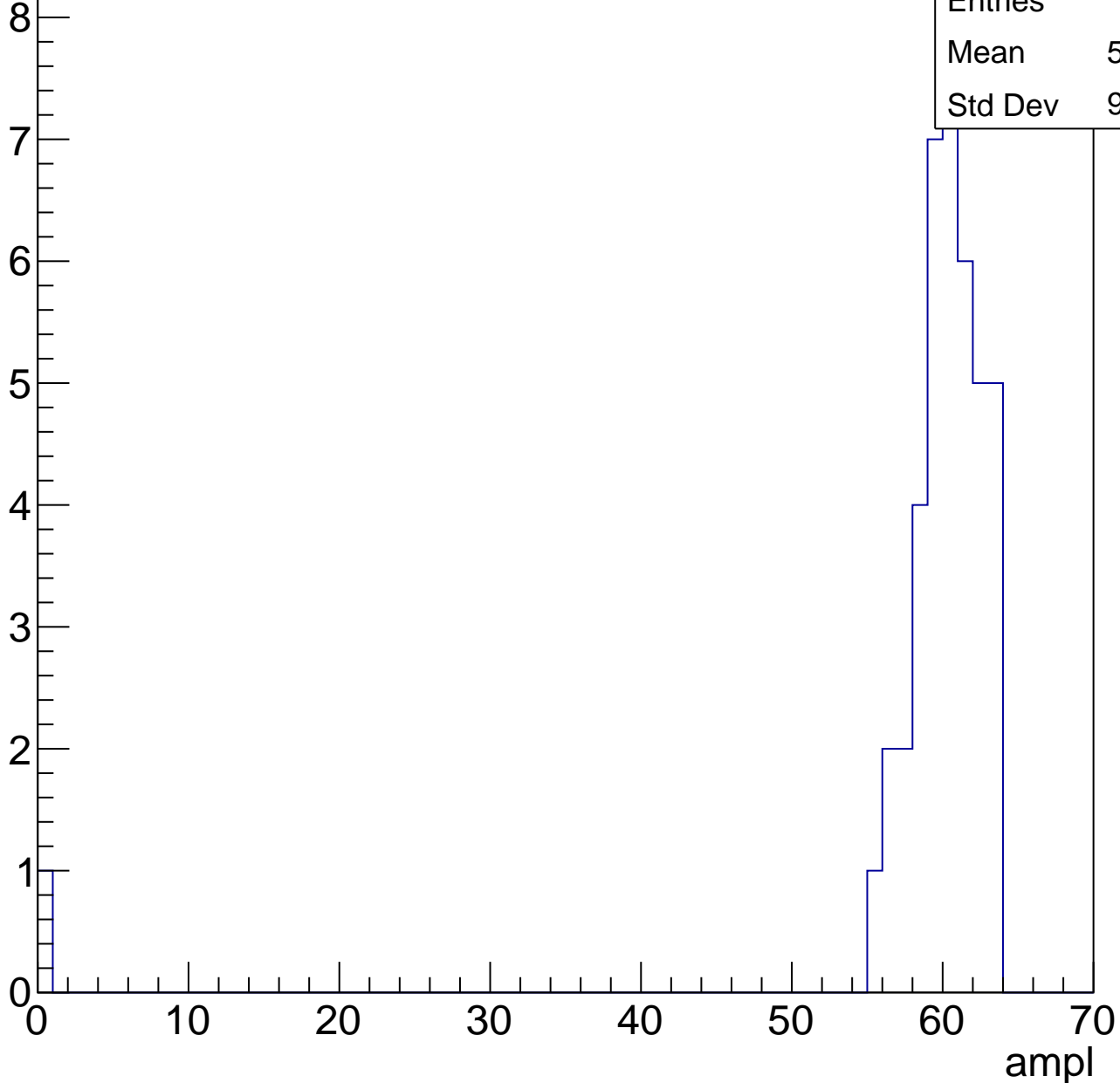


B1L101S, U15-ch107, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	58.46
Std Dev	9.464

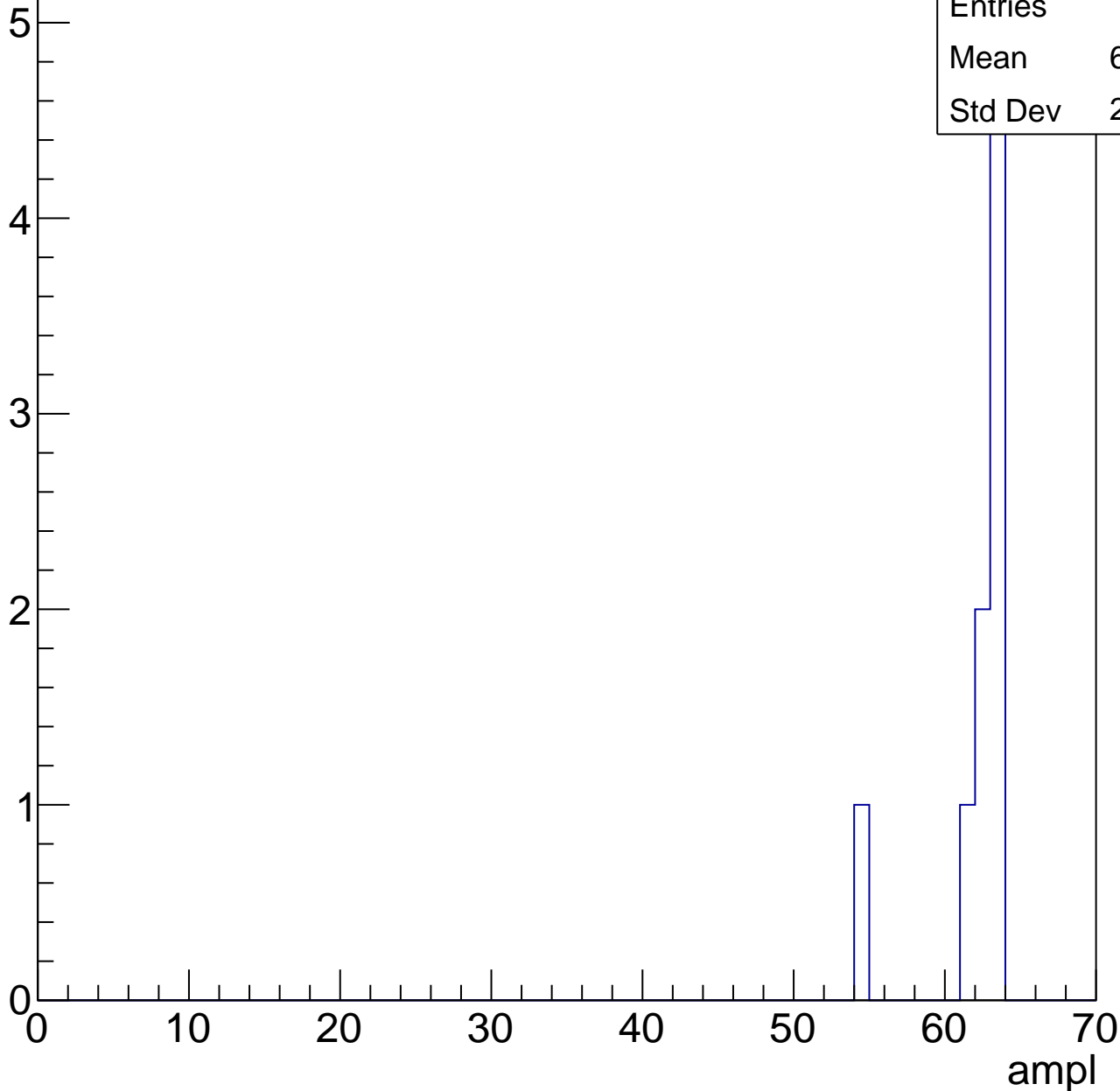


B1L101S, U15-ch107, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	9
Mean	61.56
Std Dev	2.753



B1L101S, U15-ch107, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch108, adc0

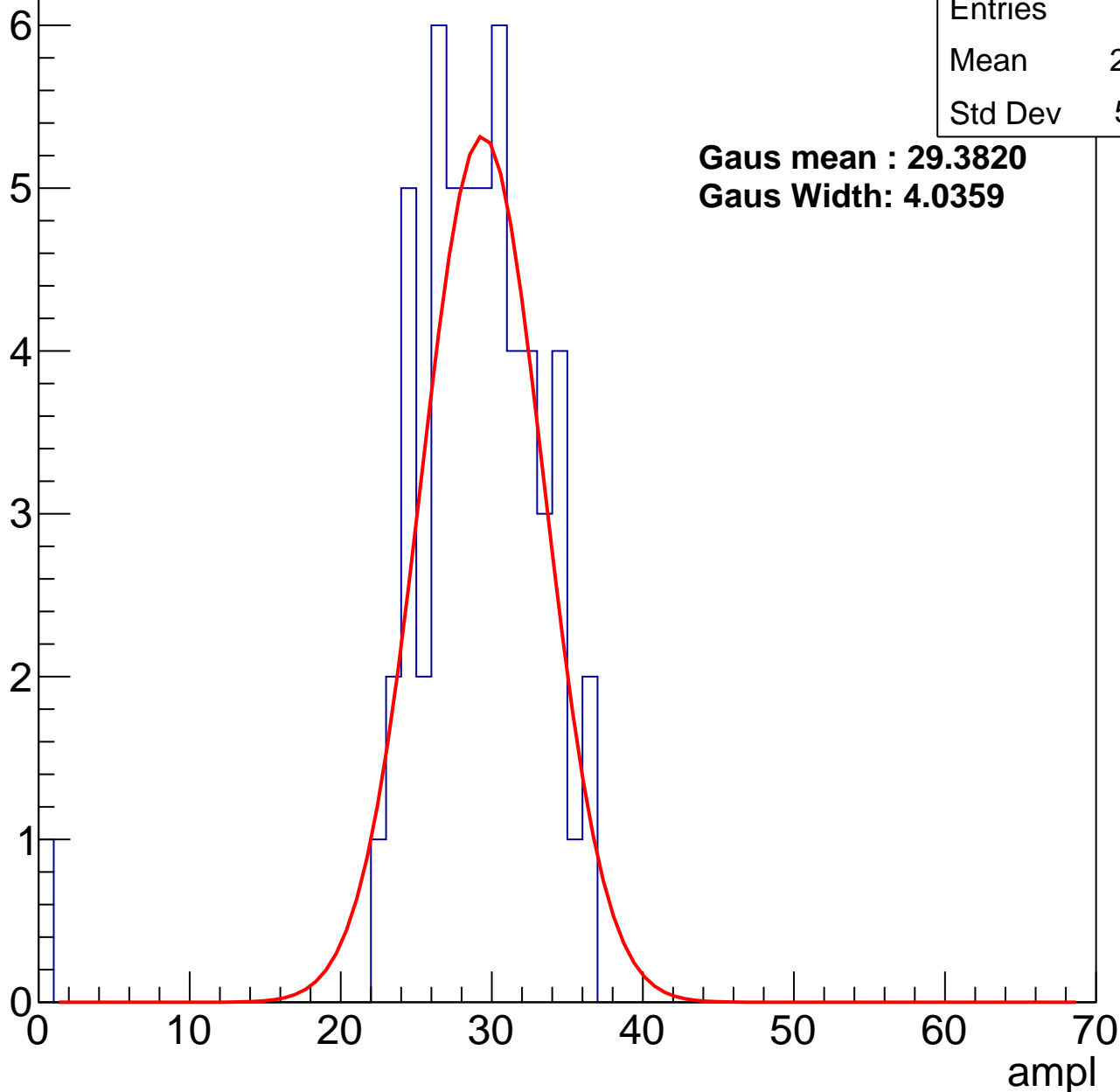
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	28.36
Std Dev	5.201

Gaus mean : 29.3820

Gaus Width: 4.0359



B1L101S, U15-ch108, adc1

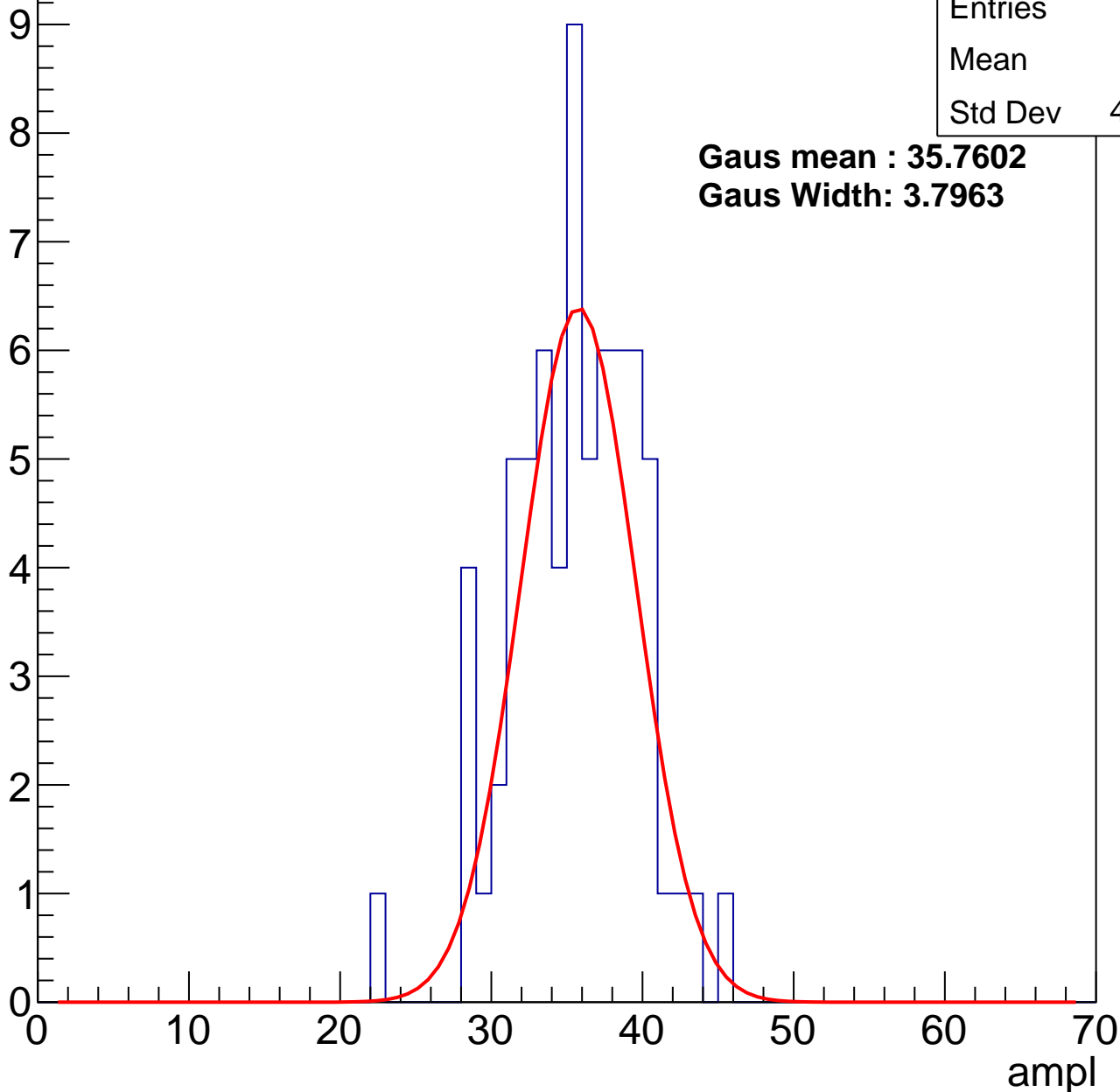
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	35.1
Std Dev	4.094

Gaus mean : 35.7602

Gaus Width: 3.7963



B1L101S, U15-ch108, adc2

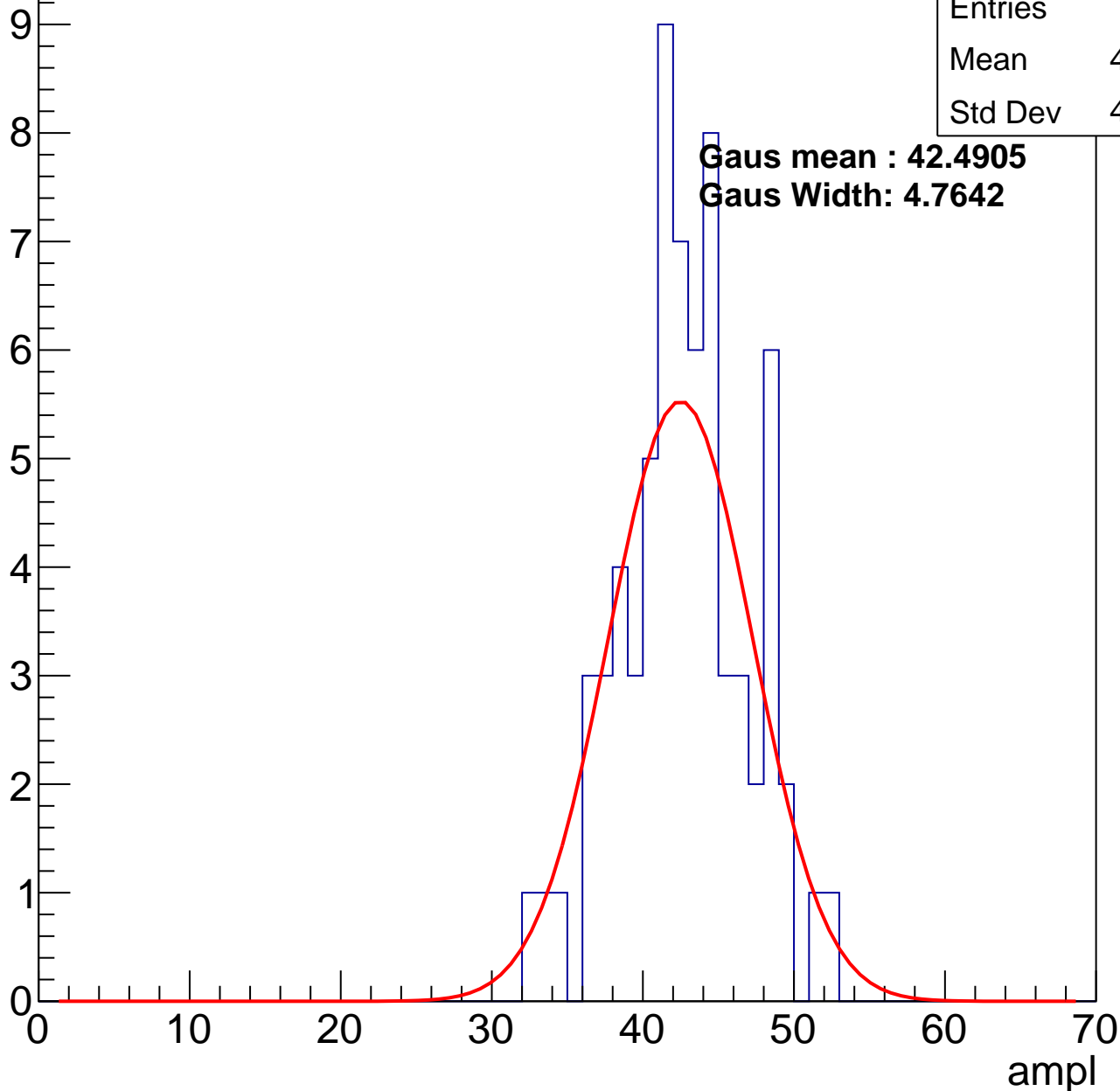
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	42.26
Std Dev	4.173

Gaus mean : 42.4905

Gaus Width: 4.7642

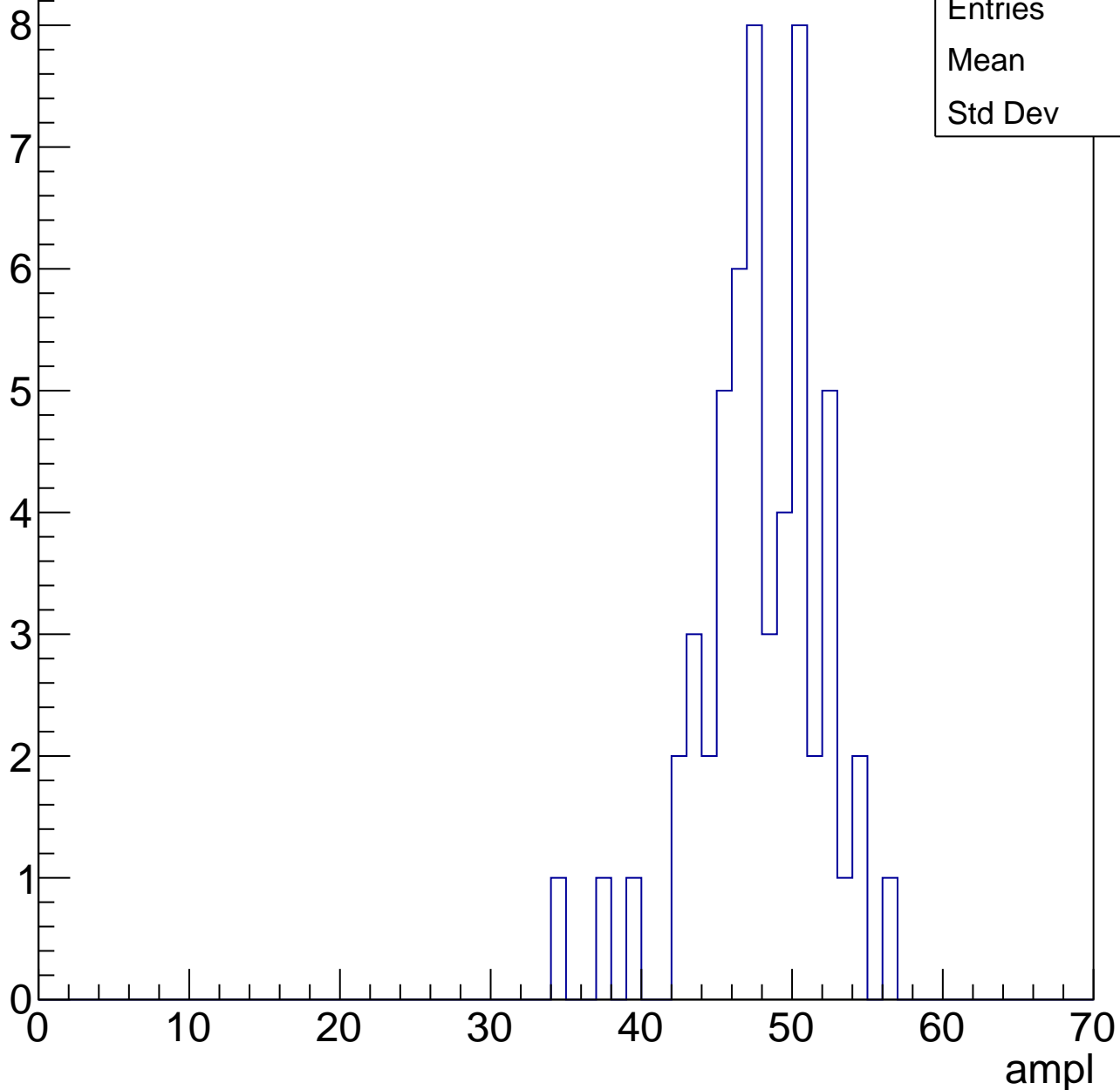


B1L101S, U15-ch108, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	47.4
Std Dev	4.11

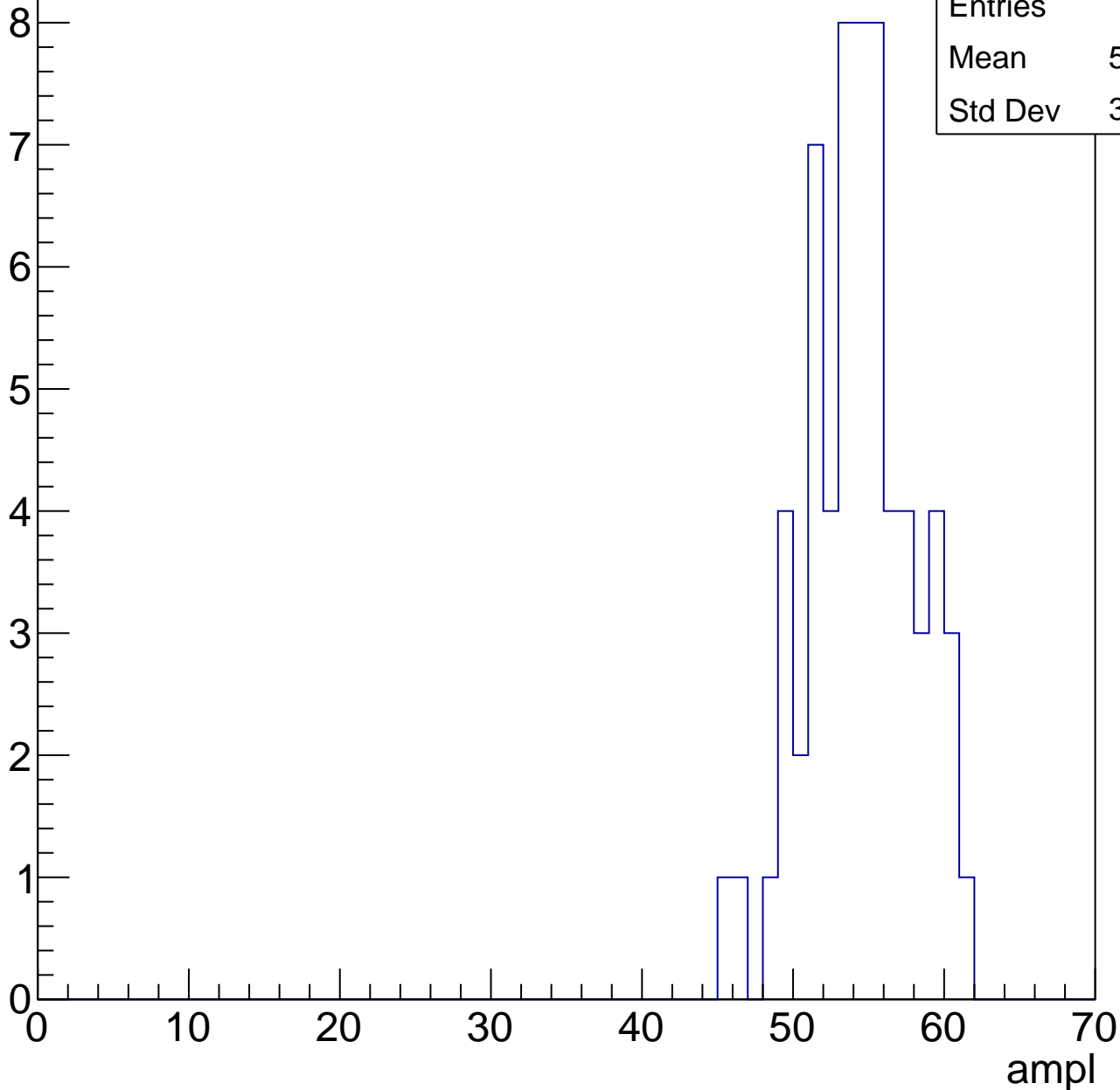


B1L101S, U15-ch108, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	53.95
Std Dev	3.484

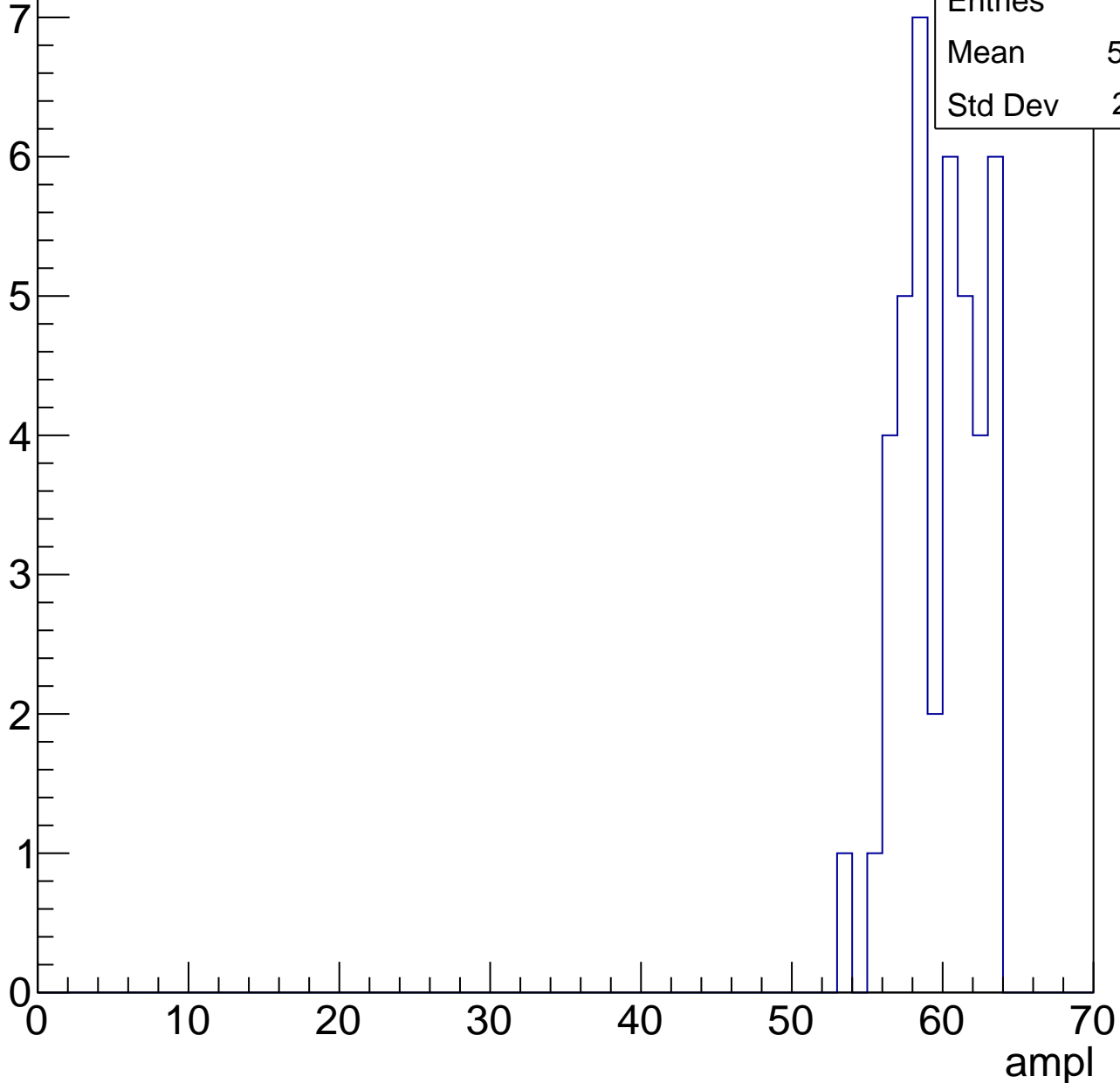


B1L101S, U15-ch108, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.32
Std Dev	2.561

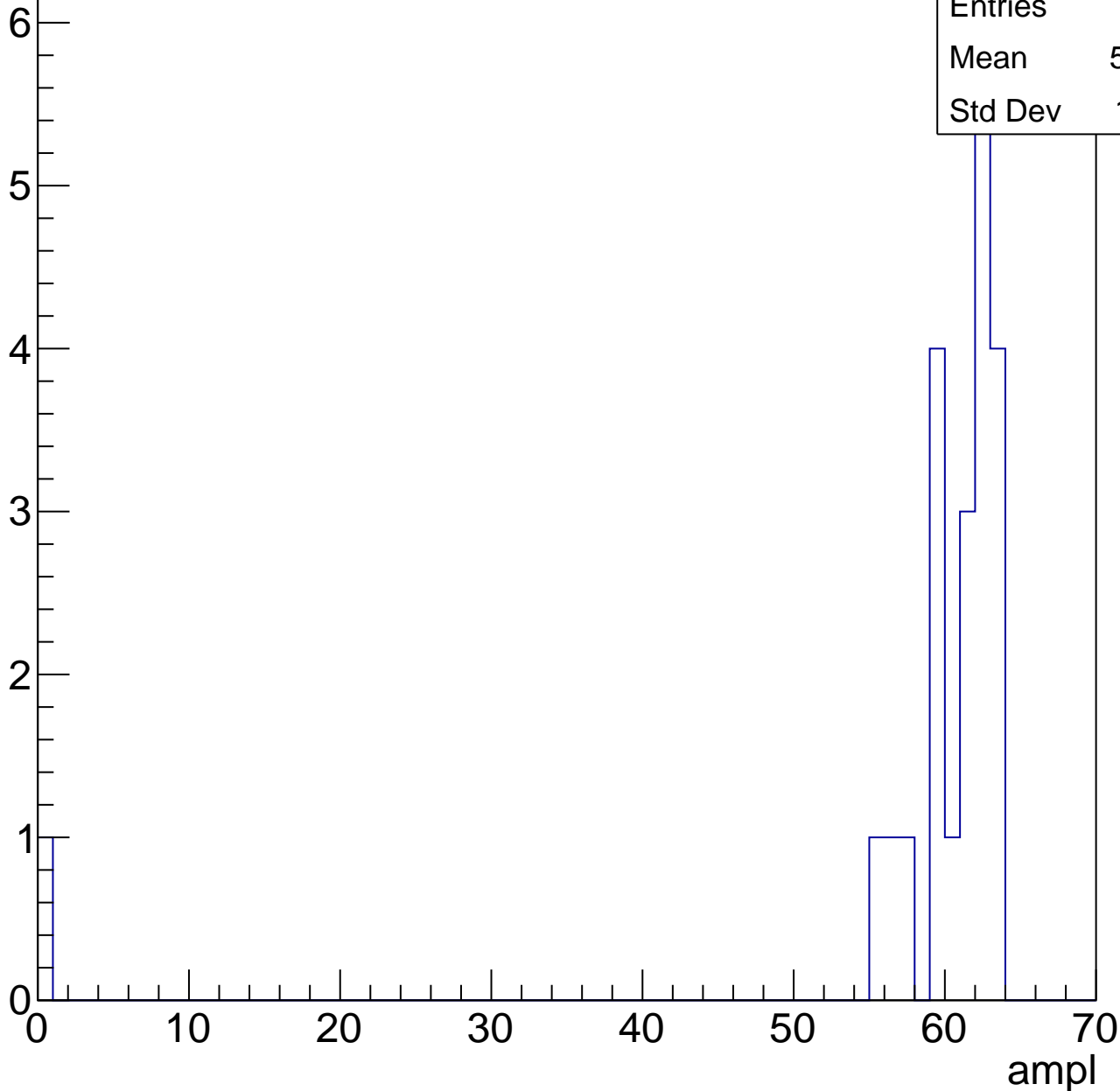


B1L101S, U15-ch108, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

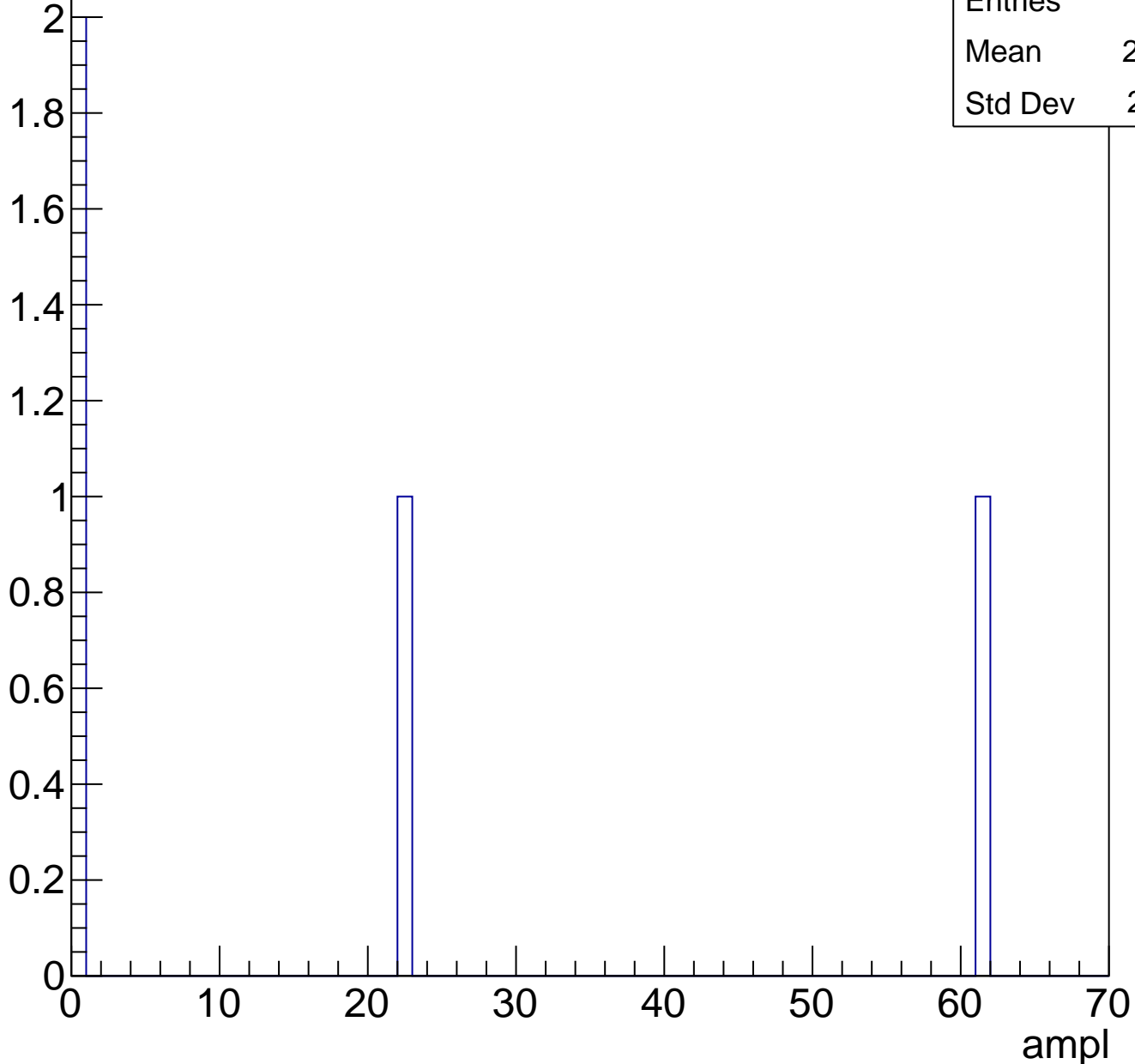
Entries	22
Mean	57.77
Std Dev	12.81



B1L101S, U15-ch108, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	4
Mean	20.75
Std Dev	24.91

B1L101S, U15-ch109, adc0

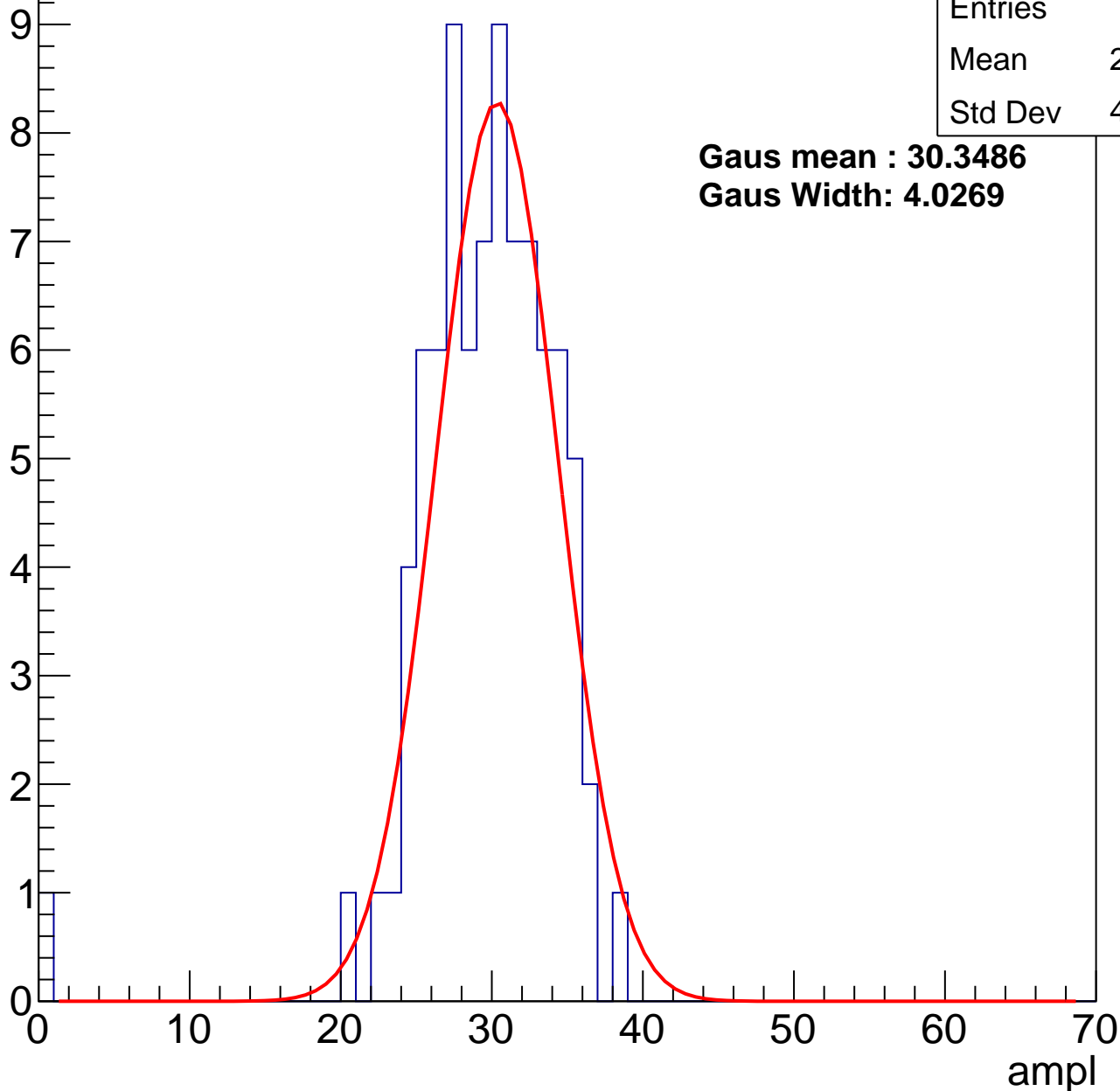
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	85
Mean	29.16
Std Dev	4.853

Gaus mean : 30.3486

Gaus Width: 4.0269



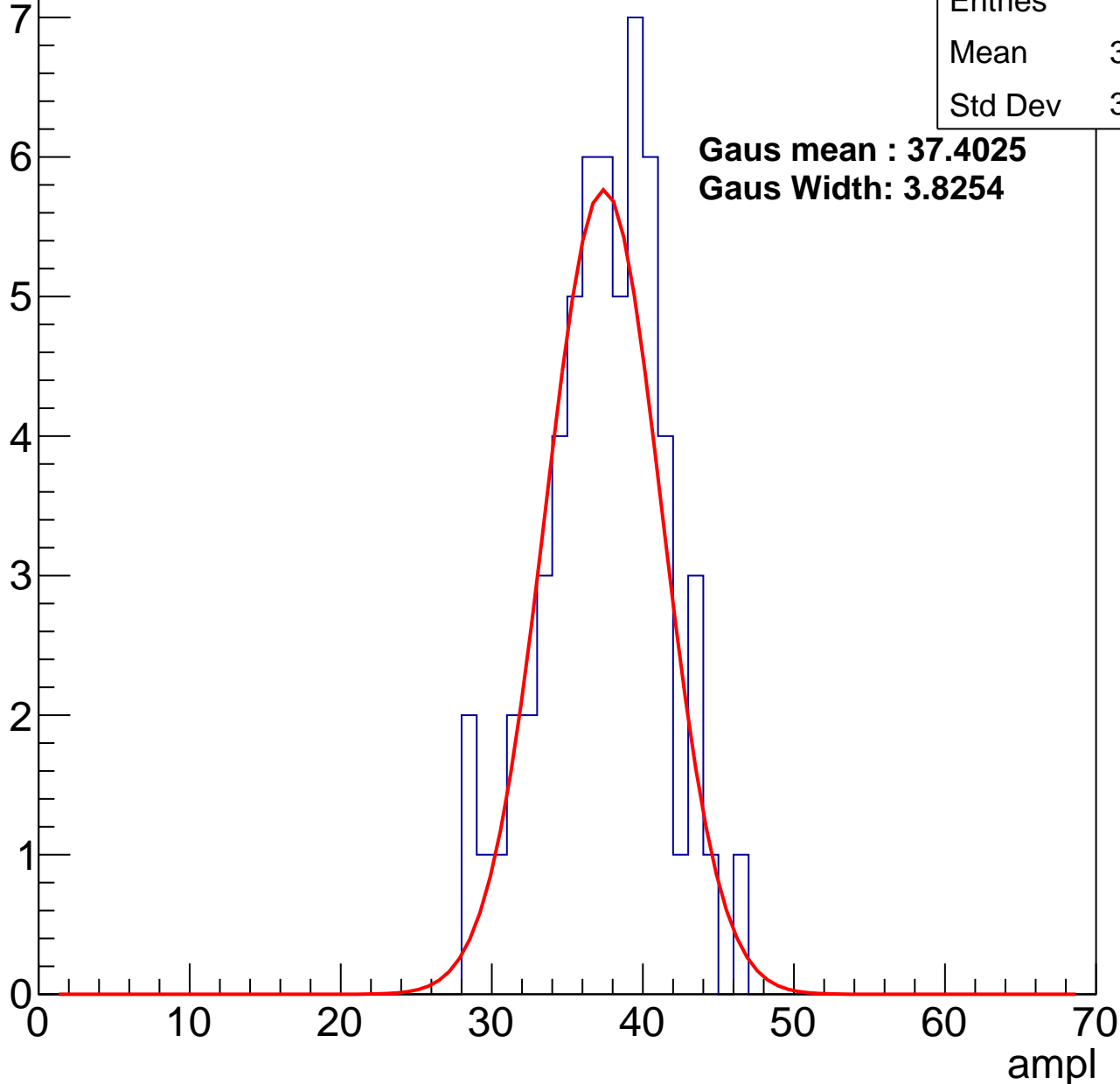
B1L101S, U15-ch109, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	36.95
Std Dev	3.918

Gaus mean : 37.4025
Gaus Width: 3.8254



B1L101S, U15-ch109, adc2

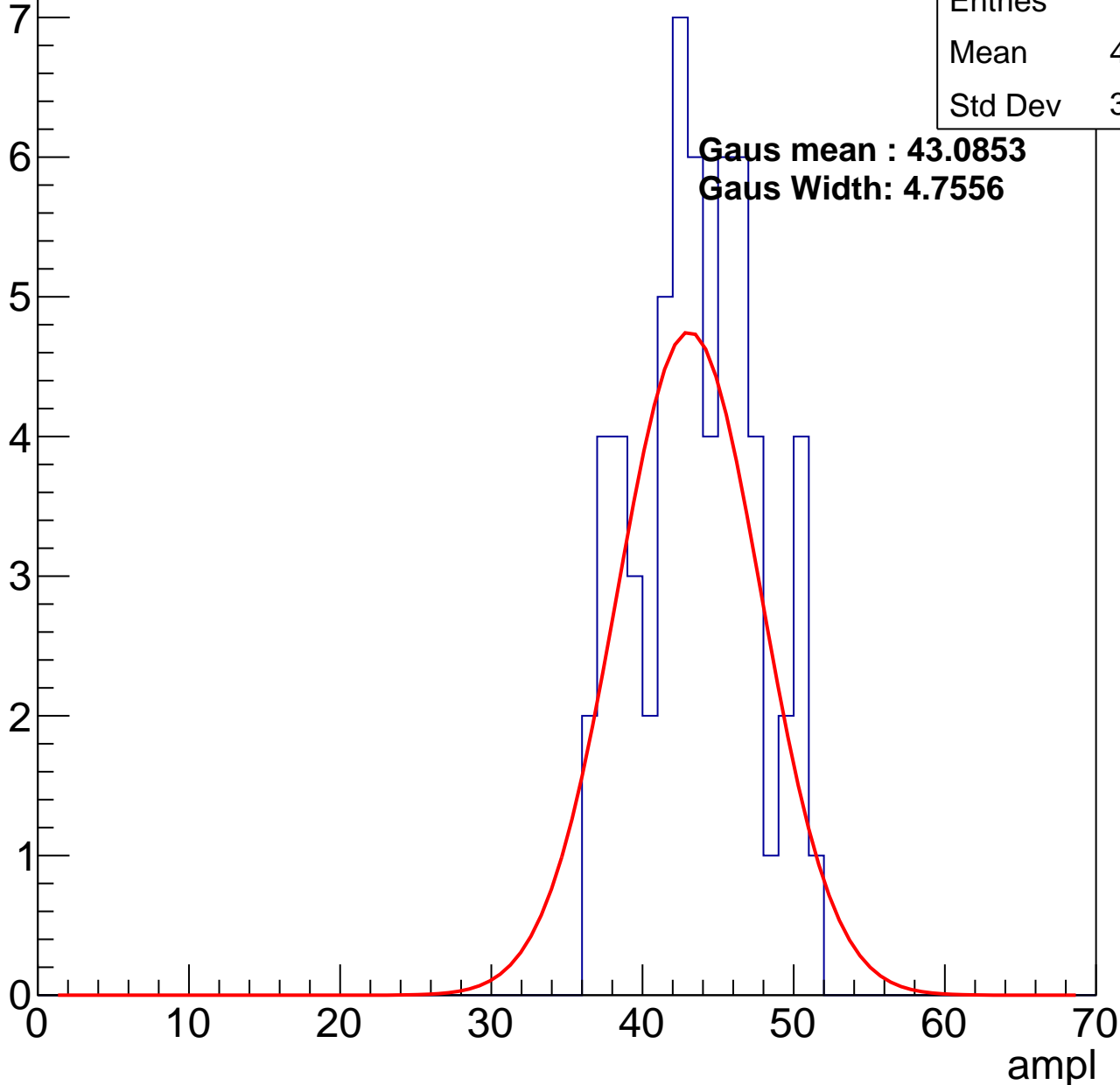
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	43.16
Std Dev	3.918

Gaus mean : 43.0853

Gaus Width: 4.7556

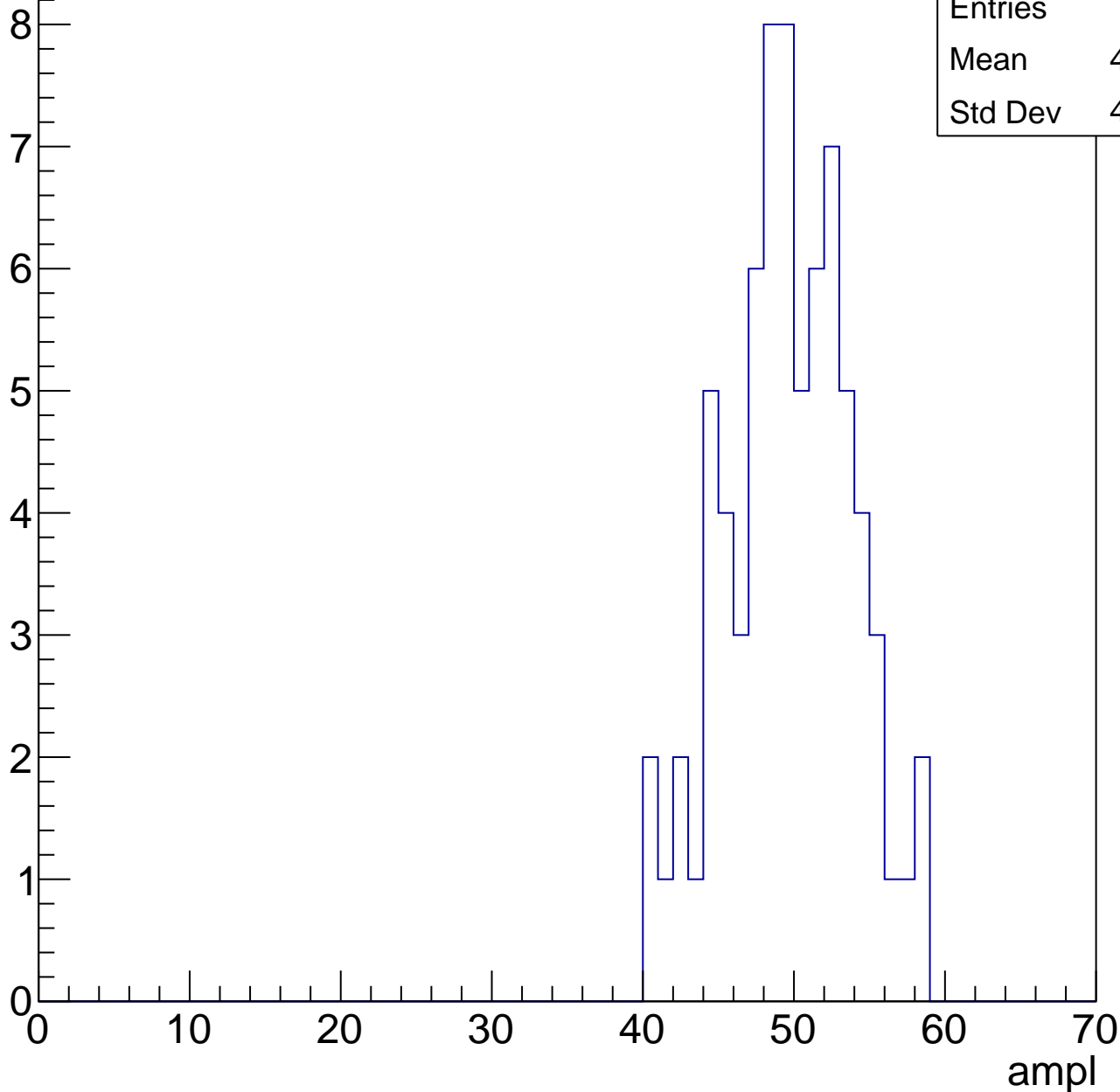


B1L101S, U15-ch109, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	49.18
Std Dev	4.128

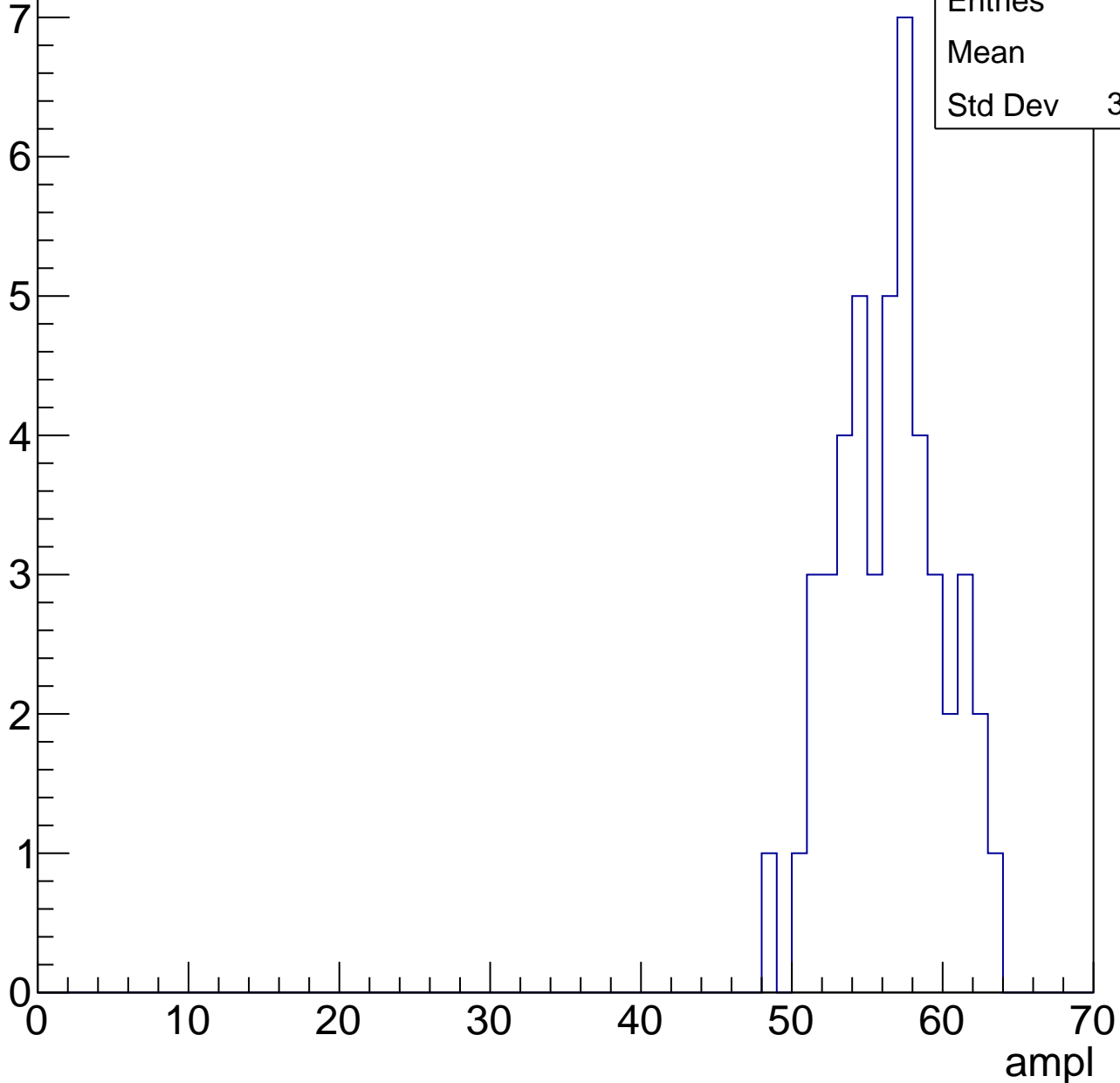


B1L101S, U15-ch109, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	56
Std Dev	3.452

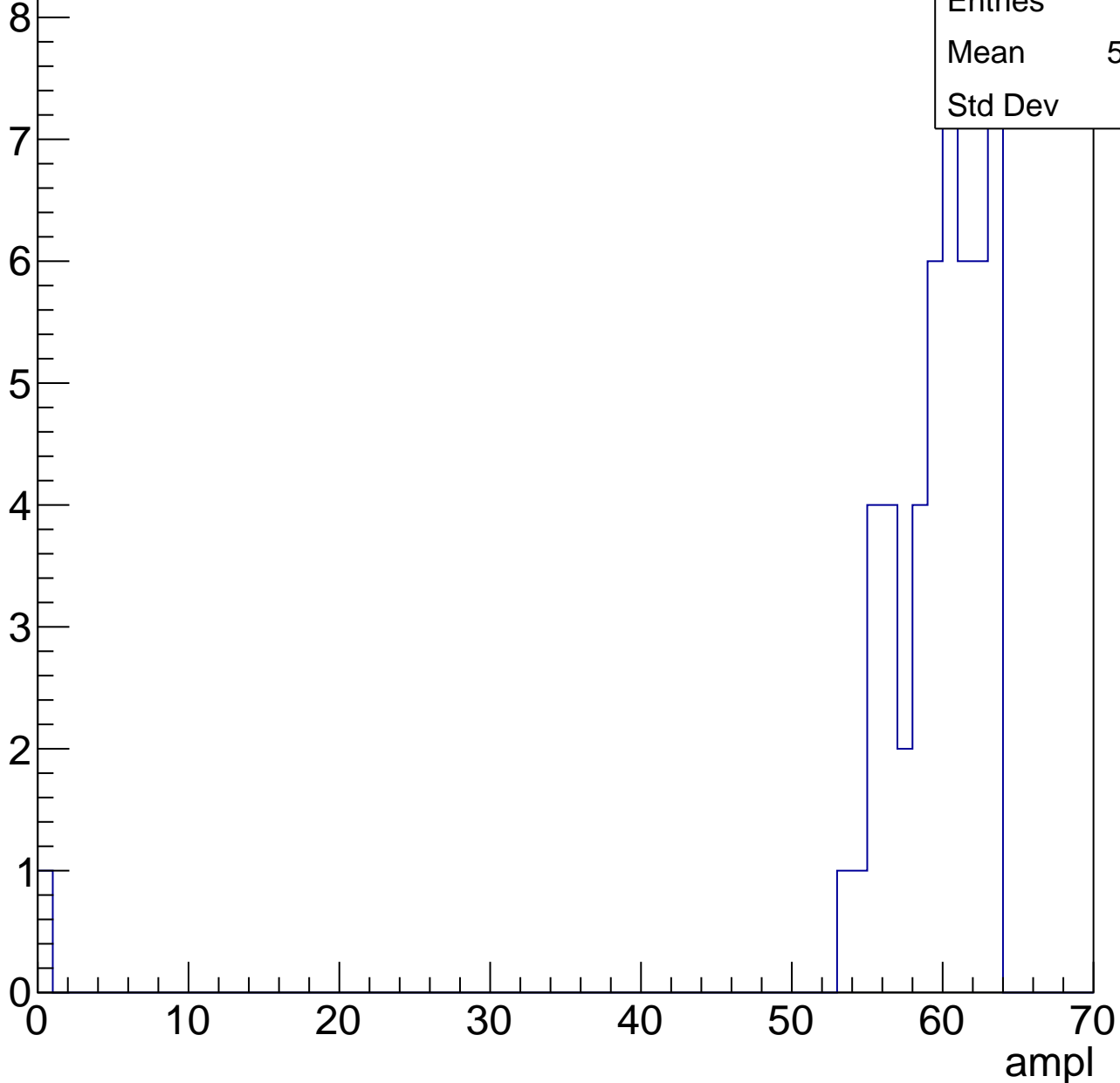


B1L101S, U15-ch109, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

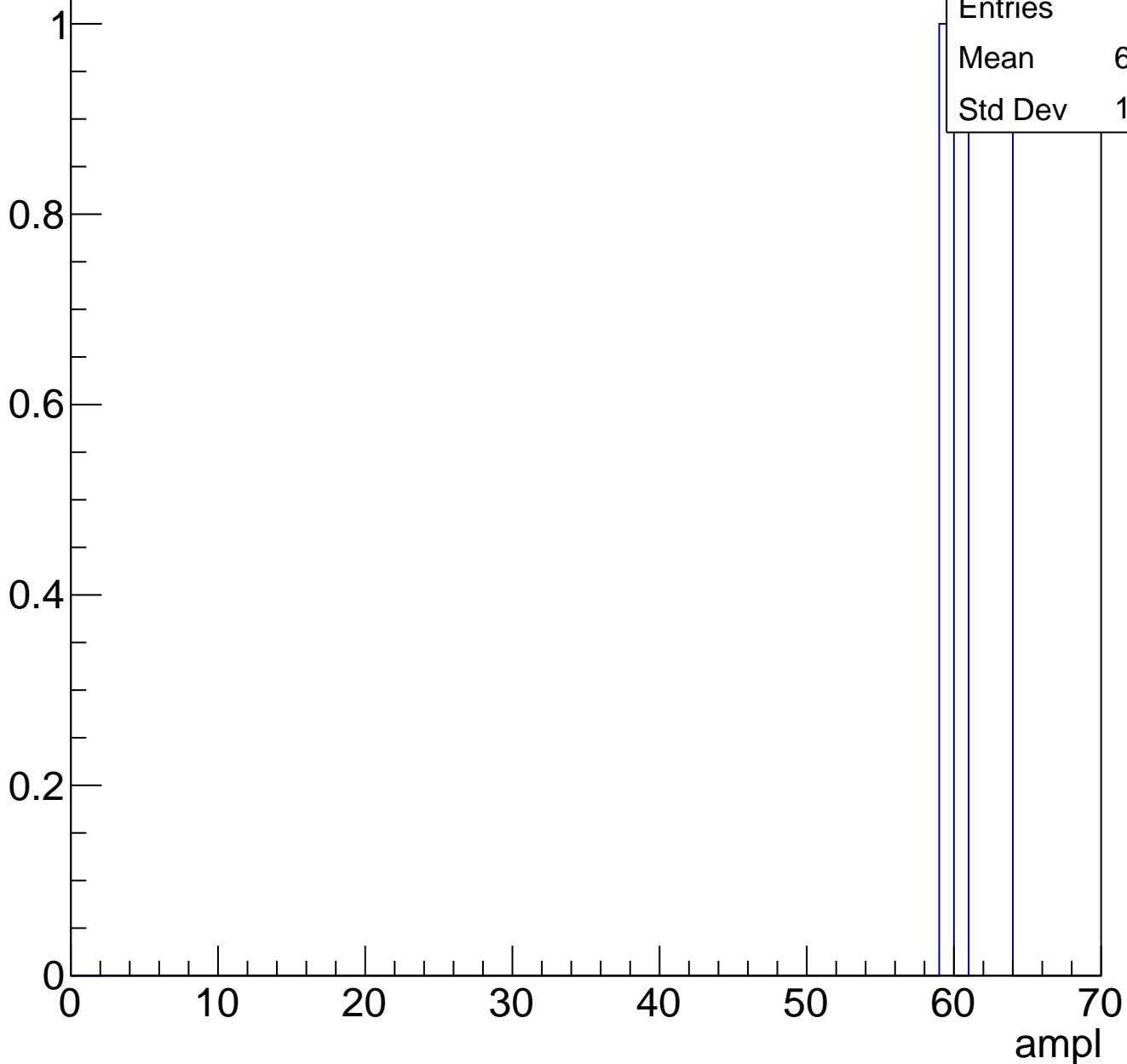
Entries	51
Mean	58.29
Std Dev	8.68



B1L101S, U15-ch109, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch109, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U15-ch110, adc0

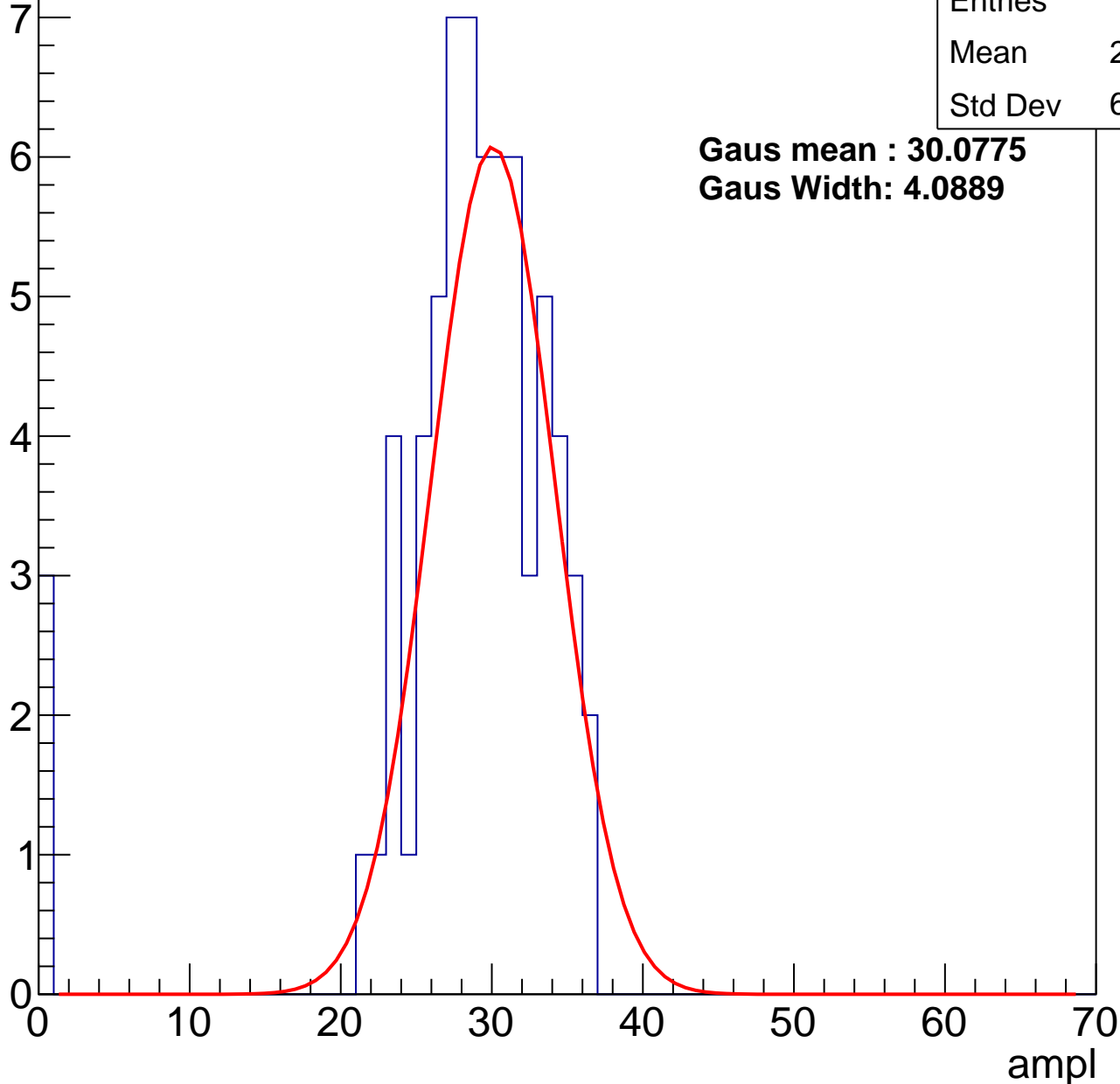
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	27.76
Std Dev	6.954

Gaus mean : 30.0775

Gaus Width: 4.0889



B1L101S, U15-ch110, adc1

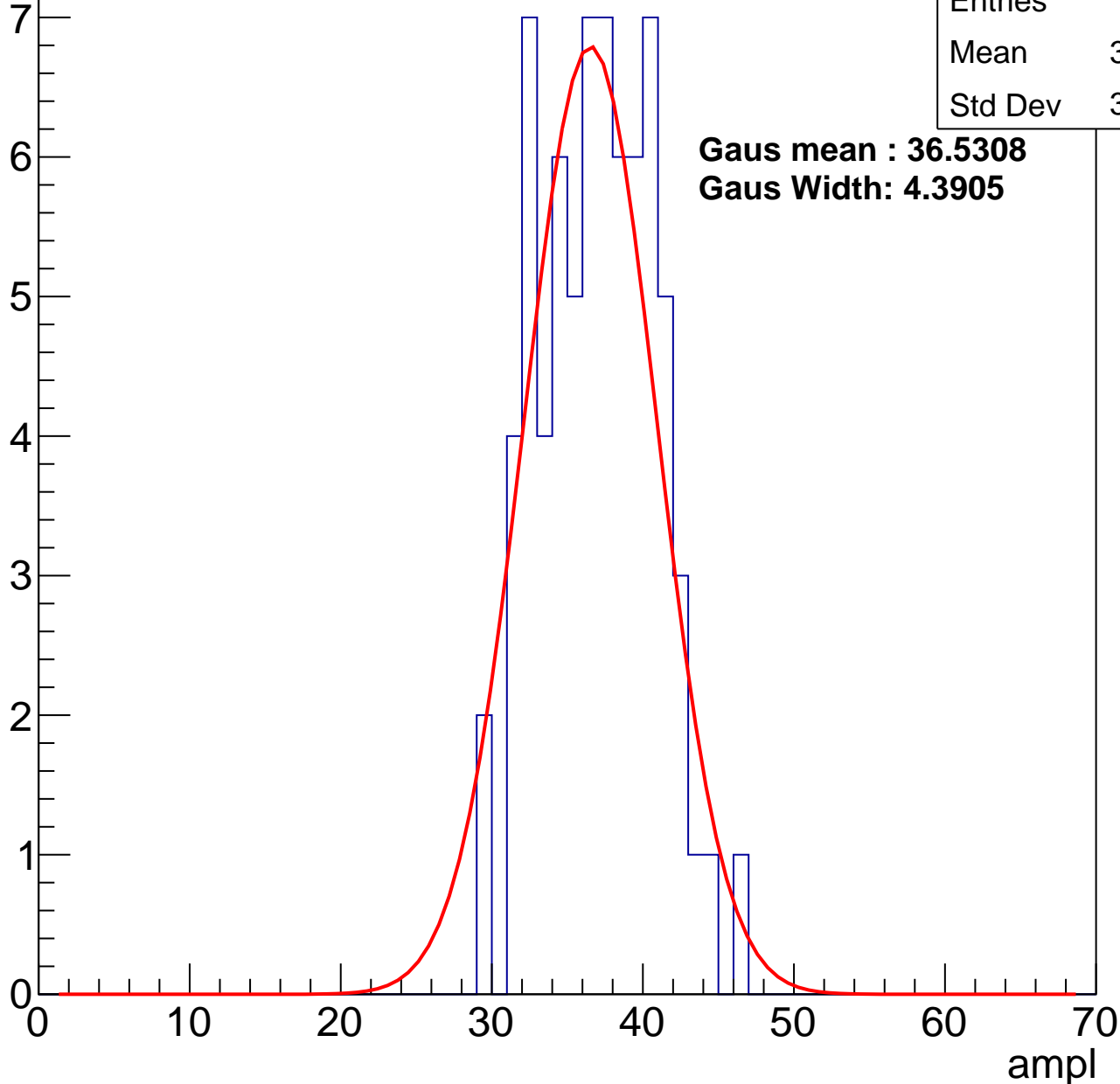
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	36.58
Std Dev	3.715

Gaus mean : 36.5308

Gaus Width: 4.3905



B1L101S, U15-ch110, adc2

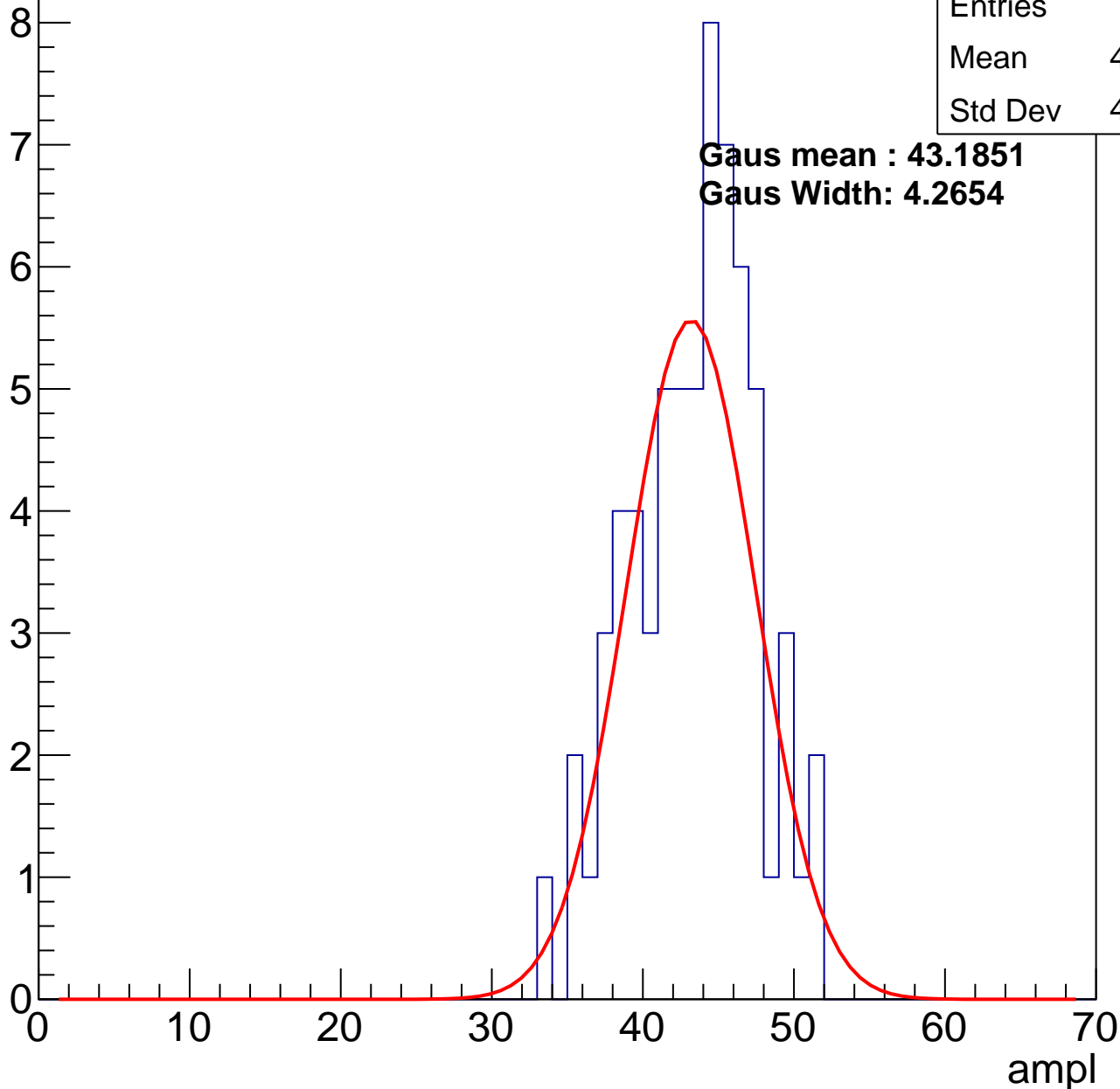
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	42.92
Std Dev	4.046

Gaus mean : 43.1851

Gaus Width: 4.2654

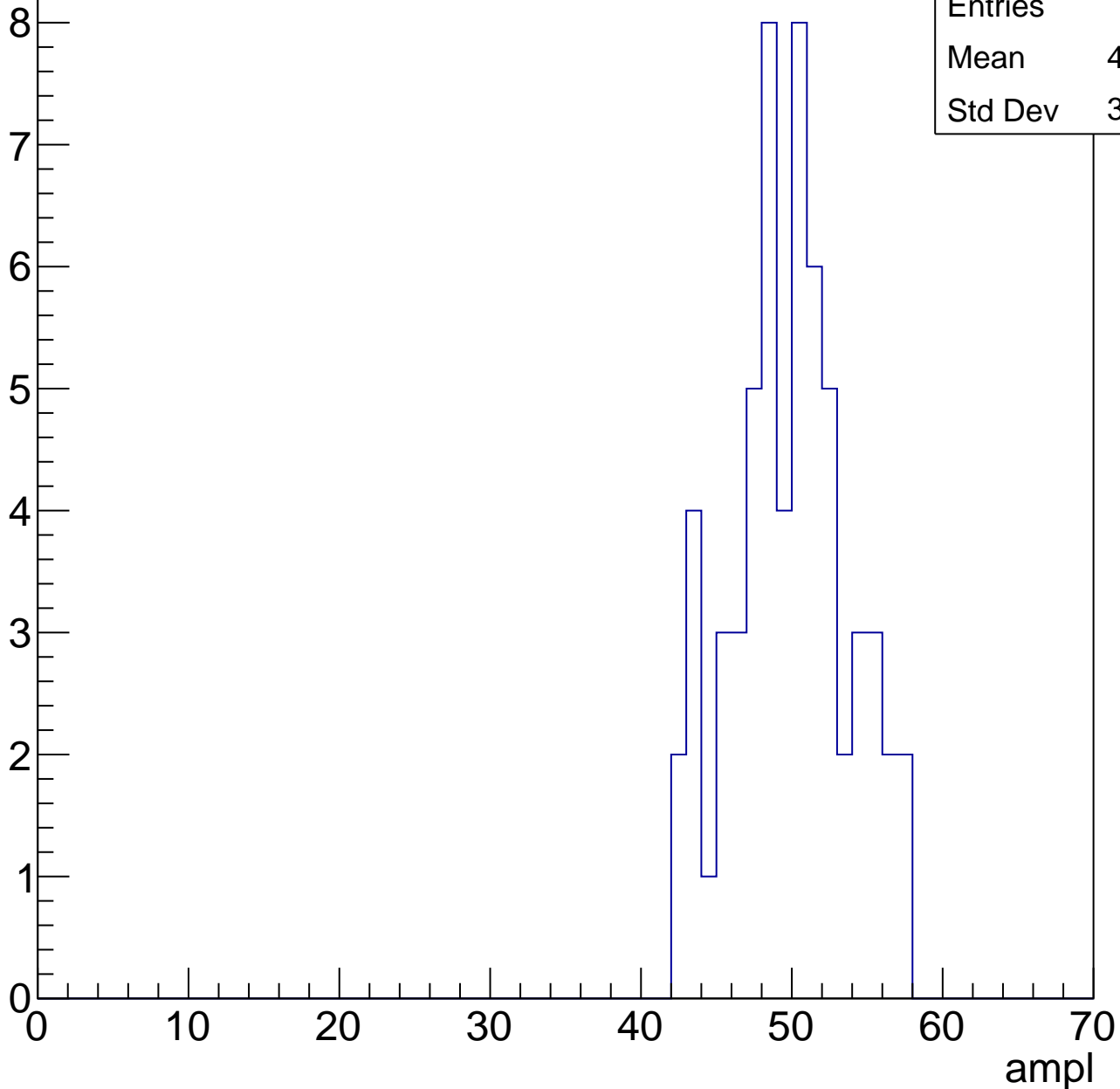


B1L101S, U15-ch110, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	49.39
Std Dev	3.795

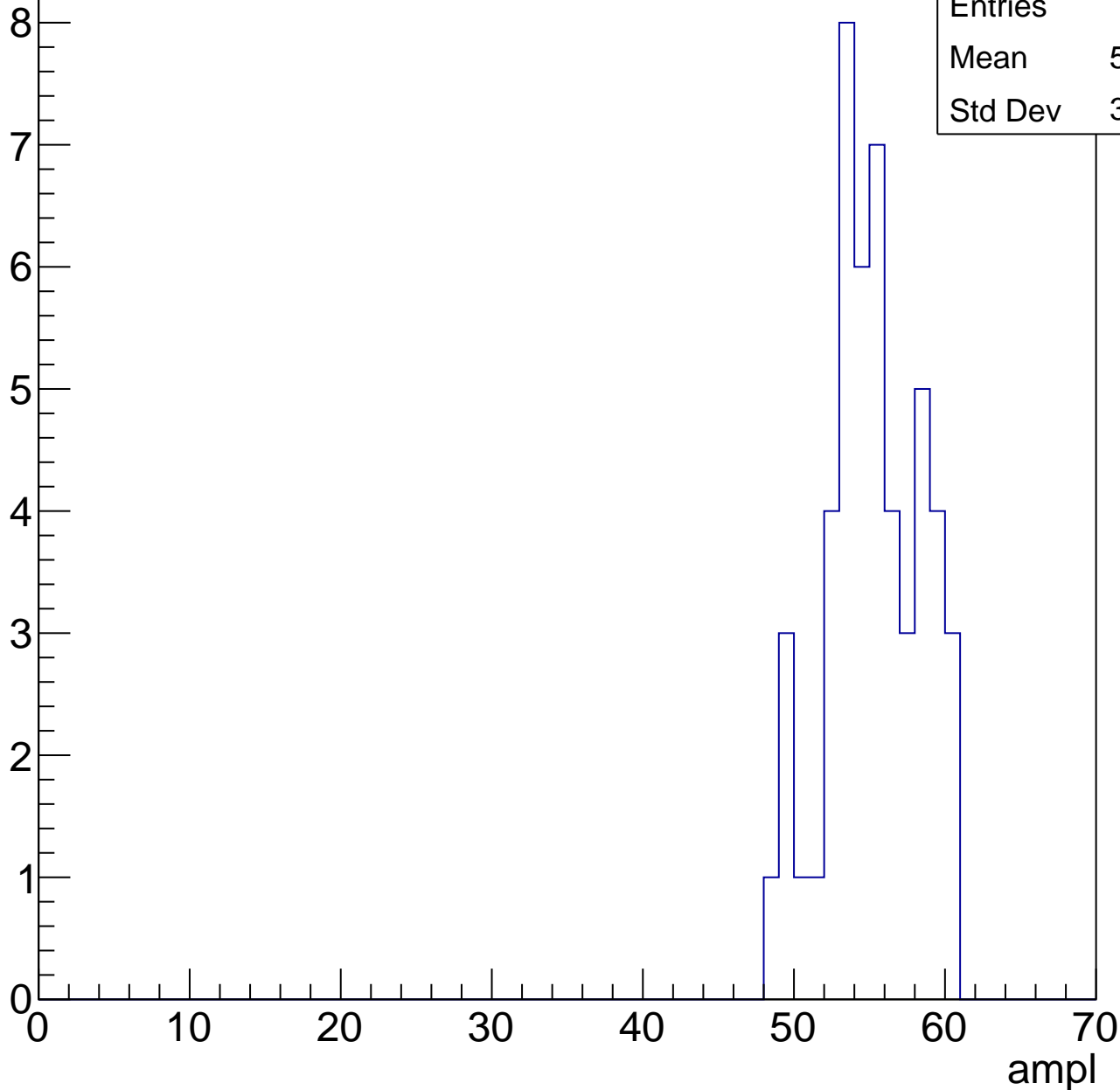


B1L101S, U15-ch110, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	54.76
Std Dev	3.063

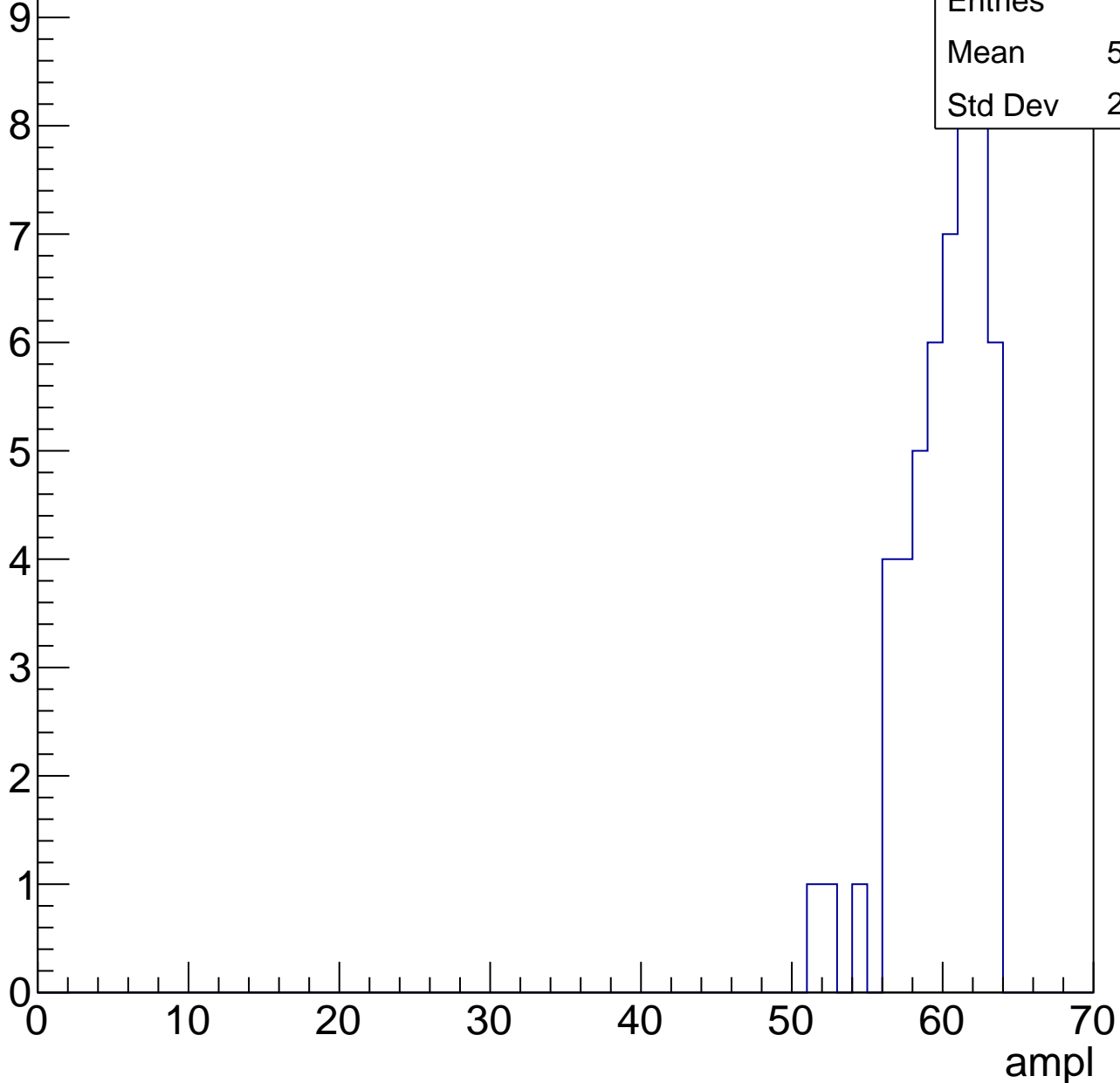


B1L101S, U15-ch110, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

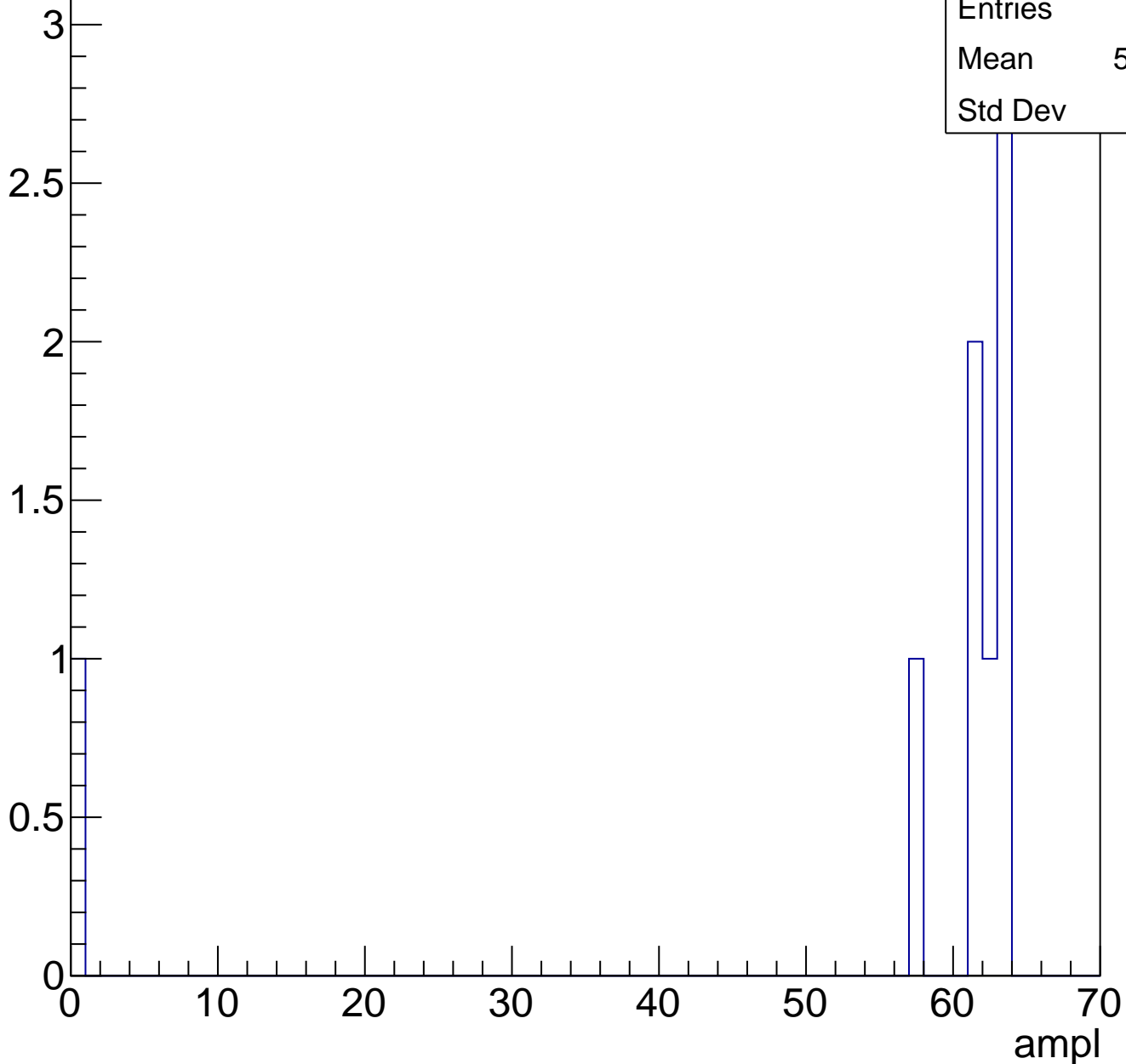
Entries	52
Mean	59.56
Std Dev	2.756



B1L101S, U15-ch110, adc6

calib_packv5_042523_0143.root, FC#0, port D2

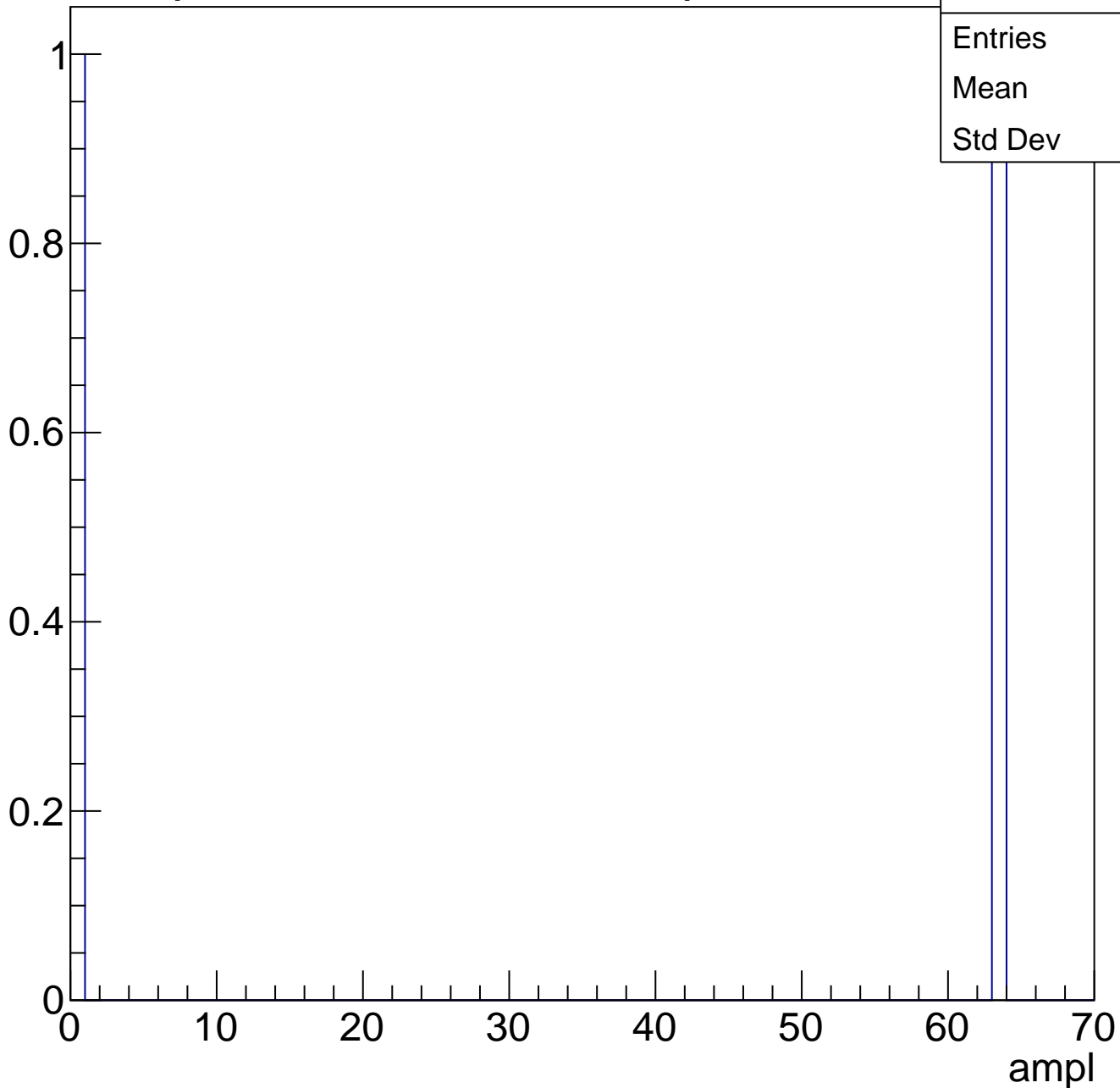
Entry



B1L101S, U15-ch110, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch111, adc0

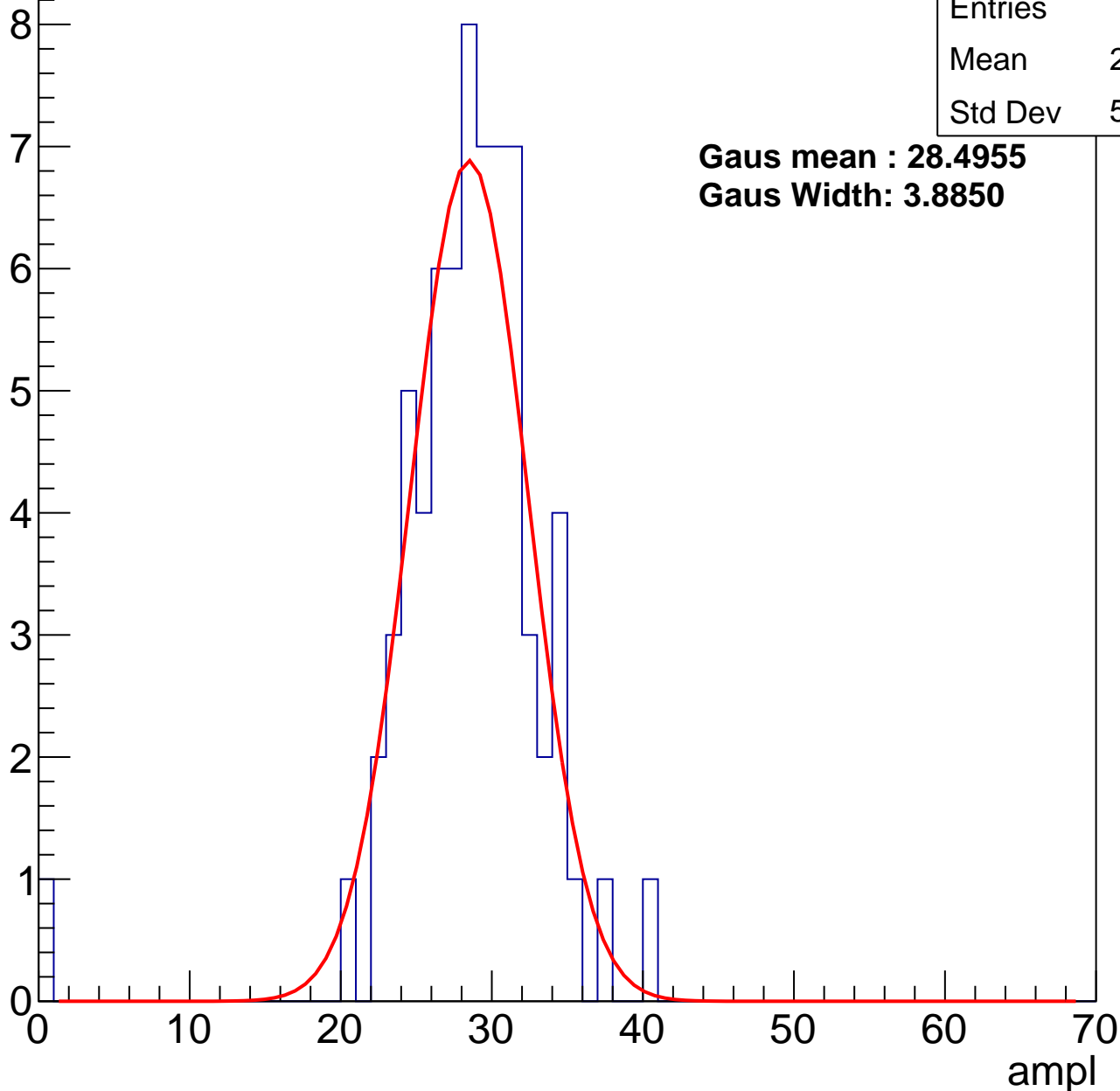
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.04
Std Dev	5.043

Gaus mean : 28.4955

Gaus Width: 3.8850



B1L101S, U15-ch111, adc1

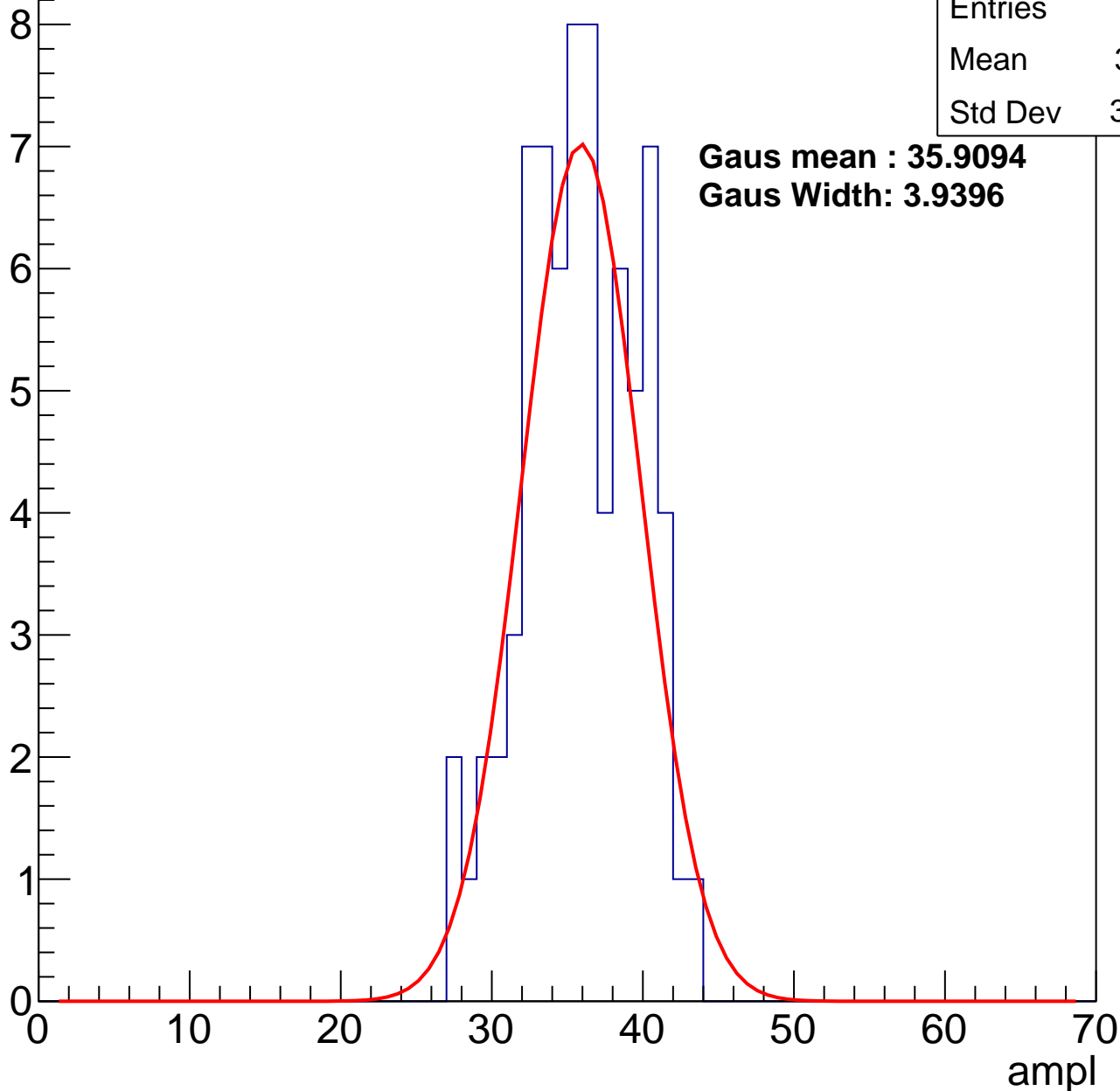
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	35.41
Std Dev	3.716

Gaus mean : 35.9094

Gaus Width: 3.9396



B1L101S, U15-ch111, adc2

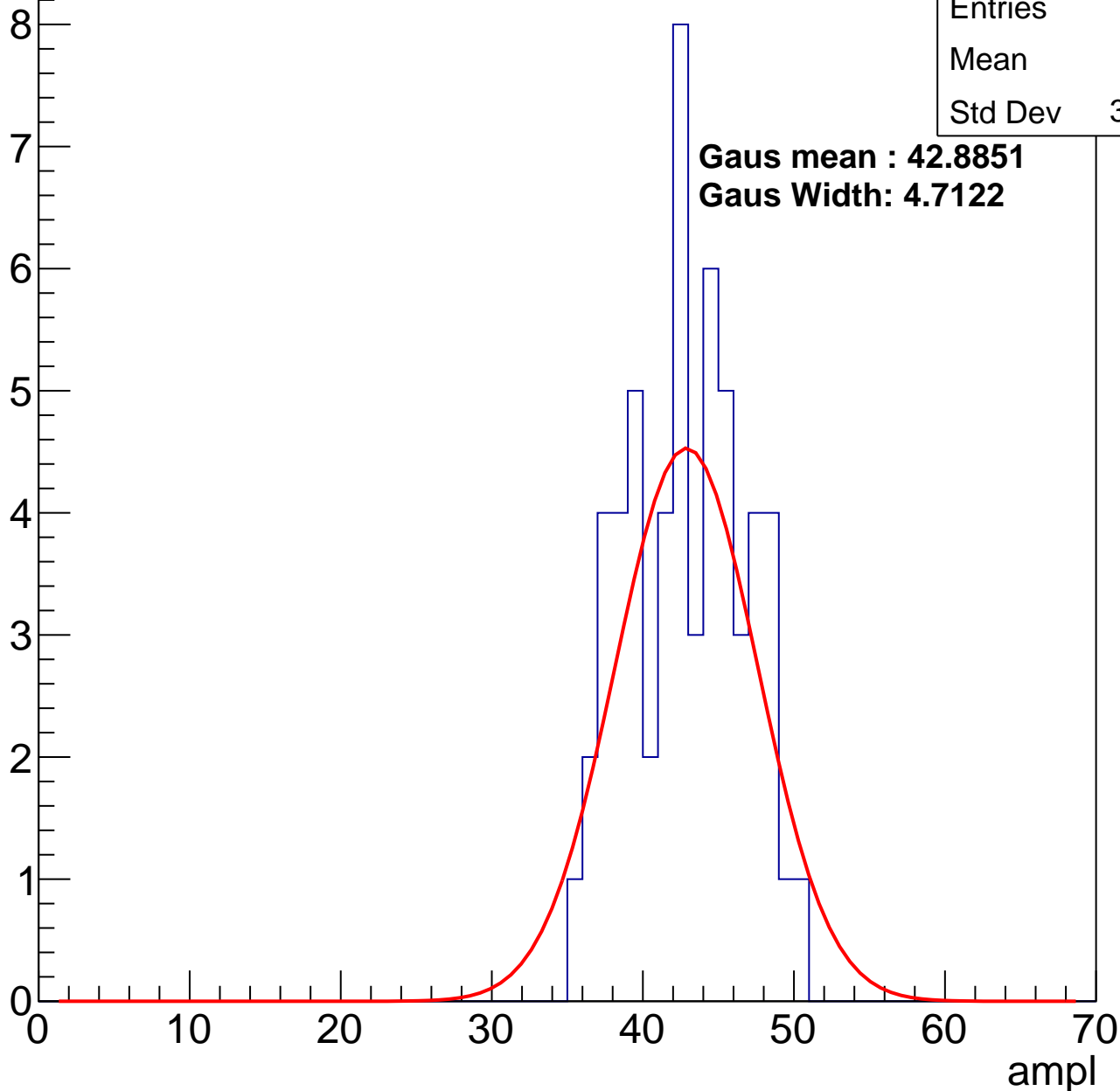
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	42.4
Std Dev	3.769

Gaus mean : 42.8851

Gaus Width: 4.7122

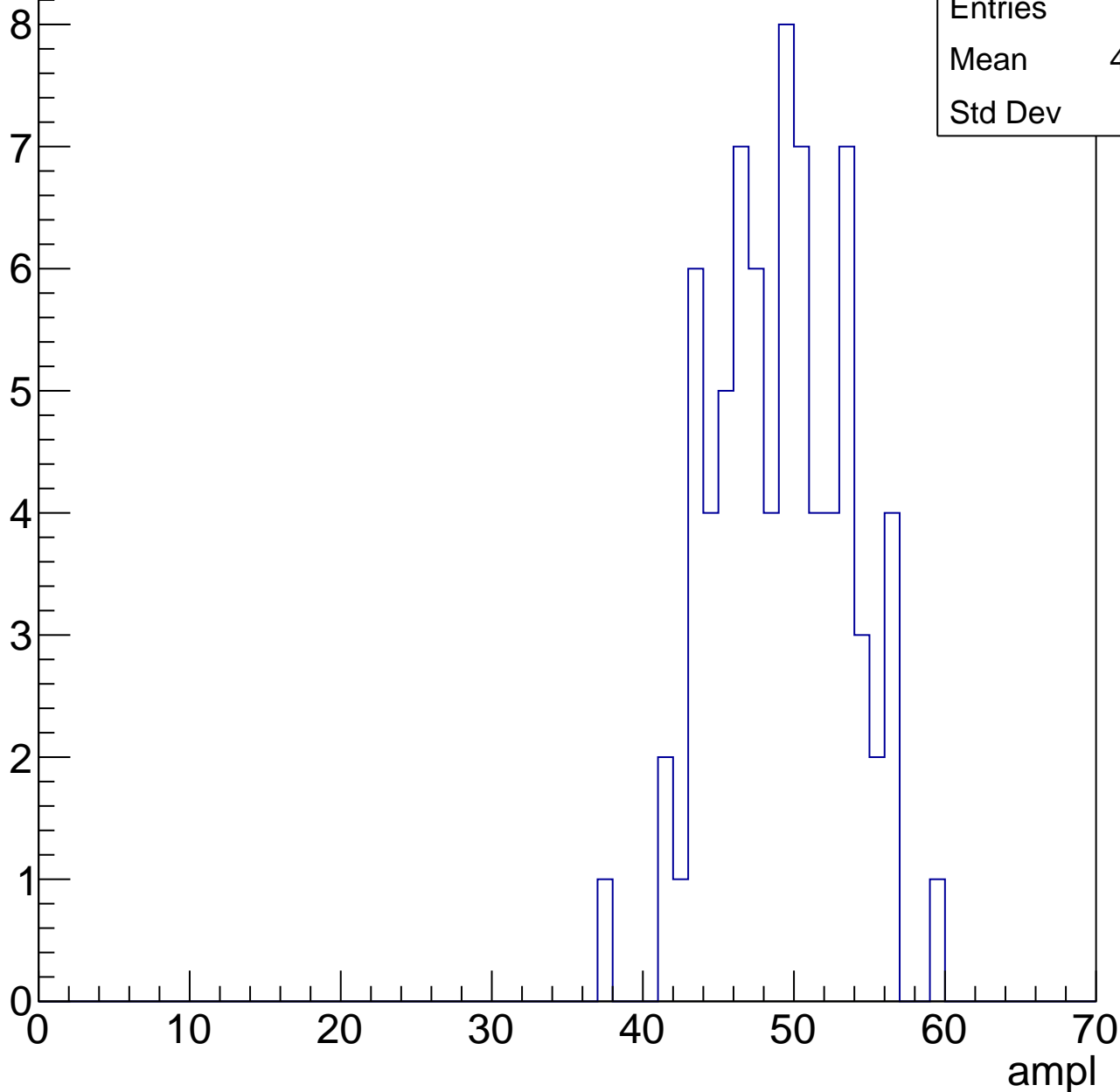


B1L101S, U15-ch111, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	48.63
Std Dev	4.31

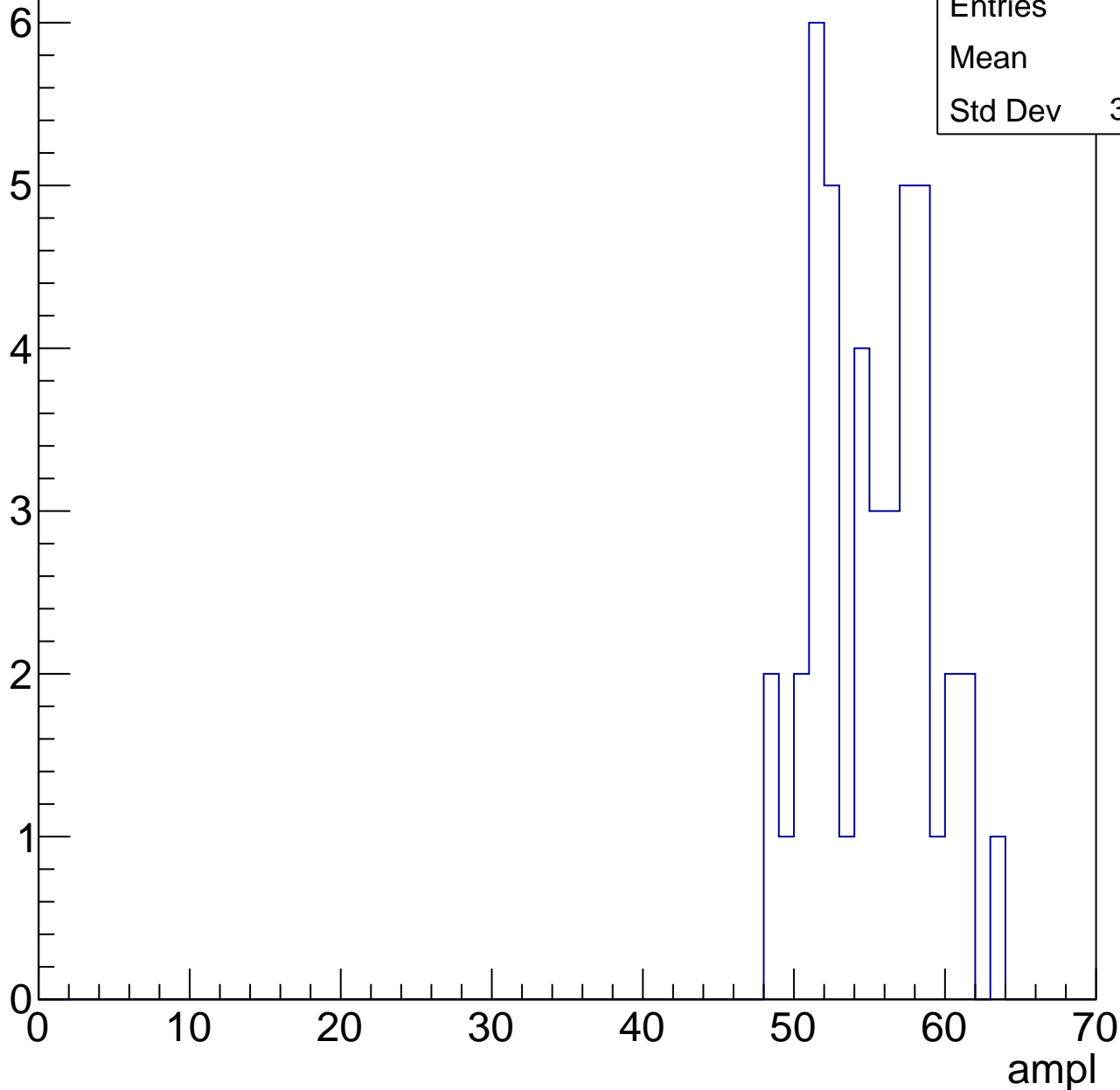


B1L101S, U15-ch111, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

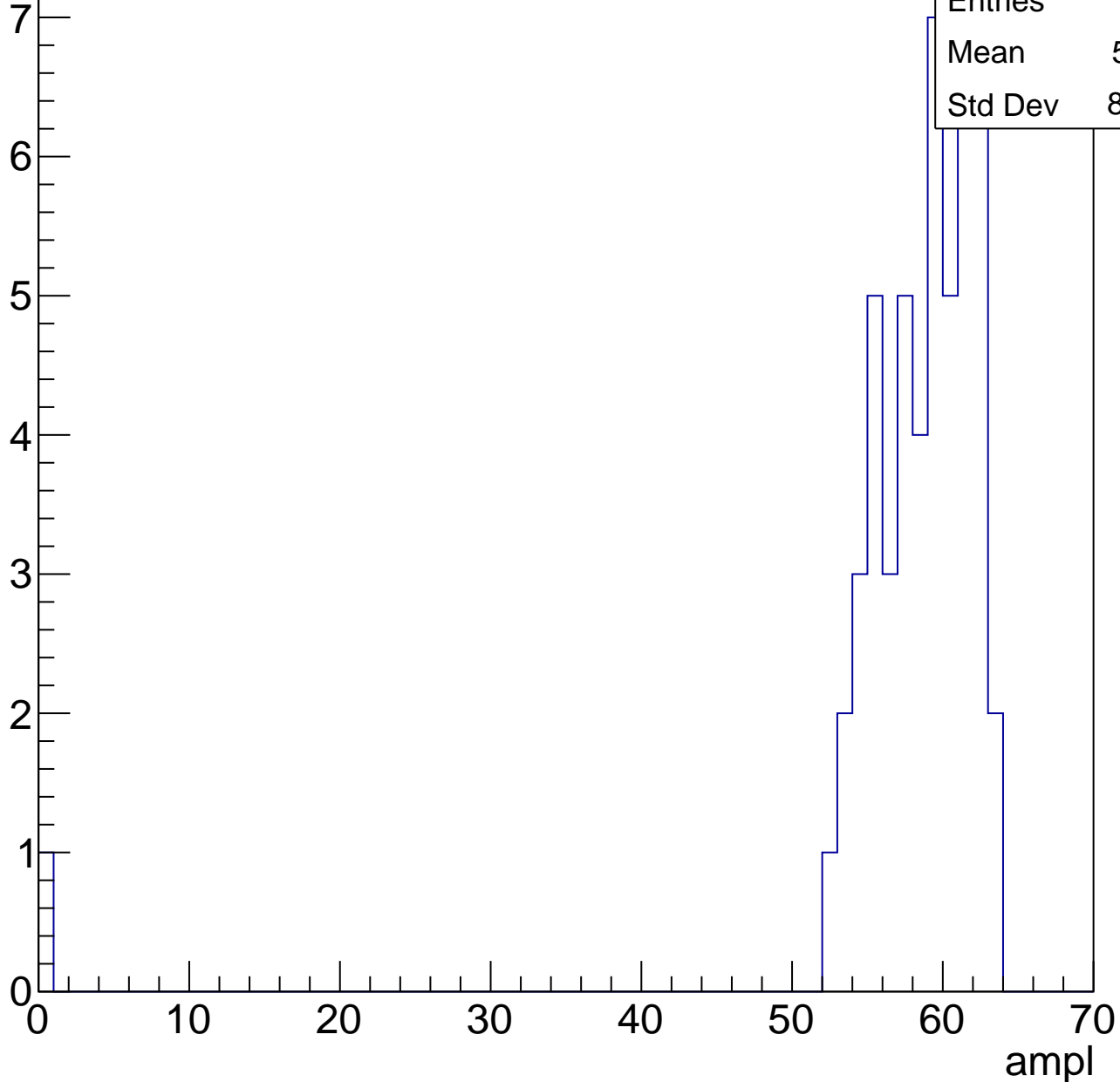
Entries	43
Mean	54.7
Std Dev	3.733



B1L101S, U15-ch111, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch111, adc6

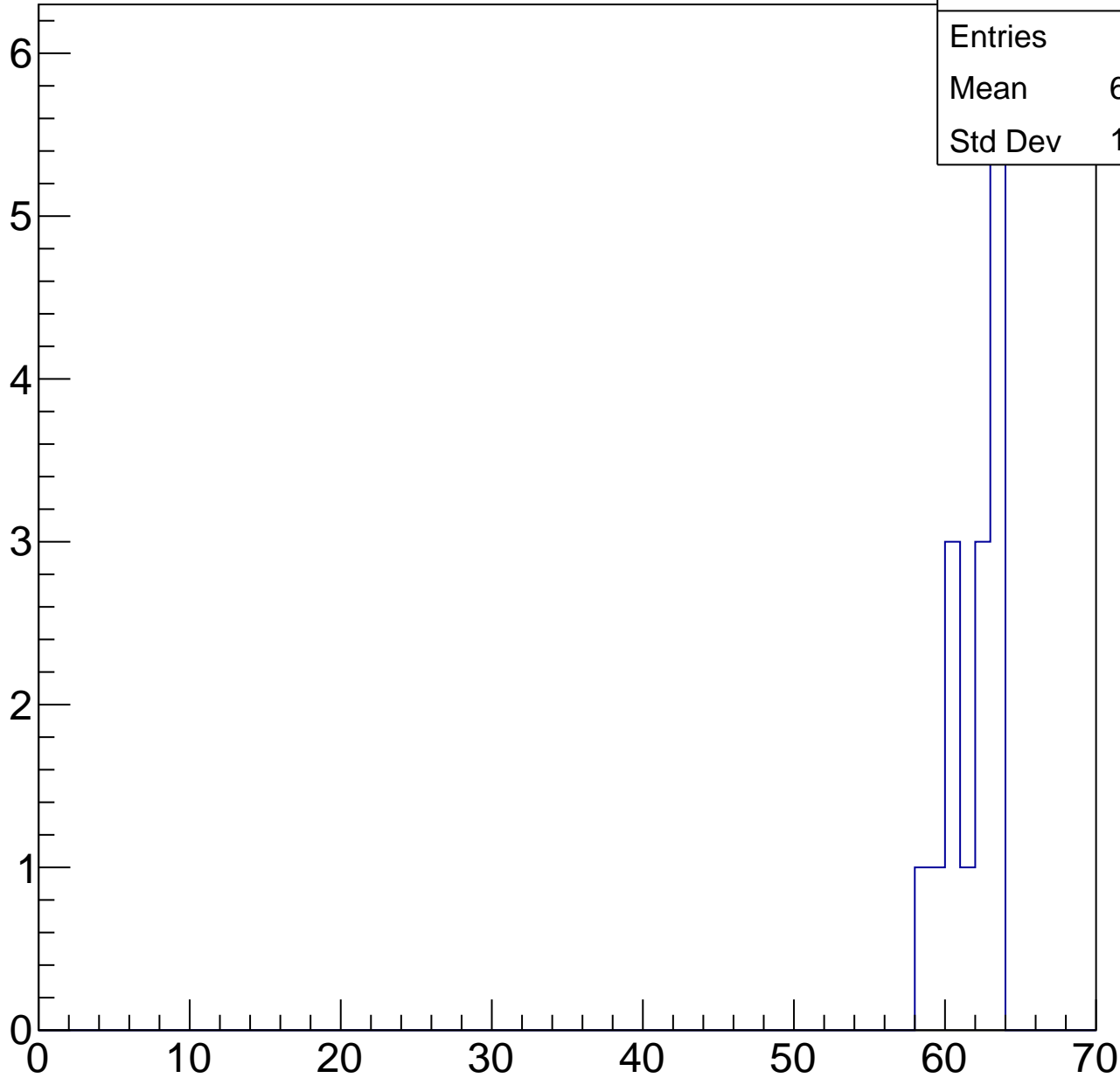
calib_packv5_042523_0143.root, FC#0, port D2

Entry

6
5
4
3
2
1
0

Entries	15
Mean	61.47
Std Dev	1.628

ampl



B1L101S, U15-ch111, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch112, adc0

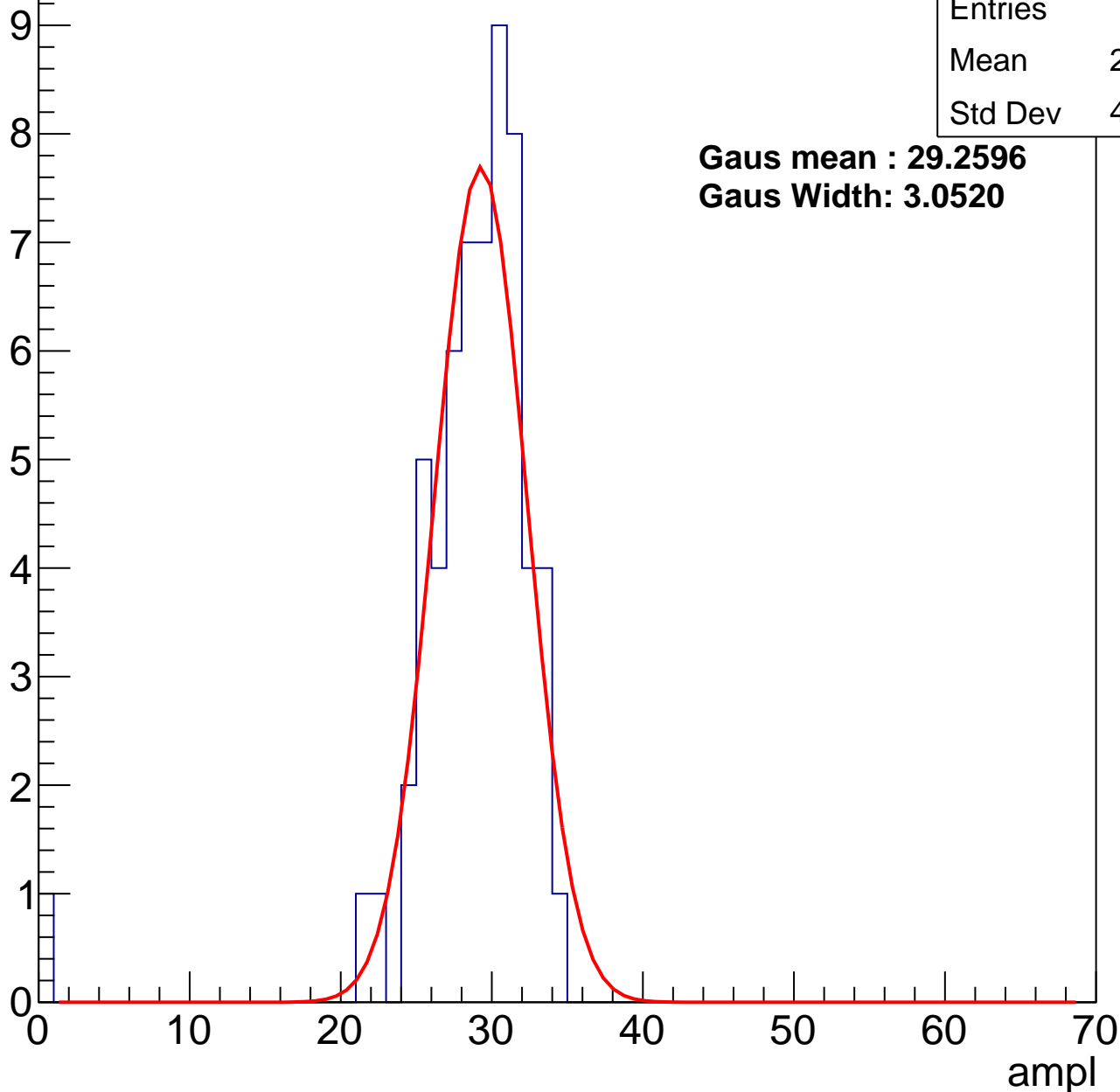
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	28.22
Std Dev	4.619

Gaus mean : 29.2596

Gaus Width: 3.0520



B1L101S, U15-ch112, adc1

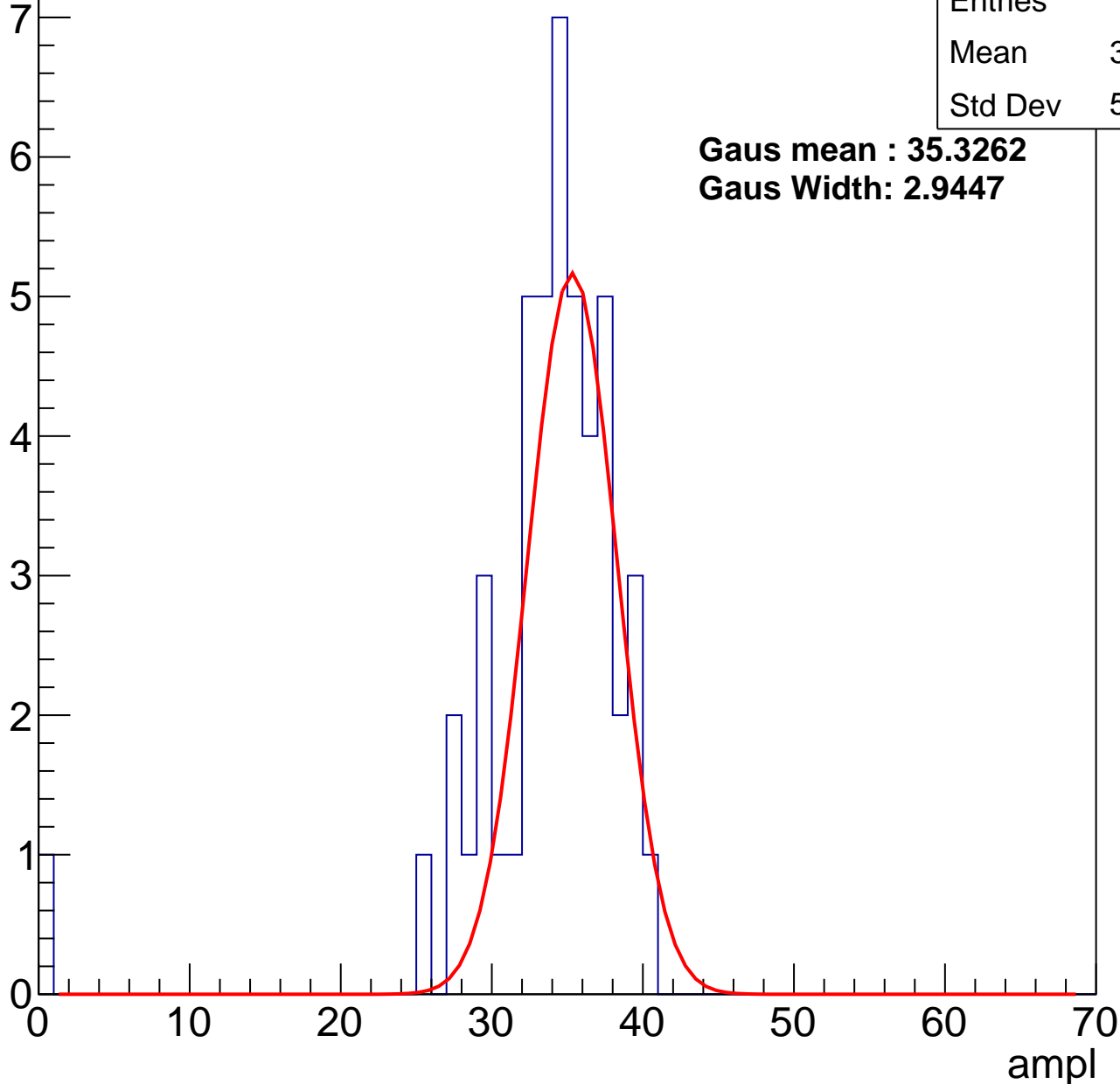
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	33.09
Std Dev	5.953

Gaus mean : 35.3262

Gaus Width: 2.9447



B1L101S, U15-ch112, adc2

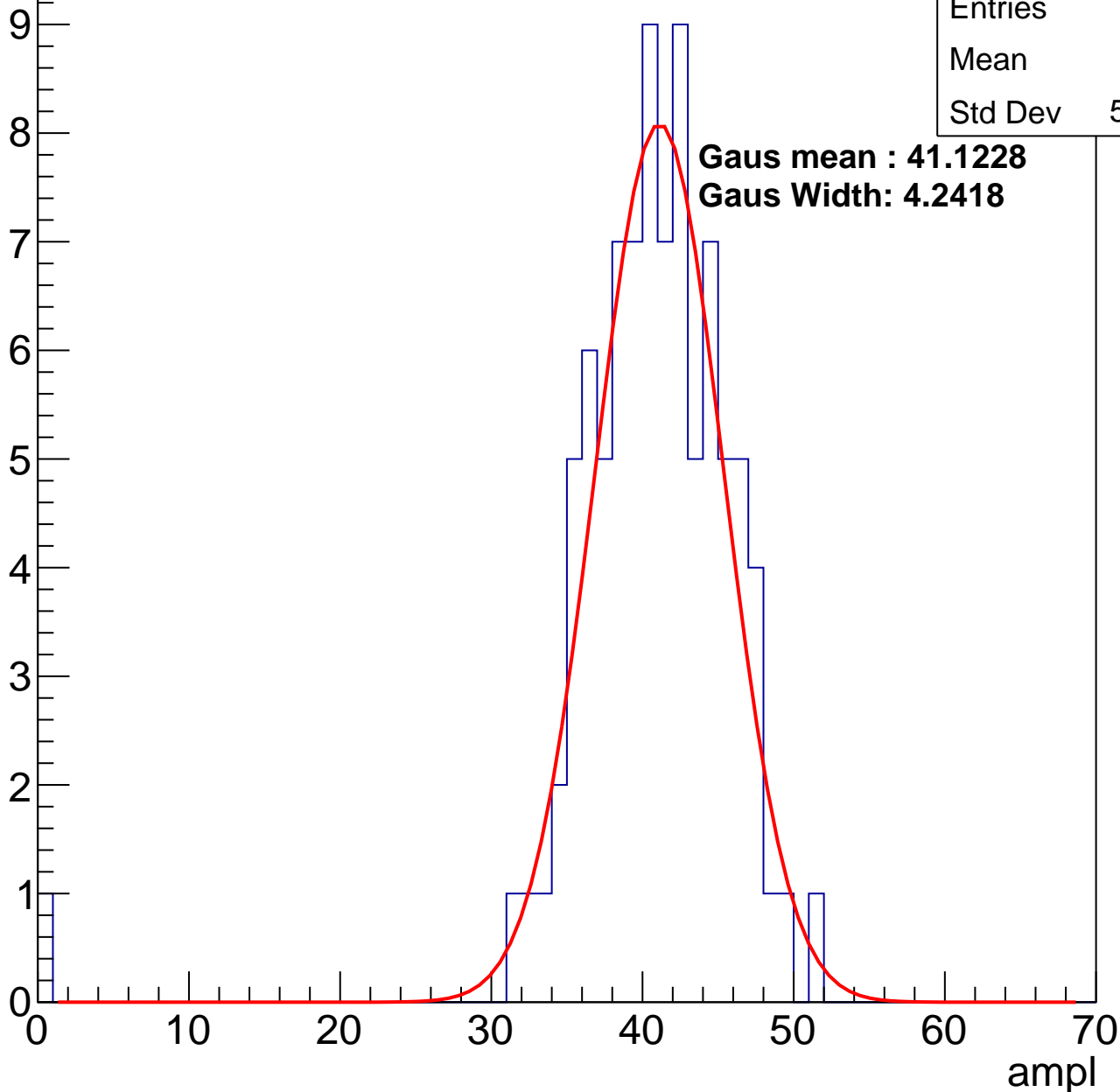
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	90
Mean	40.2
Std Dev	5.898

Gaus mean : 41.1228

Gaus Width: 4.2418

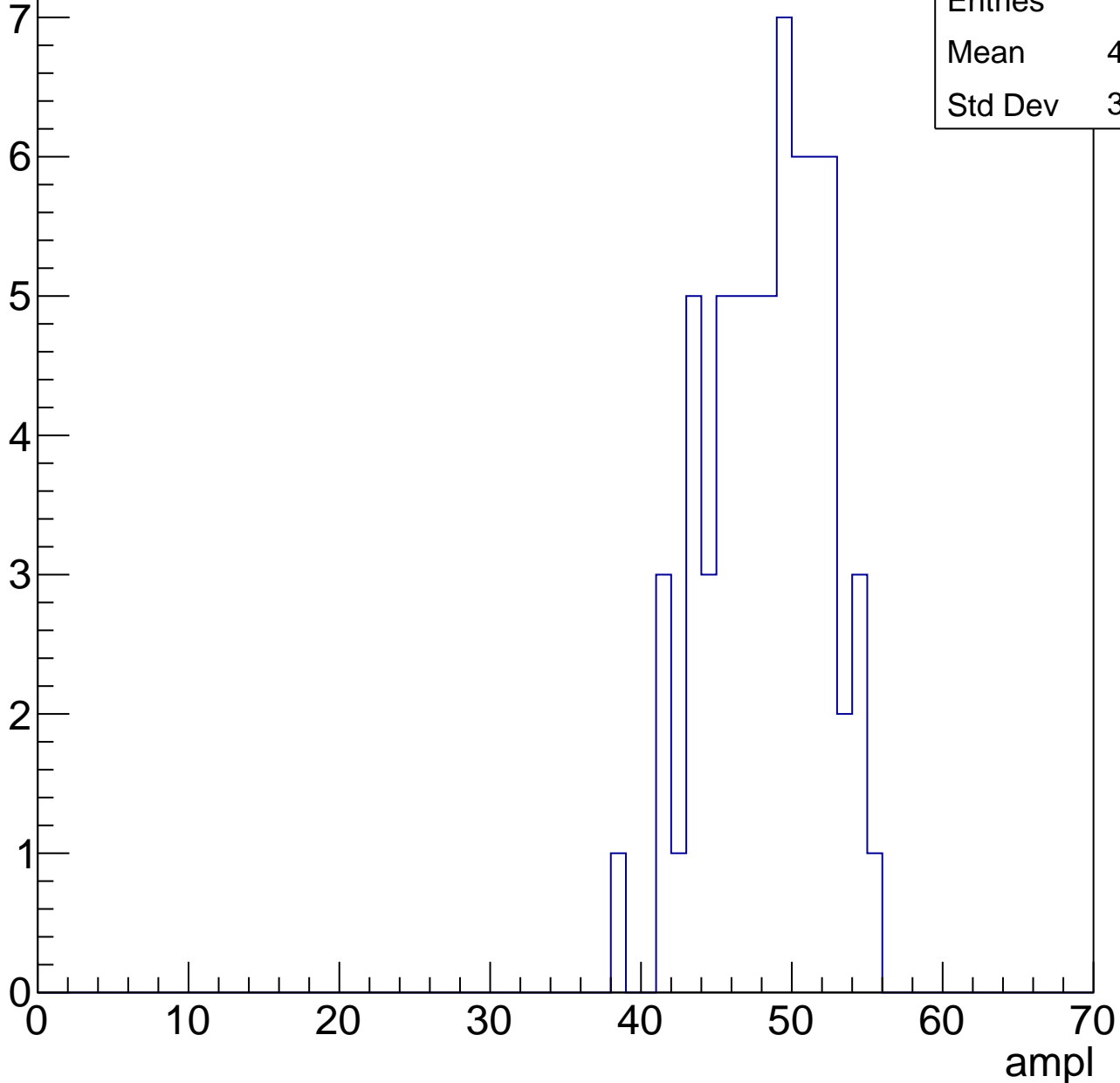


B1L101S, U15-ch112, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	64
Mean	47.88
Std Dev	3.777

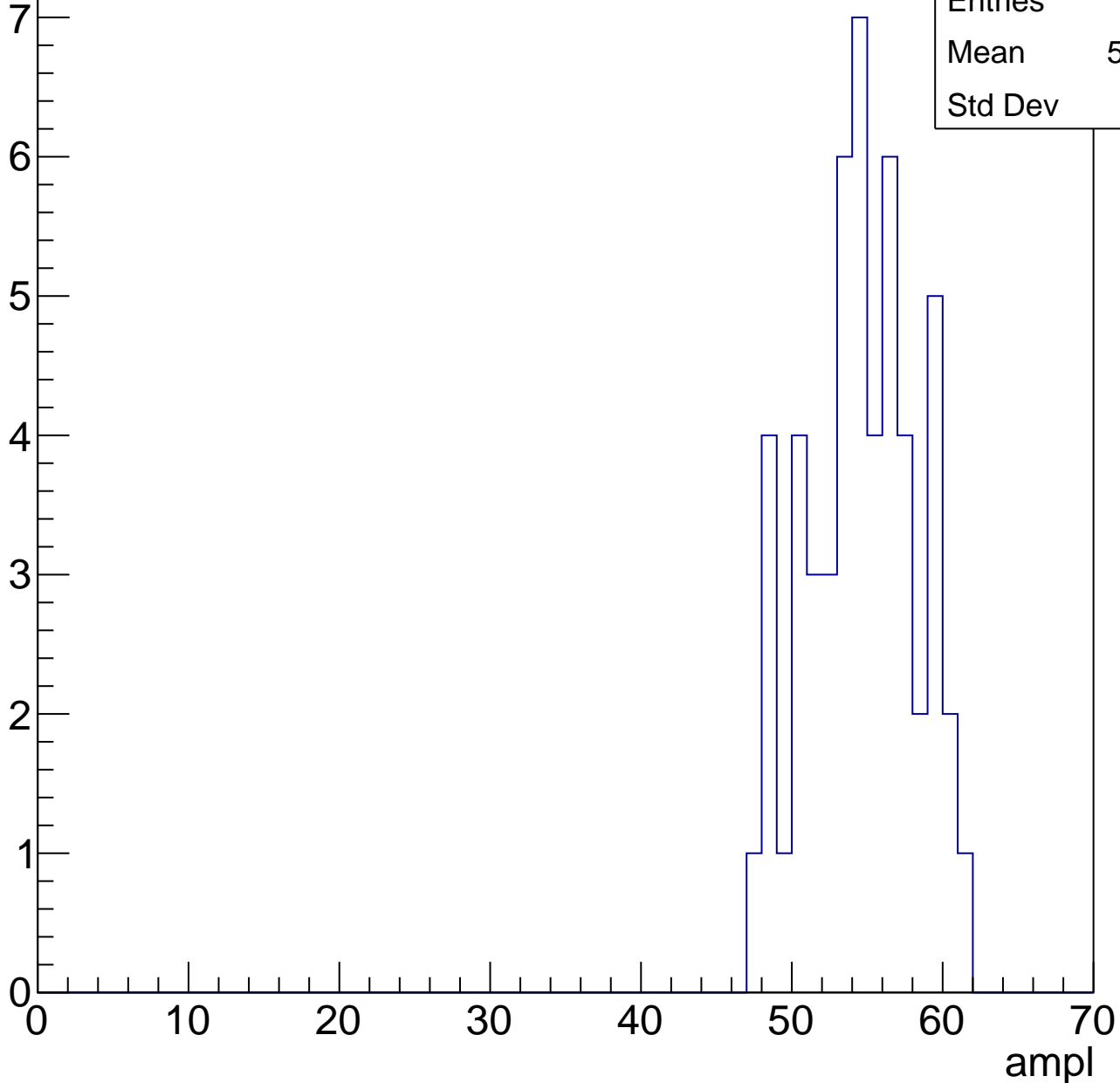


B1L101S, U15-ch112, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	53
Mean	54.13
Std Dev	3.55

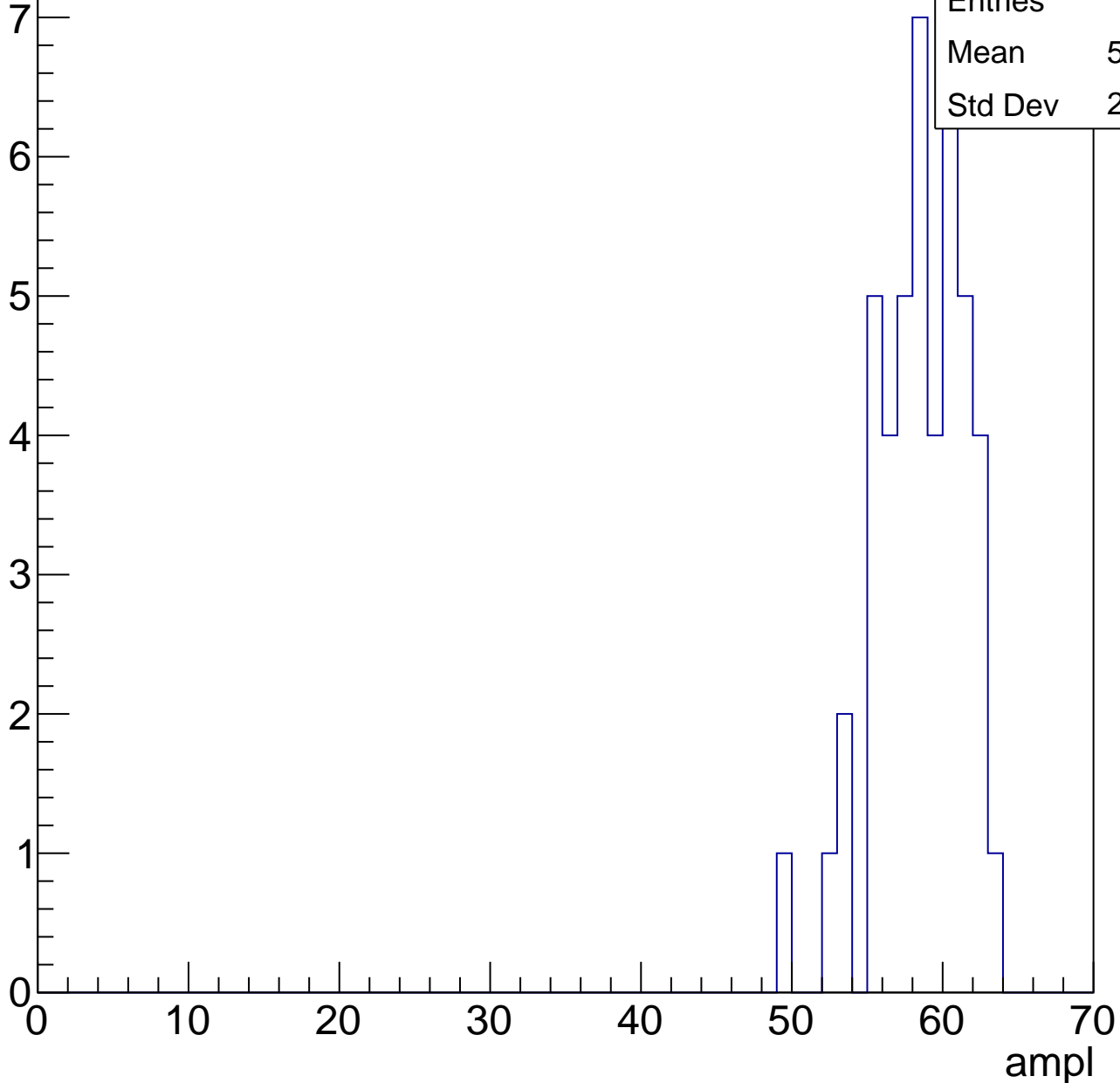


B1L101S, U15-ch112, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	58.02
Std Dev	2.945

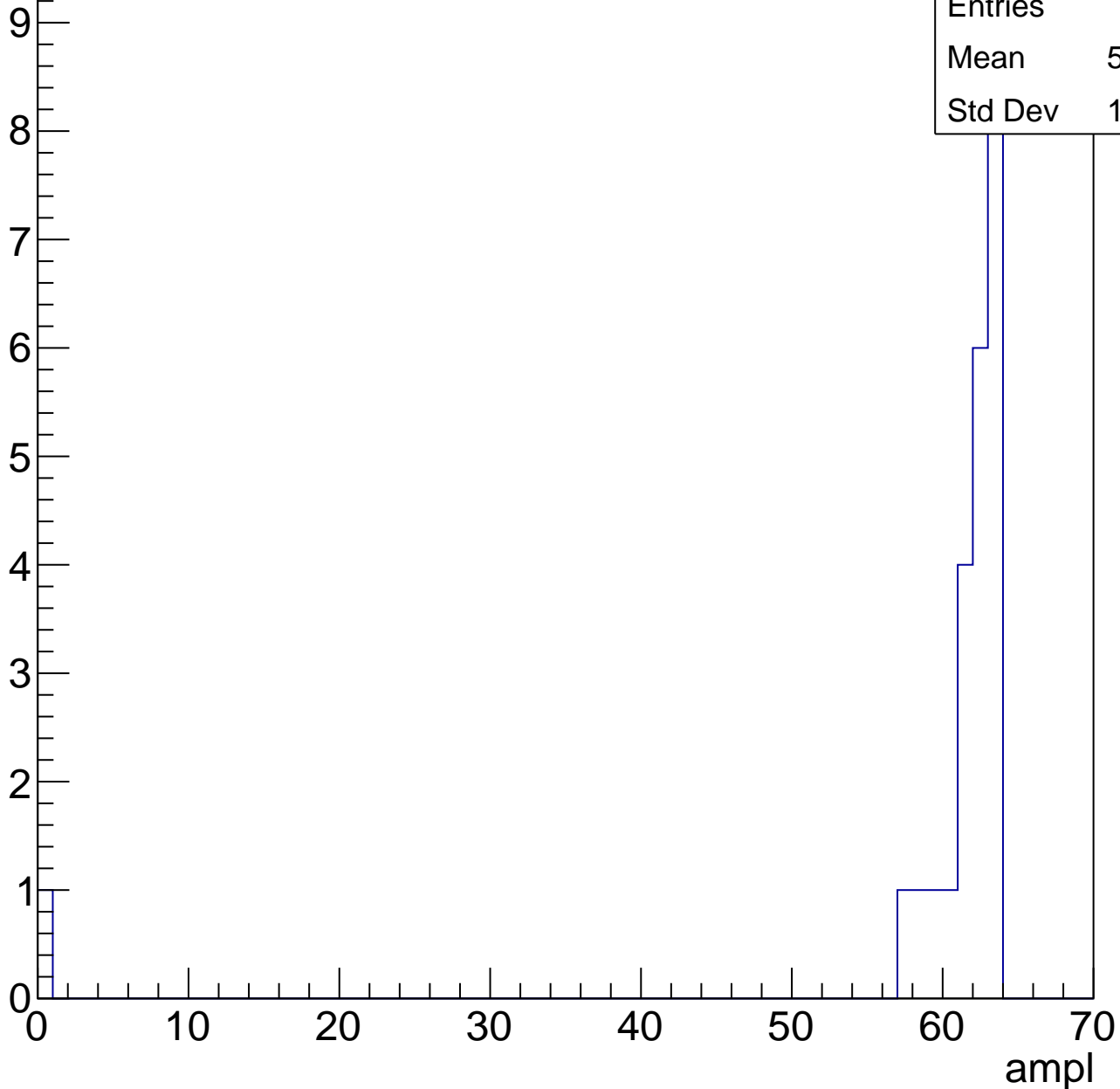


B1L101S, U15-ch112, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	24
Mean	59.04
Std Dev	12.42



B1L101S, U15-ch112, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch113, adc0

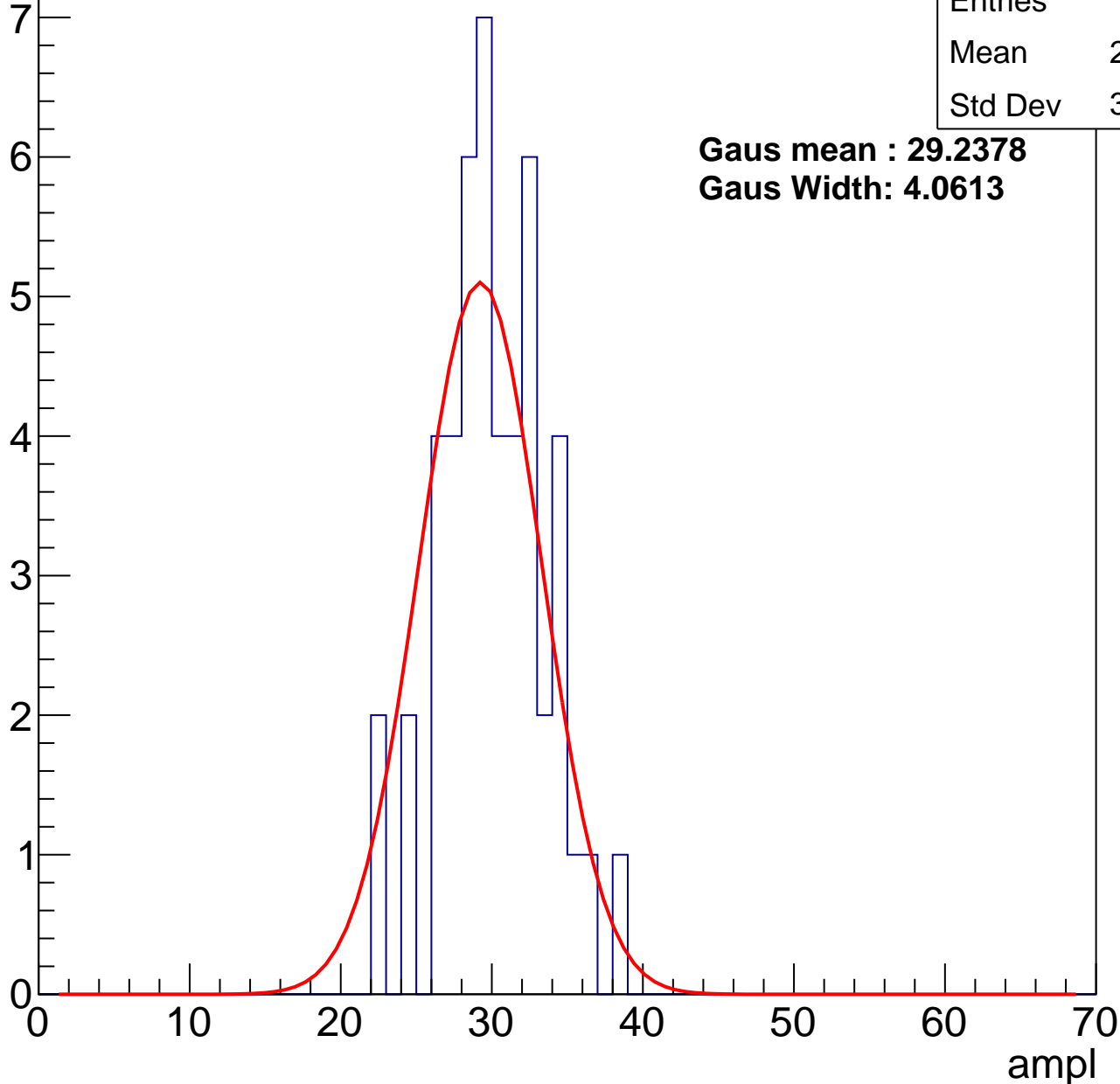
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	48
Mean	29.62
Std Dev	3.413

Gaus mean : 29.2378

Gaus Width: 4.0613



B1L101S, U15-ch113, adc1

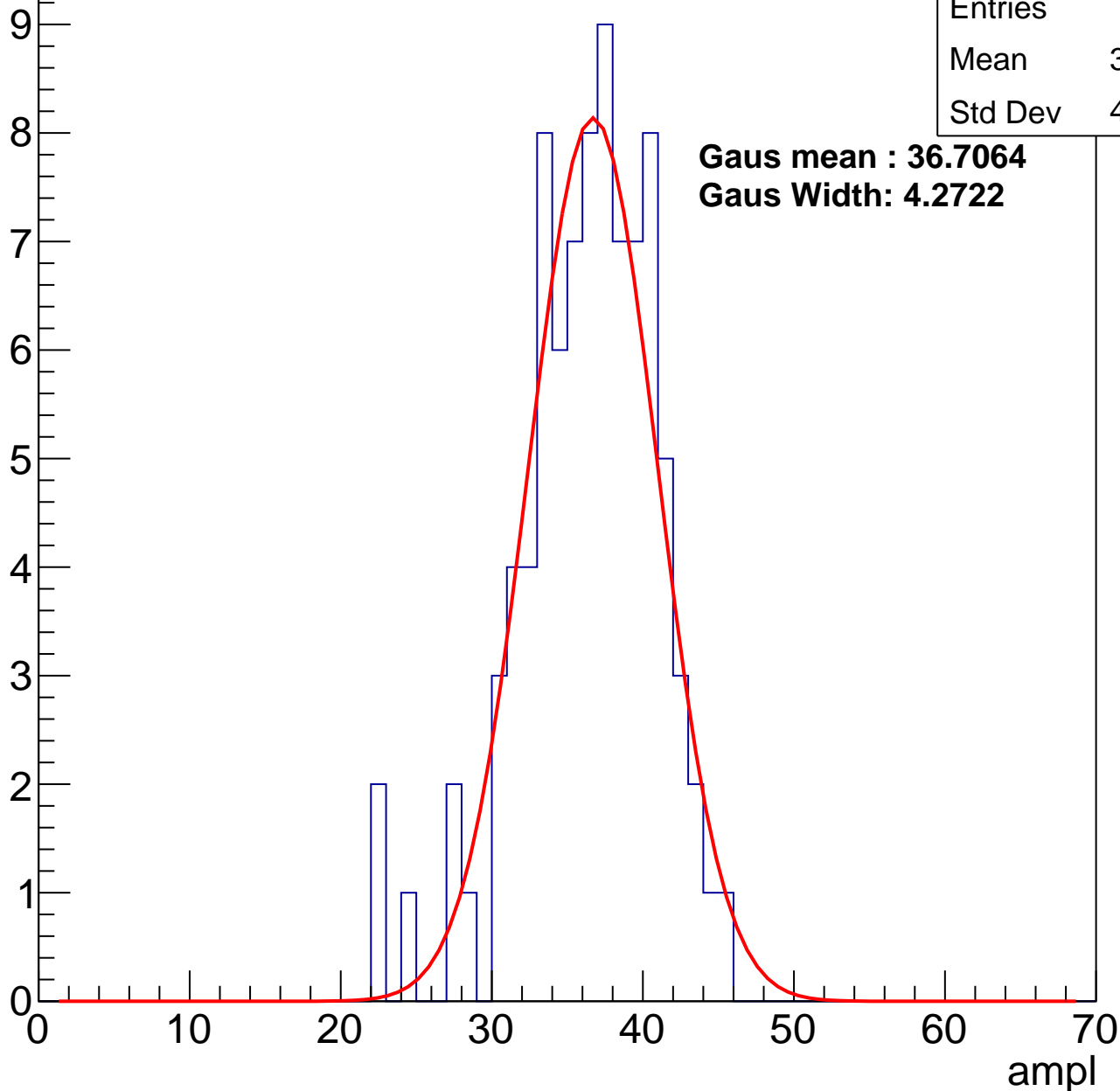
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	89
Mean	35.85
Std Dev	4.546

Gaus mean : 36.7064

Gaus Width: 4.2722



B1L101S, U15-ch113, adc2

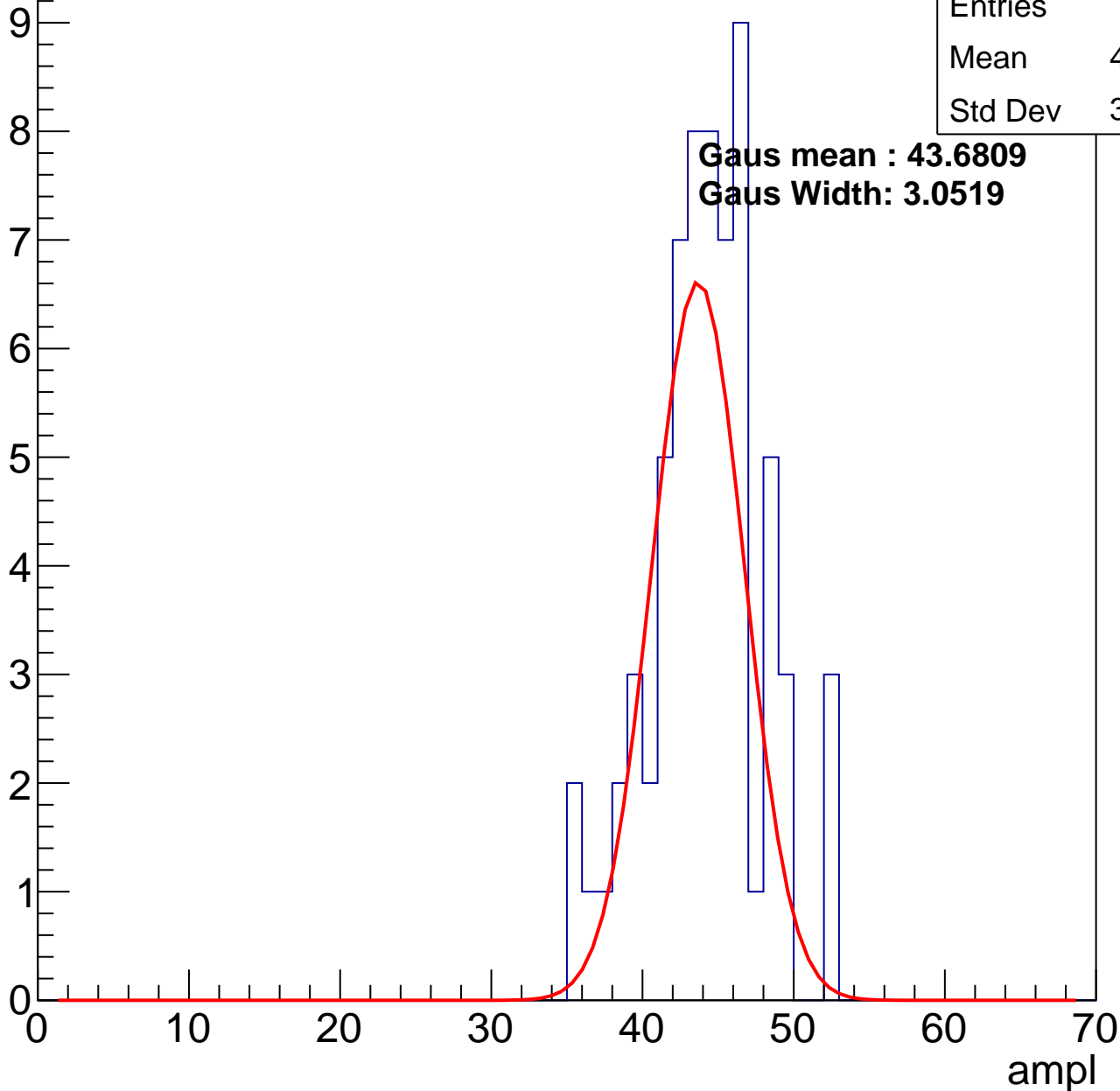
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	43.73
Std Dev	3.724

Gaus mean : 43.6809

Gaus Width: 3.0519

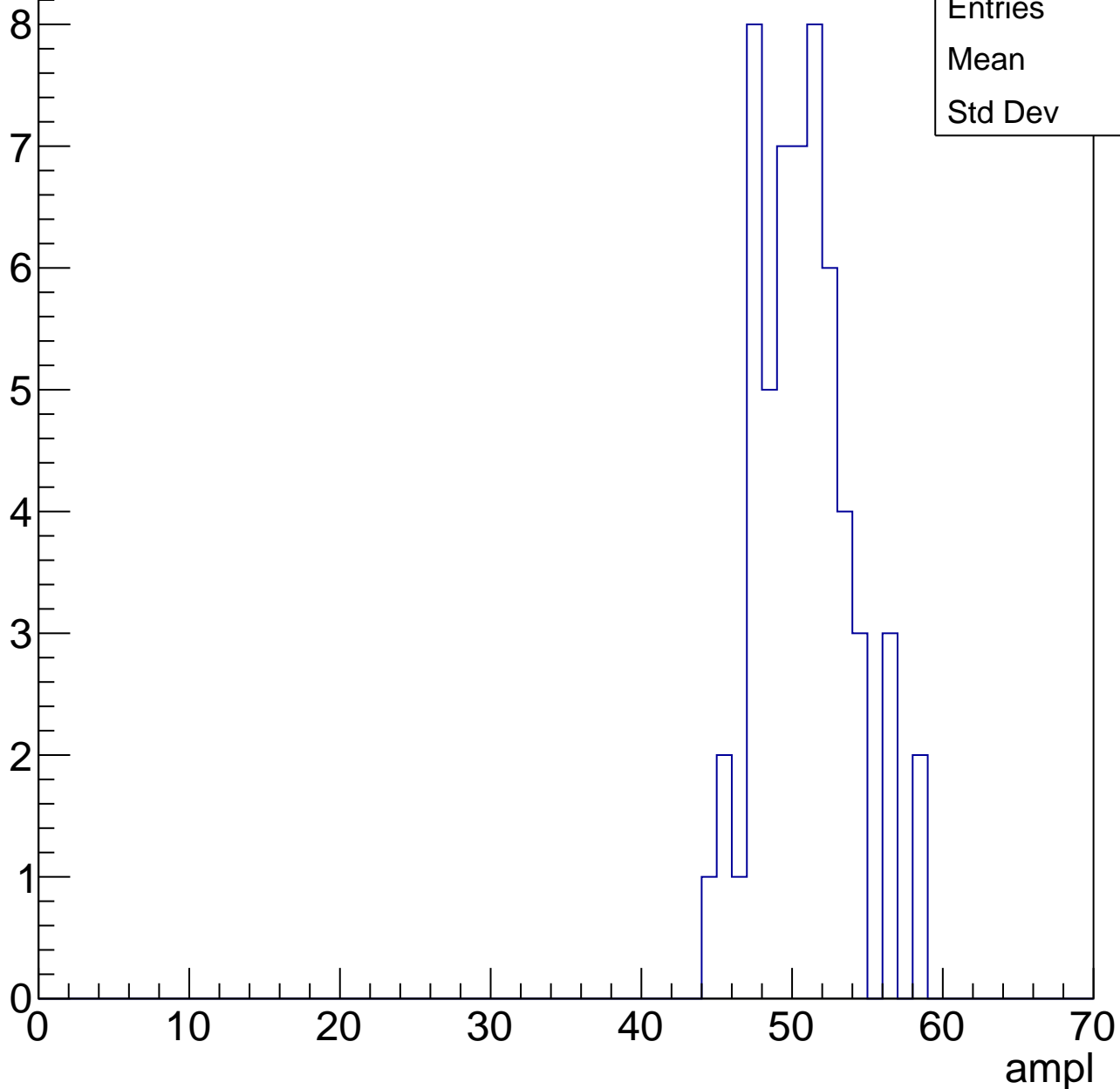


B1L101S, U15-ch113, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	50.3
Std Dev	3.1

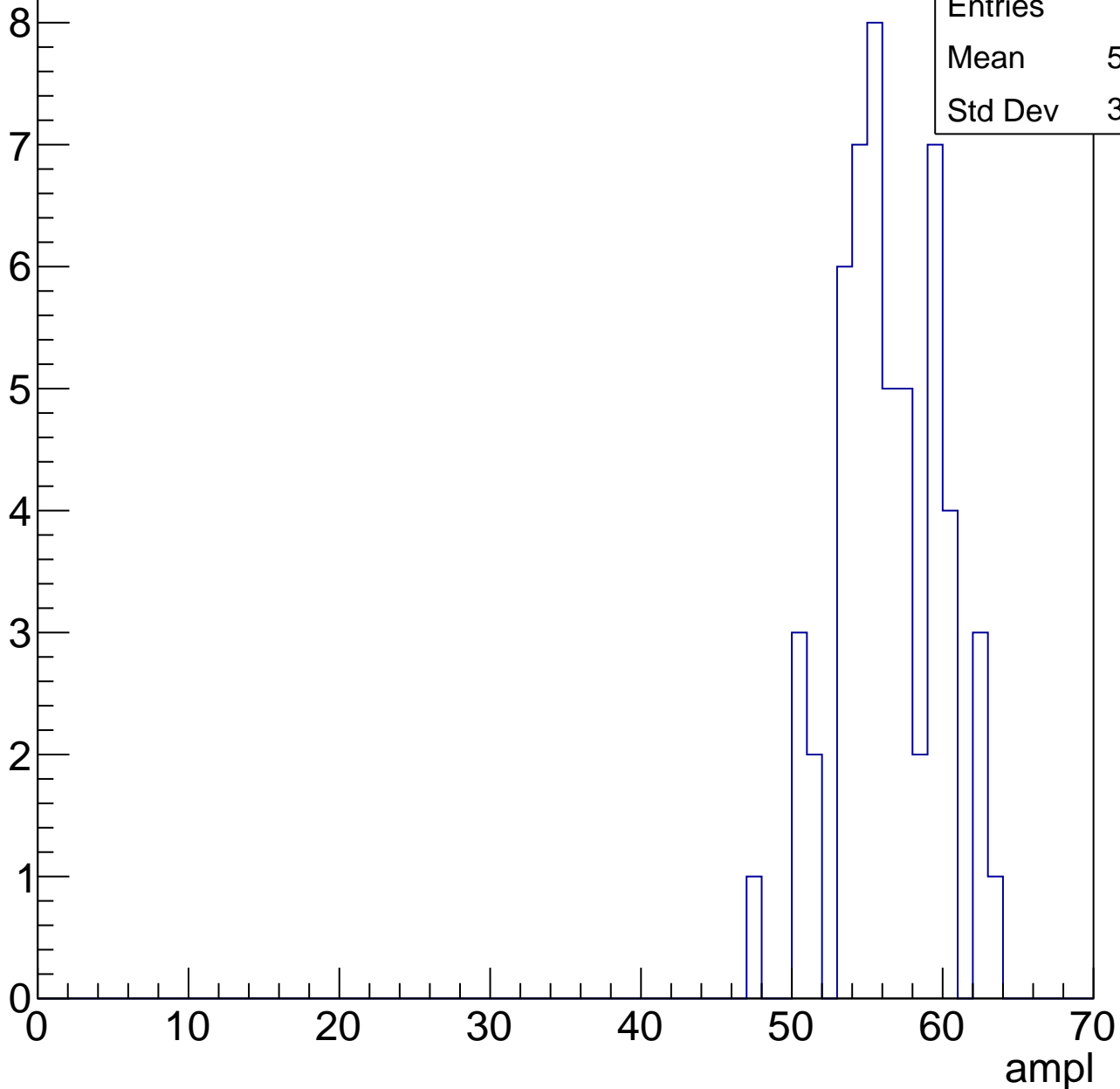


B1L101S, U15-ch113, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	55.89
Std Dev	3.403

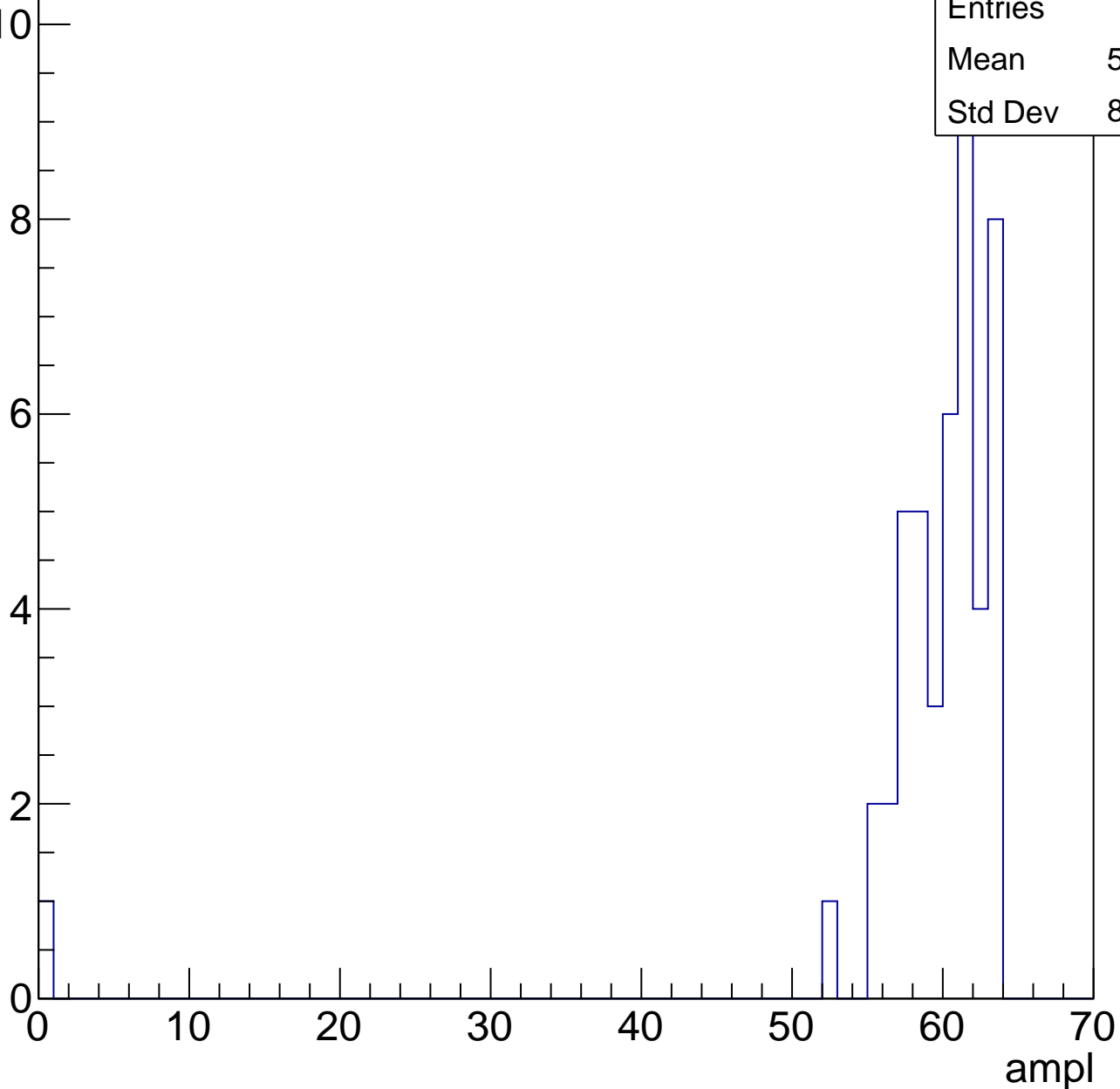


B1L101S, U15-ch113, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

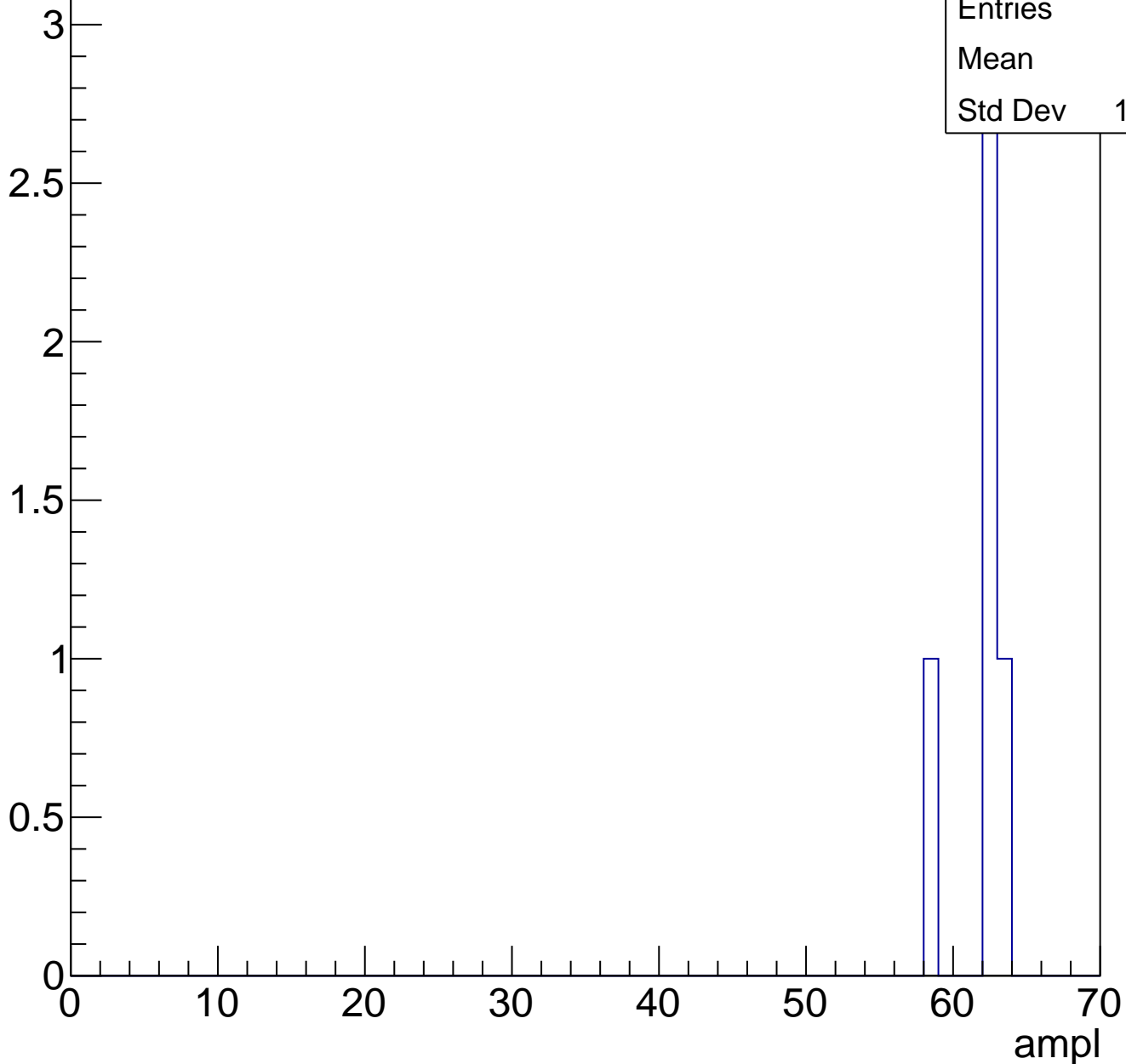
Entries	47
Mean	58.47
Std Dev	8.994



B1L101S, U15-ch113, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch113, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch114, adc0

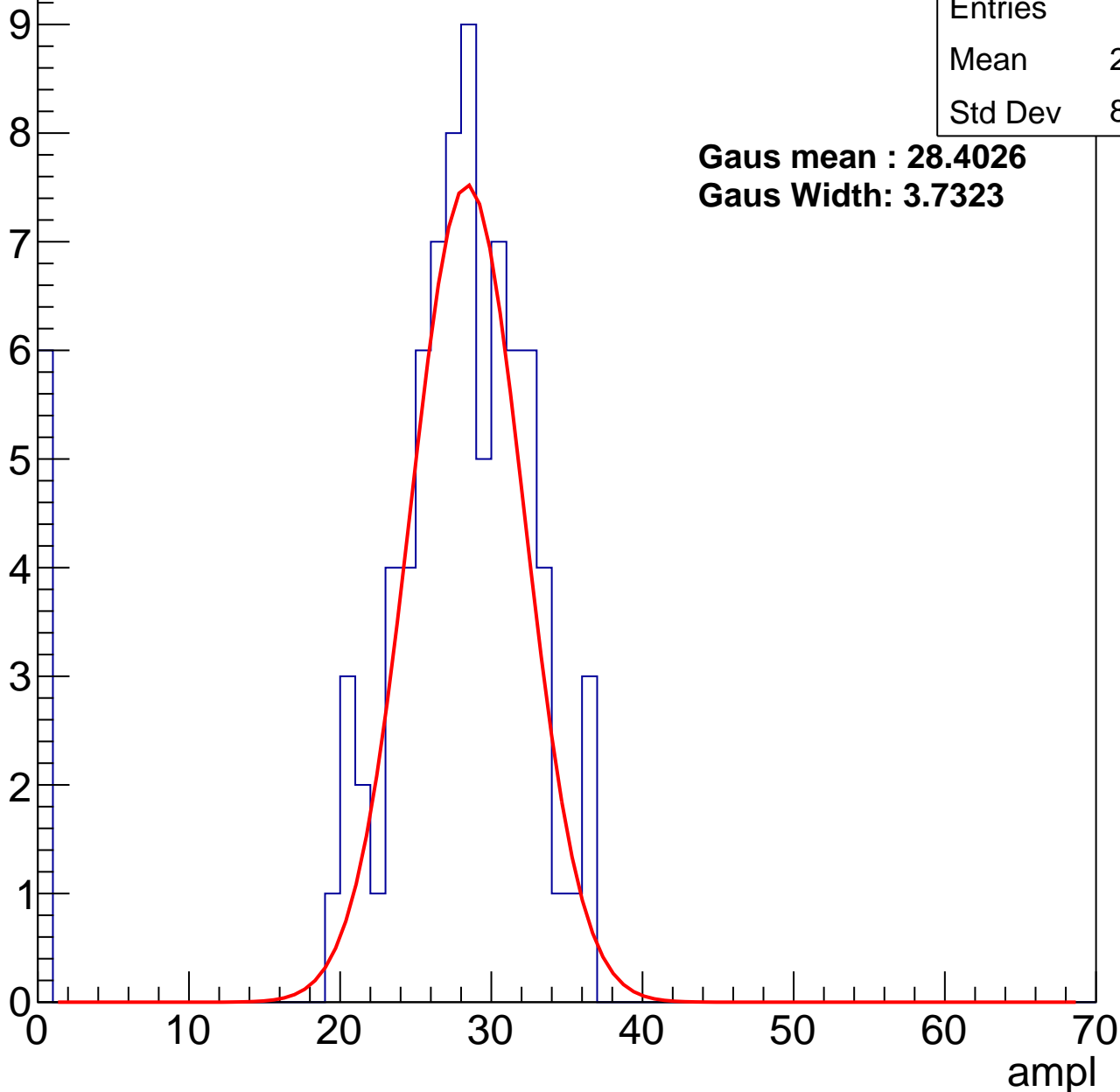
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	25.87
Std Dev	8.124

Gaus mean : 28.4026

Gaus Width: 3.7323



B1L101S, U15-ch114, adc1

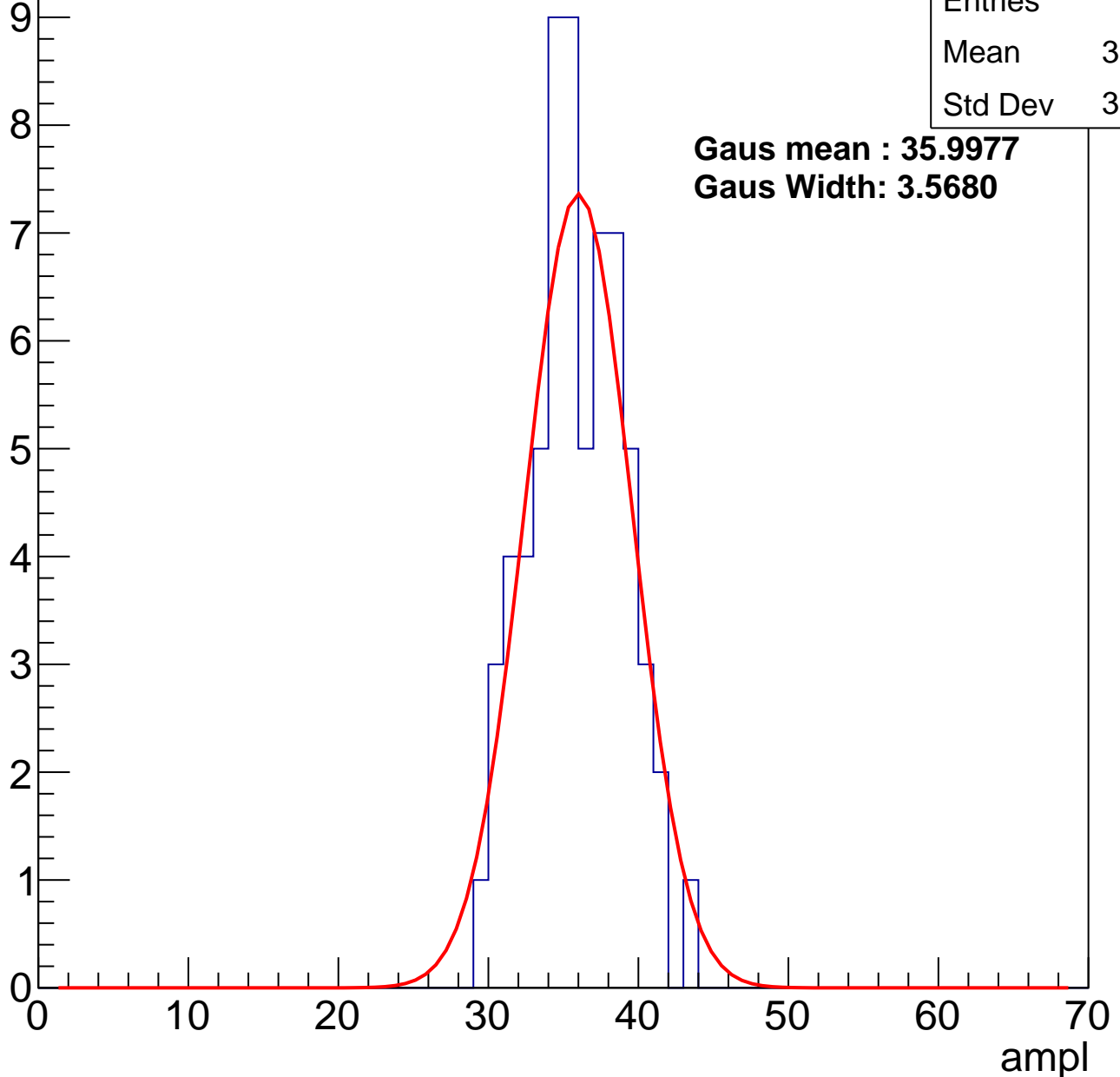
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	65
Mean	35.42
Std Dev	3.078

Gaus mean : 35.9977

Gaus Width: 3.5680



B1L101S, U15-ch114, adc2

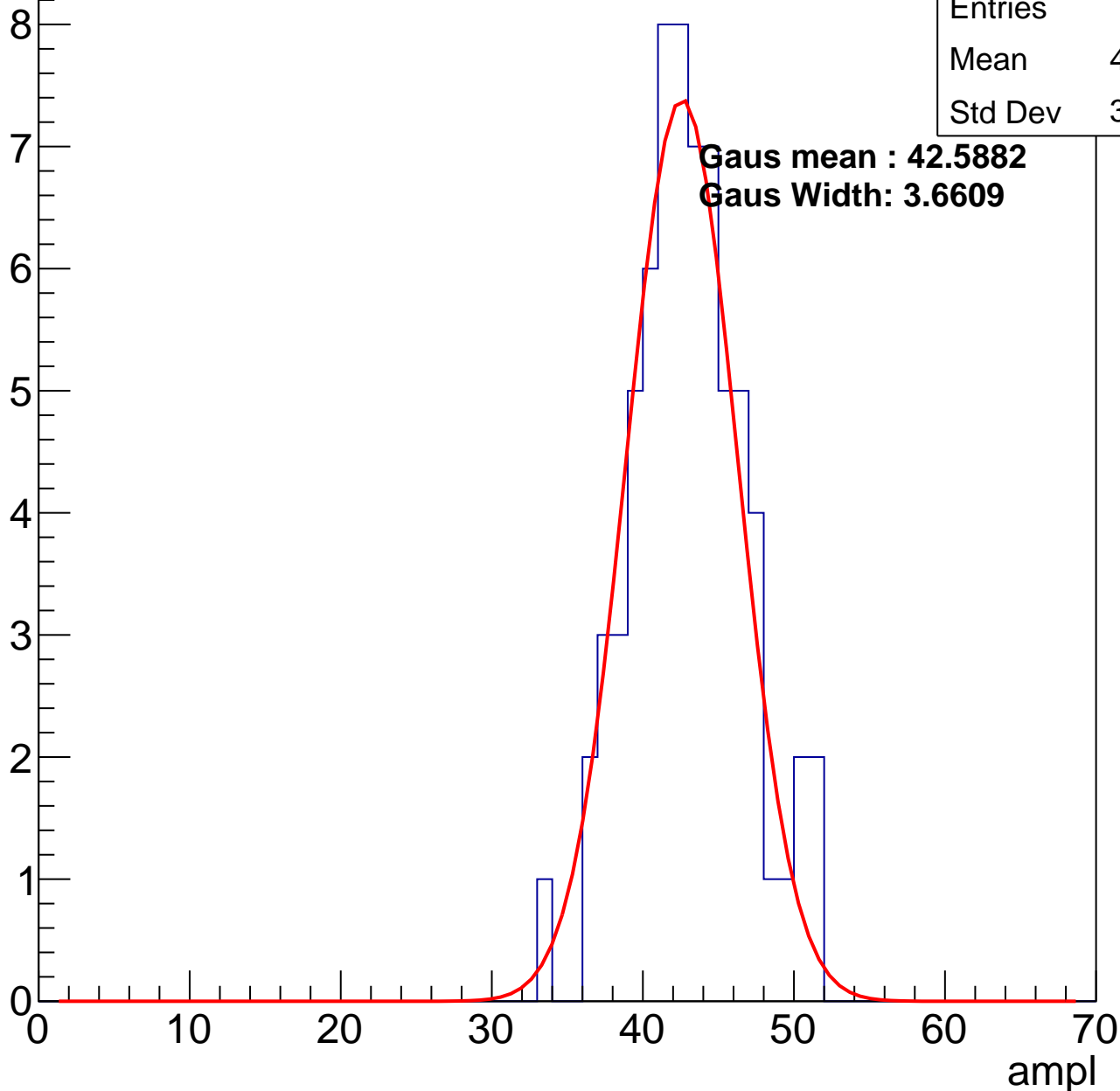
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	42.57
Std Dev	3.713

Gaus mean : 42.5882

Gaus Width: 3.6609

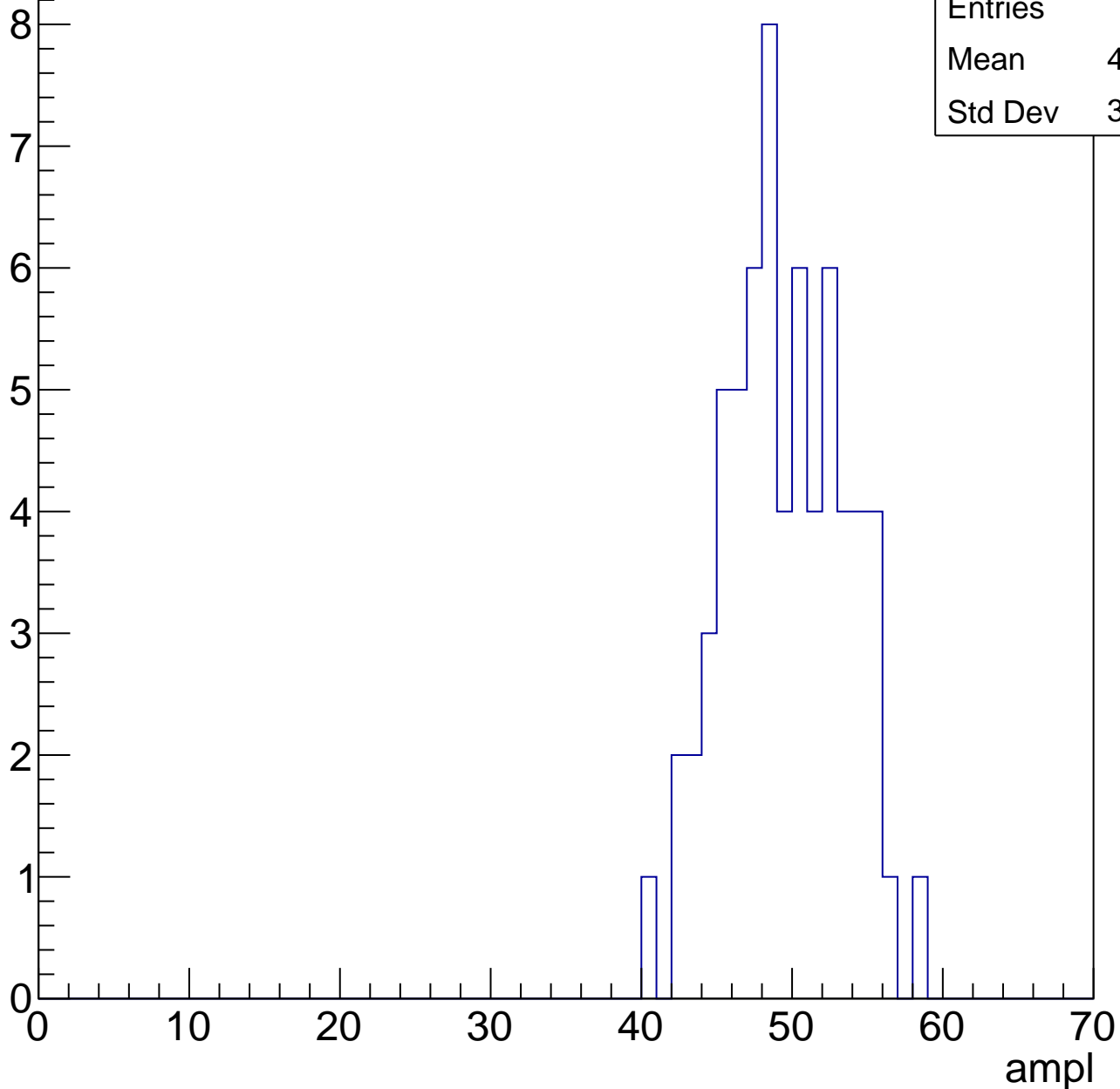


B1L101S, U15-ch114, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	49.05
Std Dev	3.886

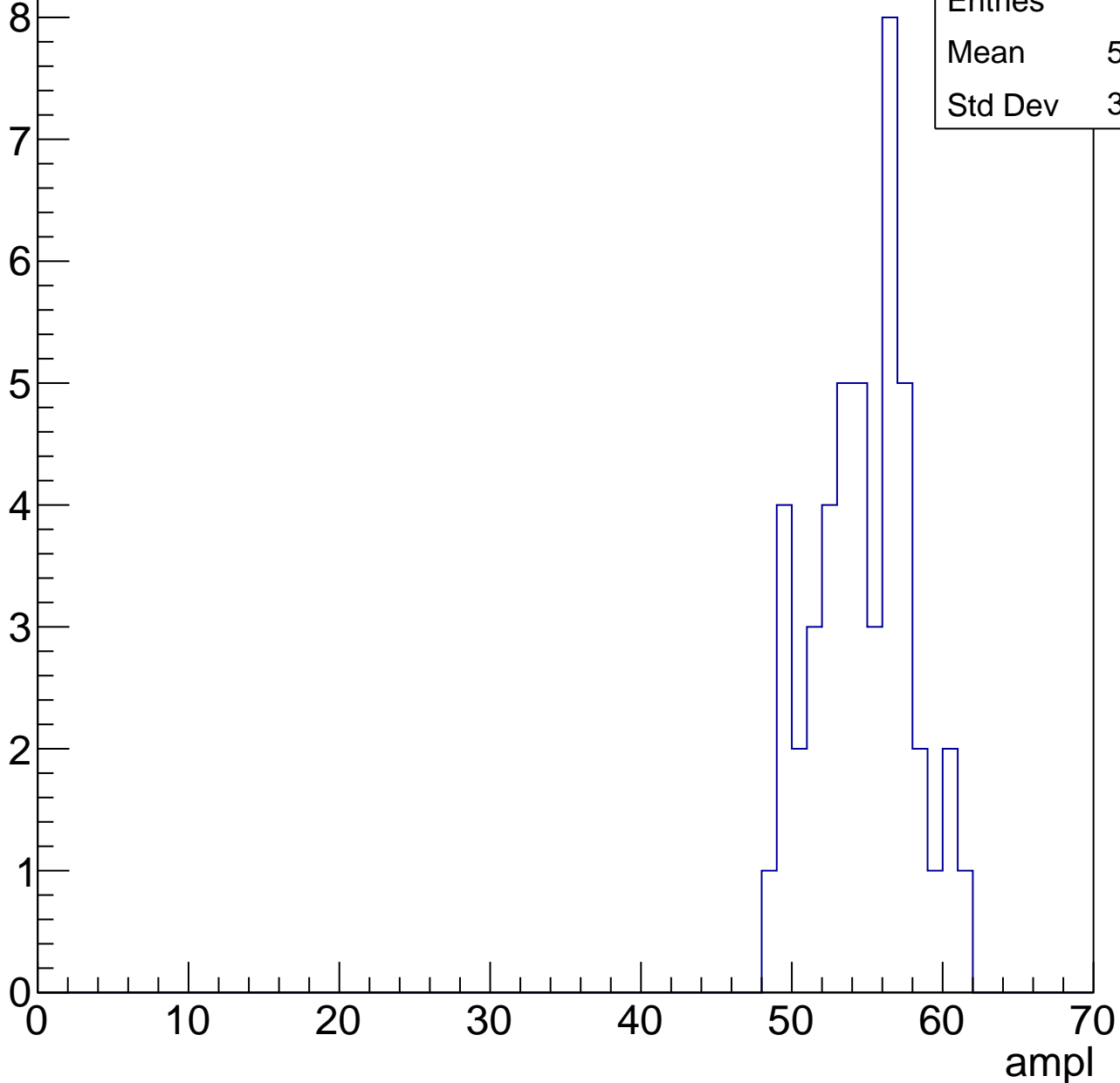


B1L101S, U15-ch114, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	46
Mean	54.22
Std Dev	3.203

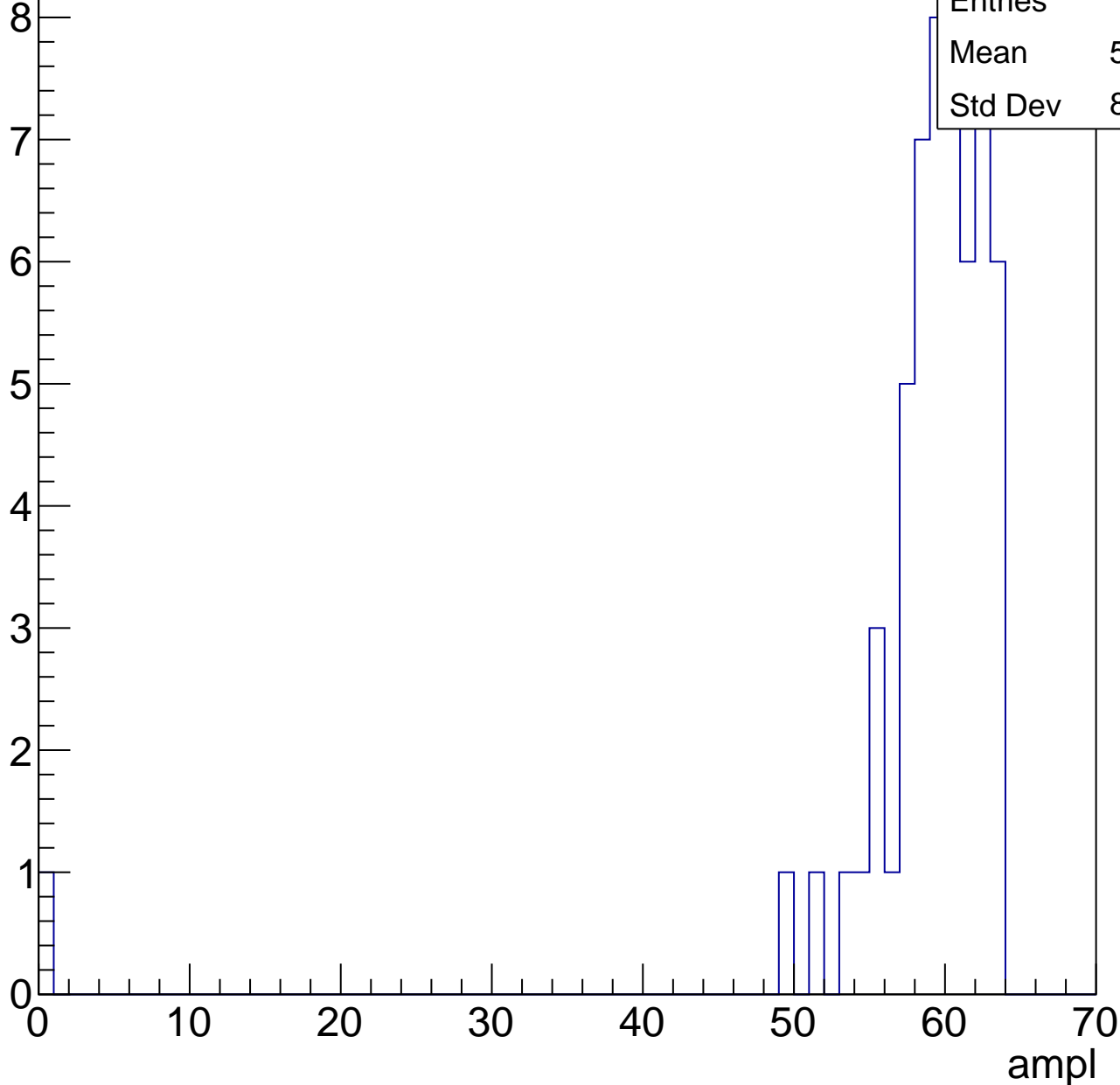


B1L101S, U15-ch114, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

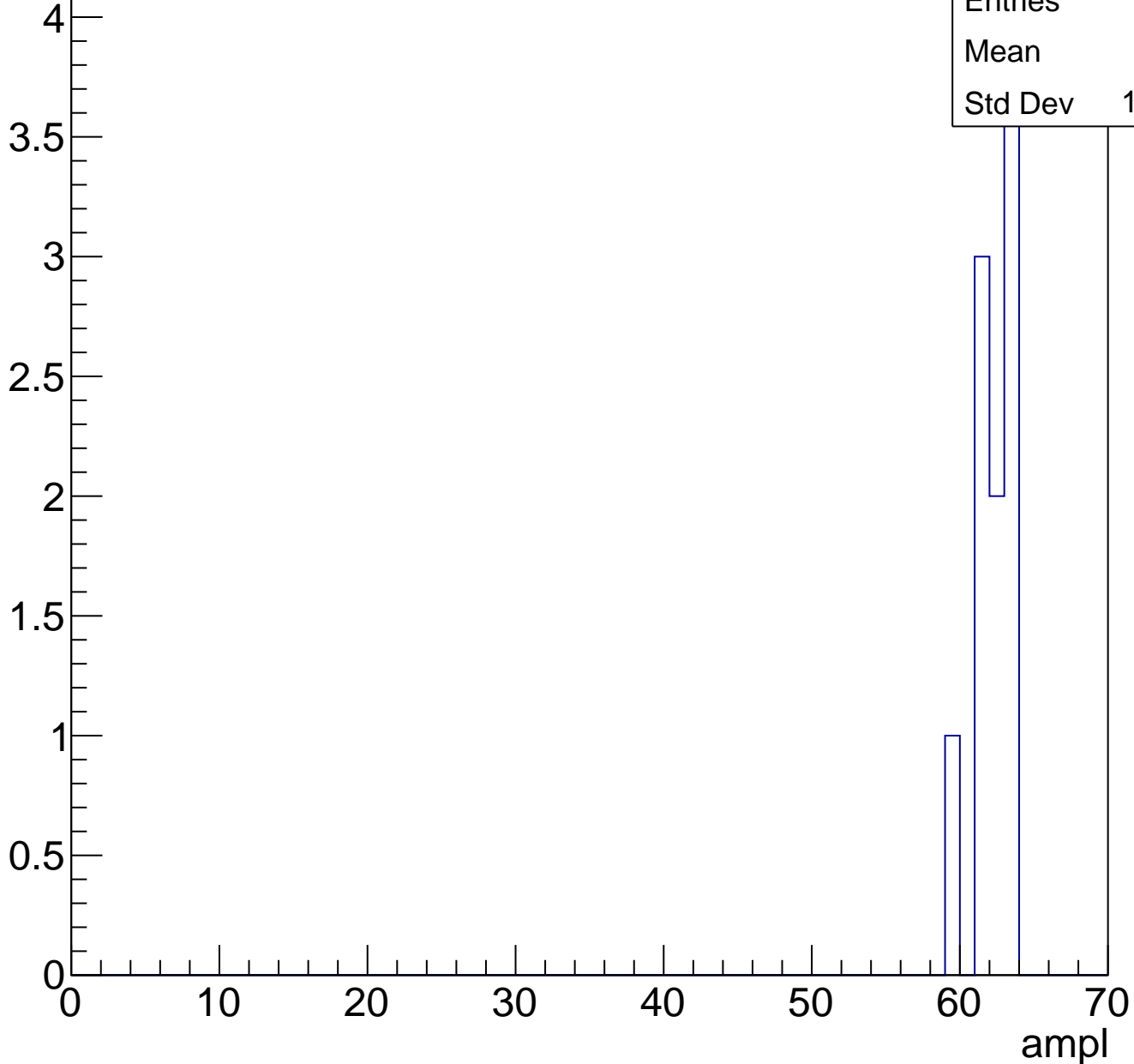
Entries	57
Mean	58.09
Std Dev	8.317



B1L101S, U15-ch114, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch114, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch115, adc0

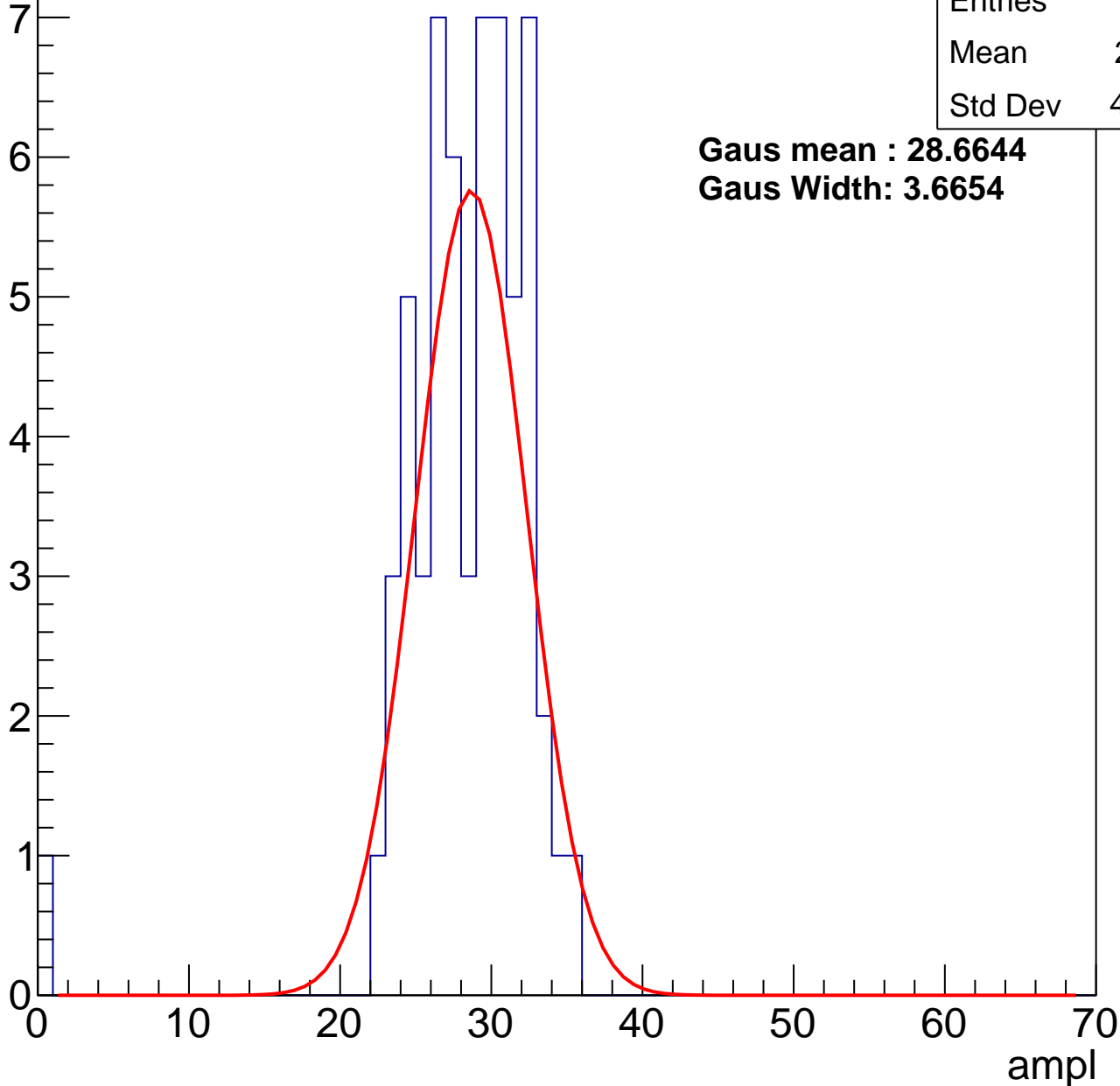
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	27.81
Std Dev	4.803

Gaus mean : 28.6644

Gaus Width: 3.6654



B1L101S, U15-ch115, adc1

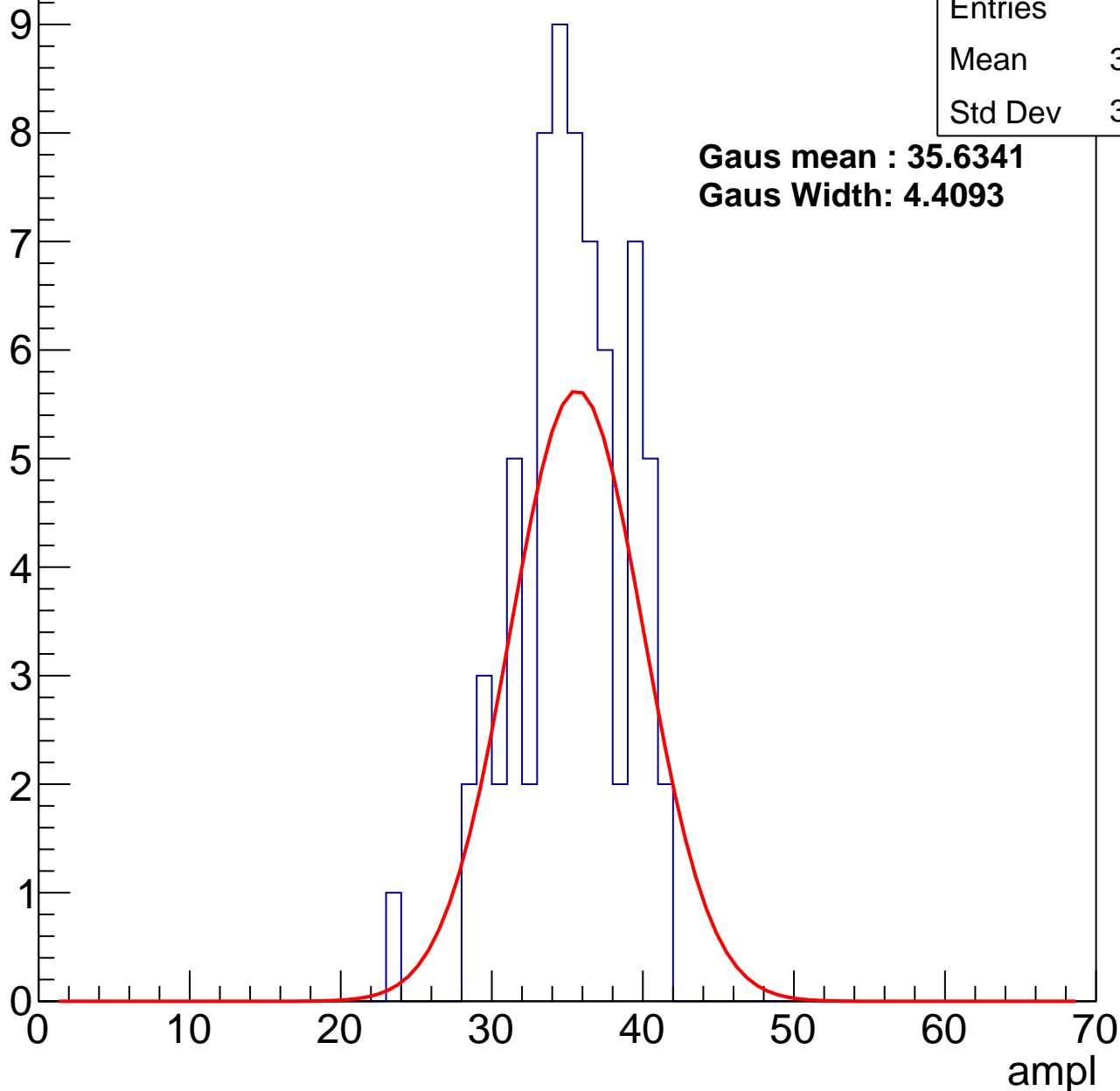
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	34.78
Std Dev	3.599

Gaus mean : 35.6341

Gaus Width: 4.4093



B1L101S, U15-ch115, adc2

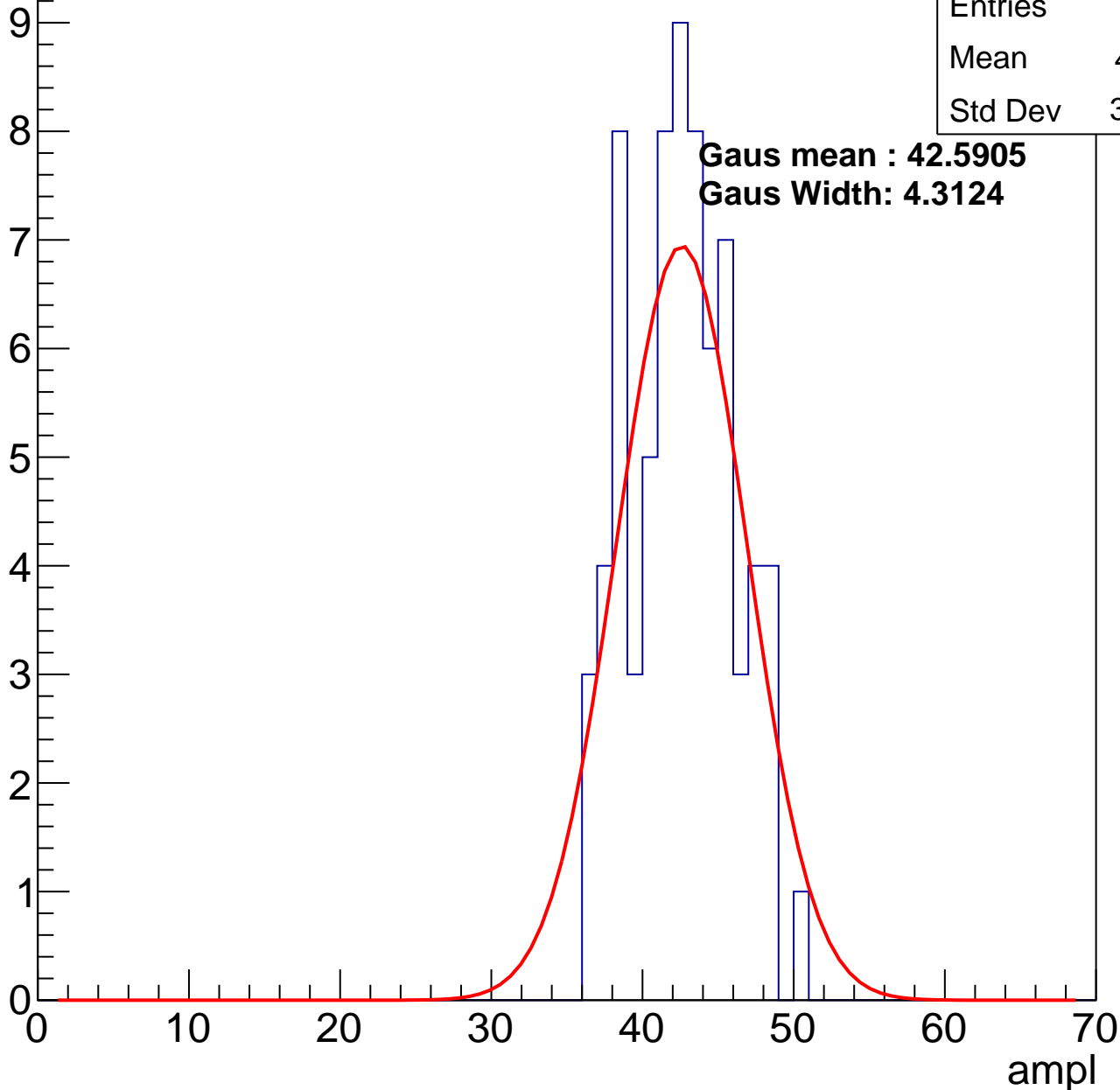
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	42.11
Std Dev	3.394

Gaus mean : 42.5905

Gaus Width: 4.3124

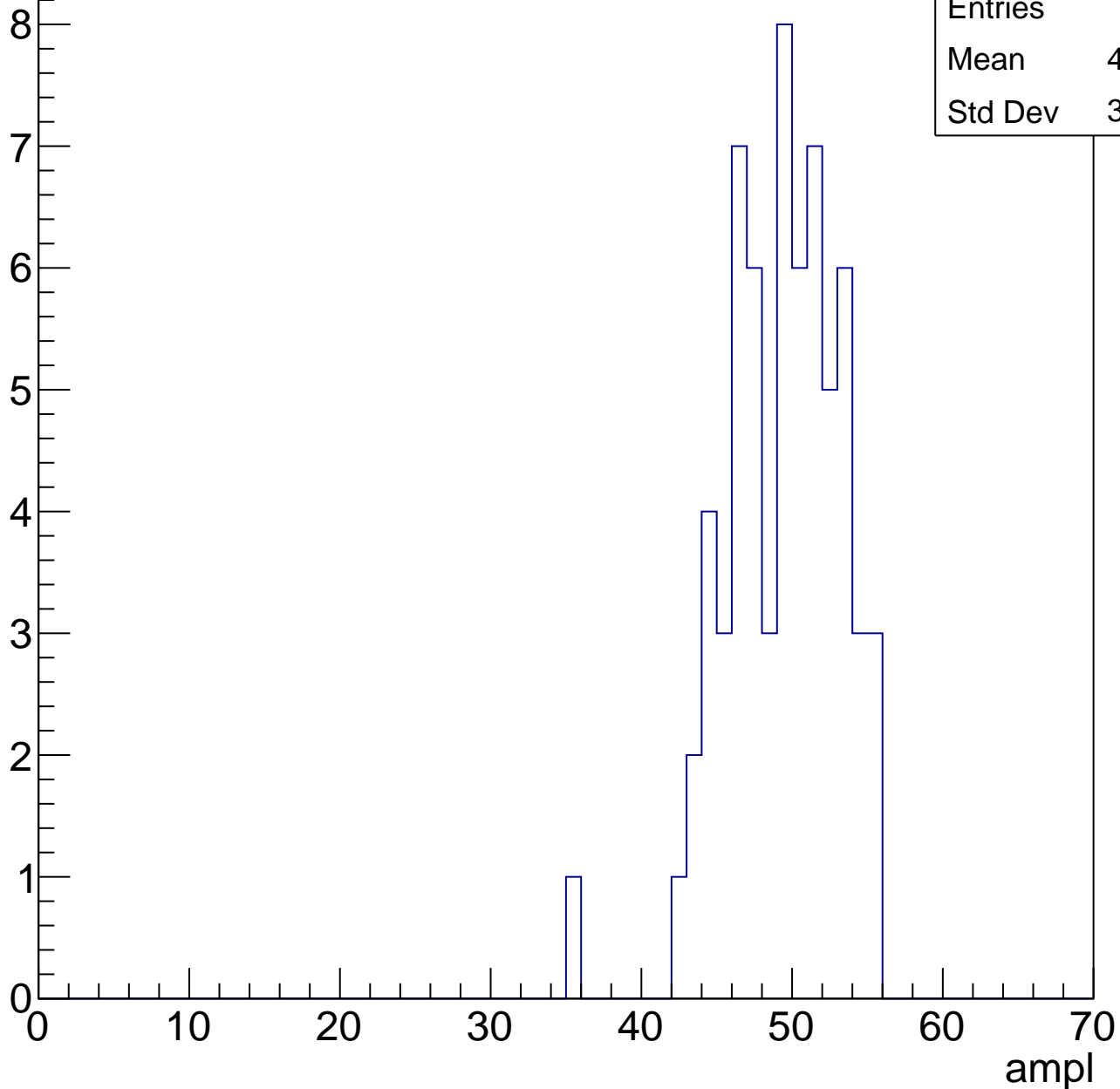


B1L101S, U15-ch115, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

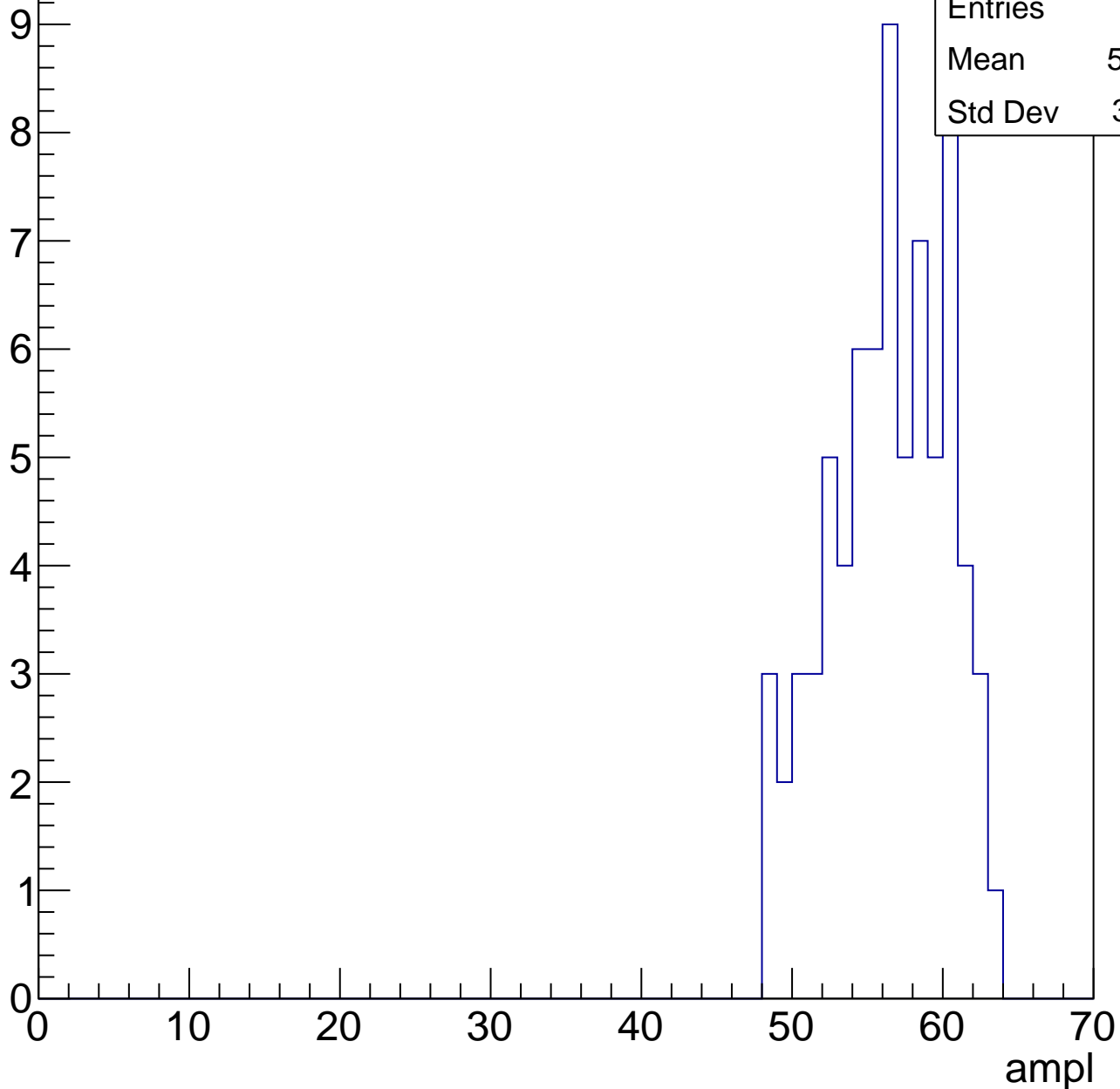
Entries	65
Mean	48.86
Std Dev	3.745



B1L101S, U15-ch115, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

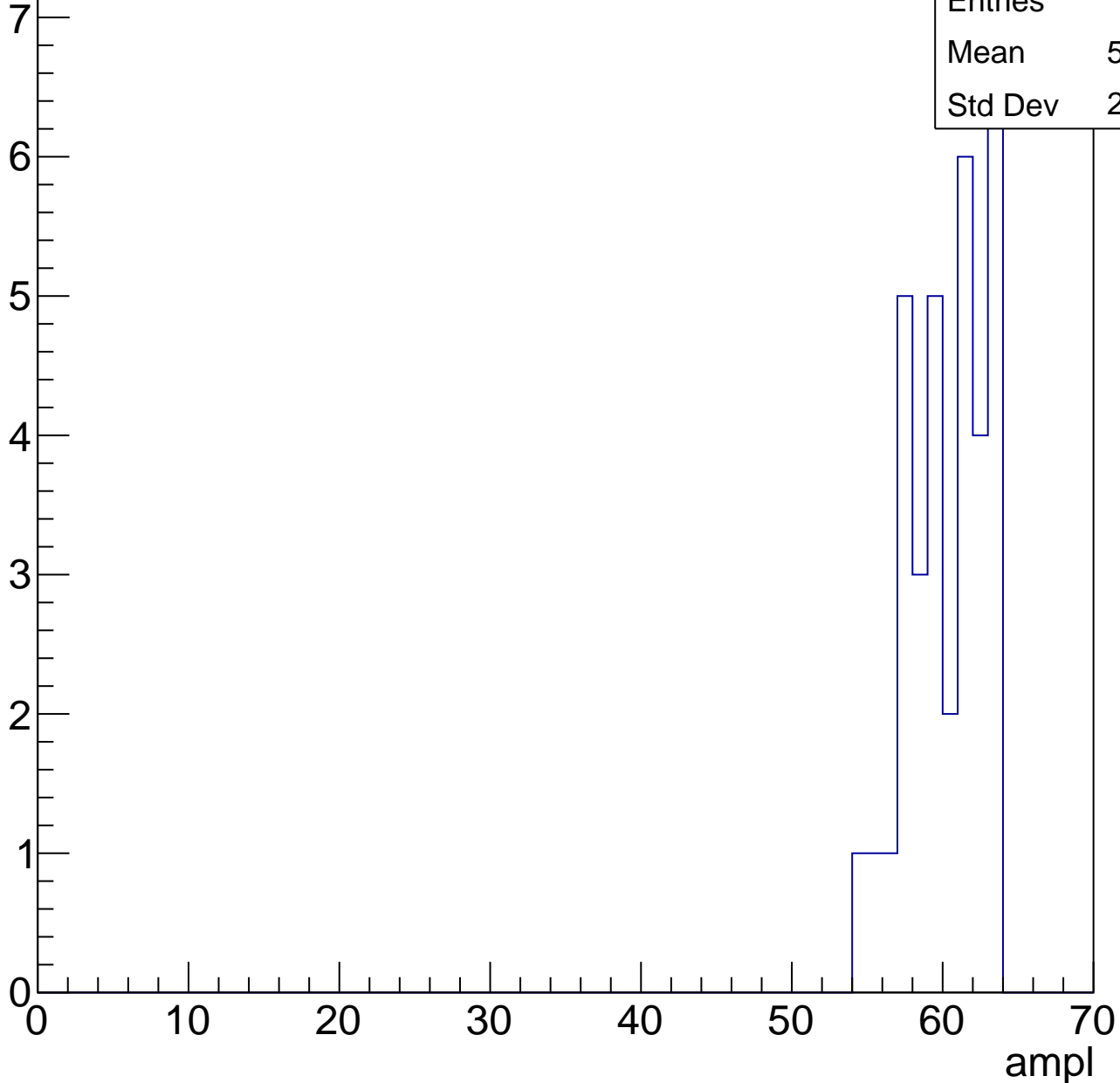


B1L101S, U15-ch115, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

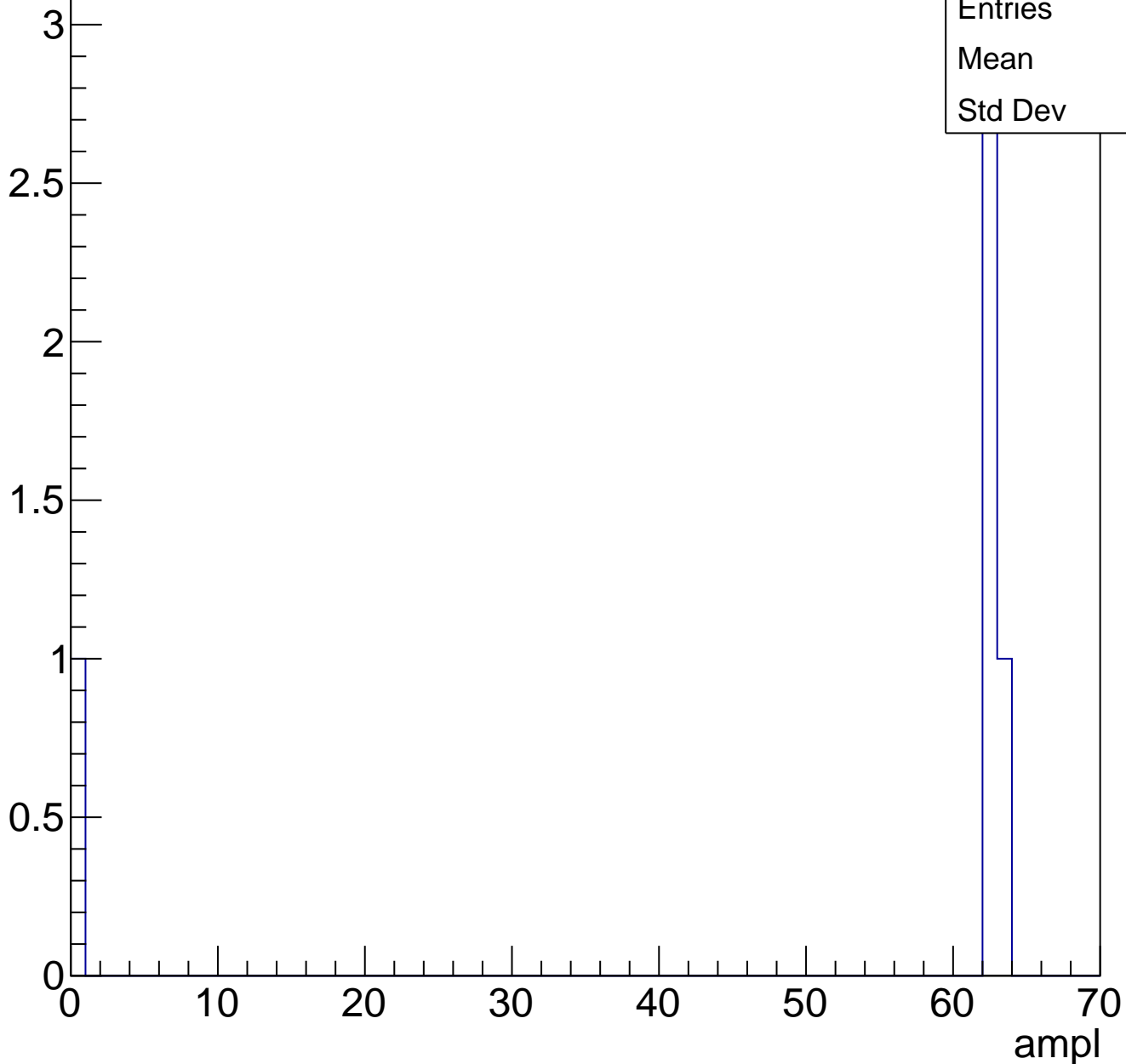
Entries	35
Mean	59.83
Std Dev	2.524



B1L101S, U15-ch115, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch115, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch116, adc0

calib_packv5_042523_0143.root, FC#0, port D2

Entries	66
Mean	29.62
Std Dev	5.131

Gaus mean : 30.8707

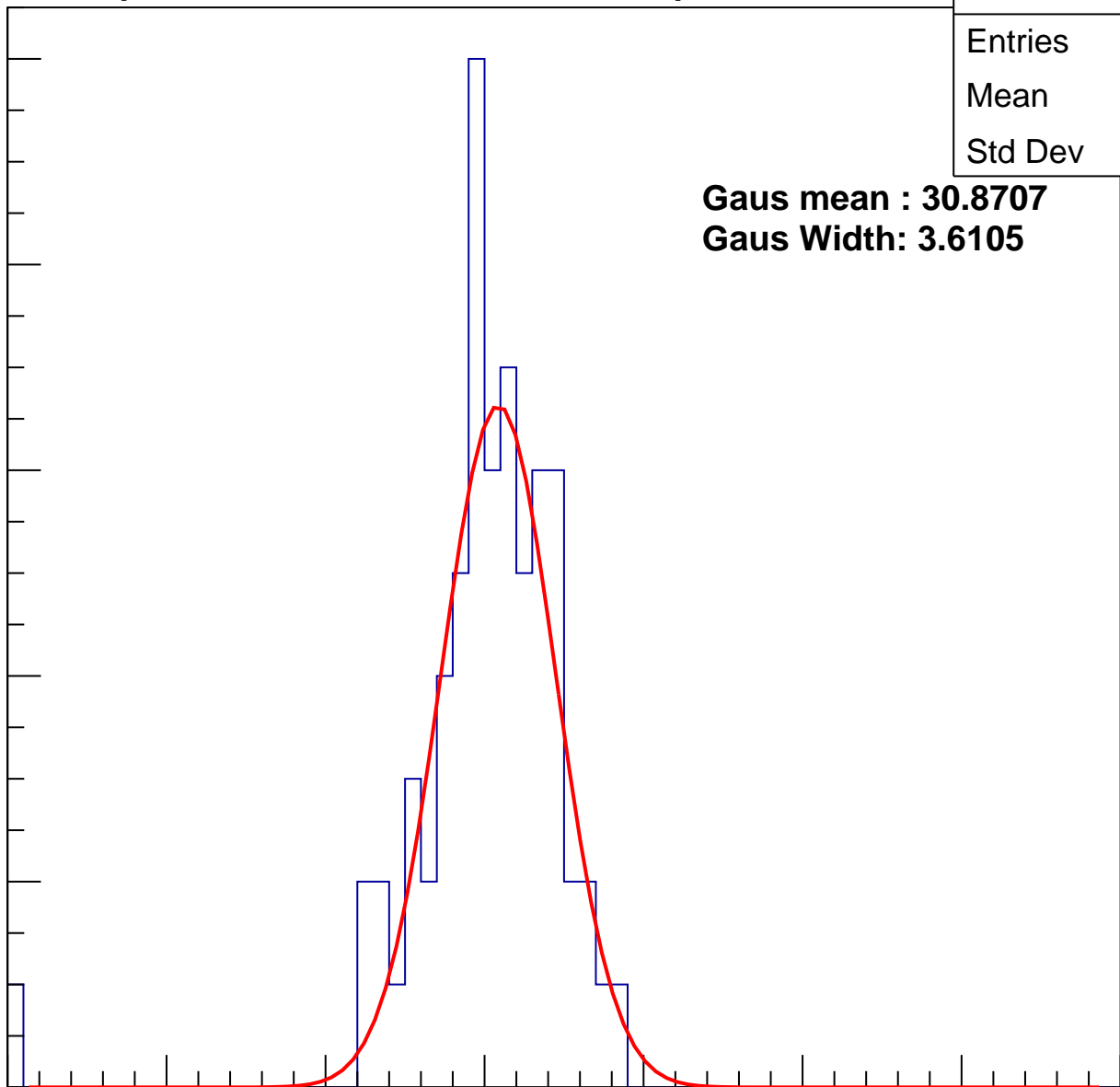
Gaus Width: 3.6105

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch116, adc1

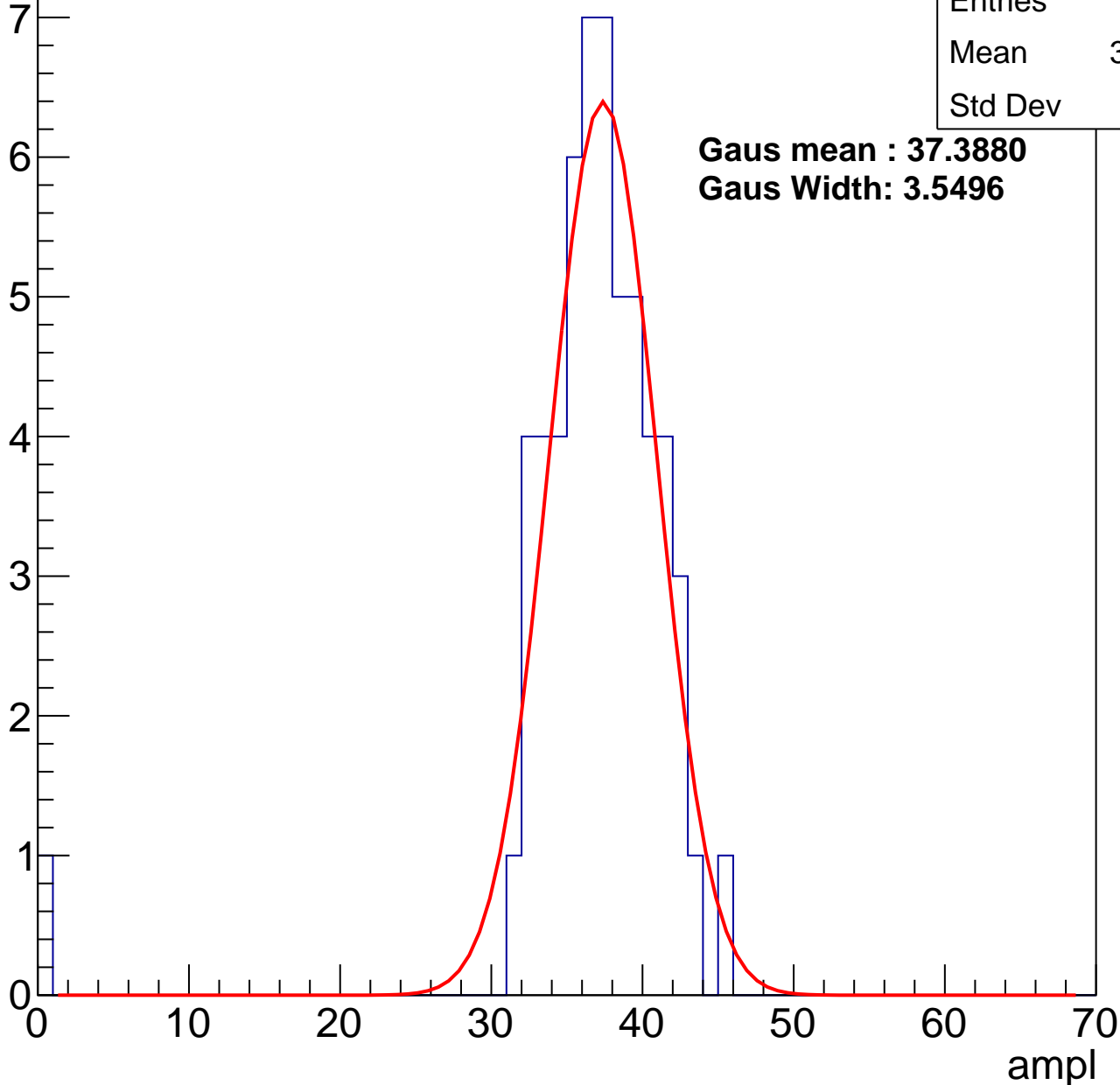
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	36.33
Std Dev	5.79

Gaus mean : 37.3880

Gaus Width: 3.5496



B1L101S, U15-ch116, adc2

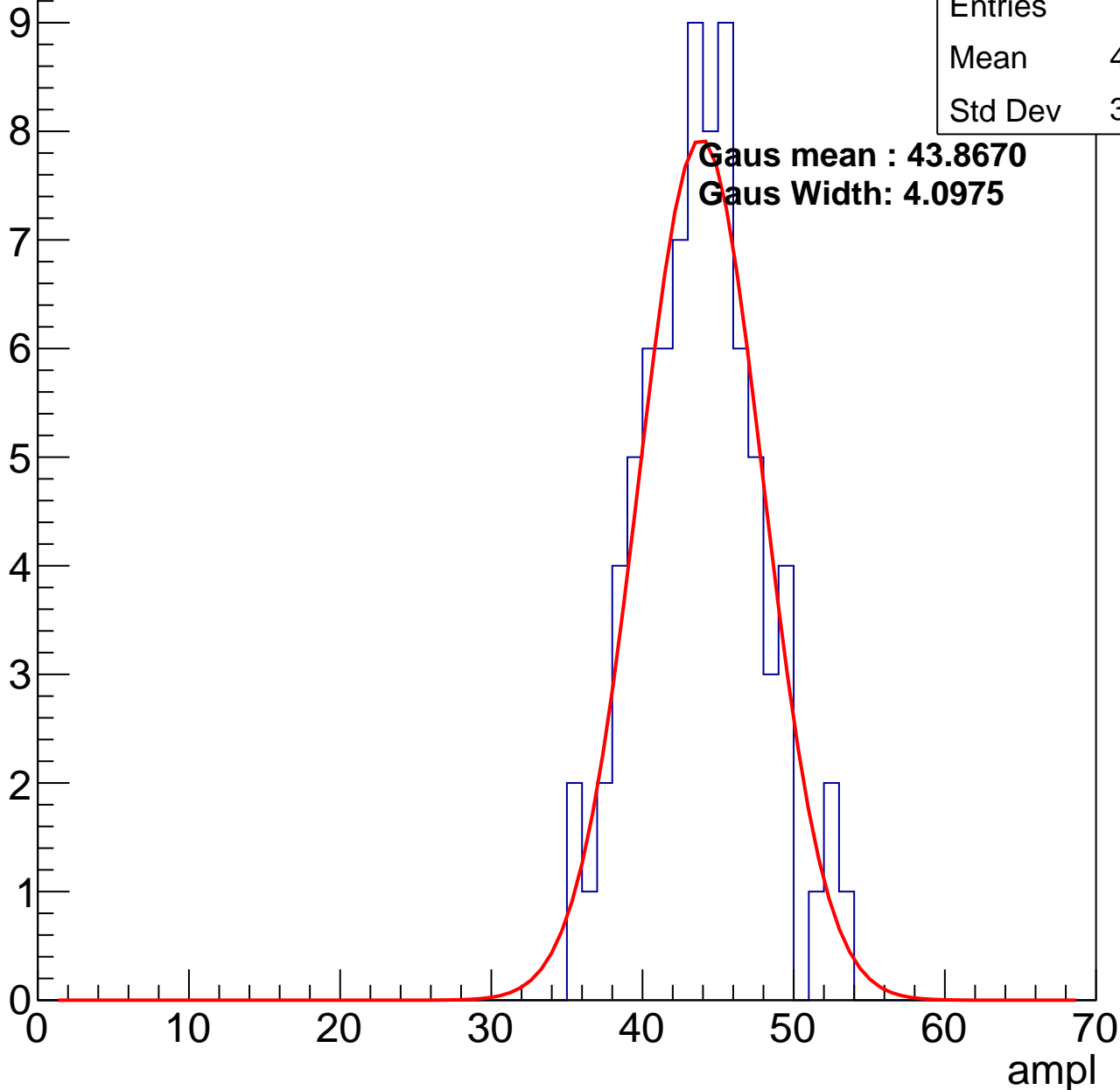
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	81
Mean	43.33
Std Dev	3.894

Gaus mean : 43.8670

Gaus Width: 4.0975

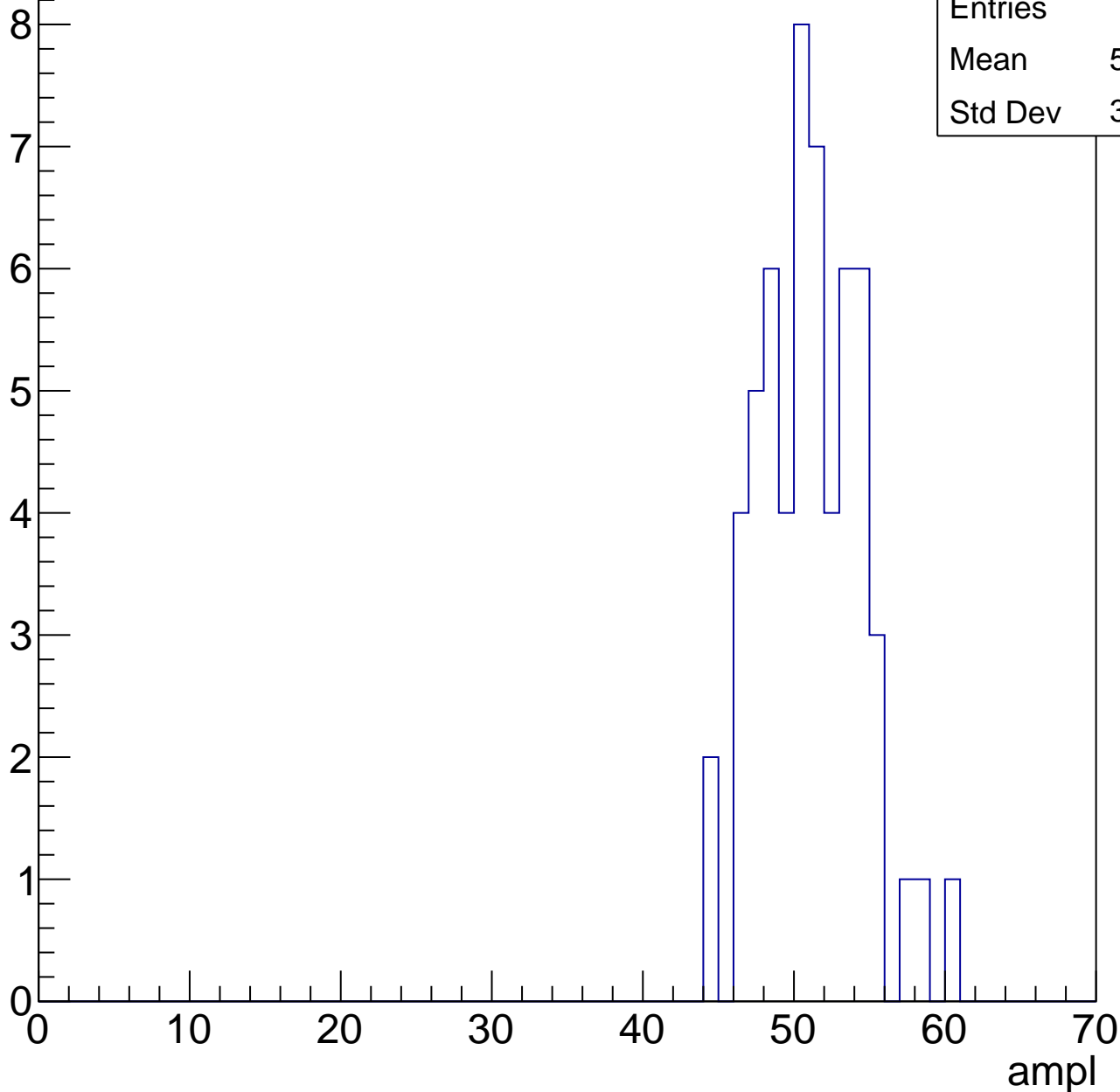


B1L101S, U15-ch116, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	58
Mean	50.66
Std Dev	3.335

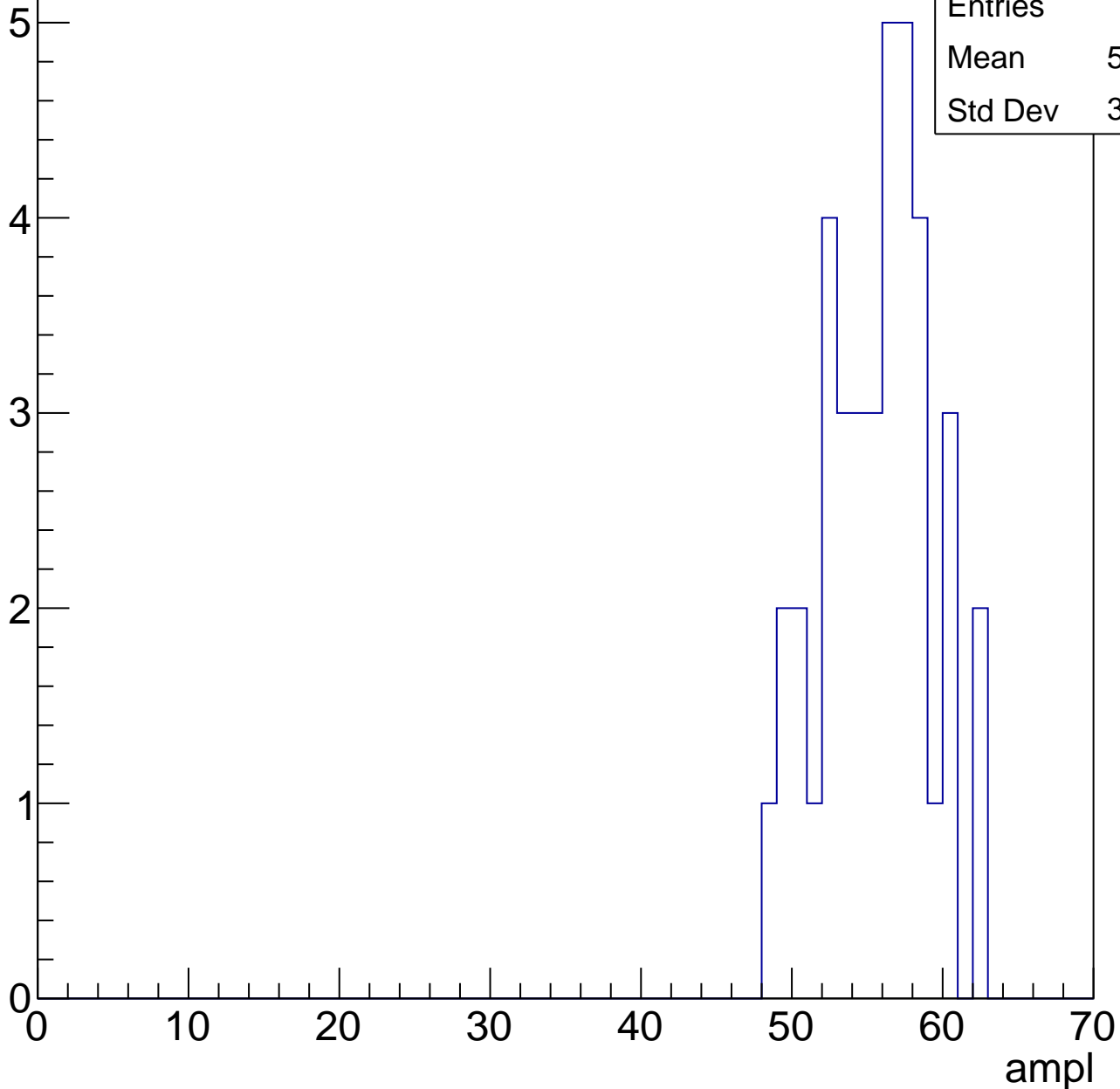


B1L101S, U15-ch116, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	39
Mean	55.15
Std Dev	3.534



B1L101S, U15-ch116, adc5

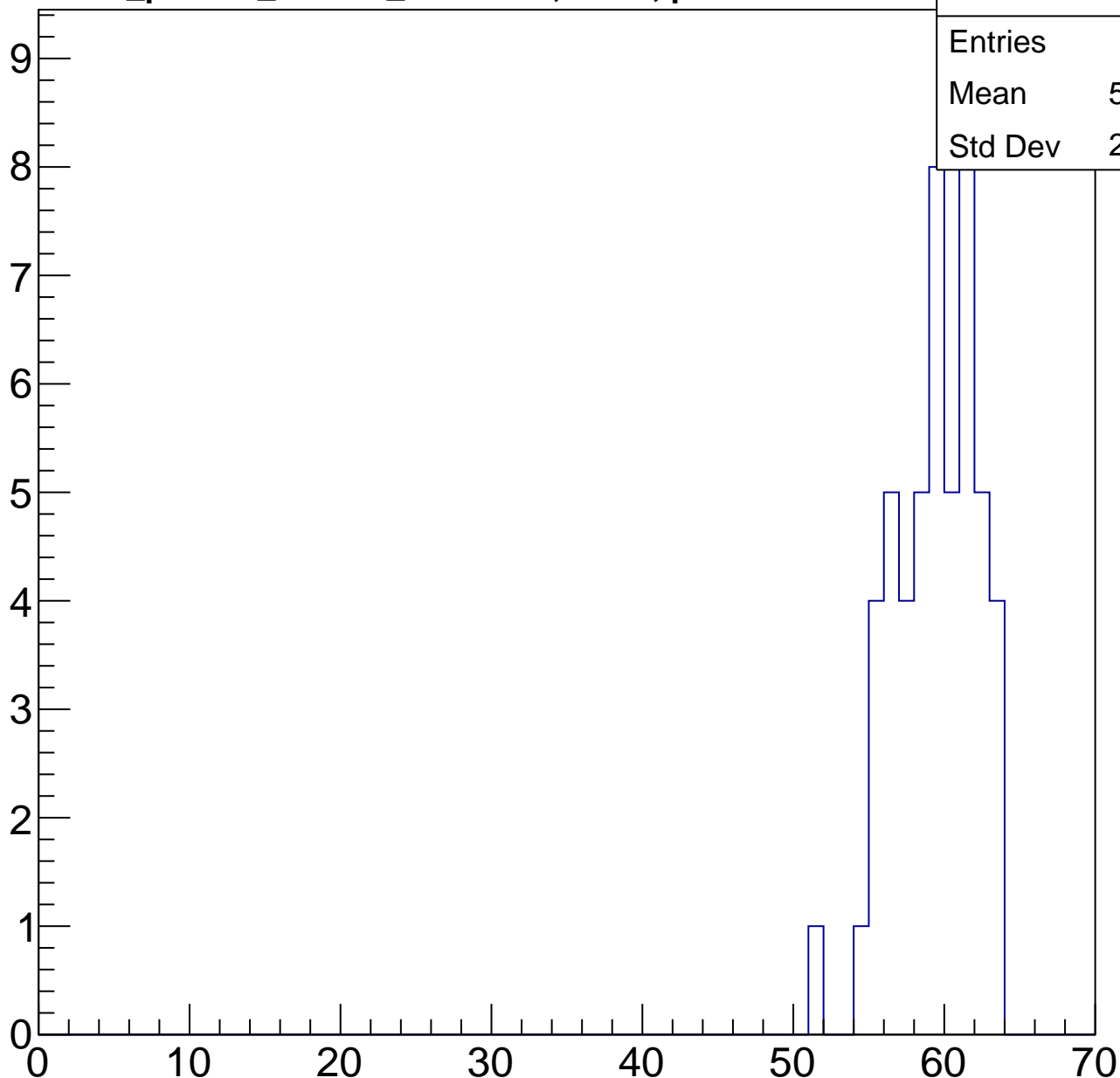
calib_packv5_042523_0143.root, FC#0, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.94
Std Dev	2.689

ampl

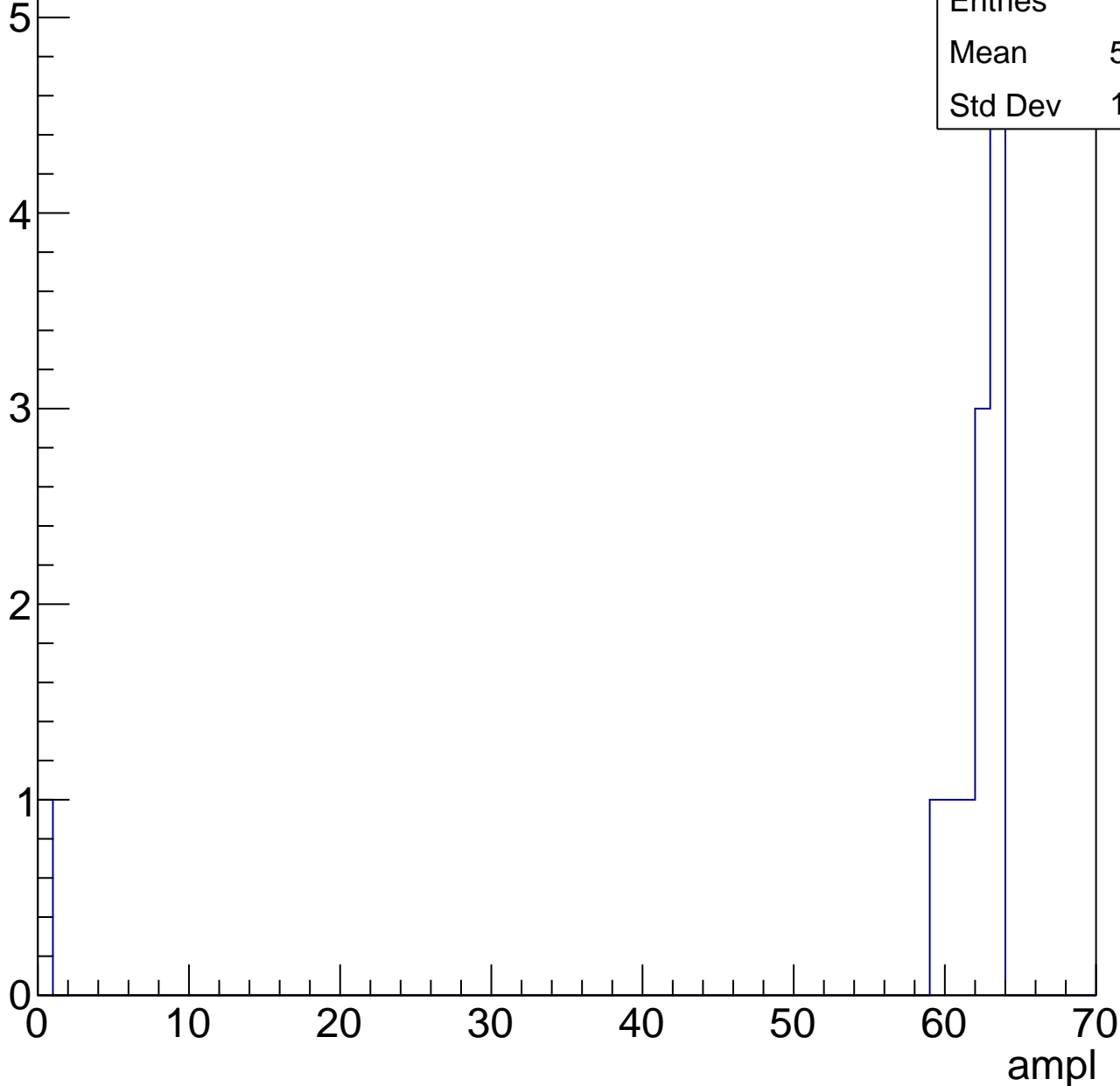


B1L101S, U15-ch116, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

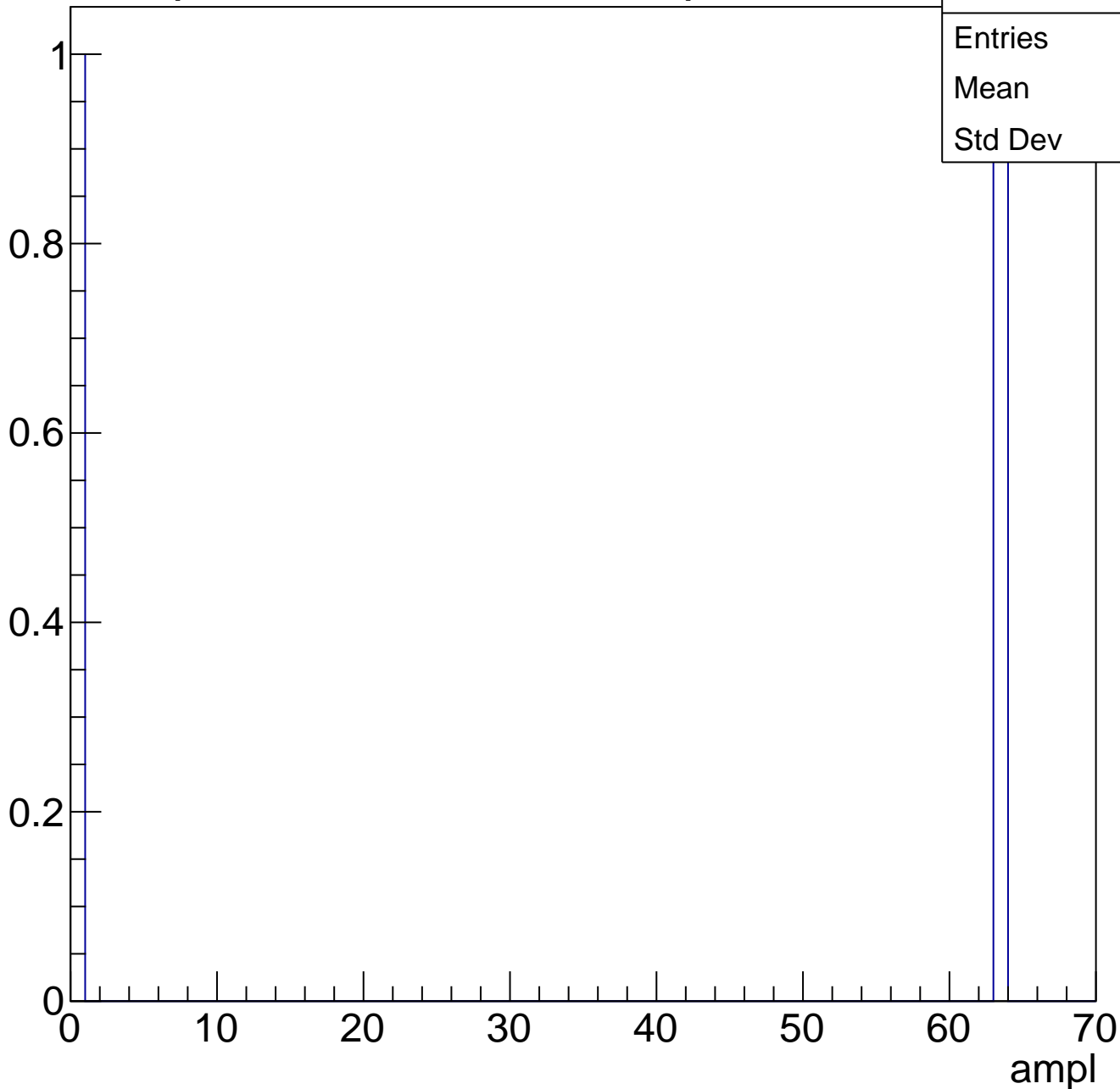
Entries	12
Mean	56.75
Std Dev	17.16



B1L101S, U15-ch116, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch117, adc0

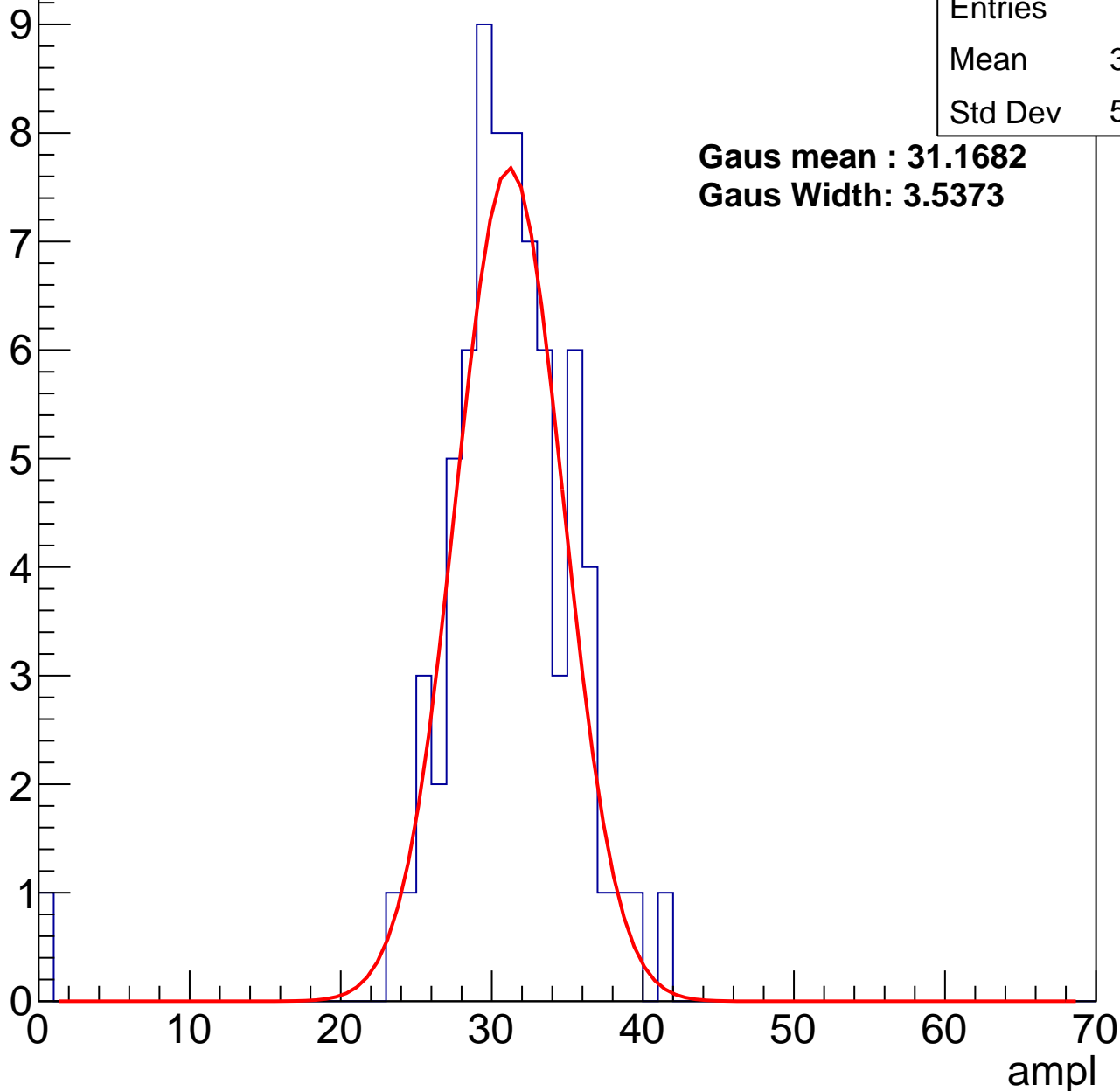
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	74
Mean	30.53
Std Dev	5.062

Gaus mean : 31.1682

Gaus Width: 3.5373



B1L101S, U15-ch117, adc1

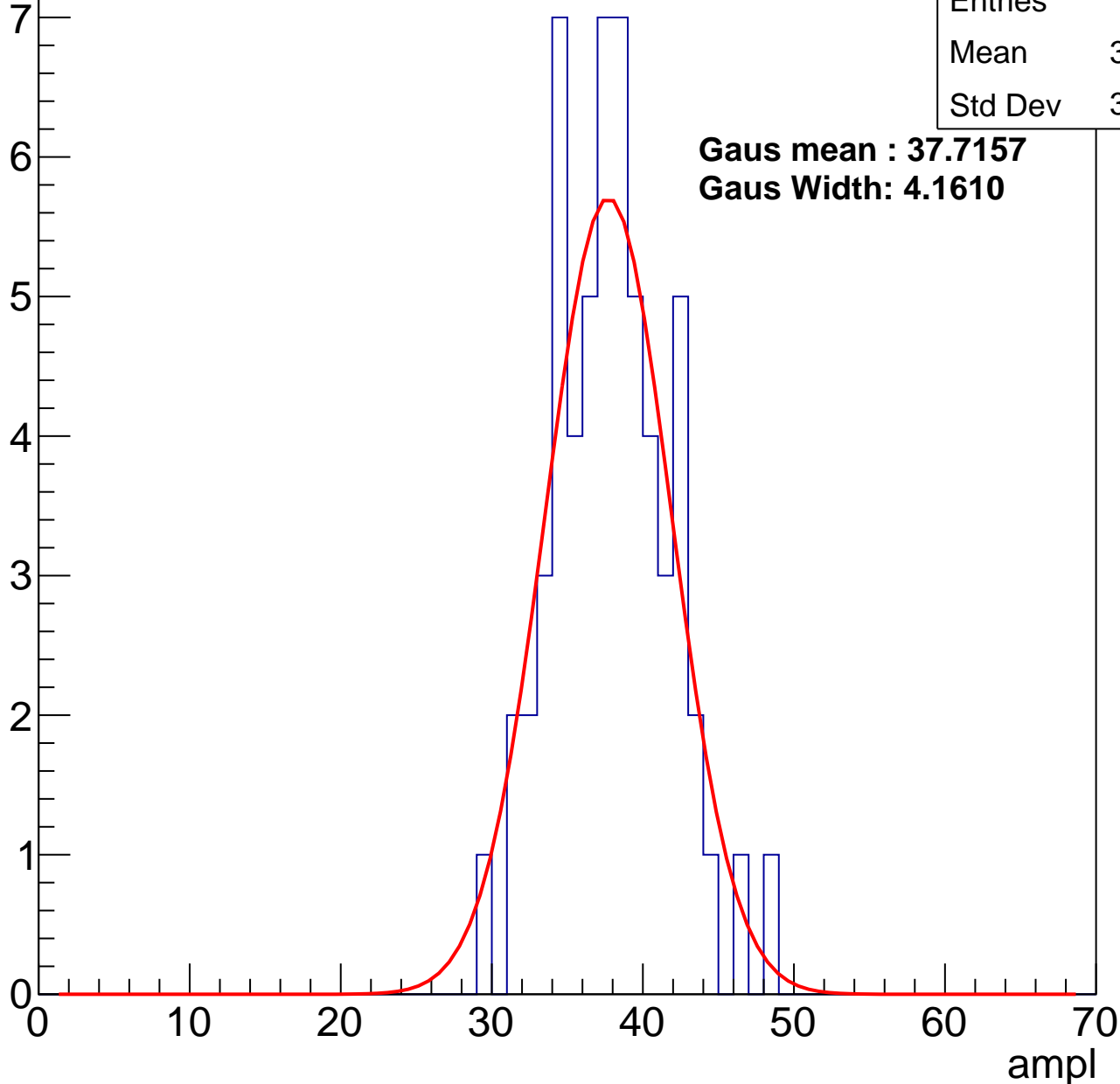
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	37.48
Std Dev	3.792

Gaus mean : 37.7157

Gaus Width: 4.1610



B1L101S, U15-ch117, adc2

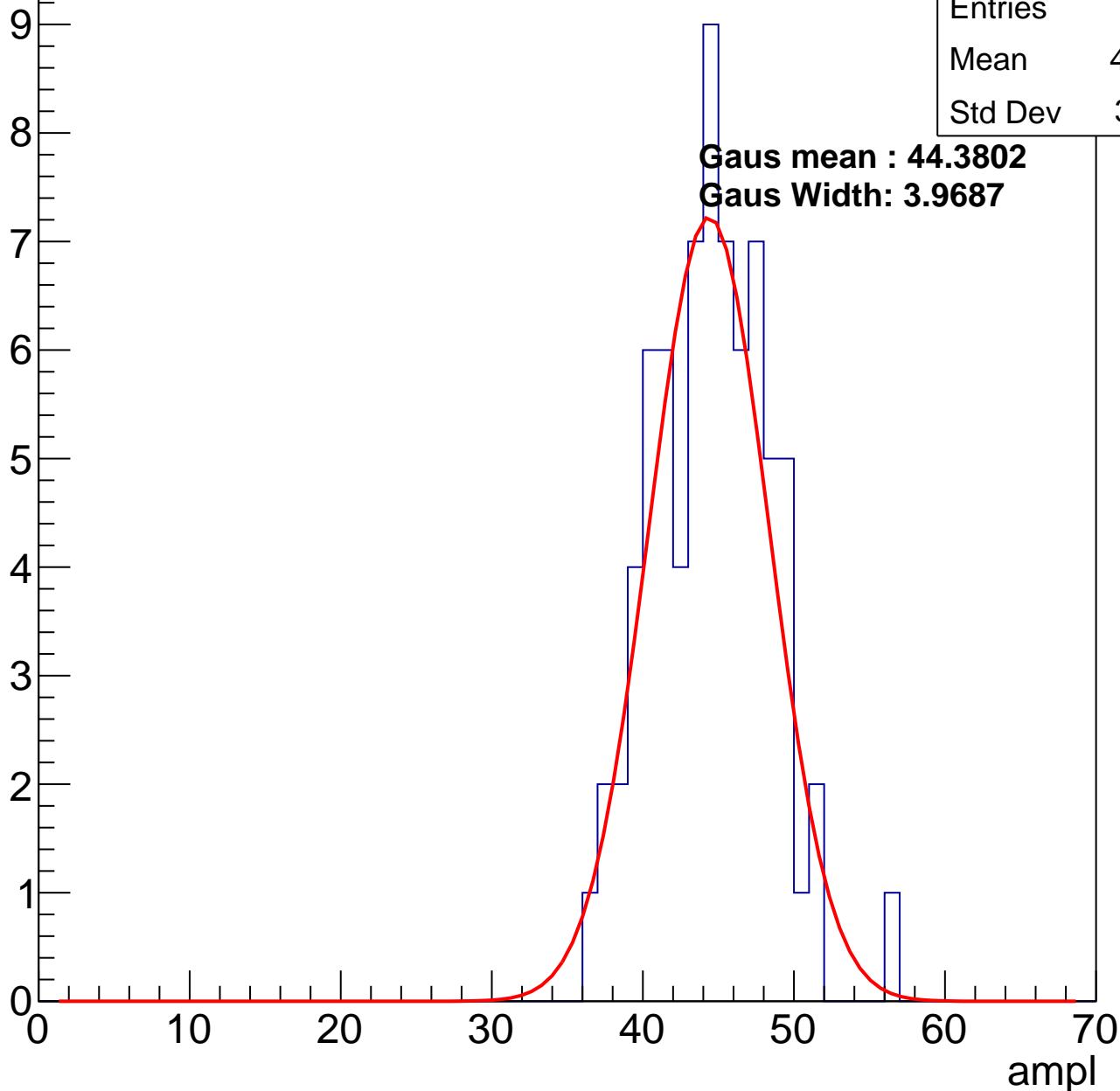
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	75
Mean	44.08
Std Dev	3.801

Gaus mean : 44.3802

Gaus Width: 3.9687

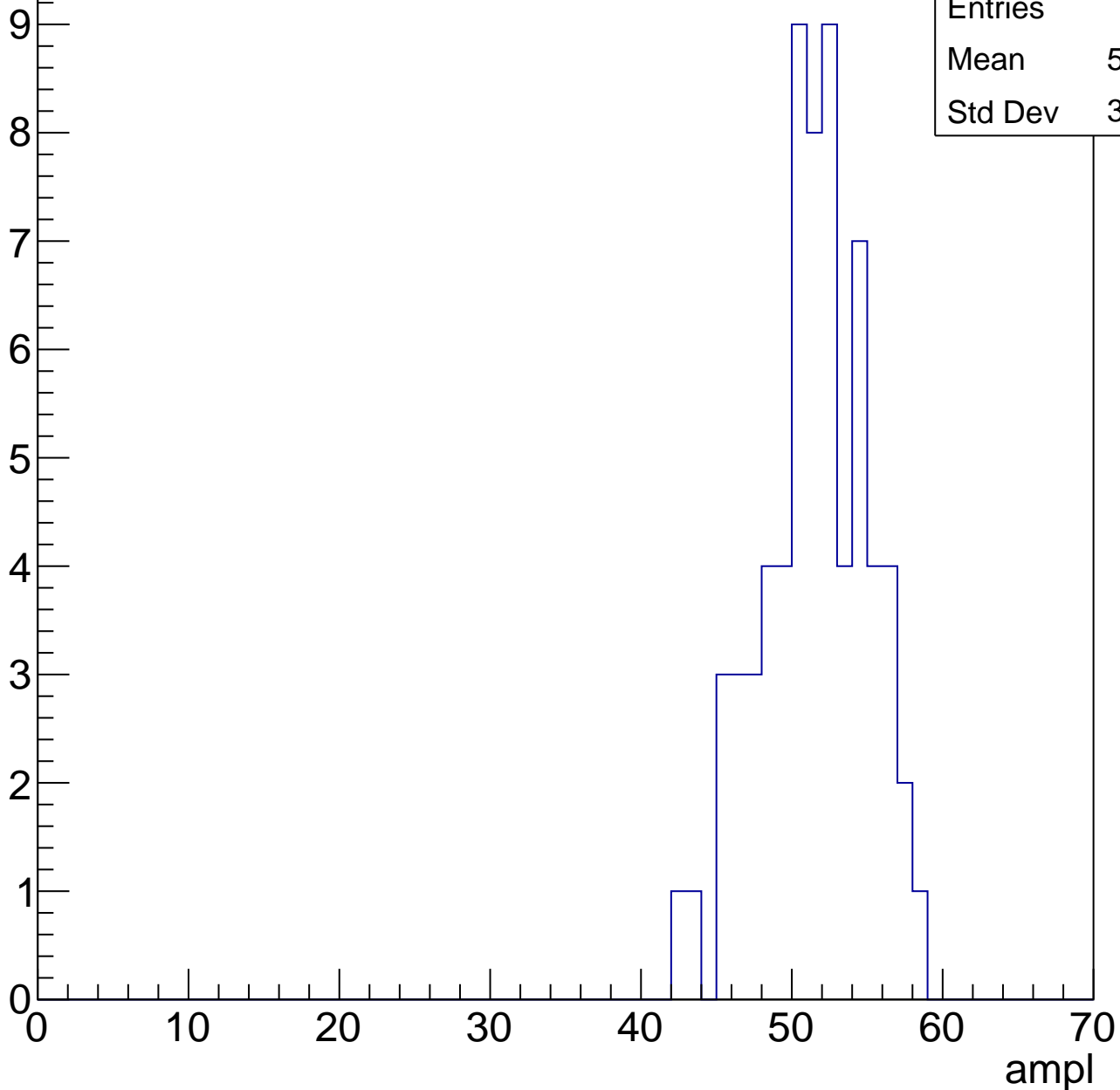


B1L101S, U15-ch117, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	51.03
Std Dev	3.477

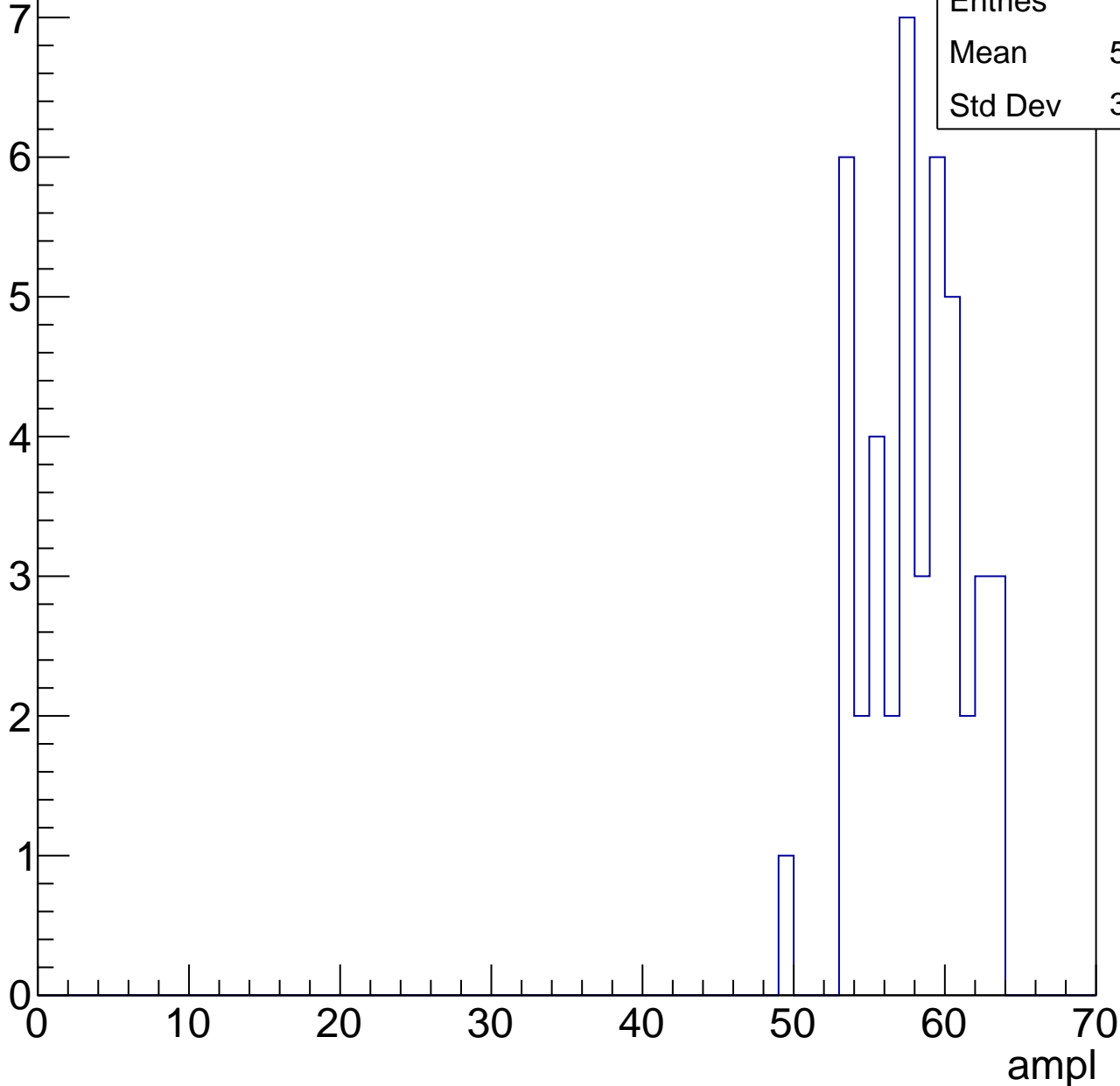


B1L101S, U15-ch117, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	44
Mean	57.52
Std Dev	3.272

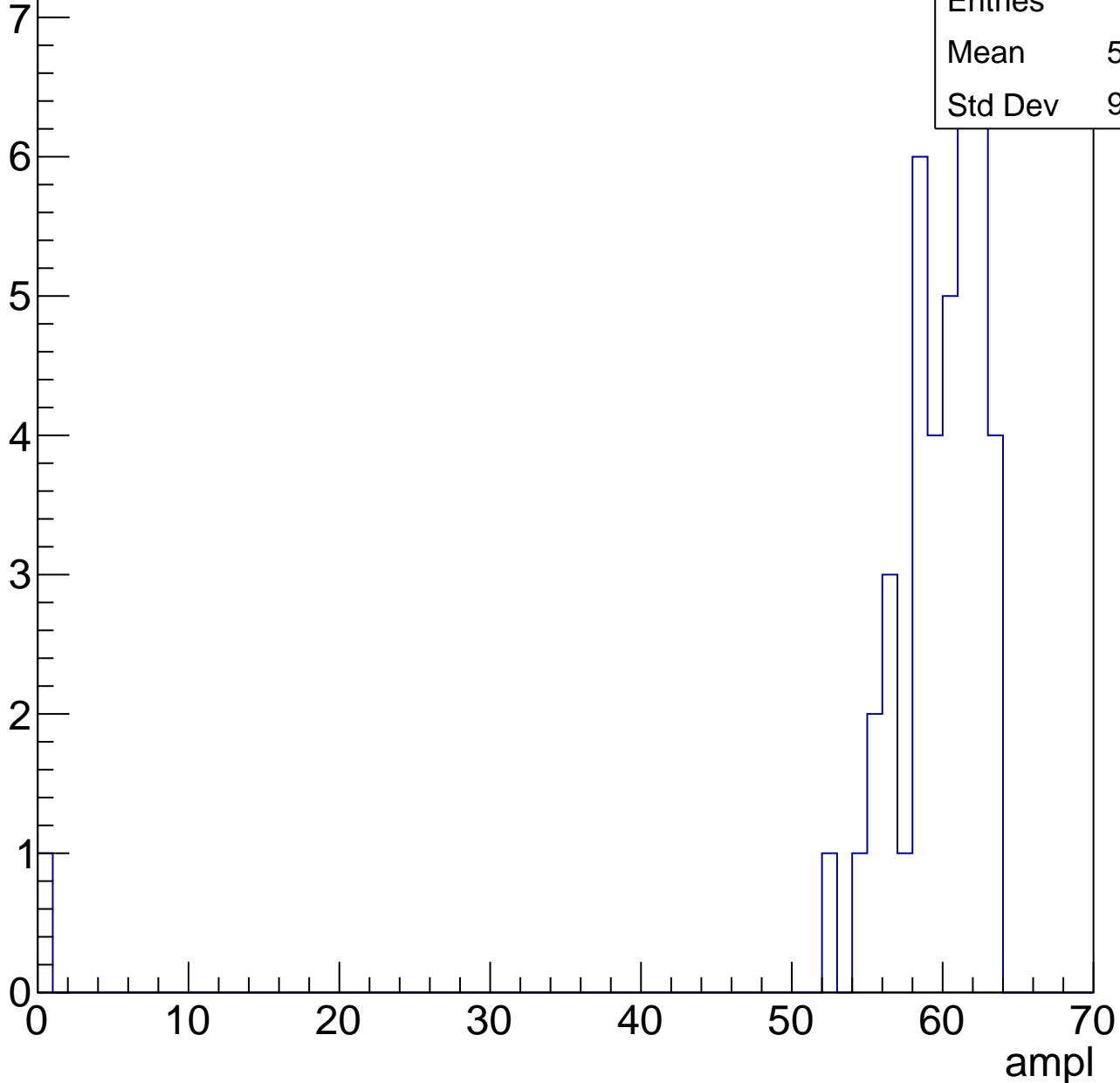


B1L101S, U15-ch117, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	42
Mean	58.05
Std Dev	9.444



B1L101S, U15-ch117, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch117, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch118, adc0

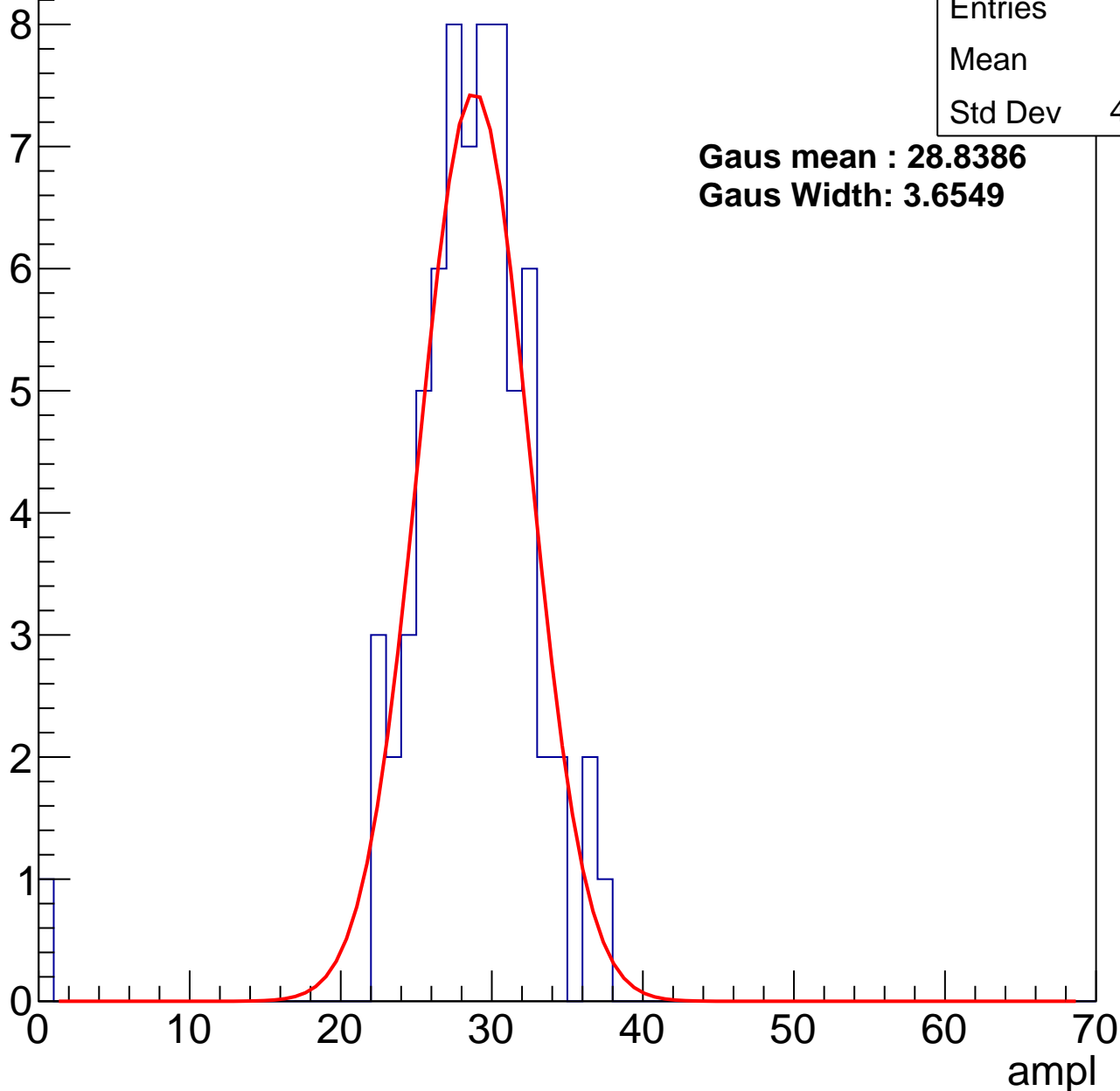
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	28.1
Std Dev	4.783

Gaus mean : 28.8386

Gaus Width: 3.6549



B1L101S, U15-ch118, adc1

calib_packv5_042523_0143.root, FC#0, port D2

Entries	67
Mean	35.18
Std Dev	3.412

Gaus mean : 36.0659

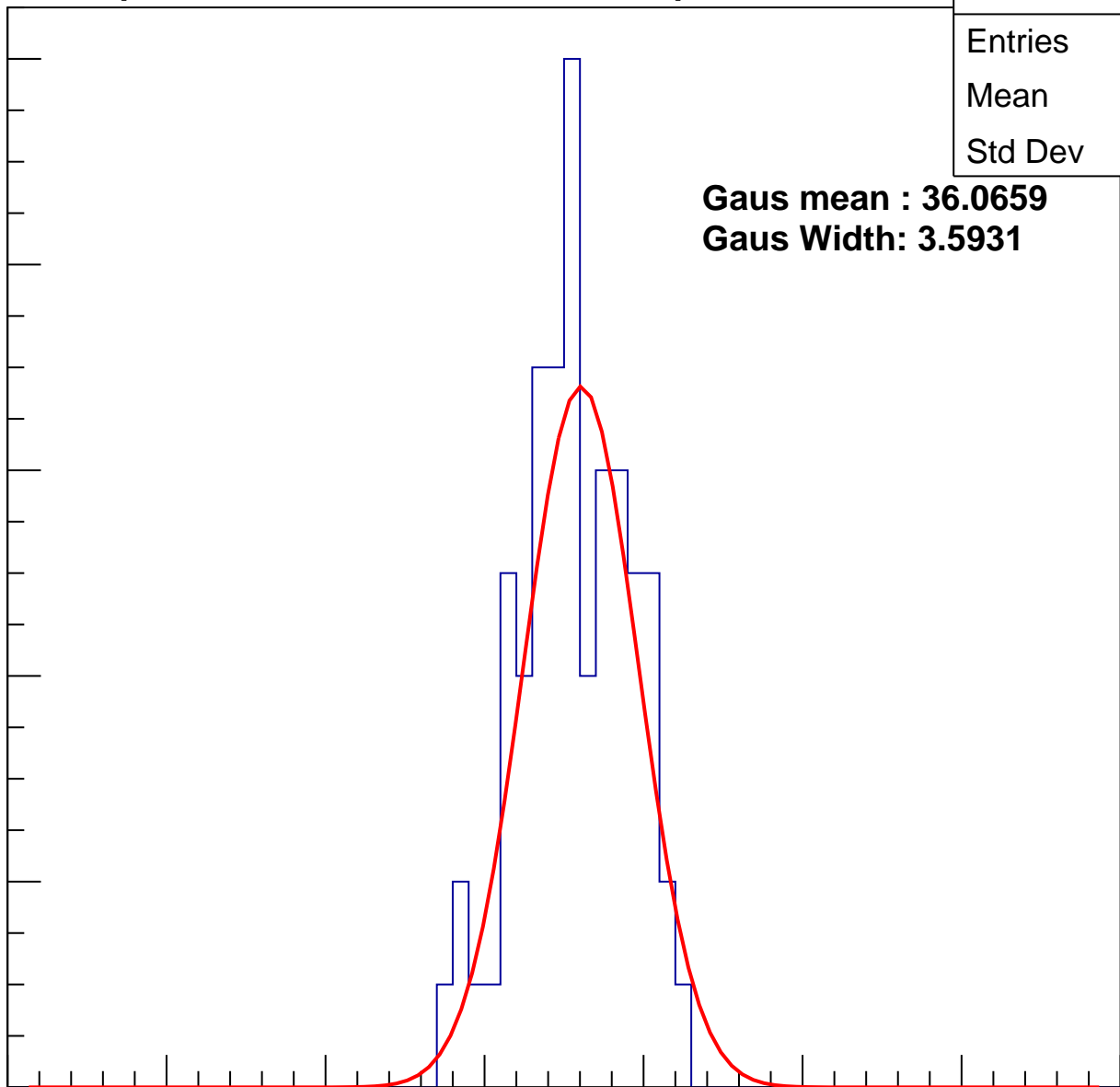
Gaus Width: 3.5931

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L101S, U15-ch118, adc2

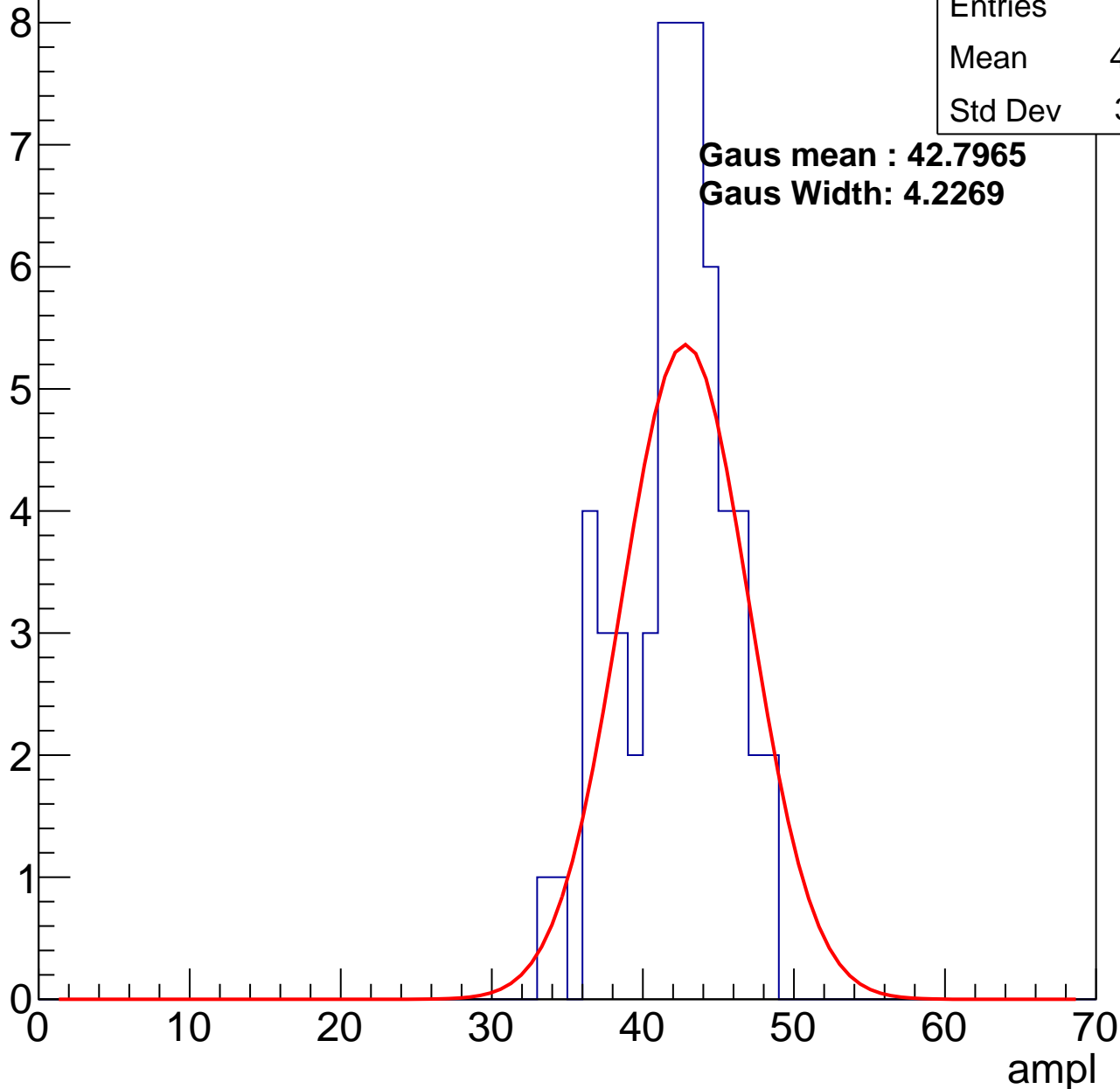
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	41.69
Std Dev	3.441

Gaus mean : 42.7965

Gaus Width: 4.2269

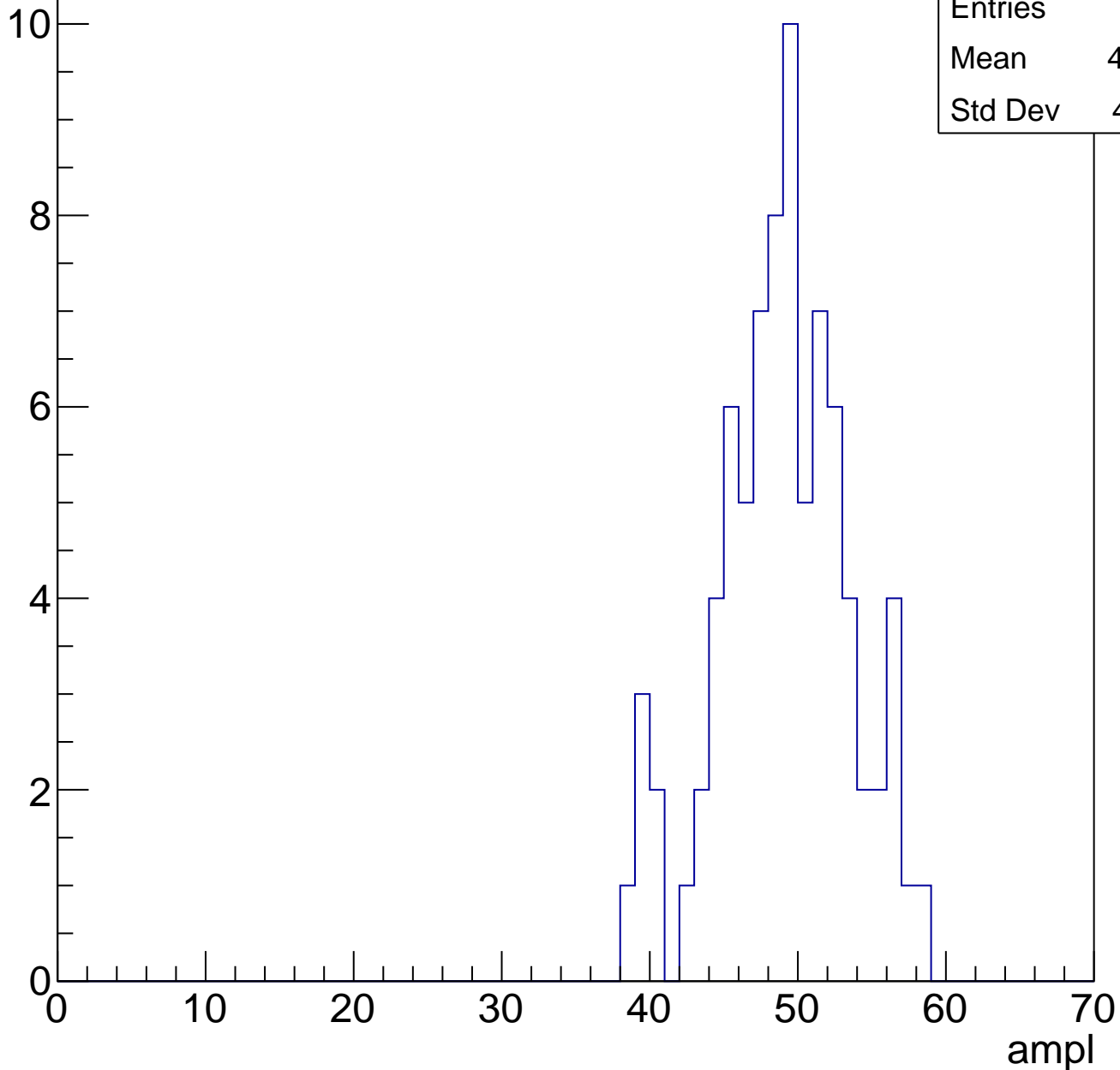


B1L101S, U15-ch118, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entries	81
Mean	48.52
Std Dev	4.411

Entry

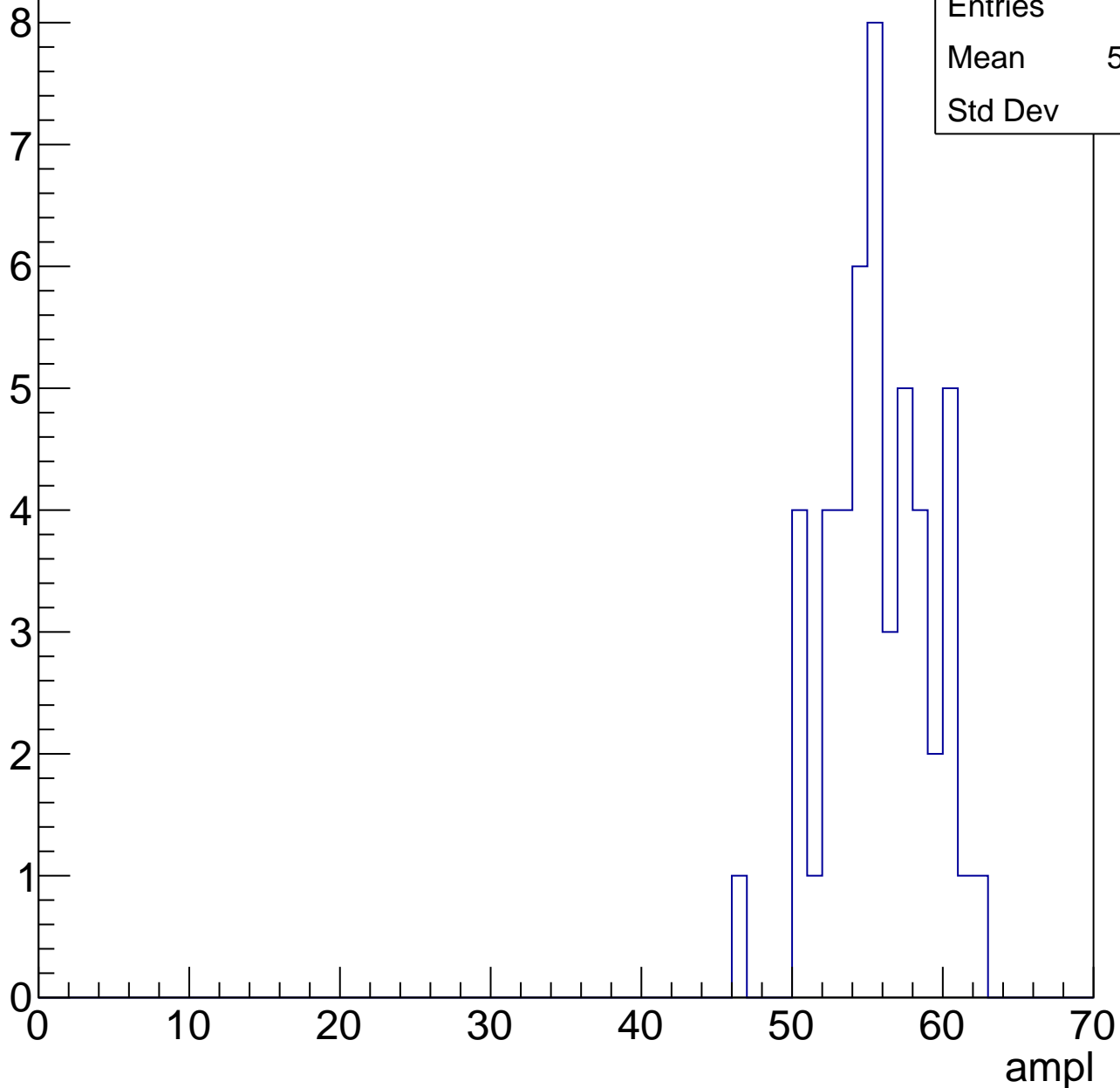


B1L101S, U15-ch118, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	49
Mean	55.24
Std Dev	3.36

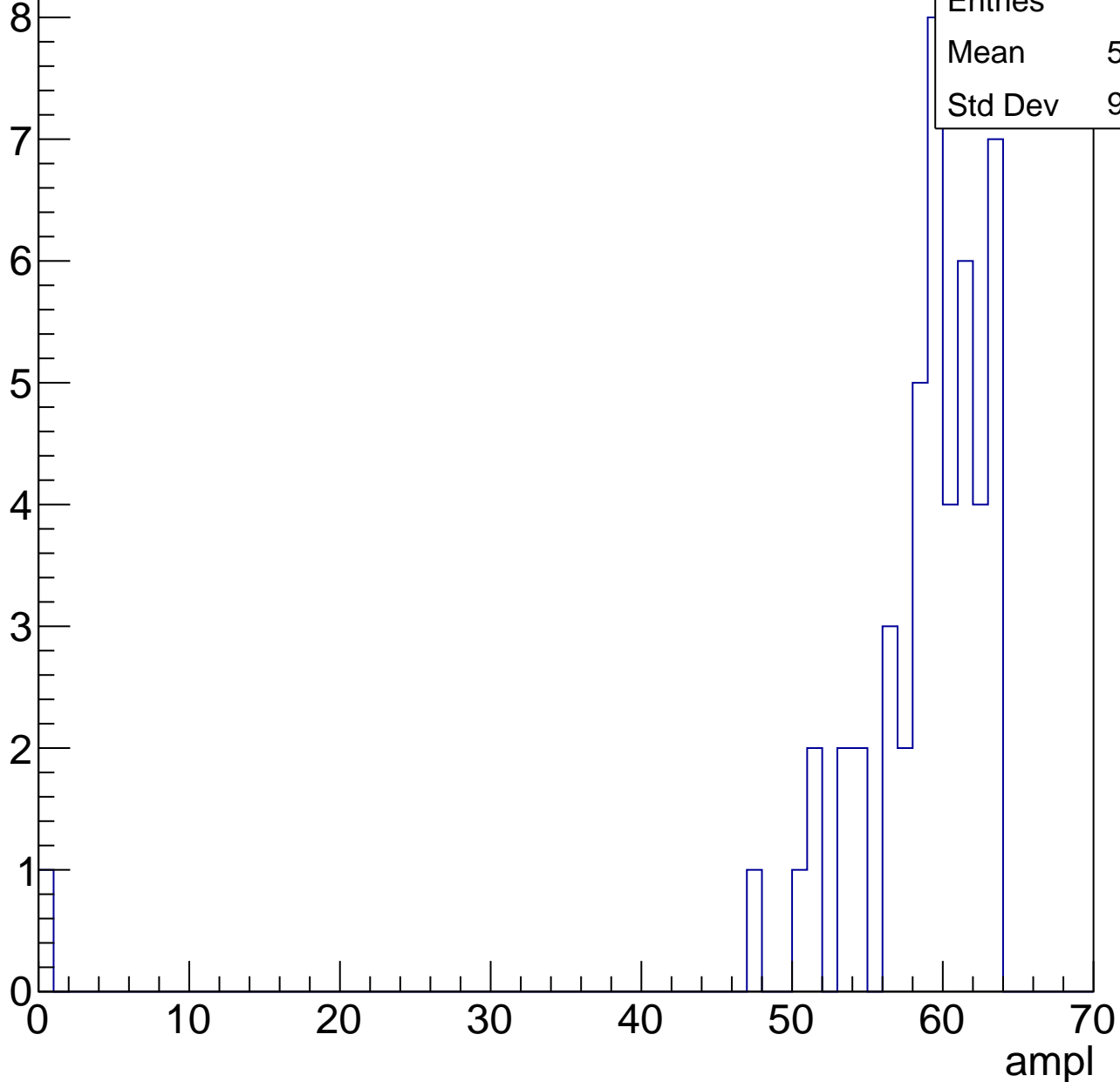


B1L101S, U15-ch118, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

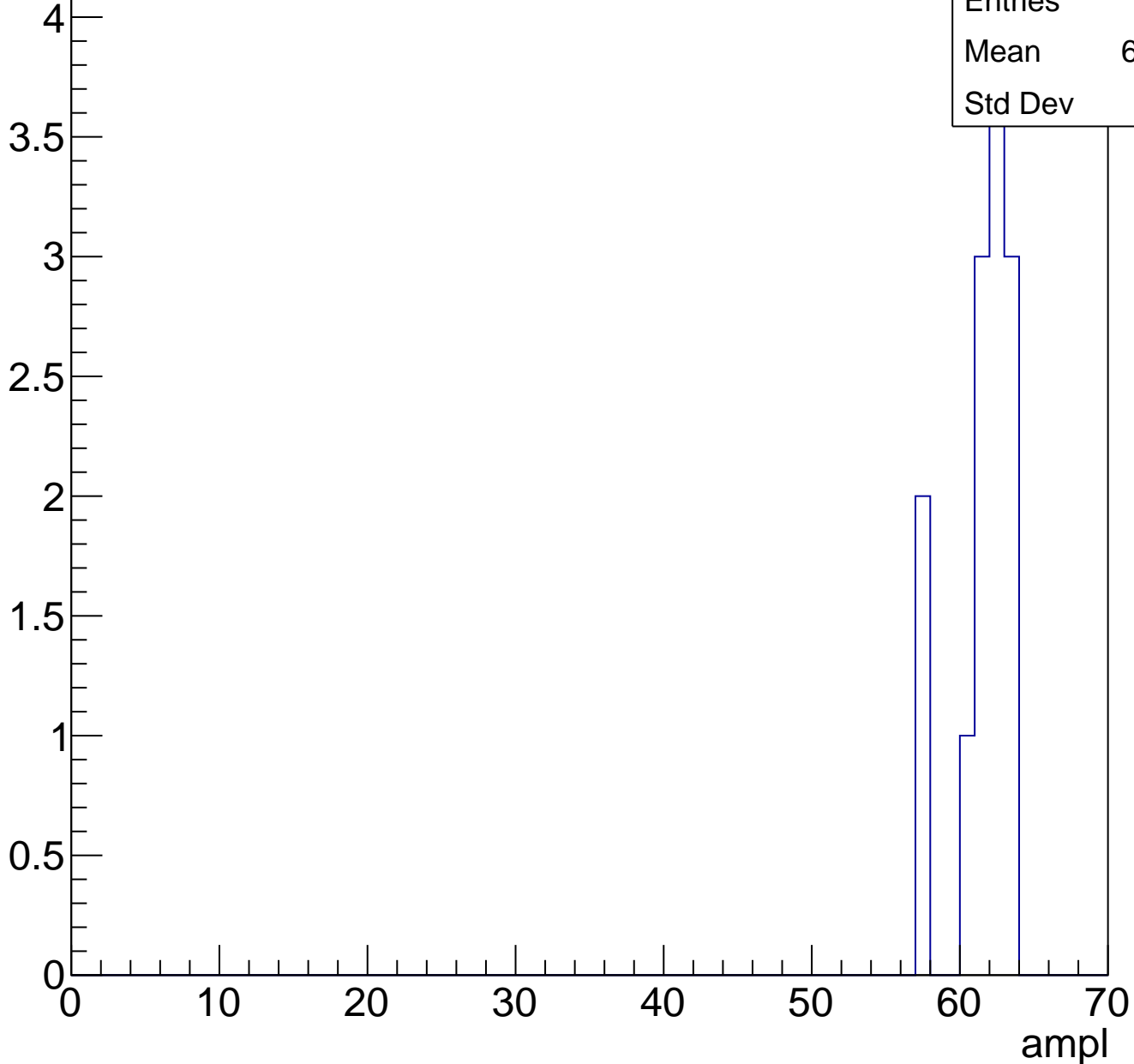
Entries	48
Mean	57.33
Std Dev	9.173



B1L101S, U15-ch118, adc6

calib_packv5_042523_0143.root, FC#0, port D2

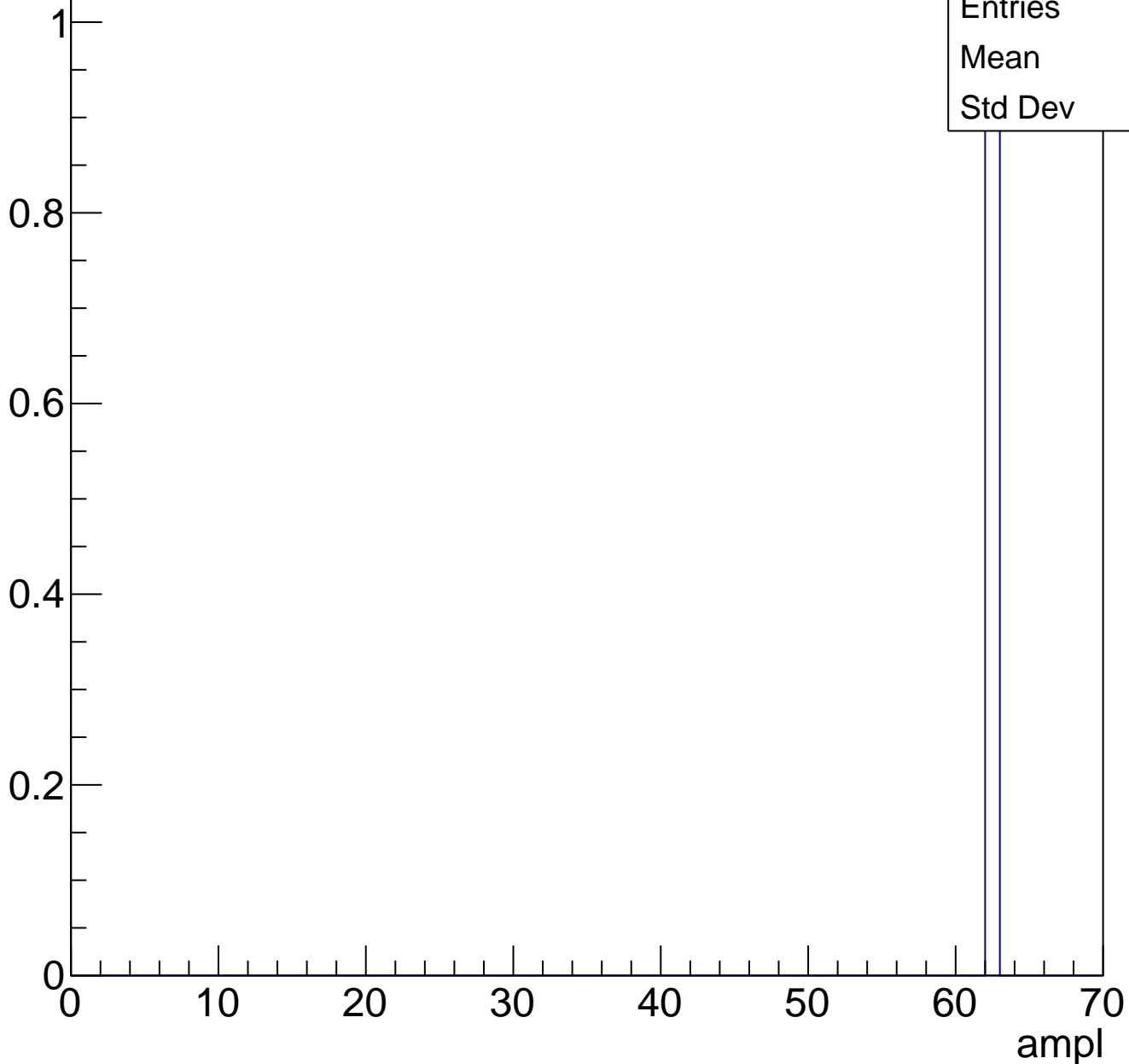
Entry



B1L101S, U15-ch118, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	62
Std Dev	0

B1L101S, U15-ch119, adc0

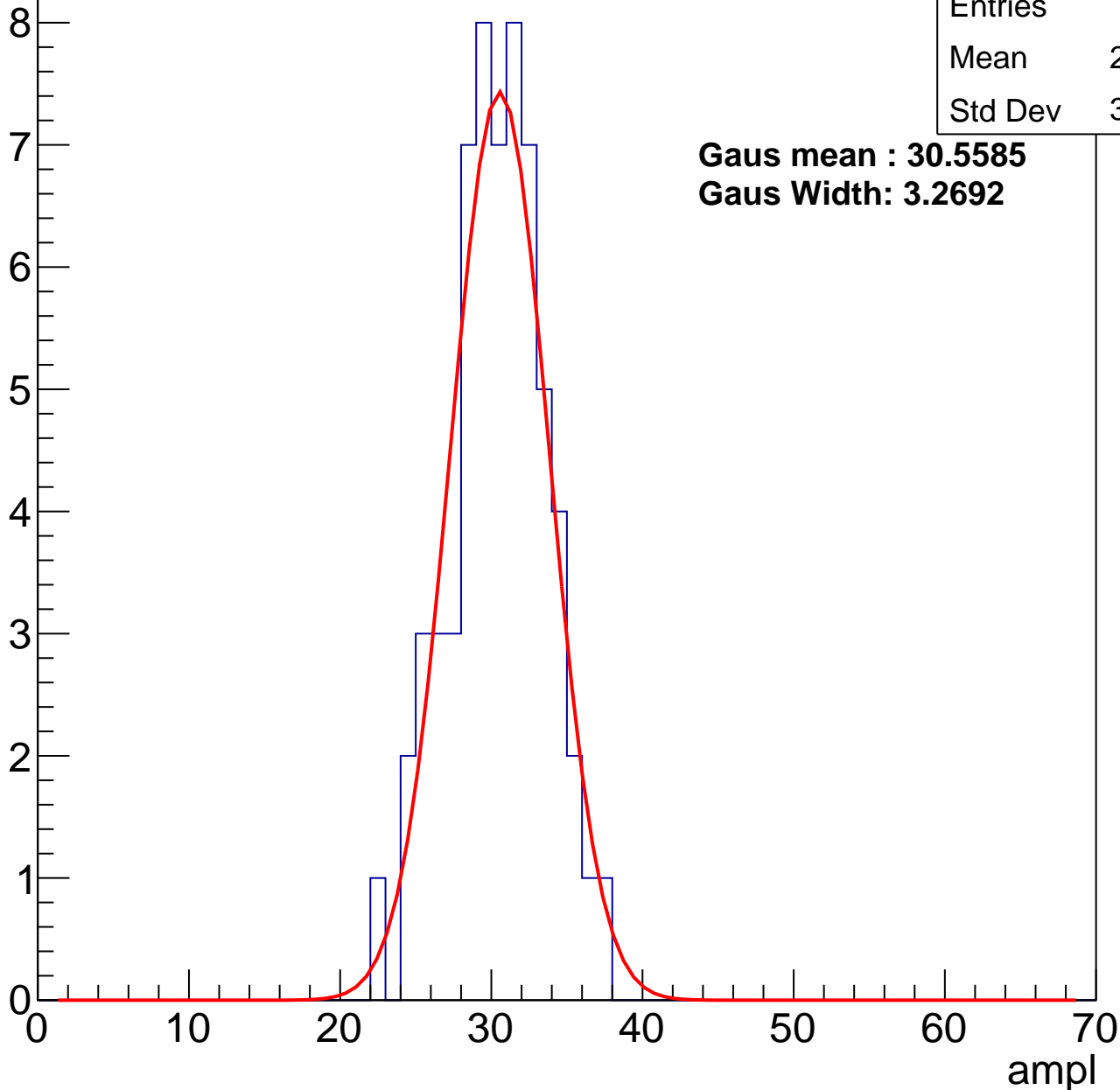
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	29.97
Std Dev	3.116

Gaus mean : 30.5585

Gaus Width: 3.2692



B1L101S, U15-ch119, adc1

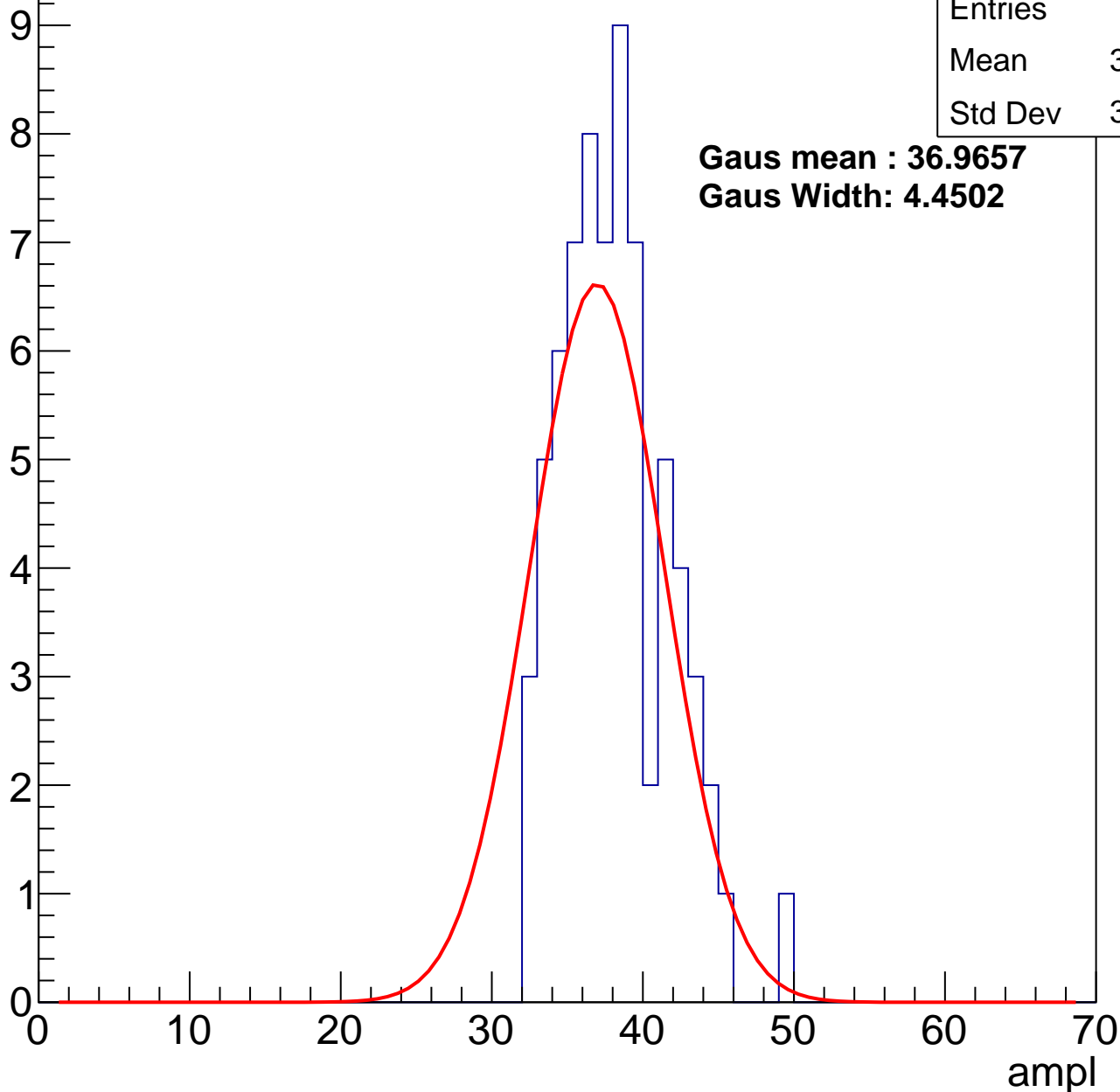
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	37.66
Std Dev	3.513

Gaus mean : 36.9657

Gaus Width: 4.4502



B1L101S, U15-ch119, adc2

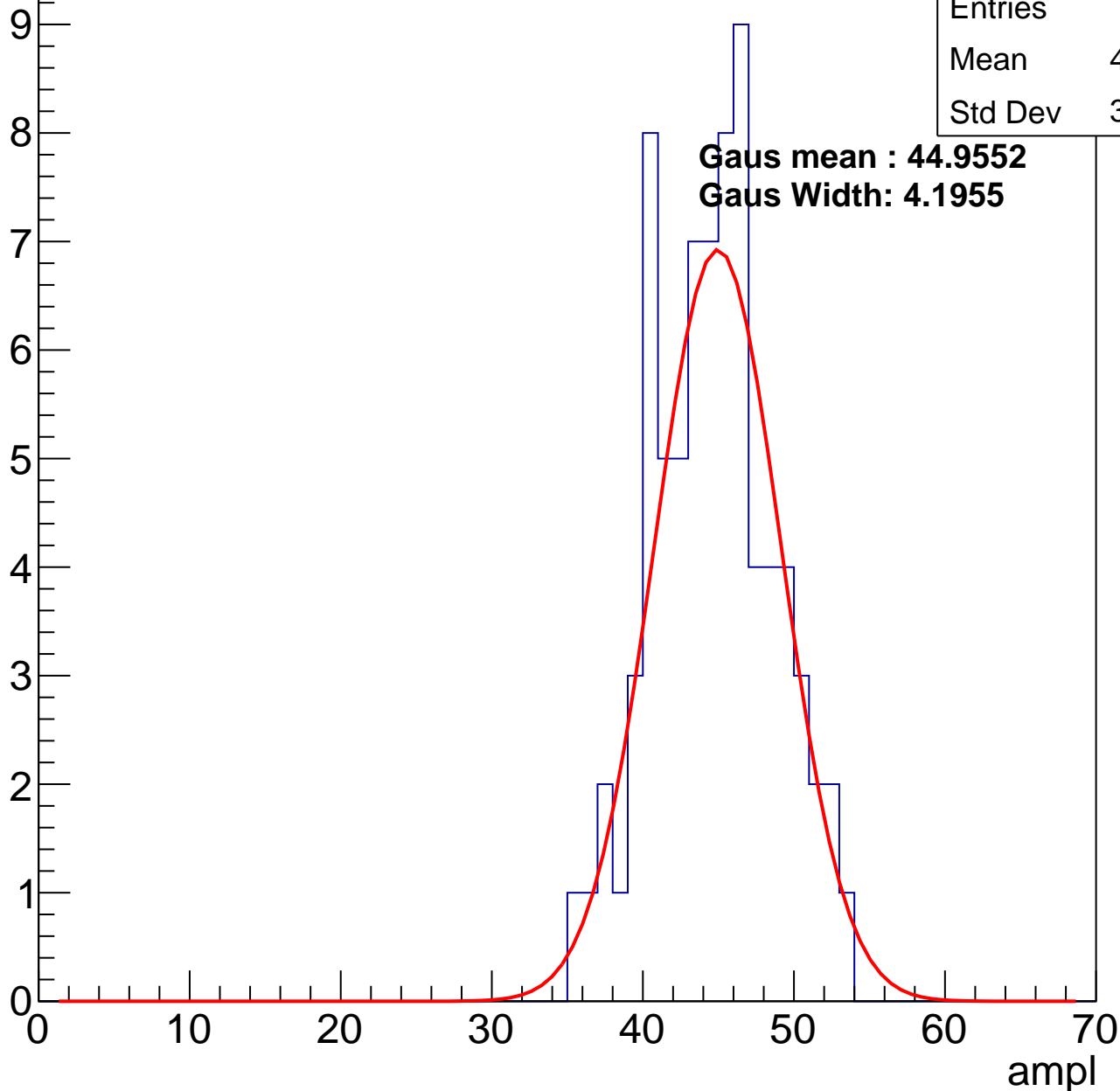
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	77
Mean	44.19
Std Dev	3.964

Gaus mean : 44.9552

Gaus Width: 4.1955

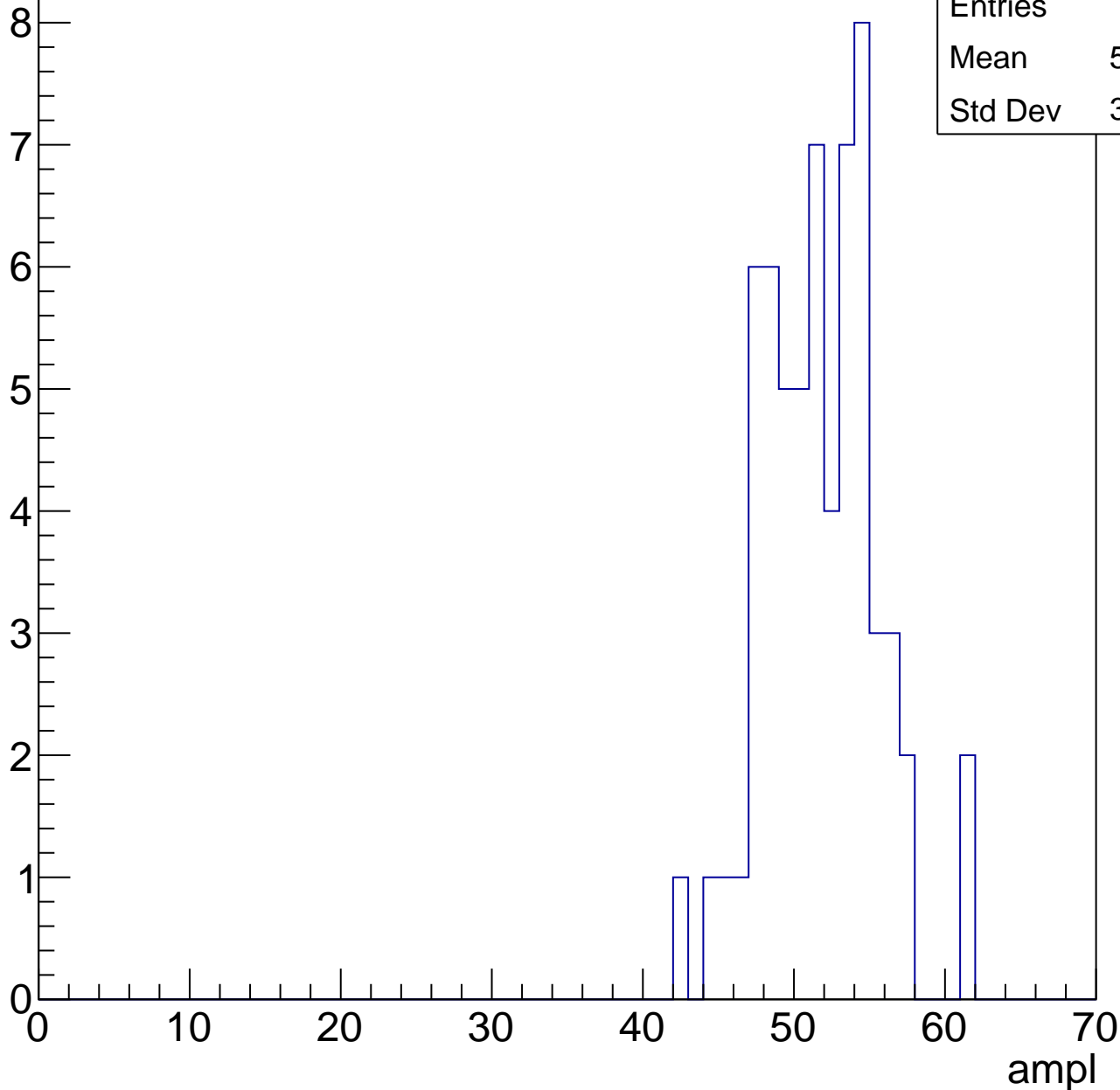


B1L101S, U15-ch119, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	62
Mean	51.27
Std Dev	3.725

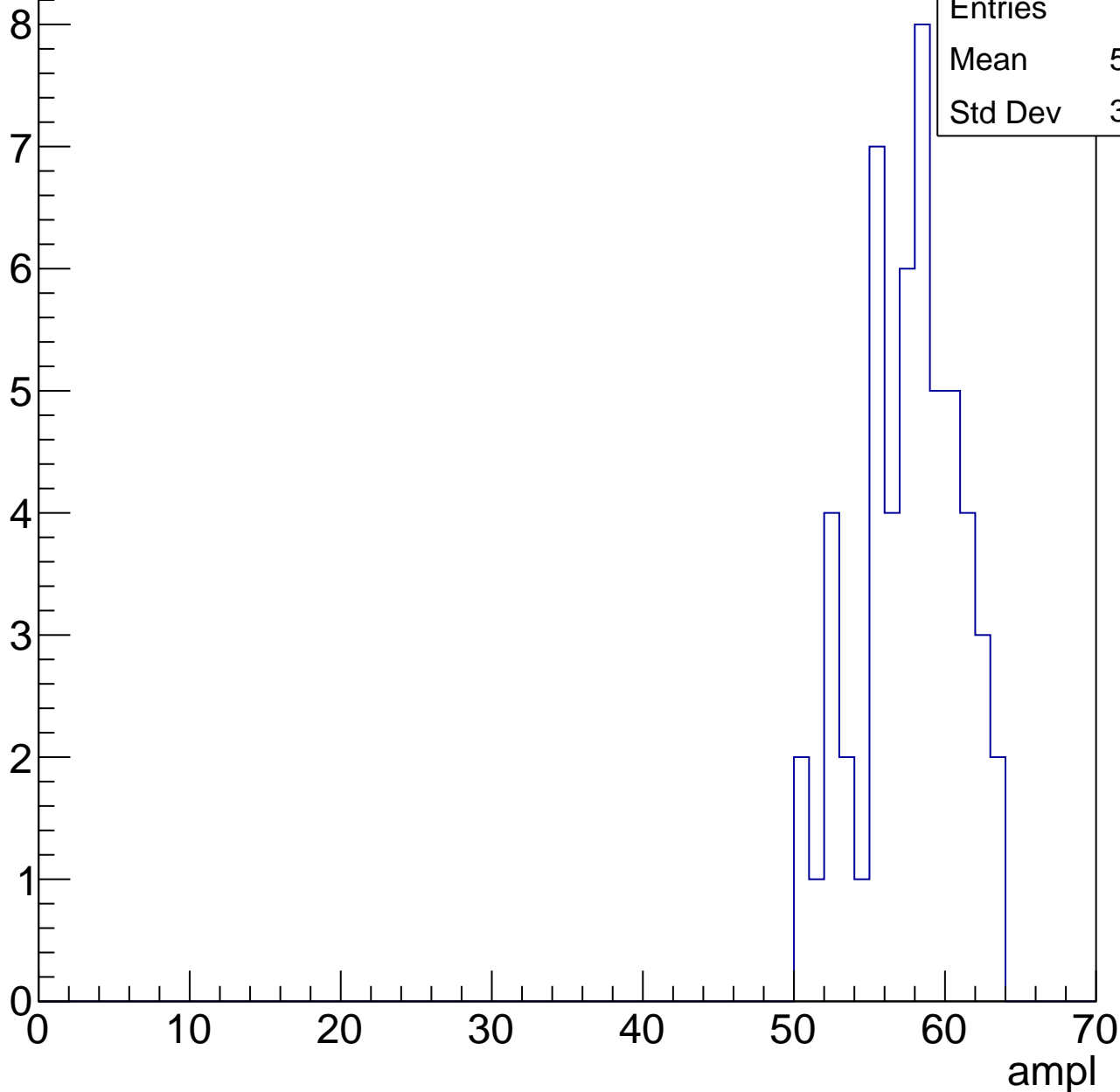


B1L101S, U15-ch119, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	54
Mean	57.13
Std Dev	3.306

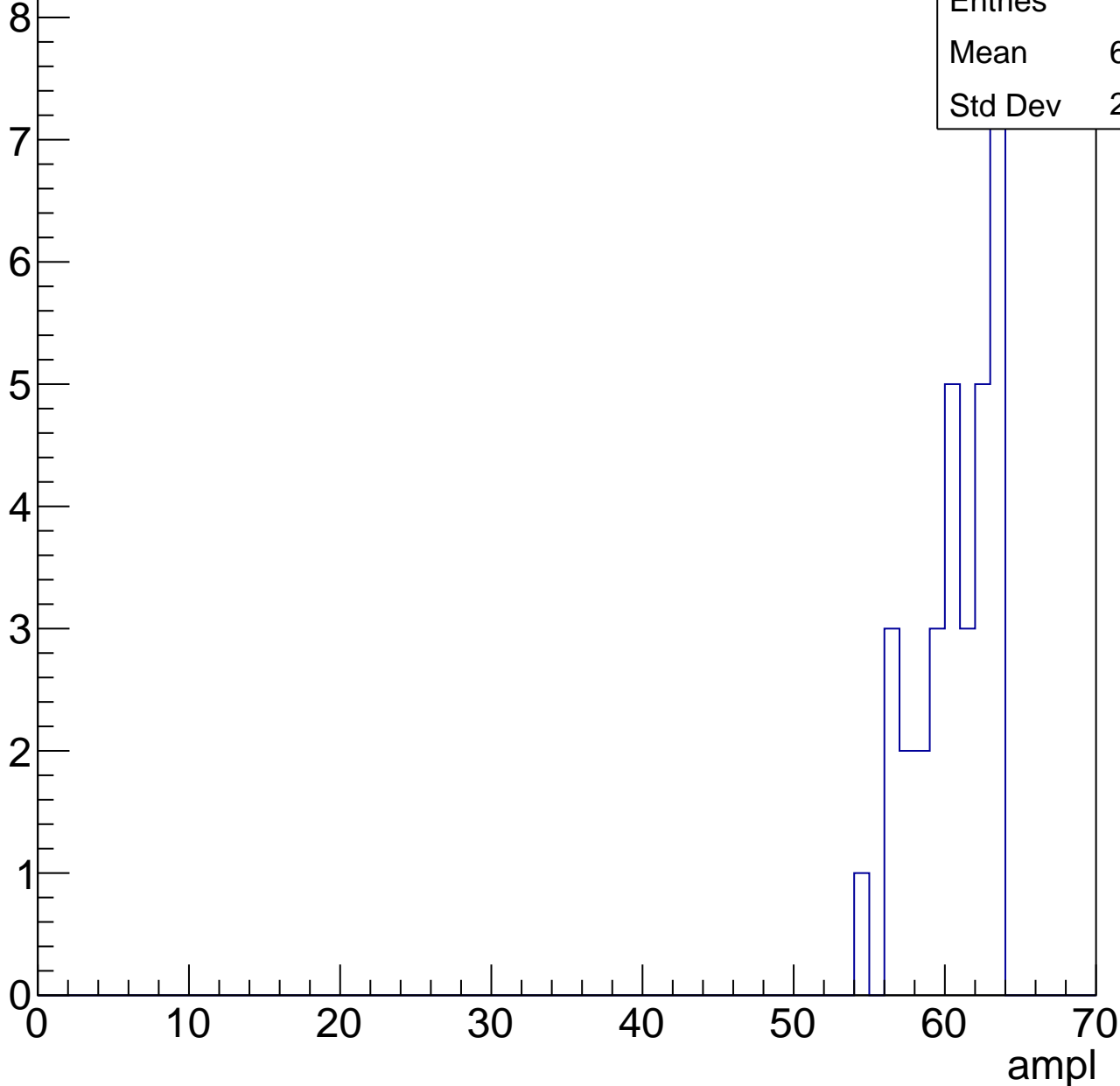


B1L101S, U15-ch119, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	32
Mean	60.19
Std Dev	2.543



B1L101S, U15-ch119, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.5
Std Dev	22.62

ampl

B1L101S, U15-ch119, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L101S, U15-ch120, adc0

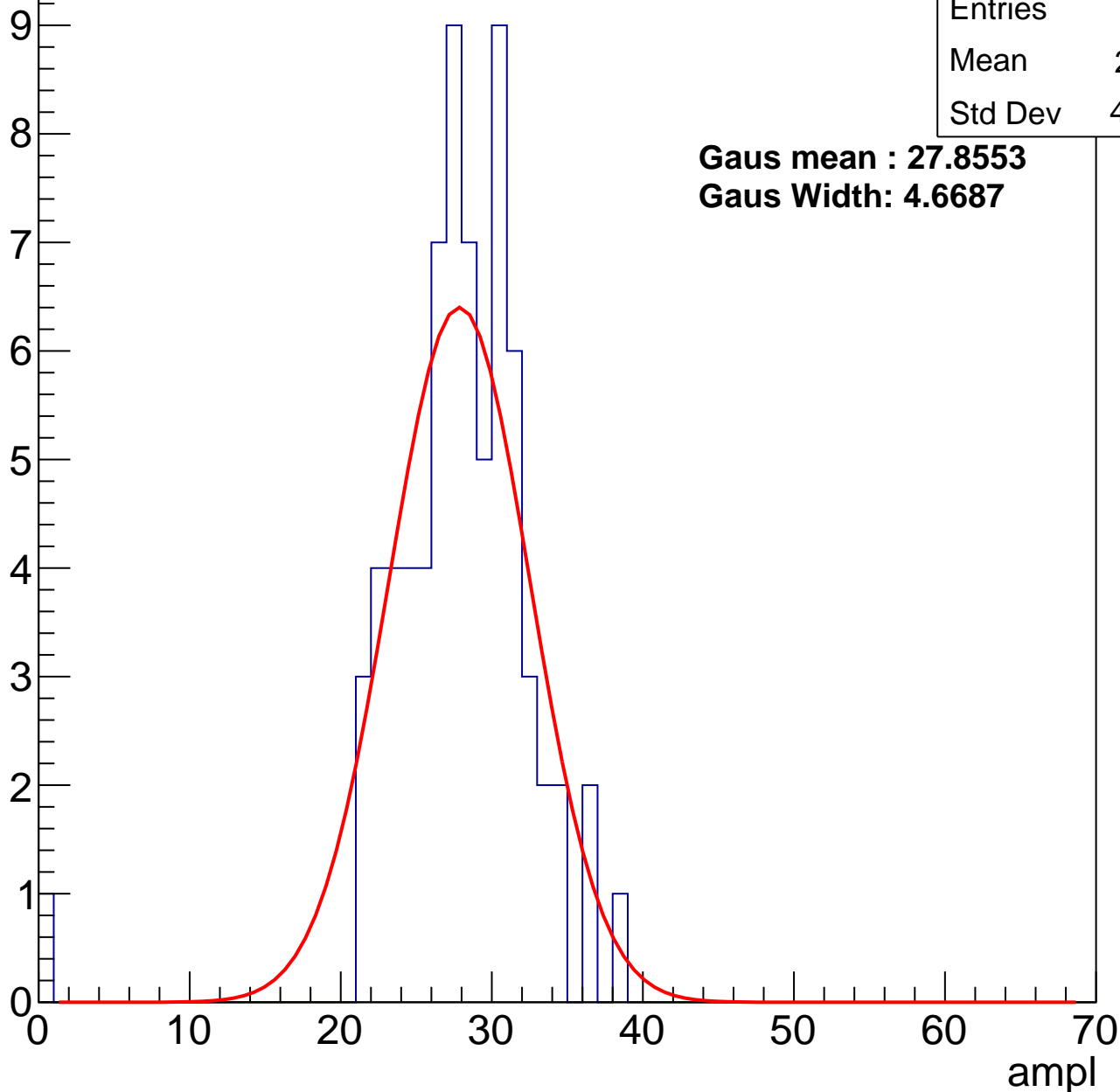
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	27.41
Std Dev	4.938

Gaus mean : 27.8553

Gaus Width: 4.6687



B1L101S, U15-ch120, adc1

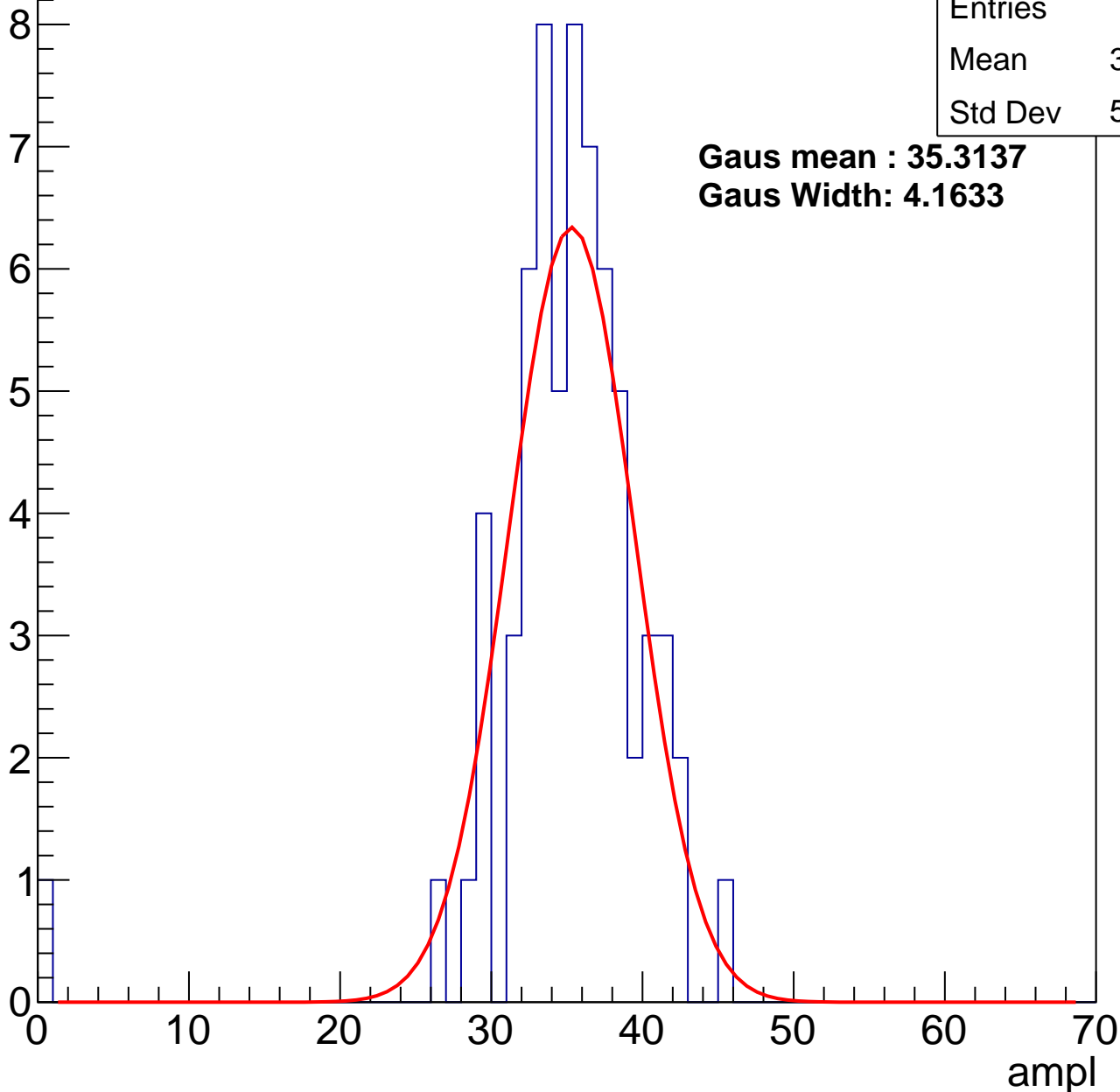
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	34.59
Std Dev	5.662

Gaus mean : 35.3137

Gaus Width: 4.1633



B1L101S, U15-ch120, adc2

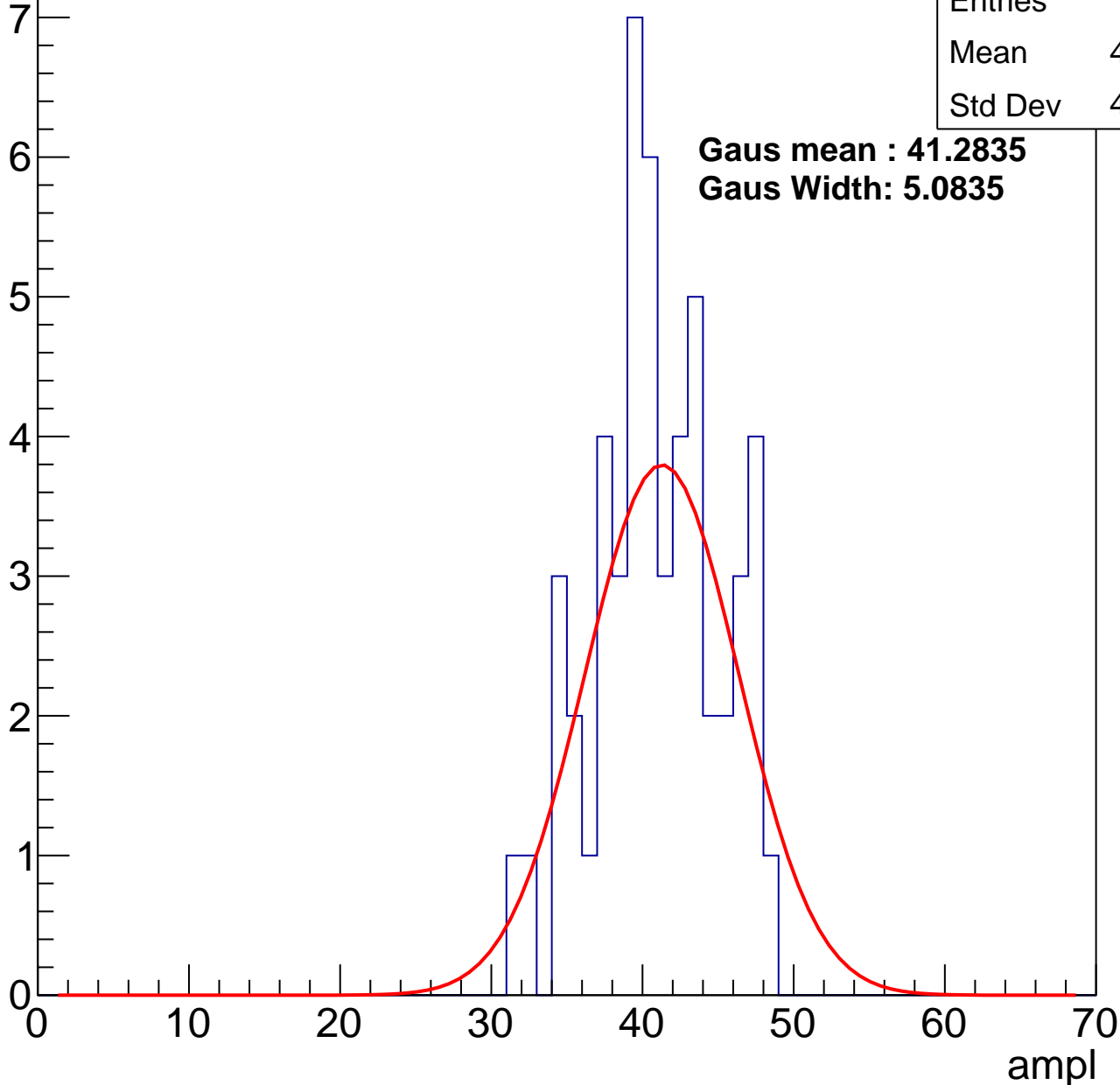
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	40.46
Std Dev	4.107

Gaus mean : 41.2835

Gaus Width: 5.0835

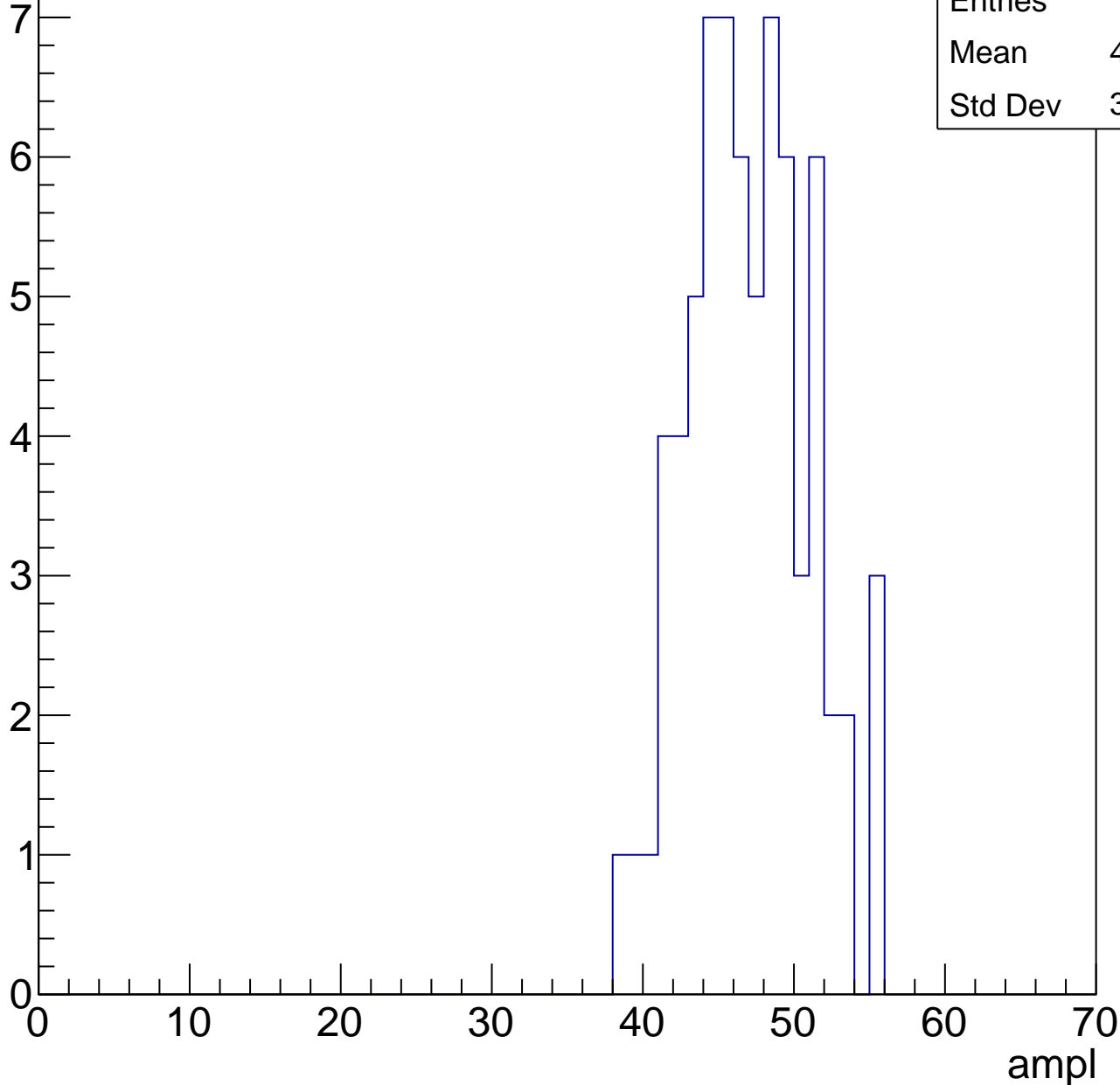


B1L101S, U15-ch120, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	46.56
Std Dev	3.908

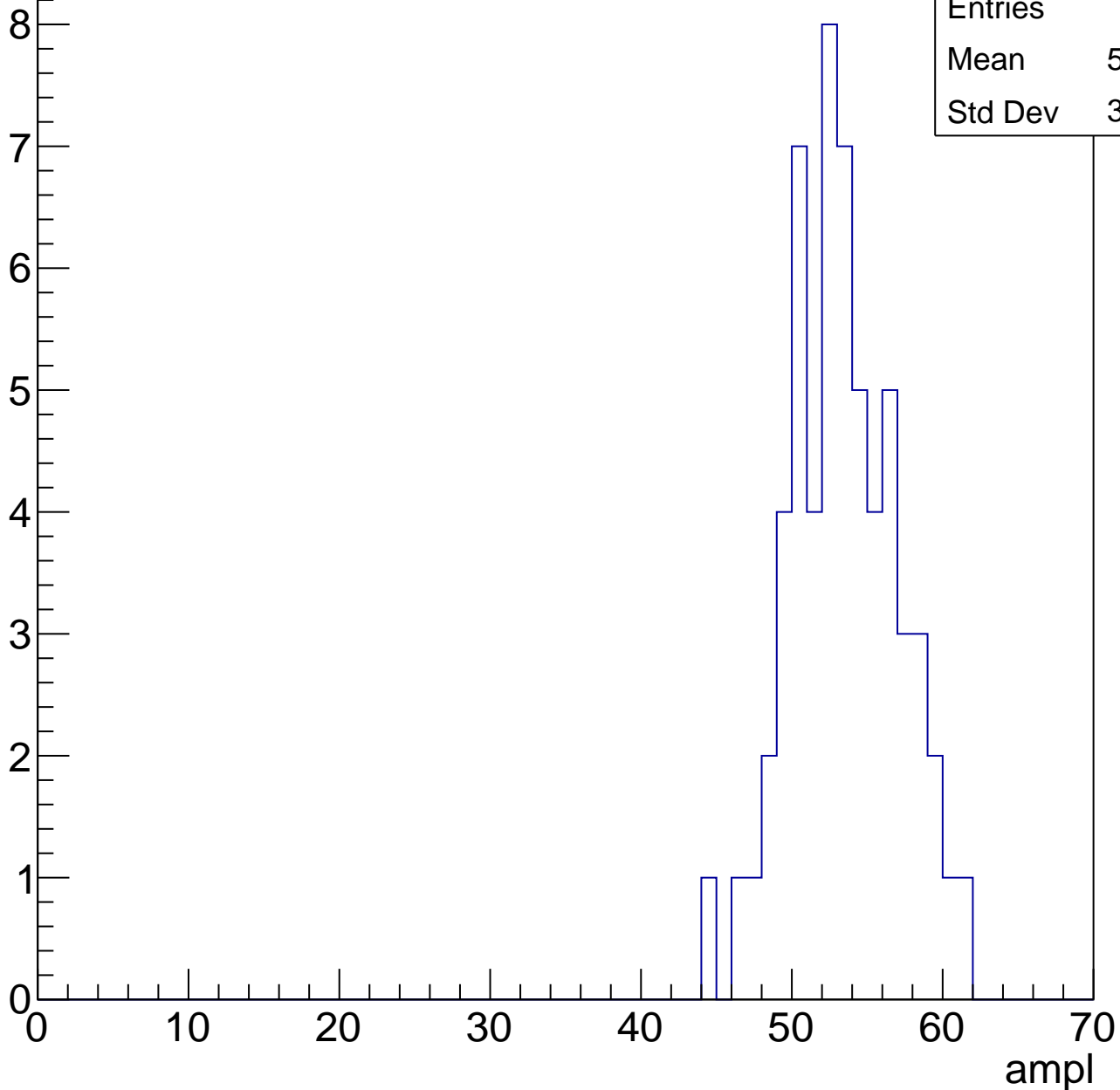


B1L101S, U15-ch120, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	52.95
Std Dev	3.544

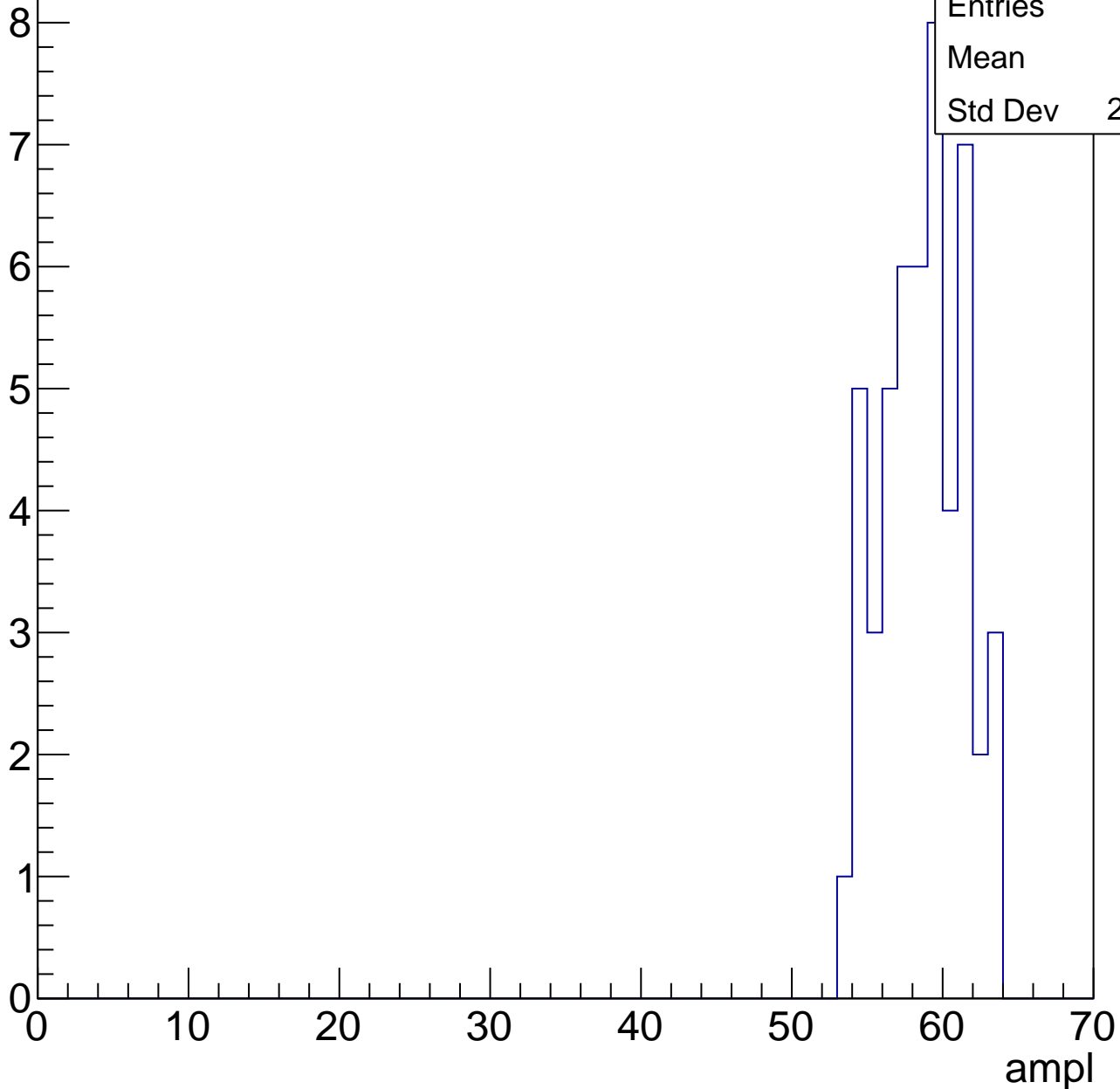


B1L101S, U15-ch120, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	58.2
Std Dev	2.646

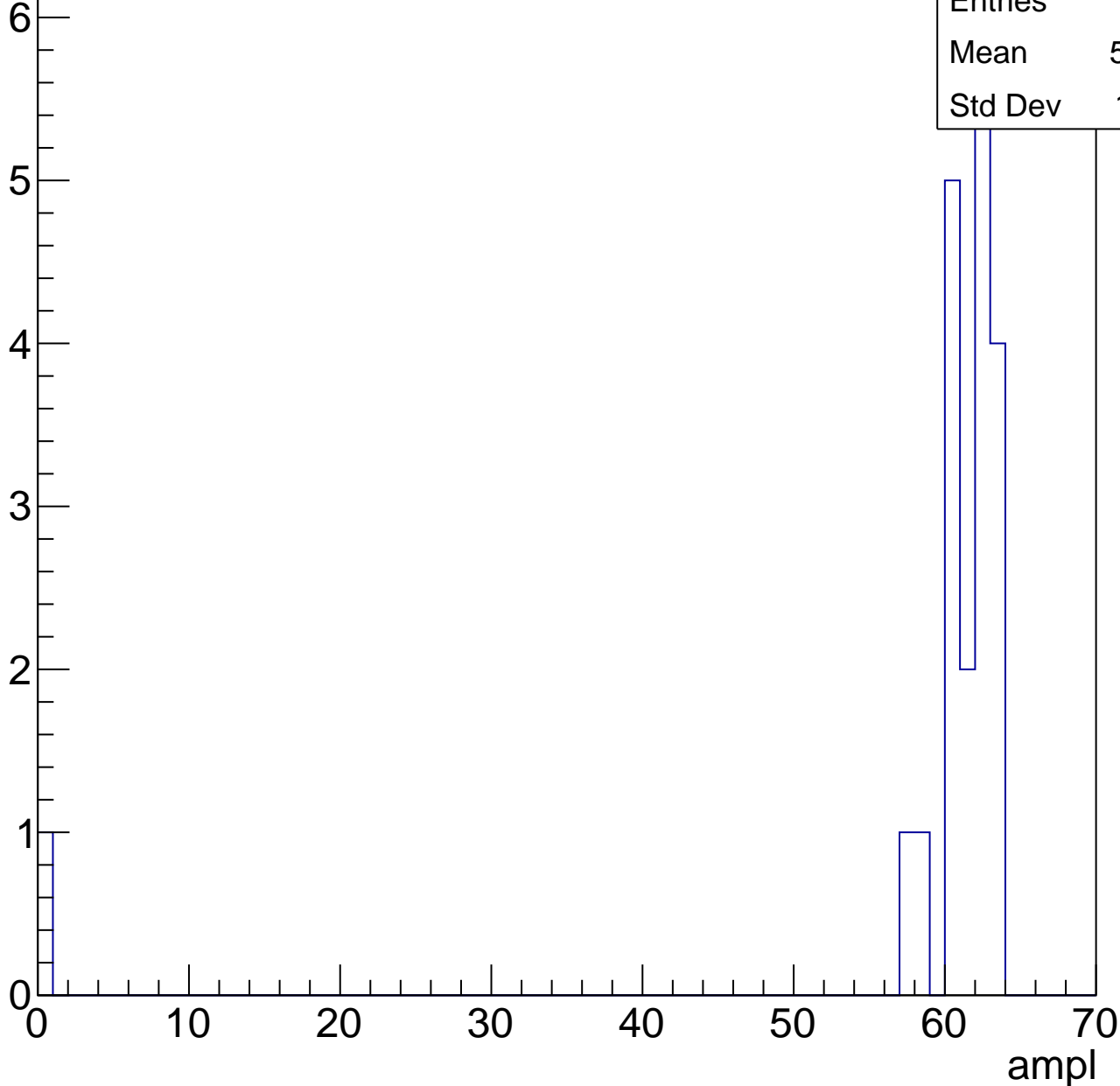


B1L101S, U15-ch120, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry

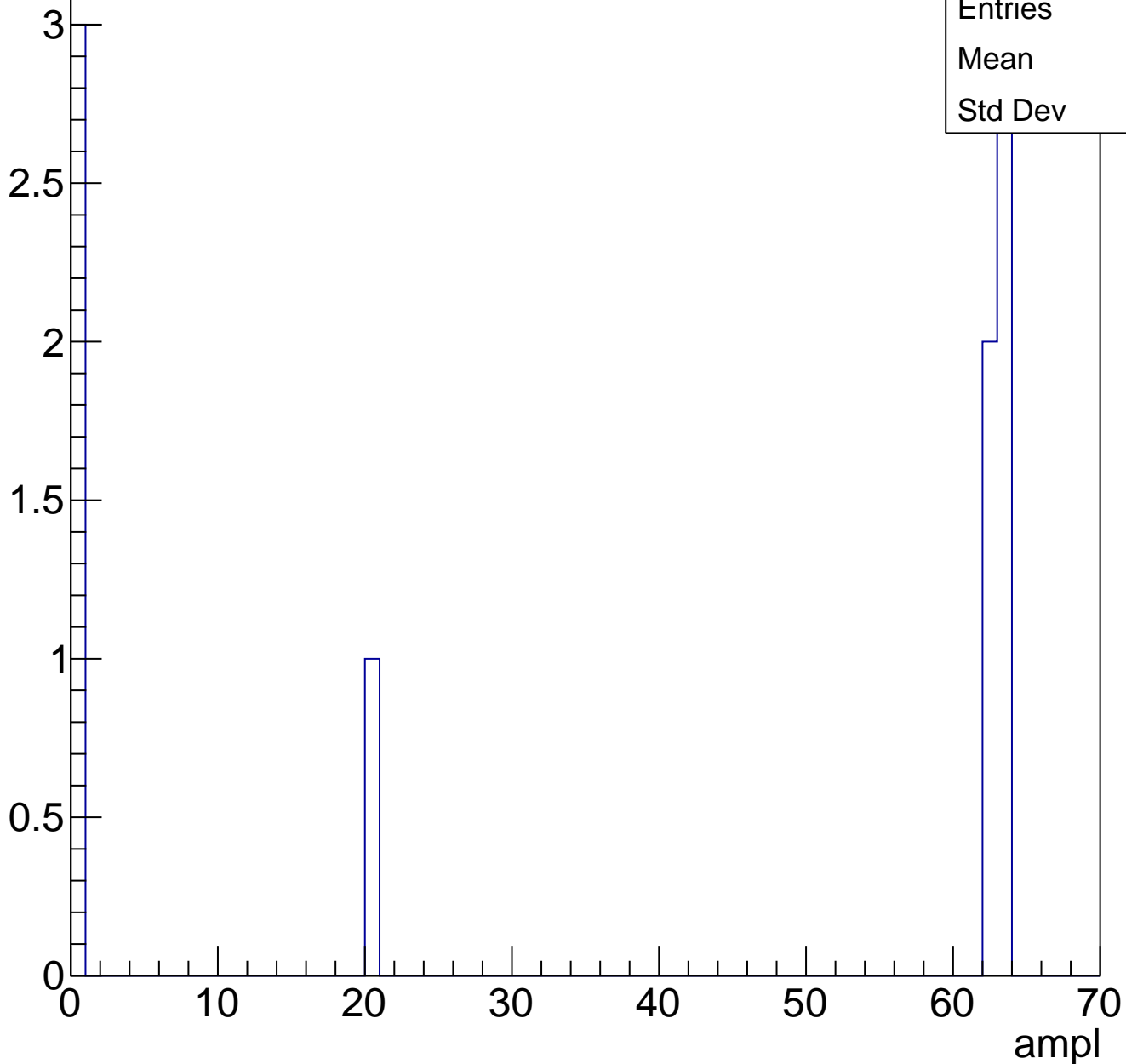
Entries	20
Mean	58.05
Std Dev	13.41



B1L101S, U15-ch120, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch121, adc0

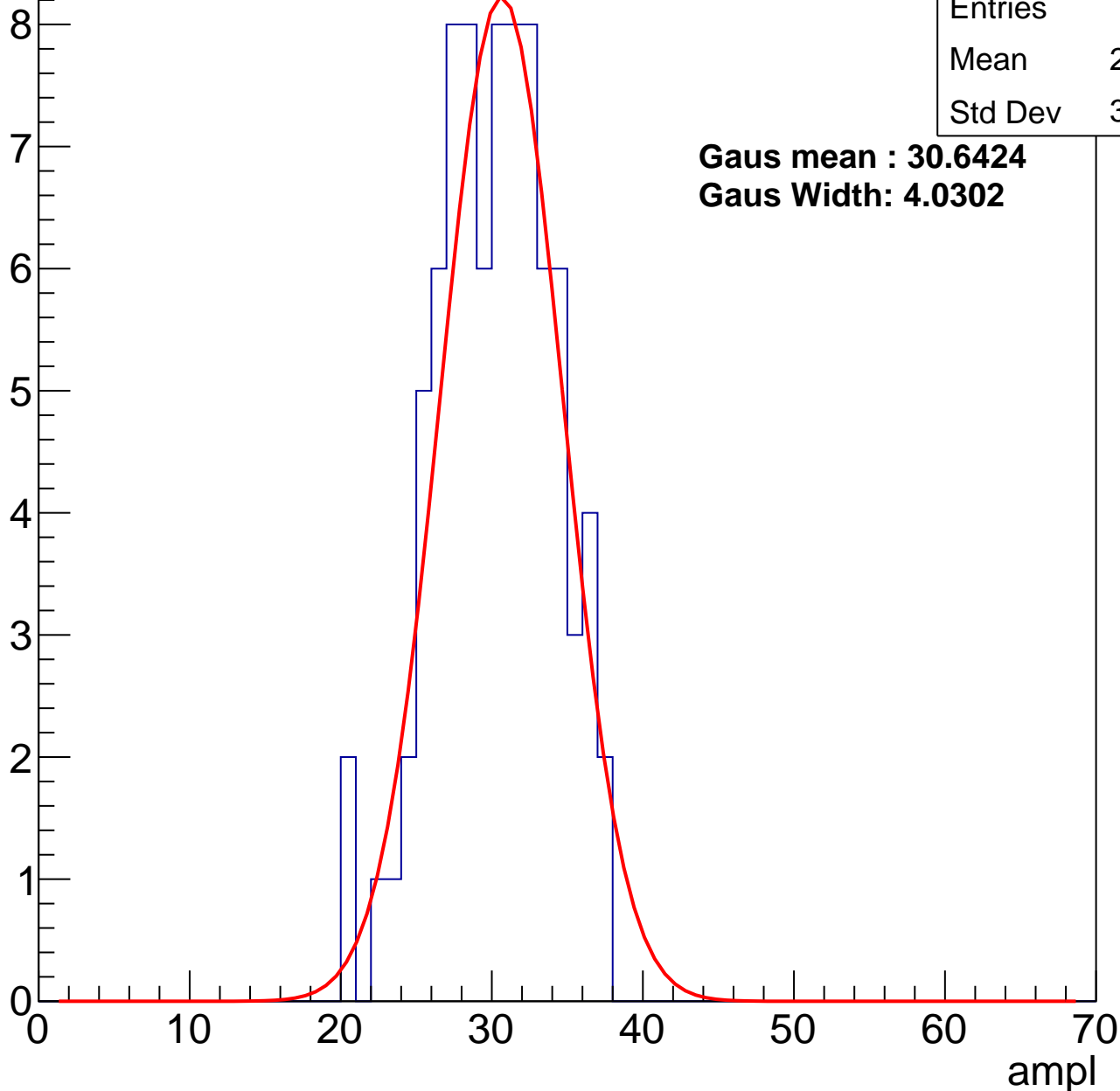
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	84
Mean	29.73
Std Dev	3.806

Gaus mean : 30.6424

Gaus Width: 4.0302



B1L101S, U15-ch121, adc1

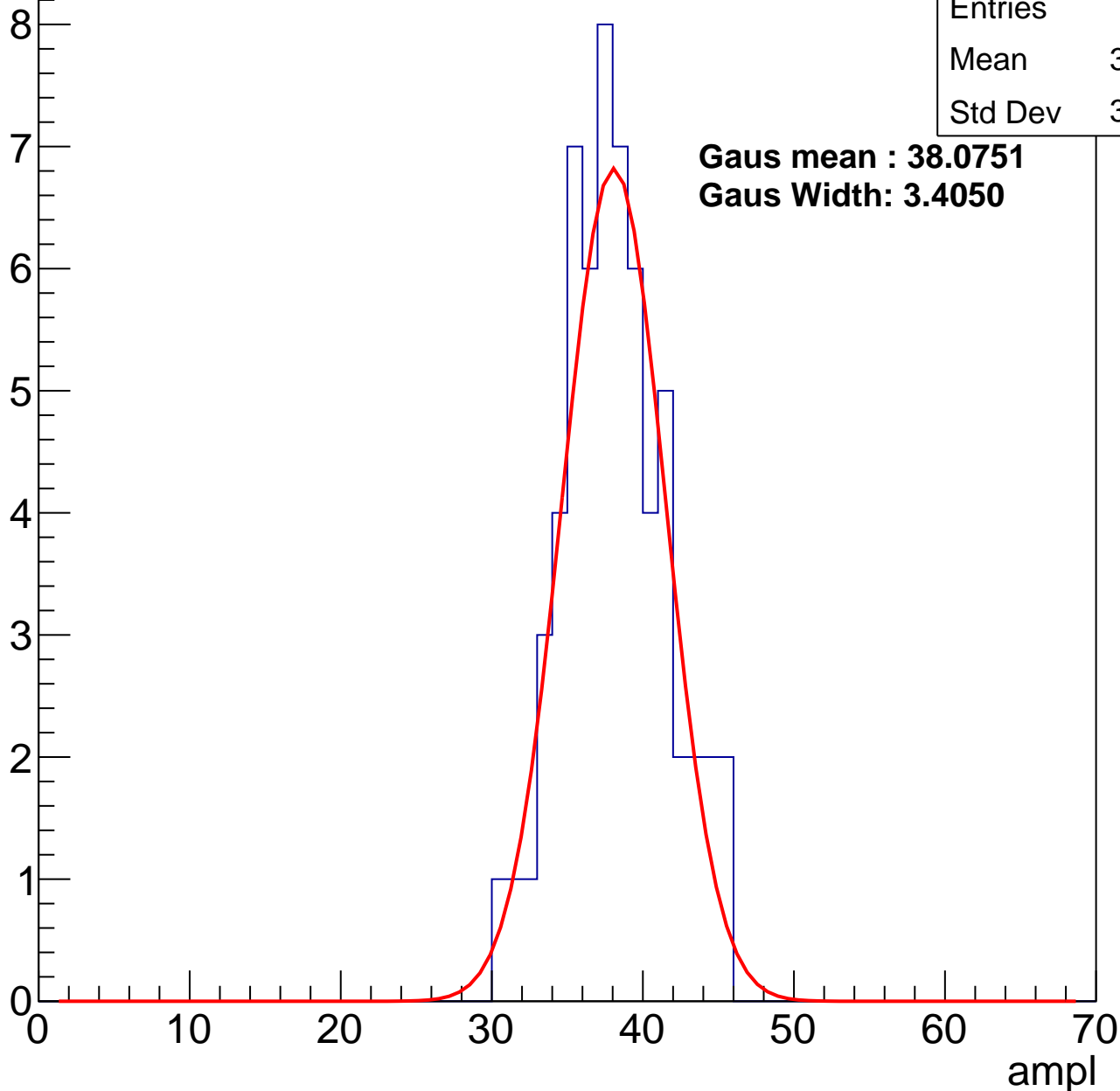
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	61
Mean	37.67
Std Dev	3.376

Gaus mean : 38.0751

Gaus Width: 3.4050



B1L101S, U15-ch121, adc2

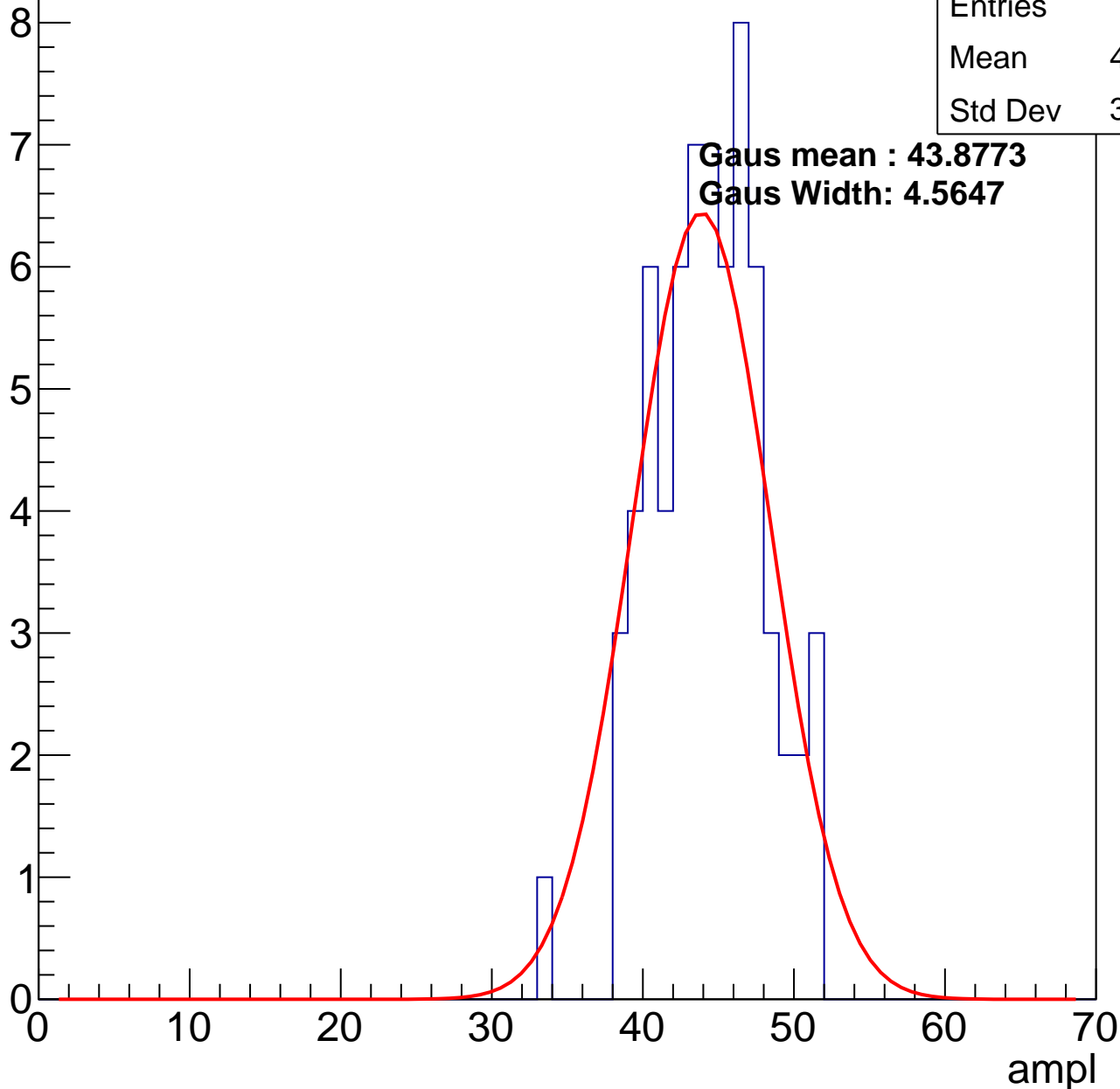
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	43.87
Std Dev	3.638

Gaus mean : 43.8773

Gaus Width: 4.5647

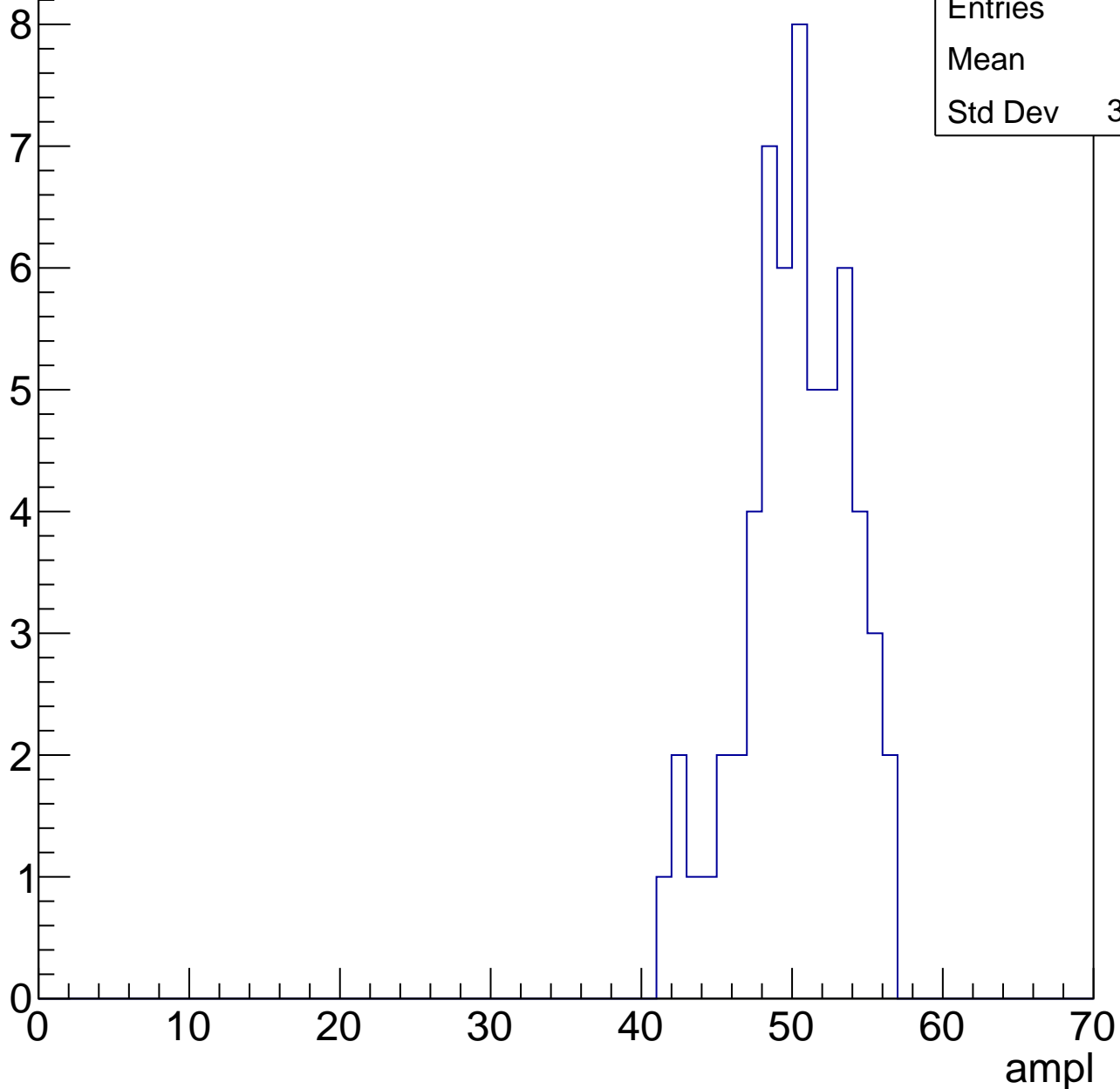


B1L101S, U15-ch121, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	59
Mean	49.8
Std Dev	3.526

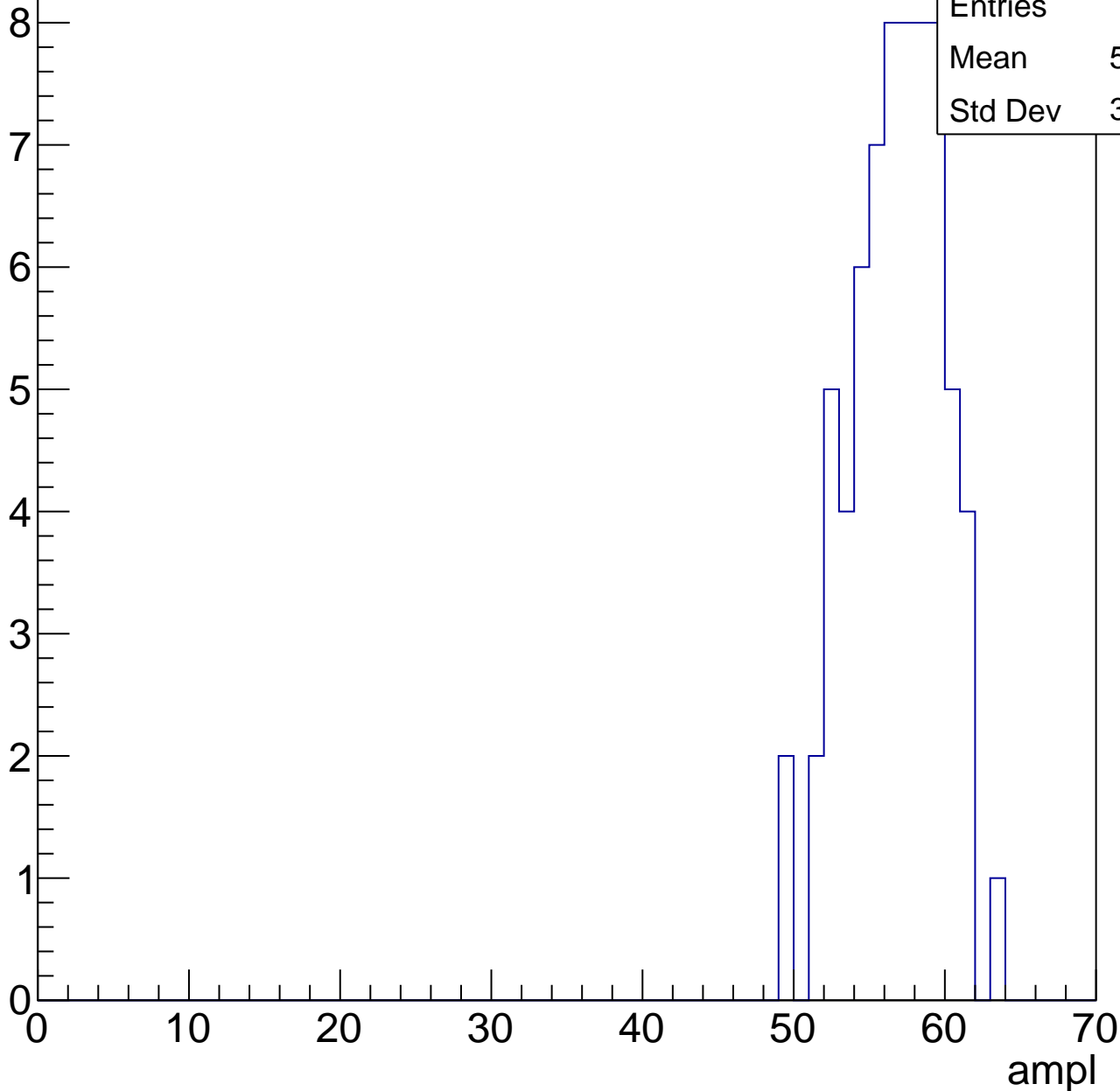


B1L101S, U15-ch121, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	68
Mean	56.29
Std Dev	3.044

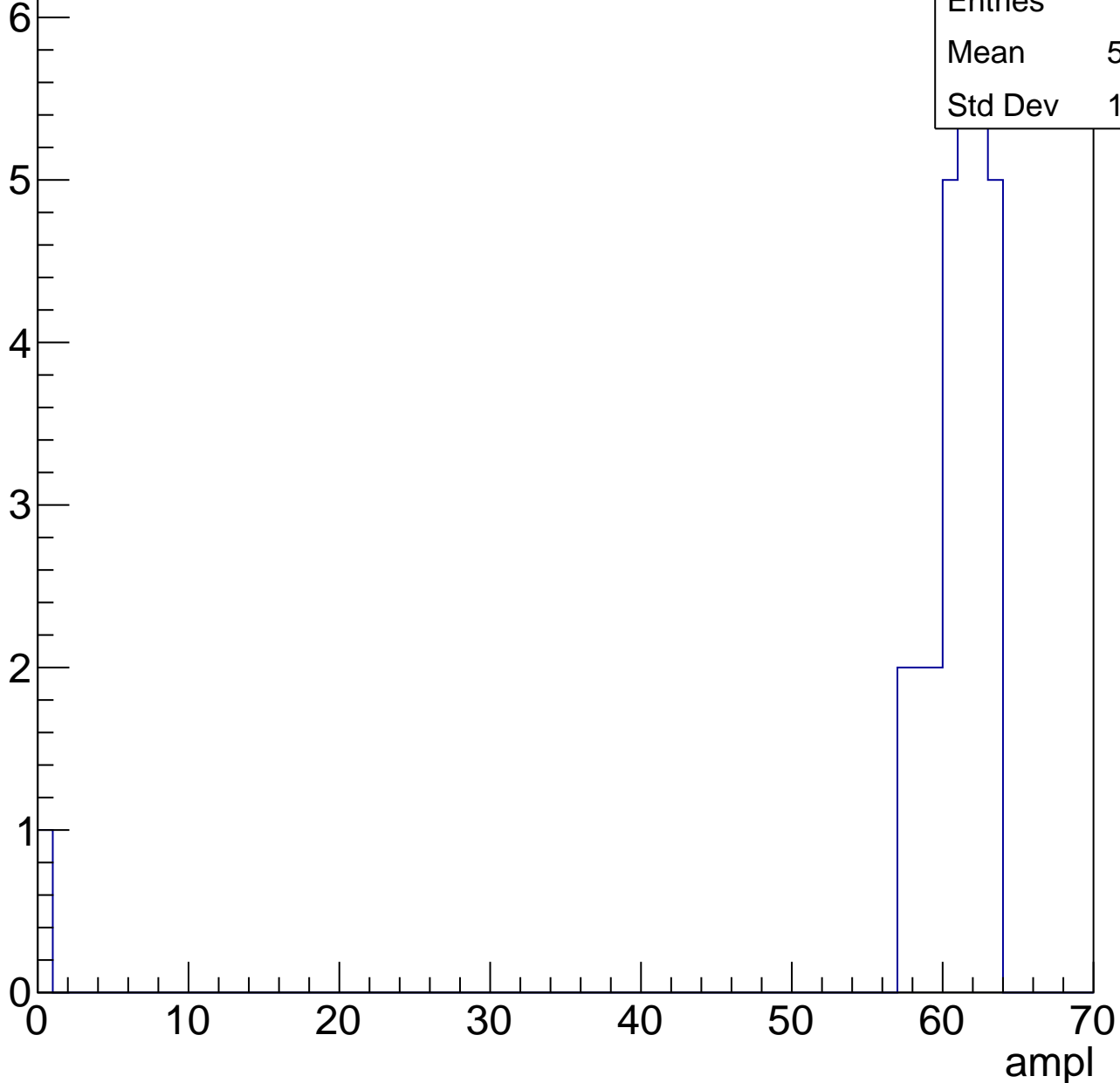


B1L101S, U15-ch121, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

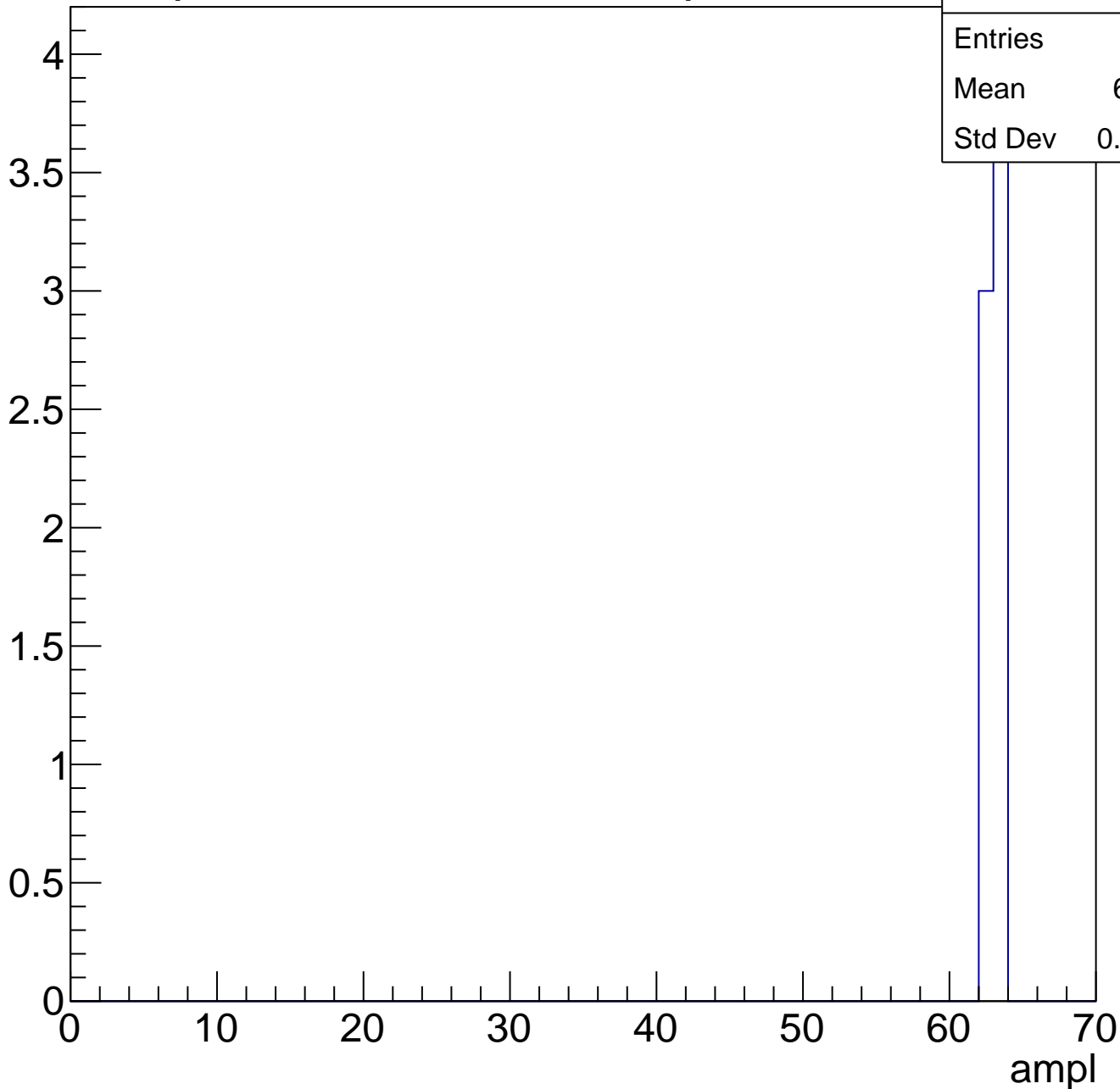
Entries	29
Mean	58.66
Std Dev	11.22



B1L101S, U15-ch121, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch121, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch122, adc0

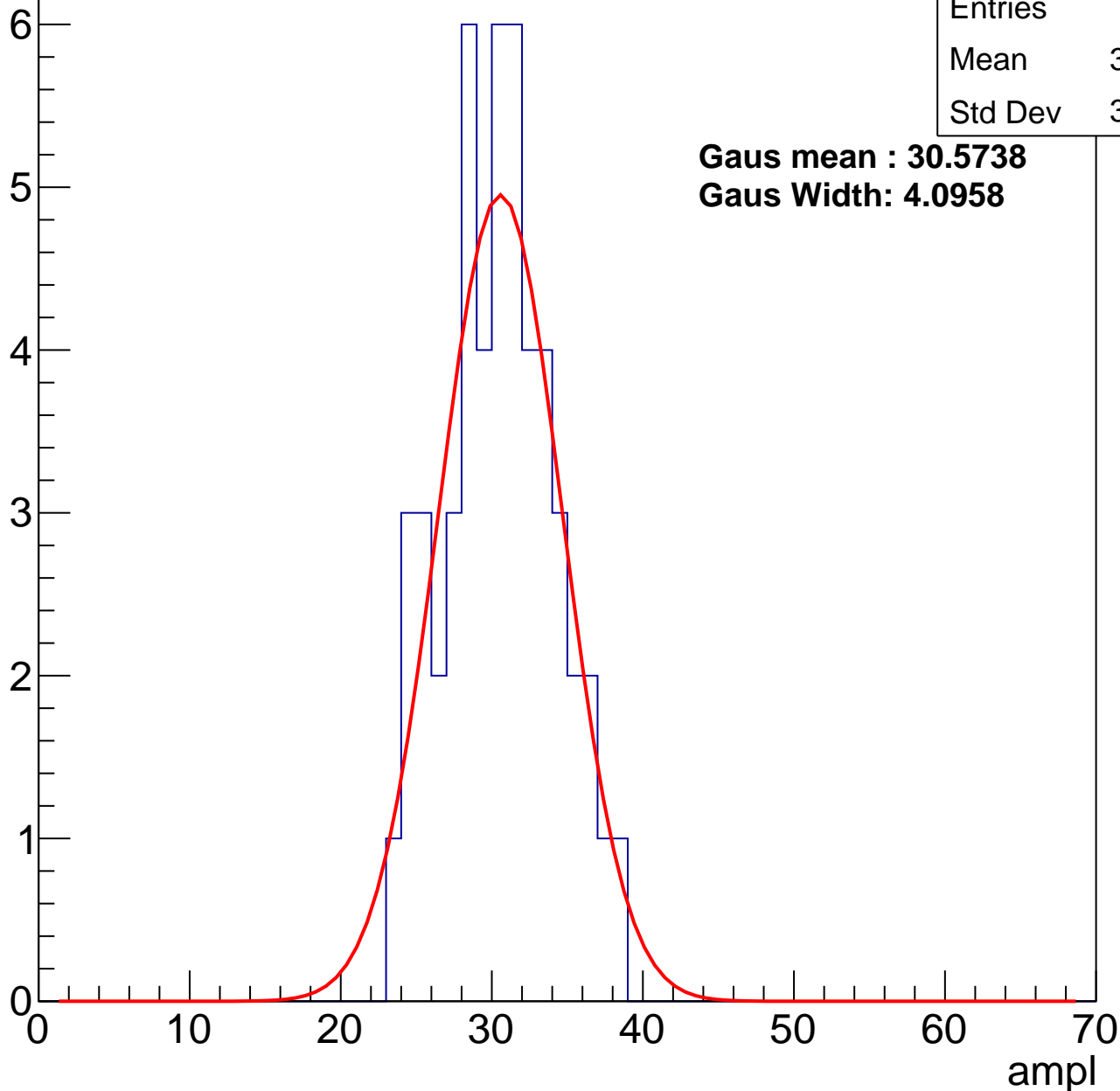
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	51
Mean	30.04
Std Dev	3.597

Gaus mean : 30.5738

Gaus Width: 4.0958



B1L101S, U15-ch122, adc1

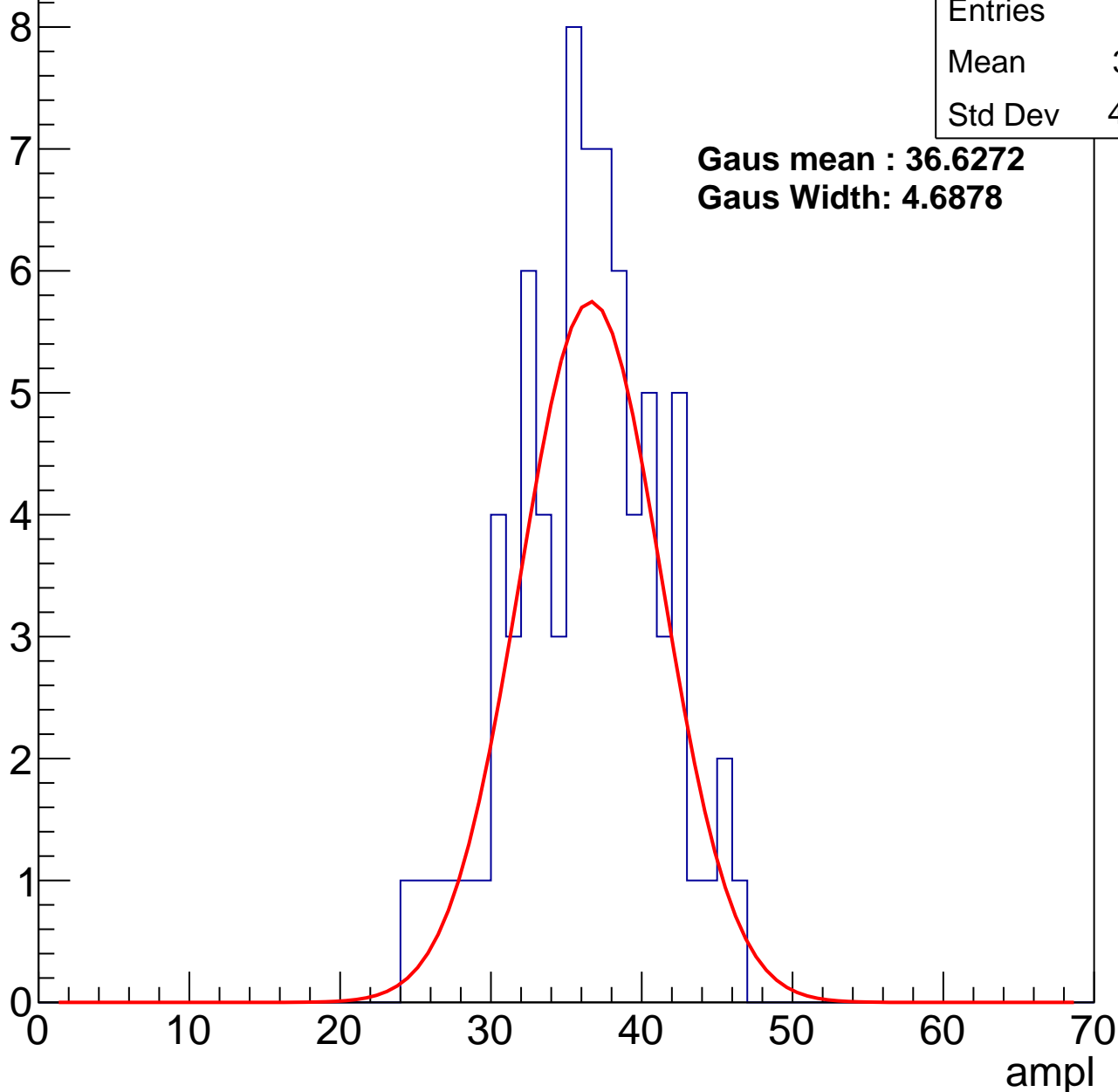
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.91
Std Dev	4.738

Gaus mean : 36.6272

Gaus Width: 4.6878



B1L101S, U15-ch122, adc2

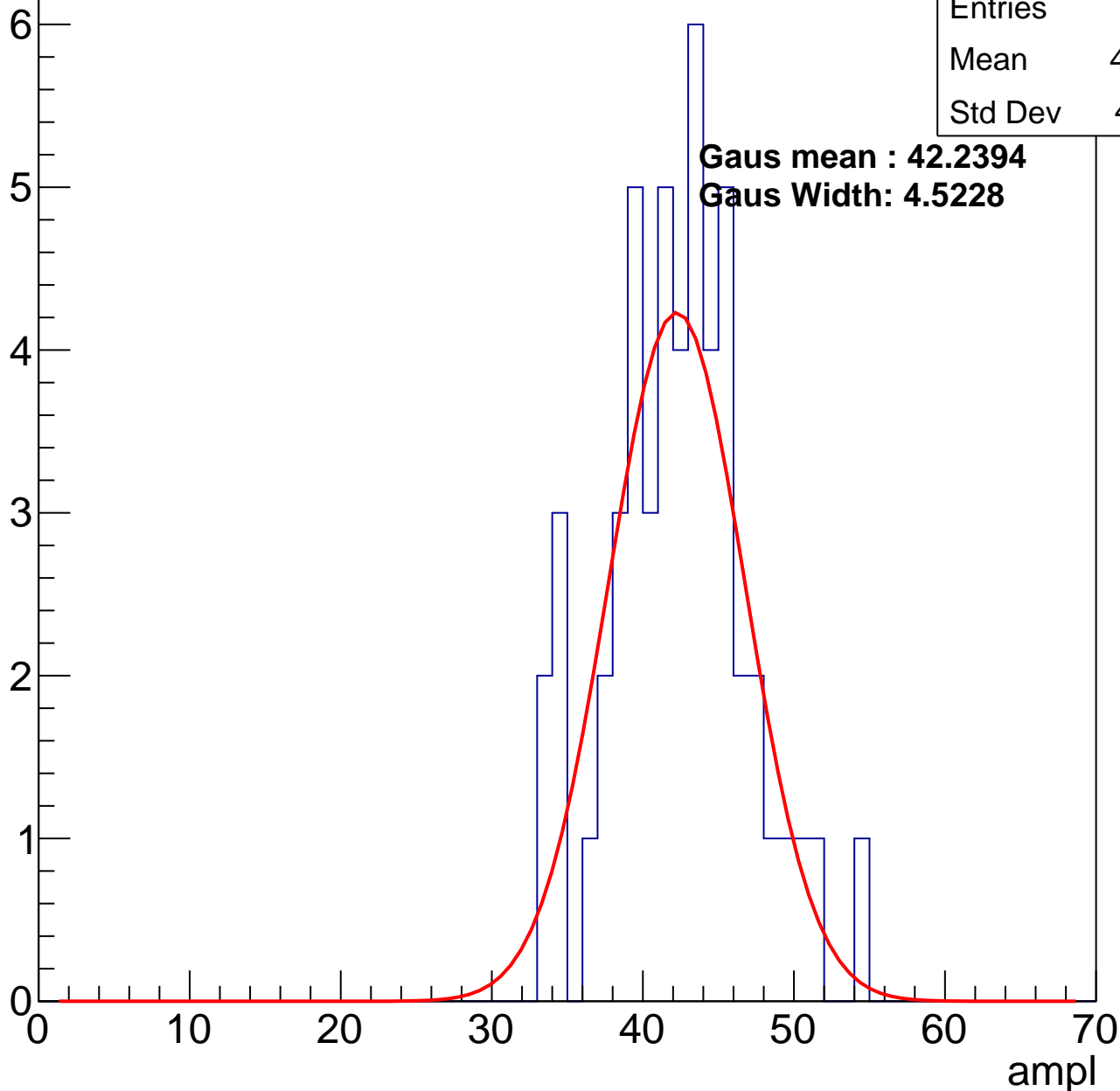
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	41.87
Std Dev	4.541

Gaus mean : 42.2394

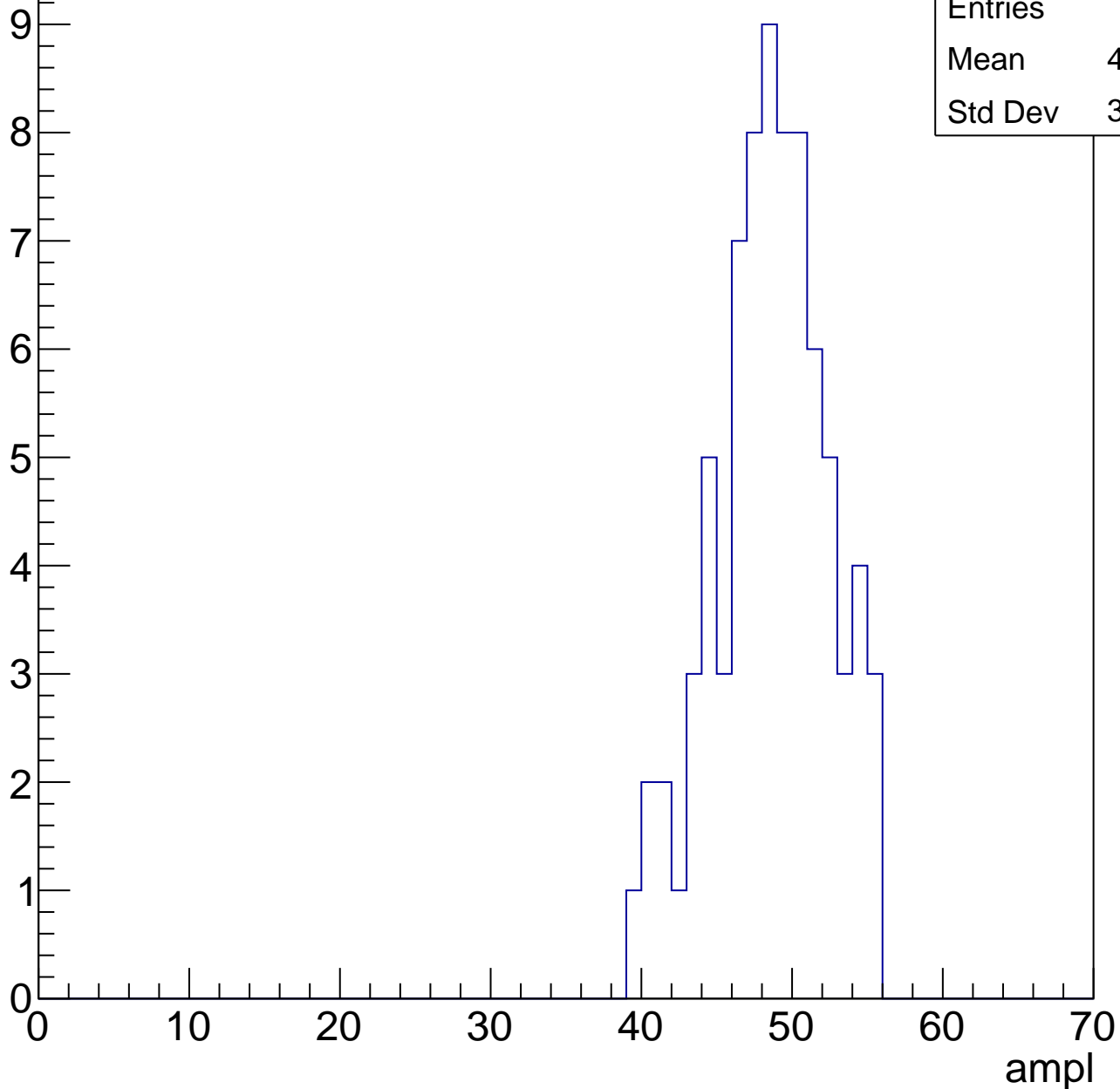
Gaus Width: 4.5228



B1L101S, U15-ch122, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry



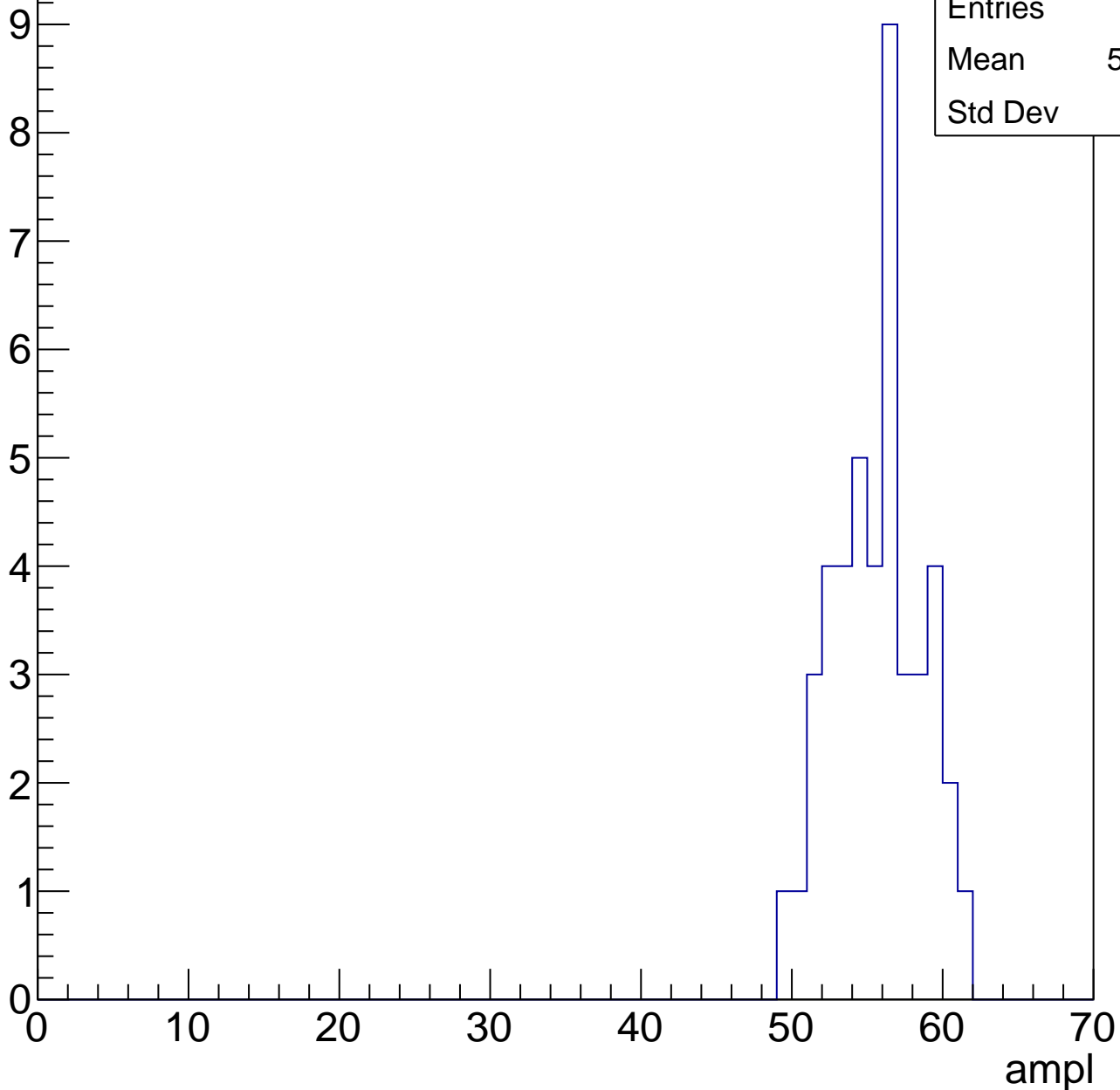
Entries	78
Mean	48.14
Std Dev	3.754

B1L101S, U15-ch122, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

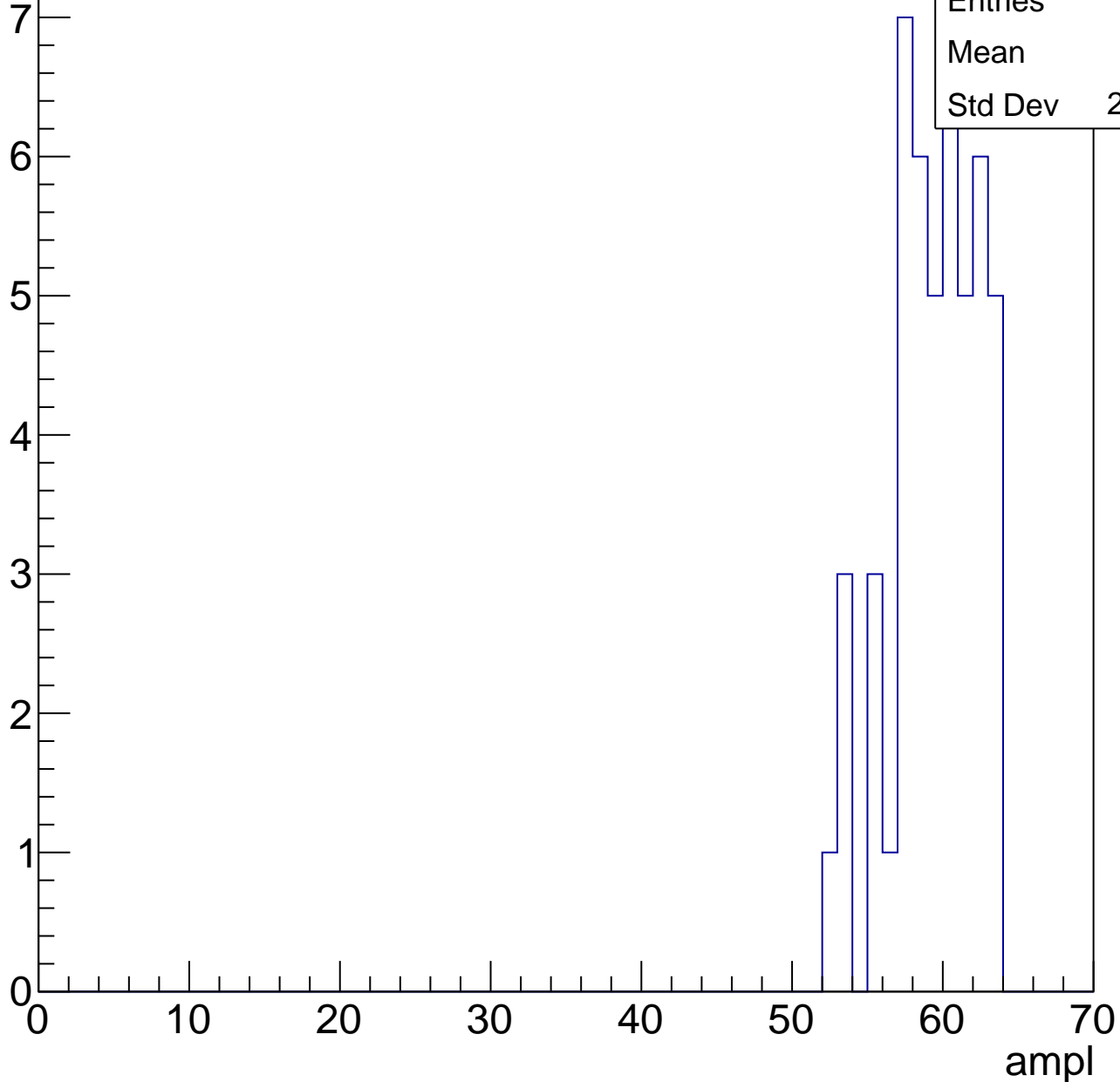
Entries	44
Mean	55.18
Std Dev	2.87



B1L101S, U15-ch122, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

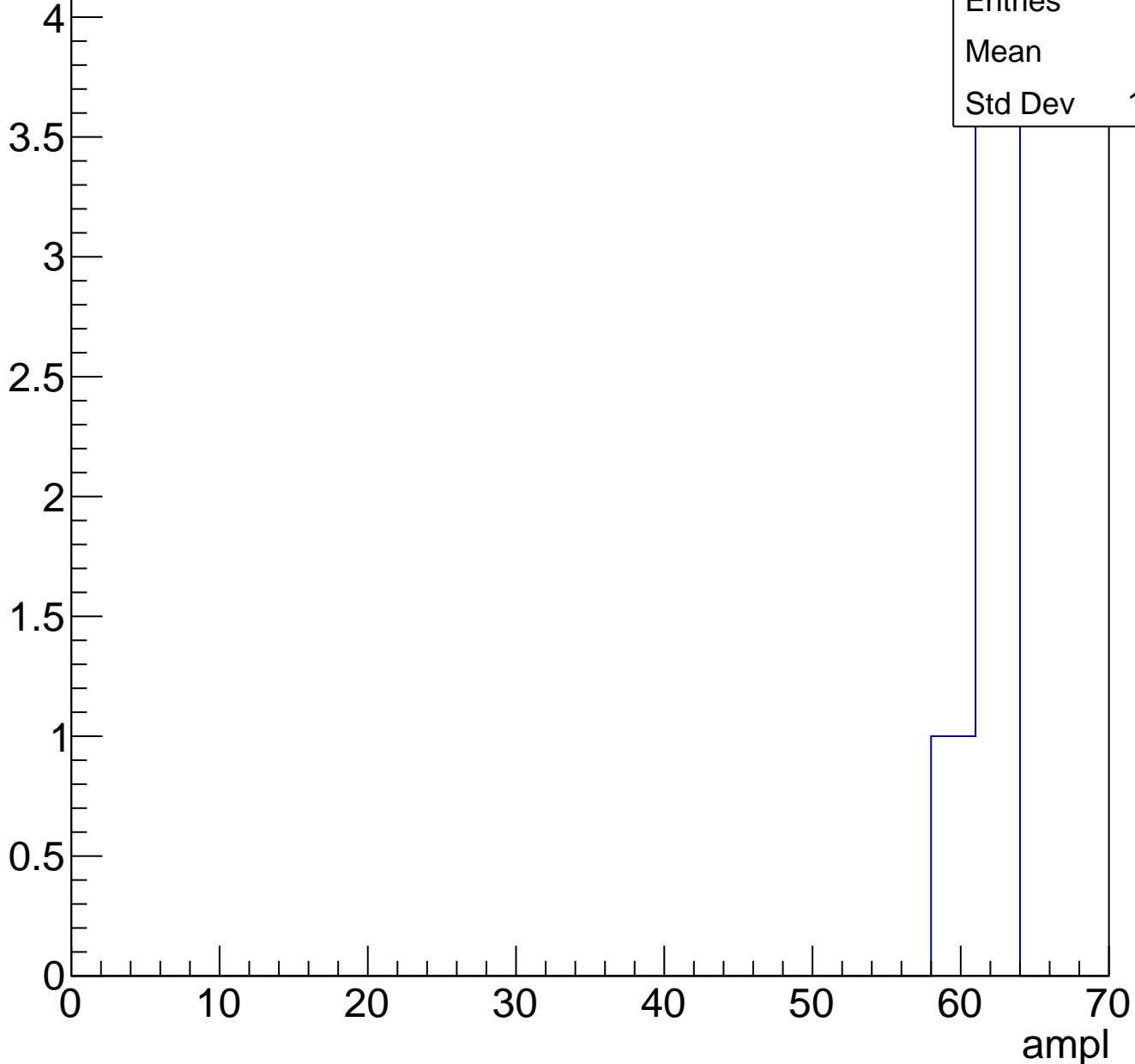


Entries	49
Mean	58.9
Std Dev	2.887

B1L101S, U15-ch122, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch122, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch123, adc0

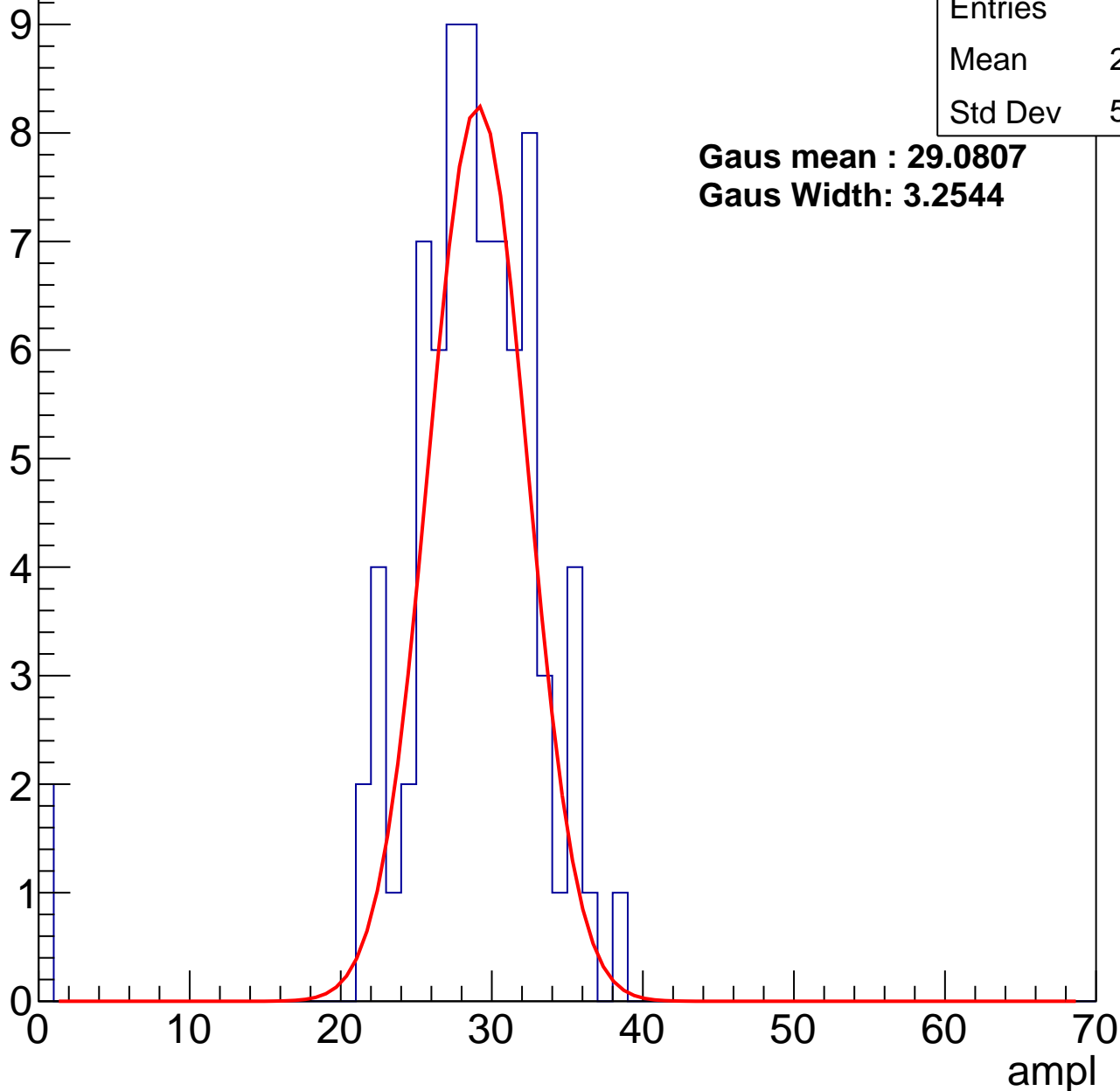
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	80
Mean	27.86
Std Dev	5.755

Gaus mean : 29.0807

Gaus Width: 3.2544



B1L101S, U15-ch123, adc1

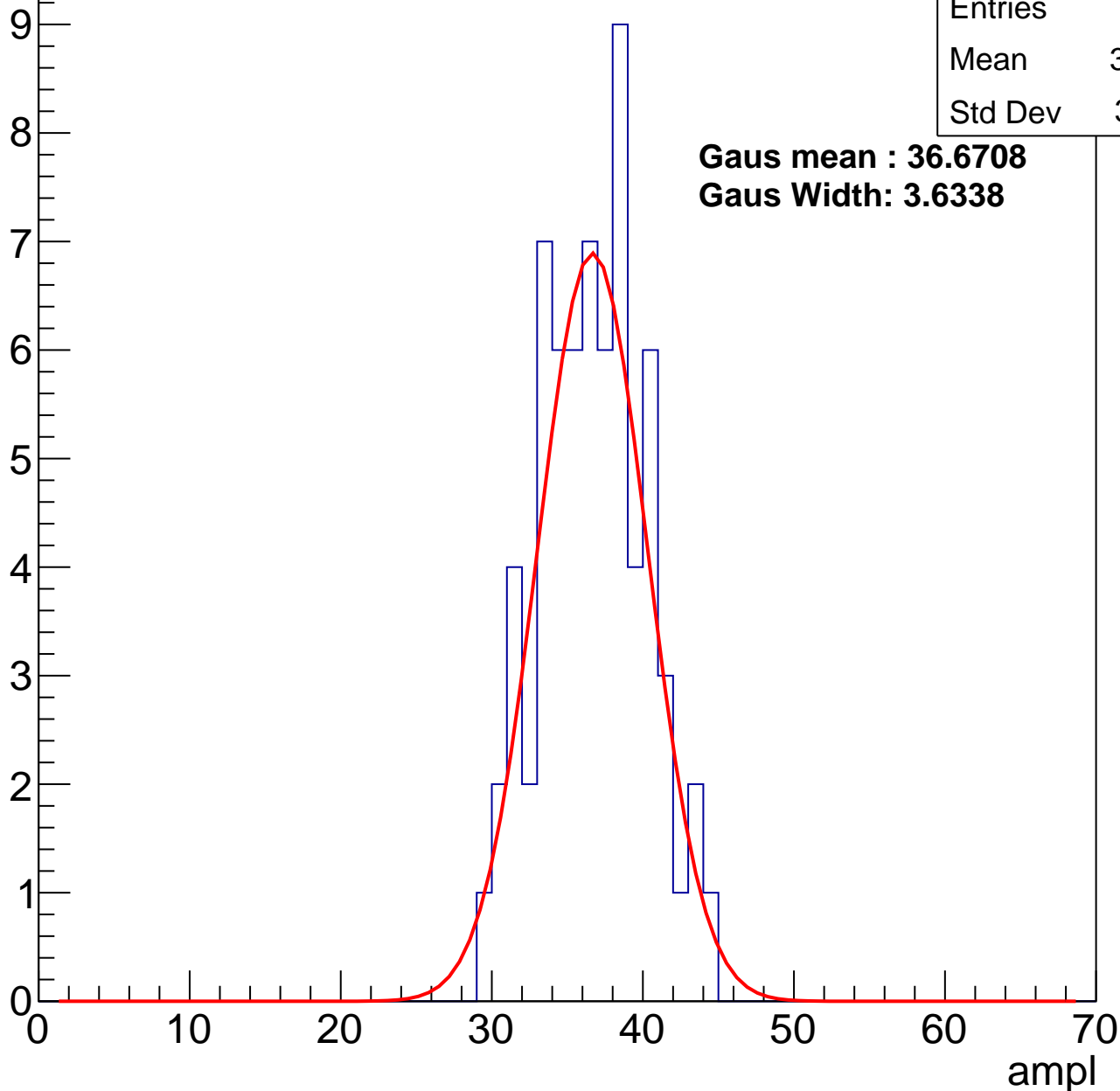
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	36.25
Std Dev	3.431

Gaus mean : 36.6708

Gaus Width: 3.6338



B1L101S, U15-ch123, adc2

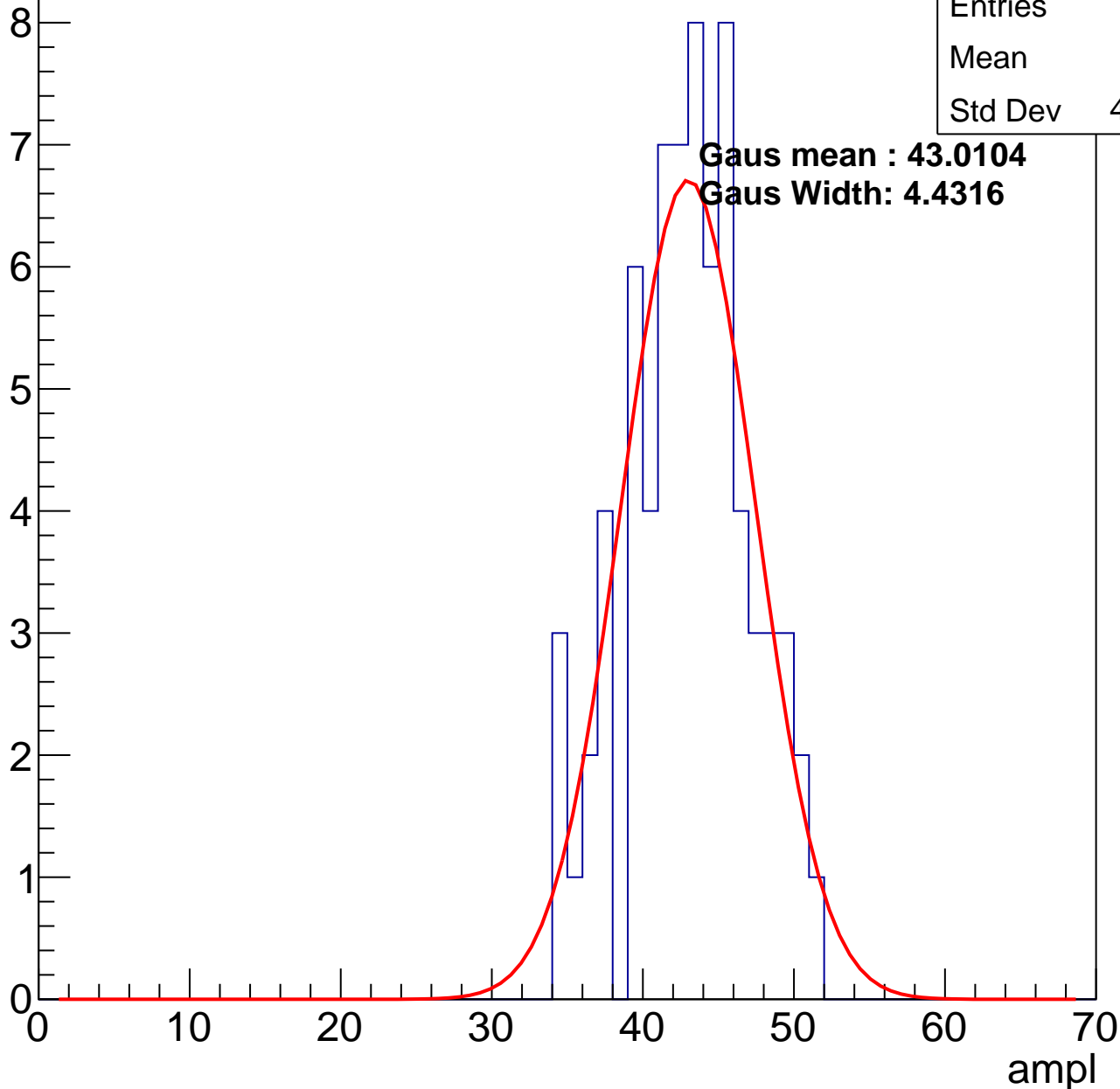
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	72
Mean	42.6
Std Dev	4.033

Gaus mean : 43.0104

Gaus Width: 4.4316

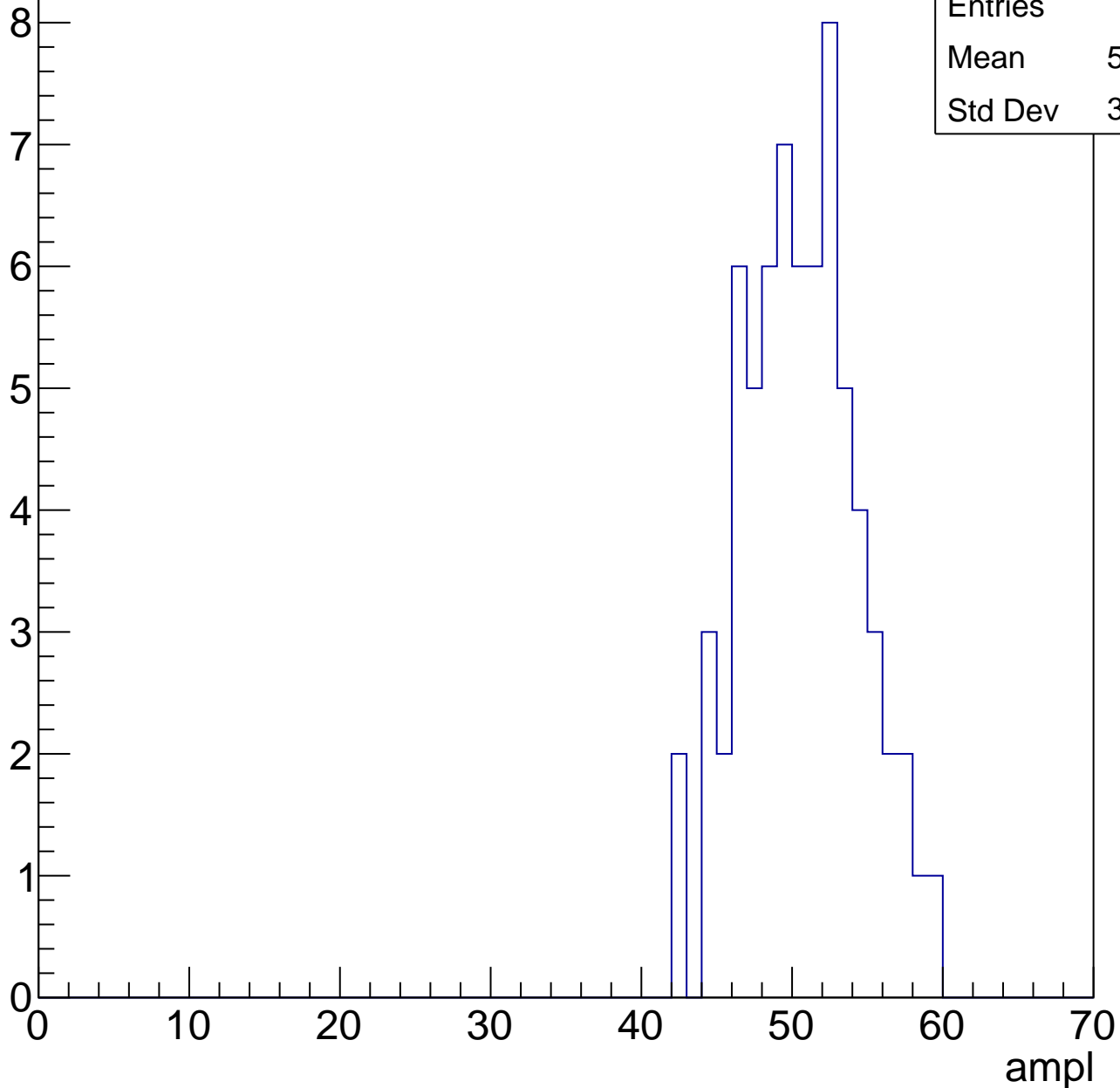


B1L101S, U15-ch123, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	50.13
Std Dev	3.795

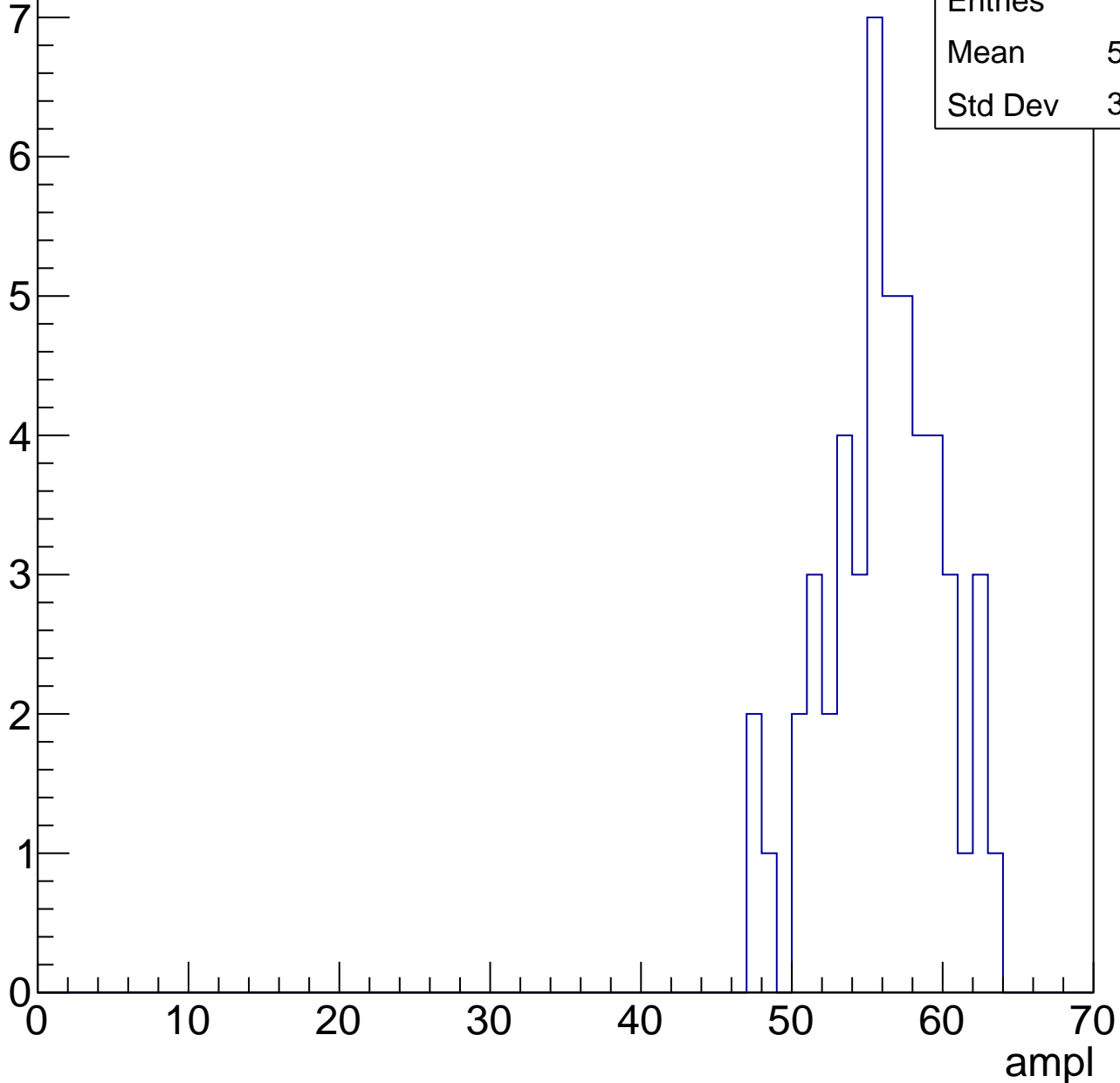


B1L101S, U15-ch123, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	55.62
Std Dev	3.847

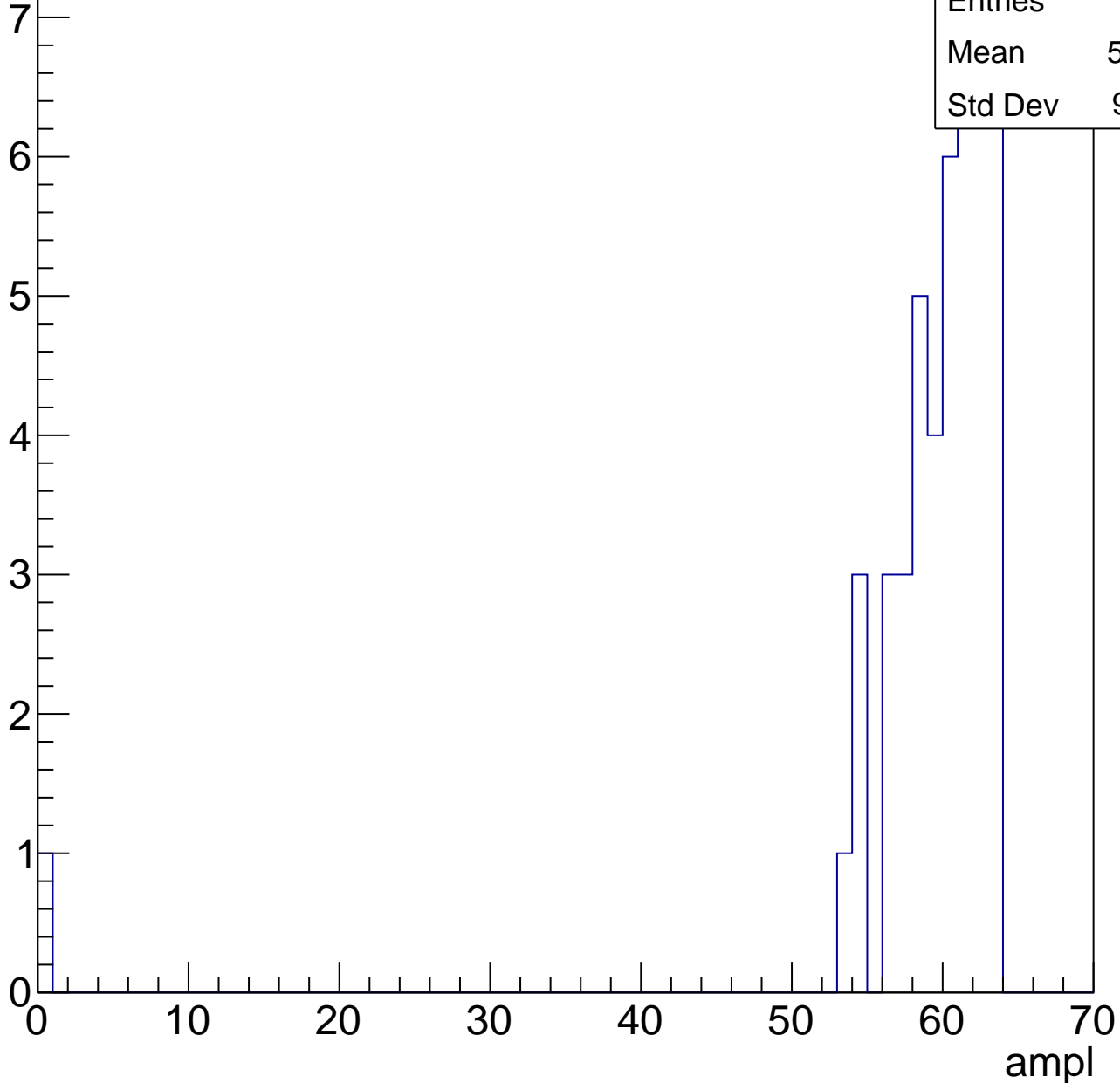


B1L101S, U15-ch123, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	47
Mean	58.34
Std Dev	9.021



B1L101S, U15-ch123, adc6

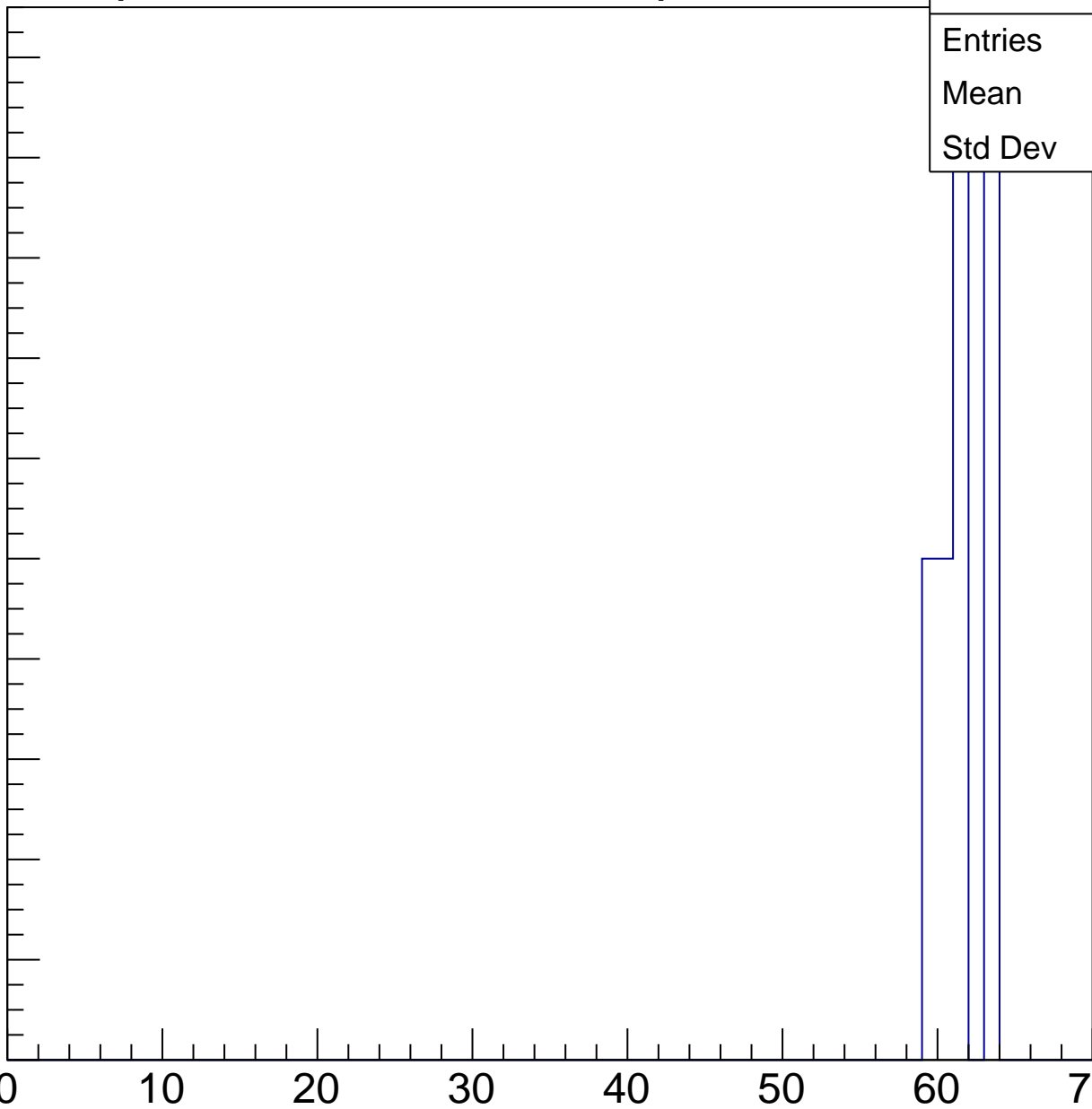
calib_packv5_042523_0143.root, FC#0, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.462

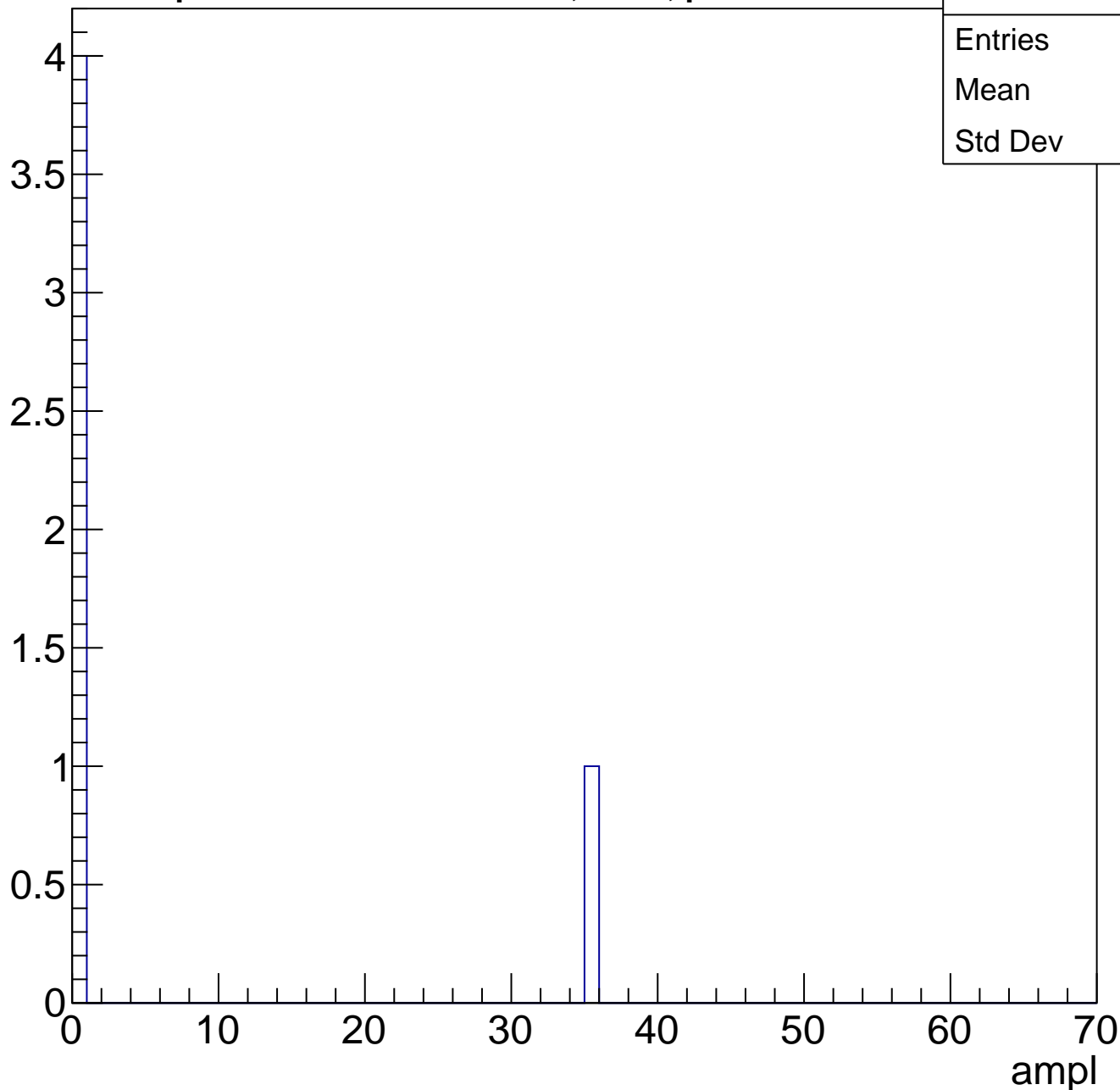
ampl



B1L101S, U15-ch123, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch124, adc0

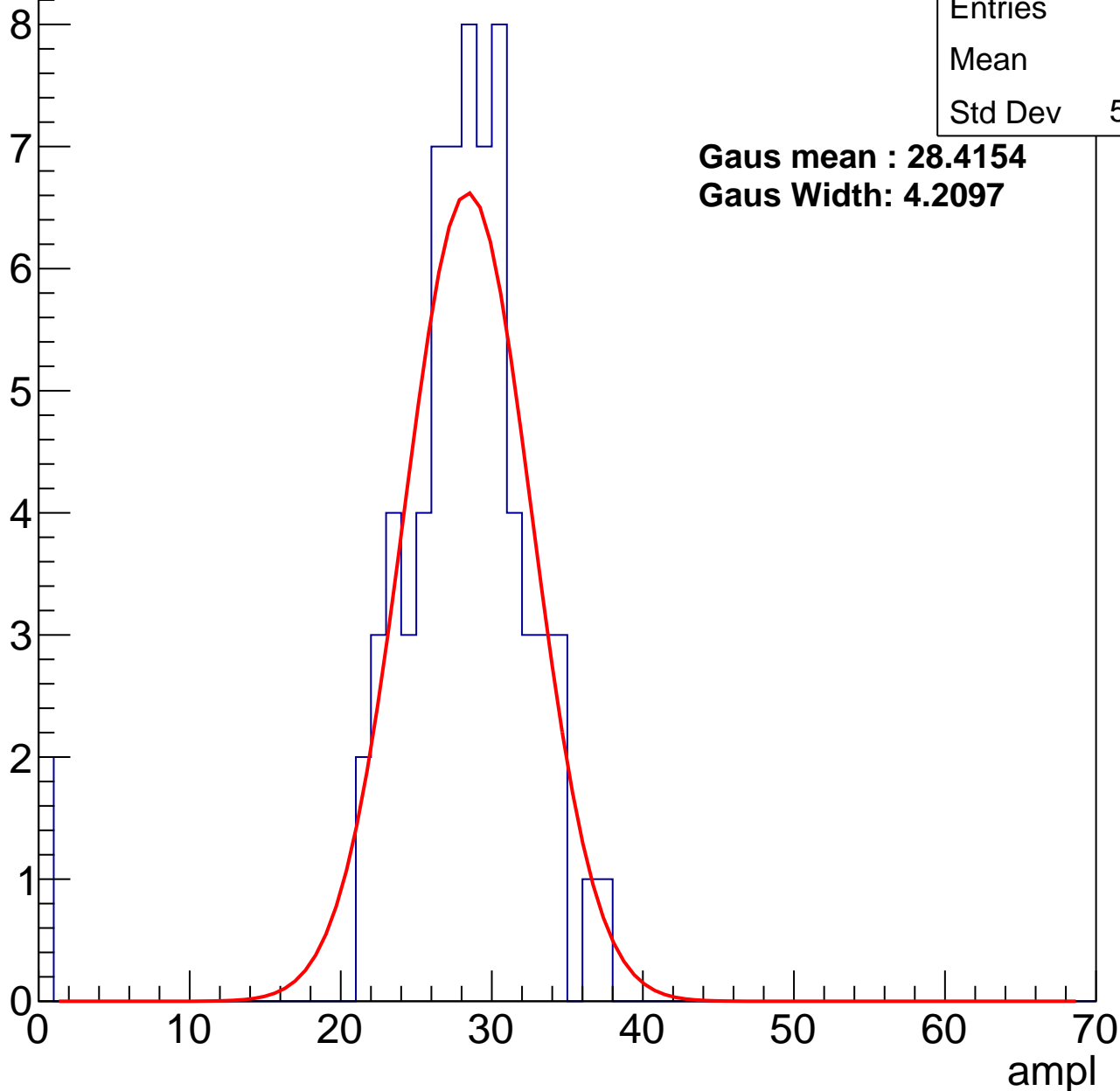
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	70
Mean	27.2
Std Dev	5.854

Gaus mean : 28.4154

Gaus Width: 4.2097



B1L101S, U15-ch124, adc1

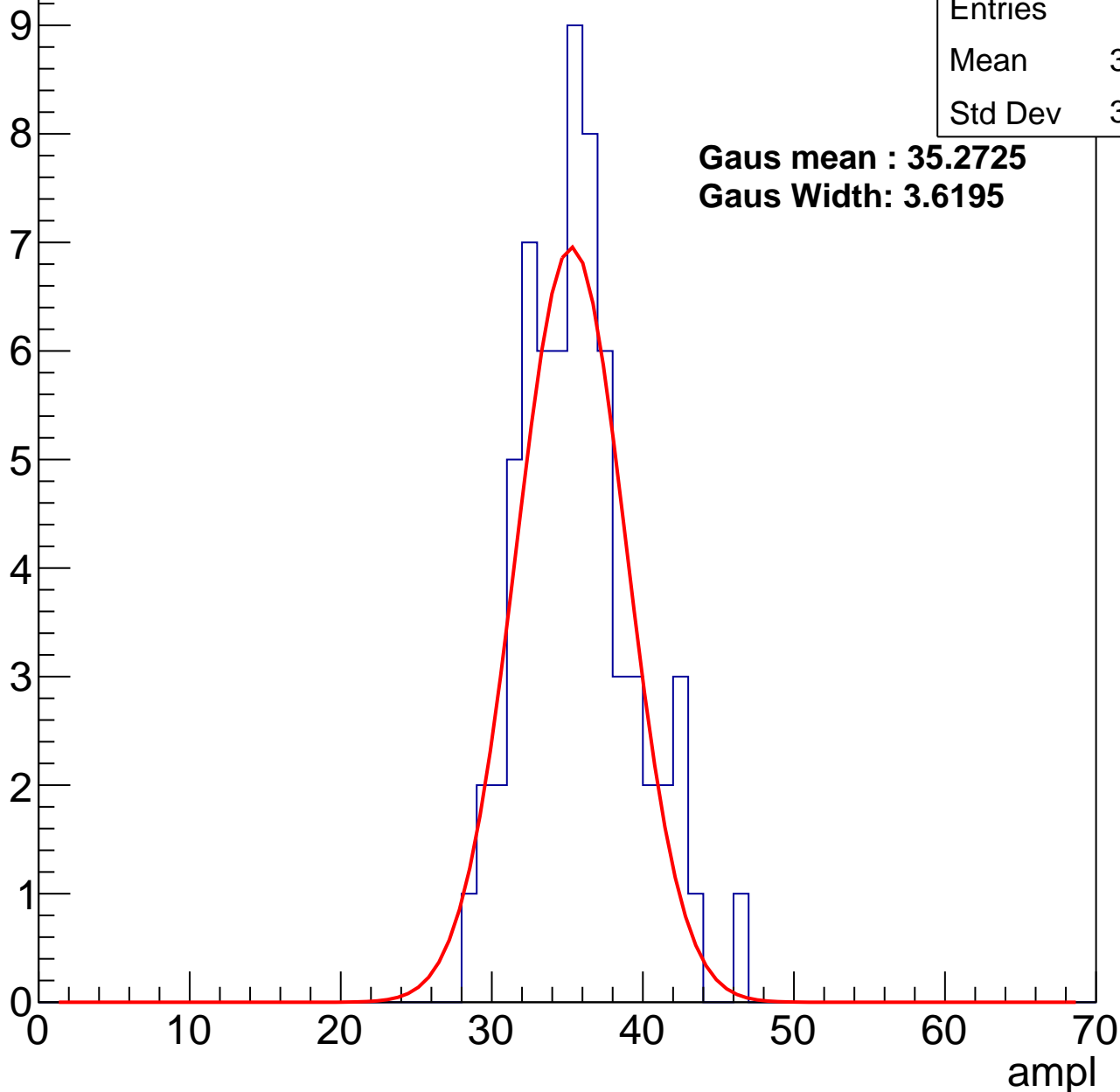
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	67
Mean	35.22
Std Dev	3.668

Gaus mean : 35.2725

Gaus Width: 3.6195



B1L101S, U15-ch124, adc2

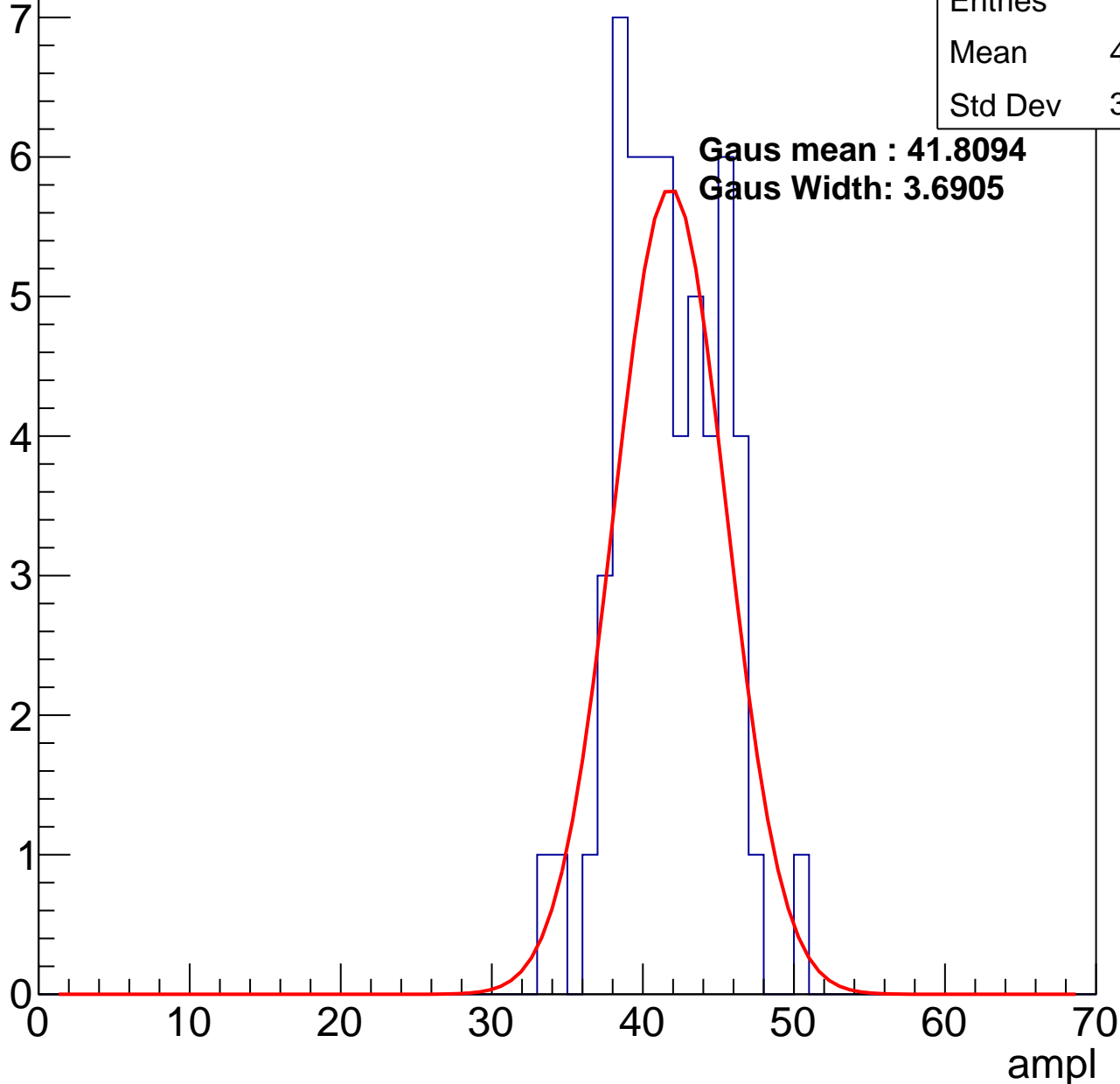
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	41.25
Std Dev	3.403

Gaus mean : 41.8094

Gaus Width: 3.6905

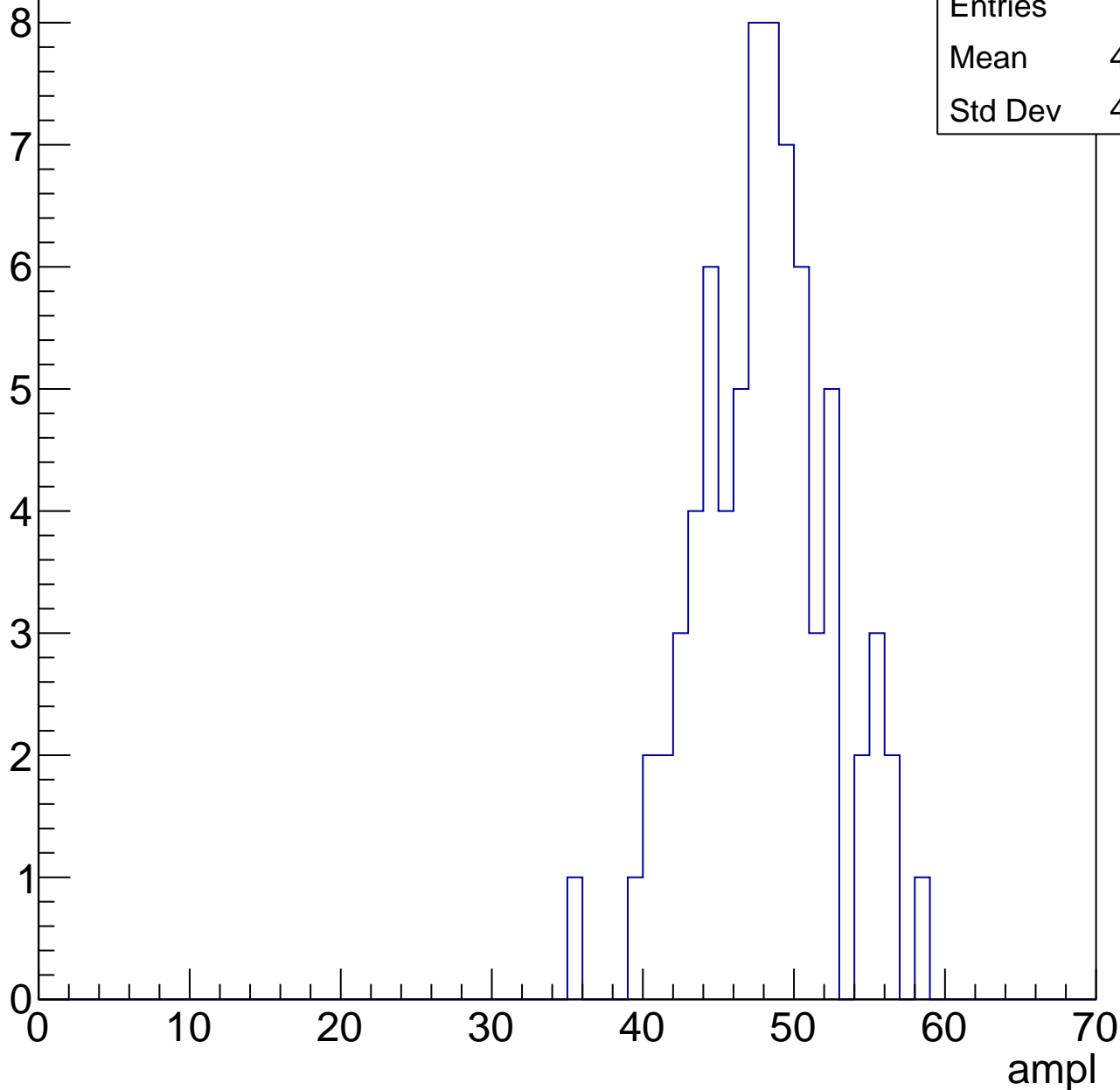


B1L101S, U15-ch124, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	47.49
Std Dev	4.395

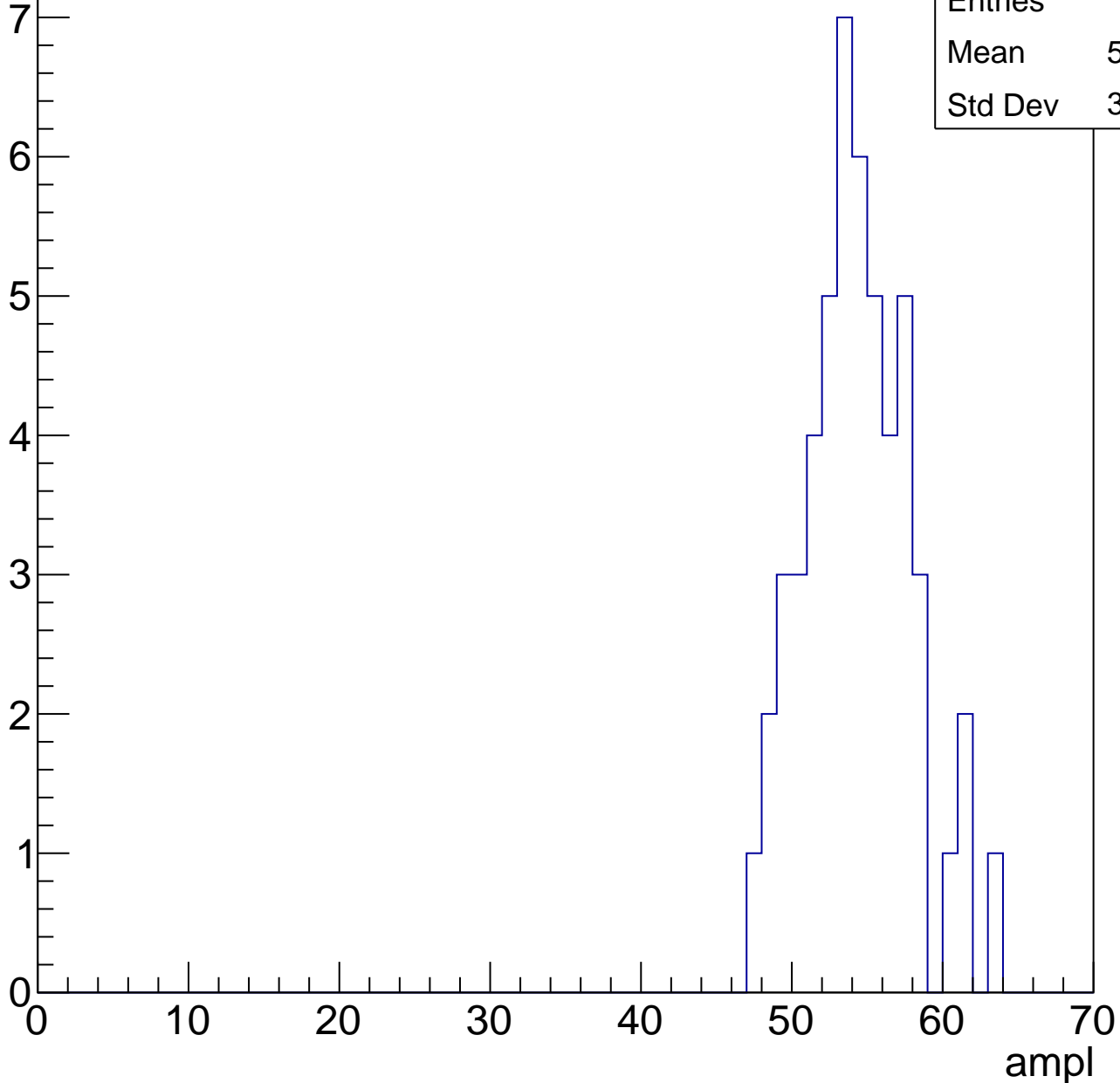


B1L101S, U15-ch124, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	52
Mean	53.88
Std Dev	3.495

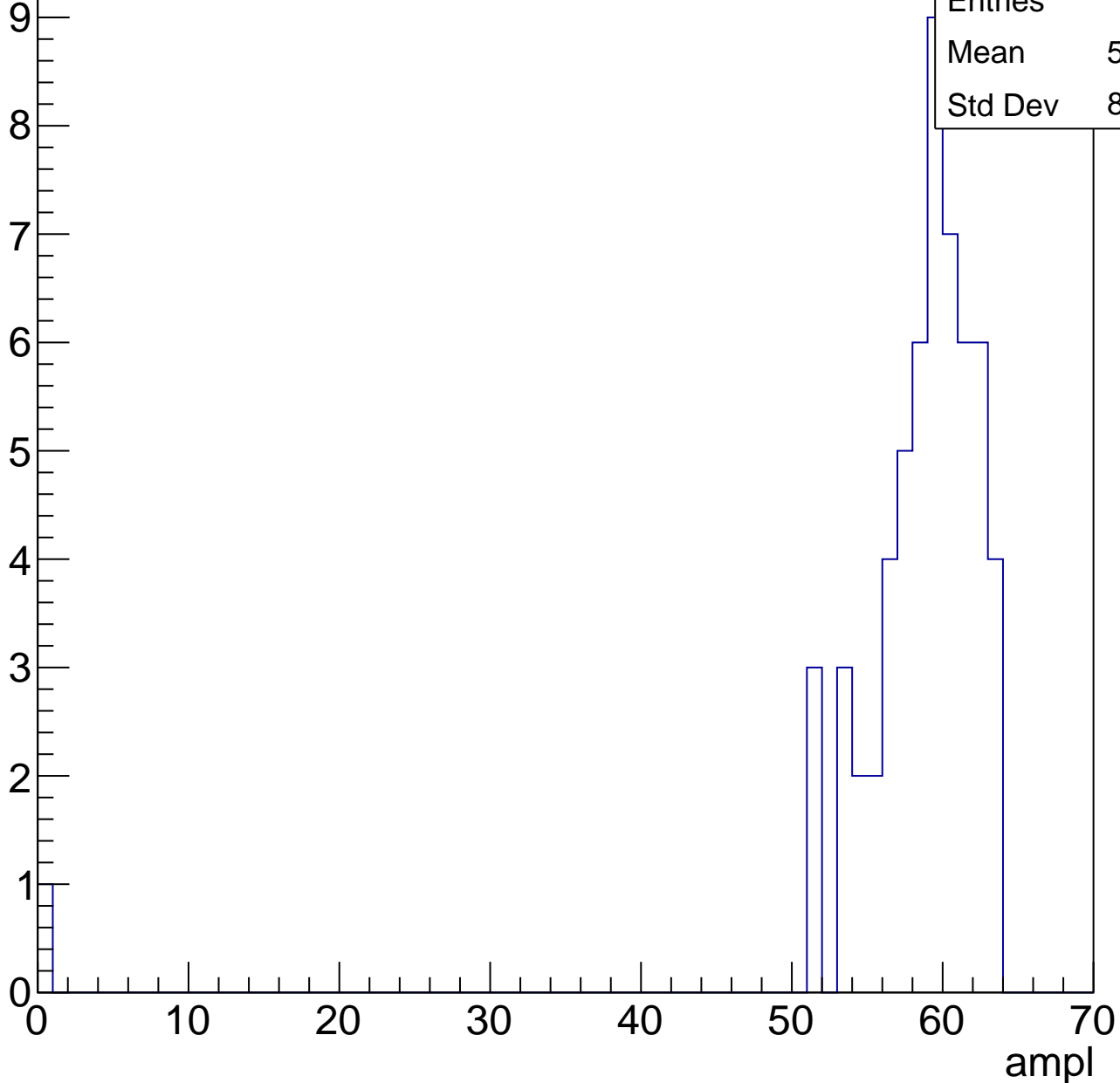


B1L101S, U15-ch124, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

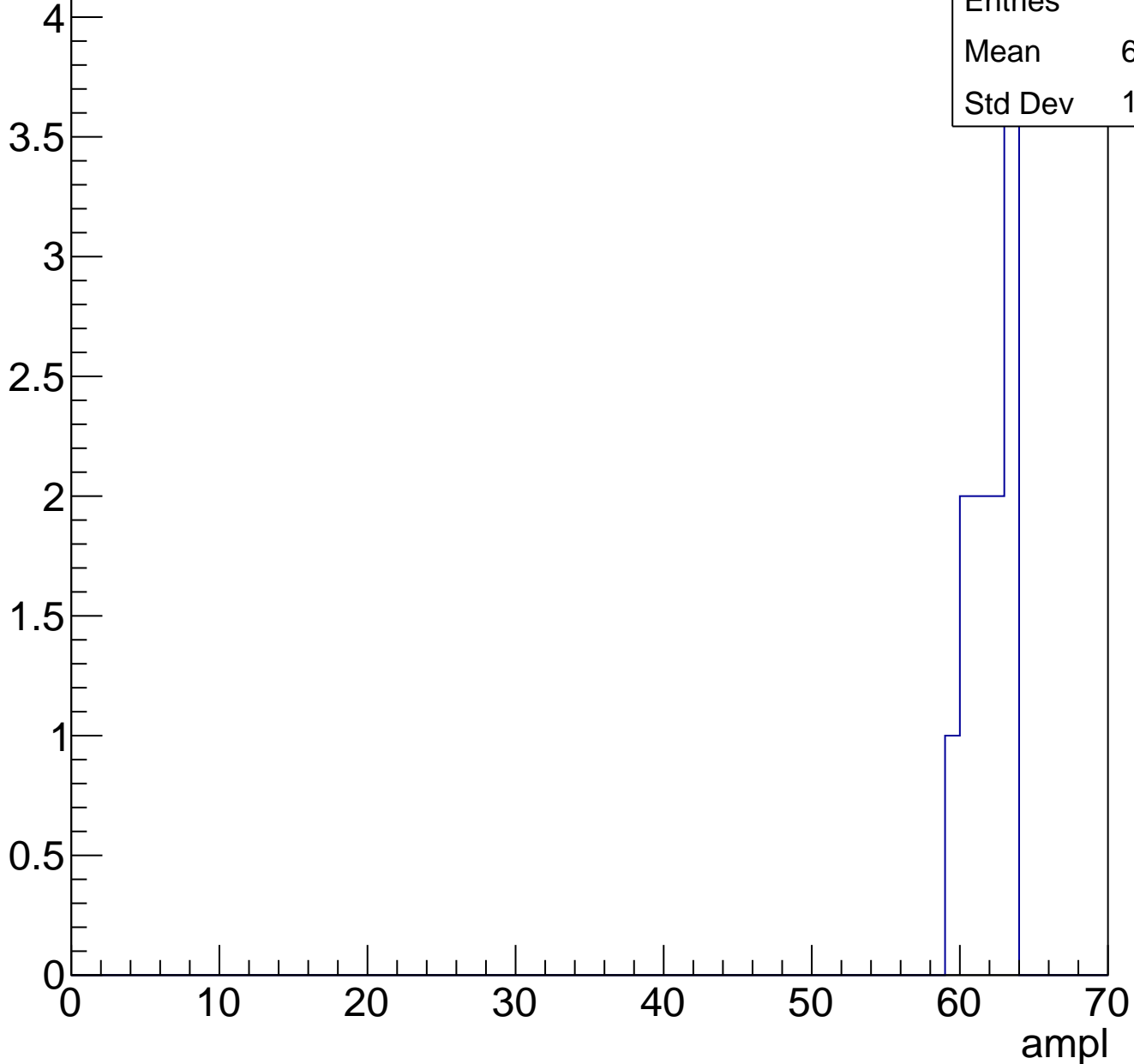
Entries	58
Mean	57.38
Std Dev	8.223



B1L101S, U15-ch124, adc6

calib_packv5_042523_0143.root, FC#0, port D2

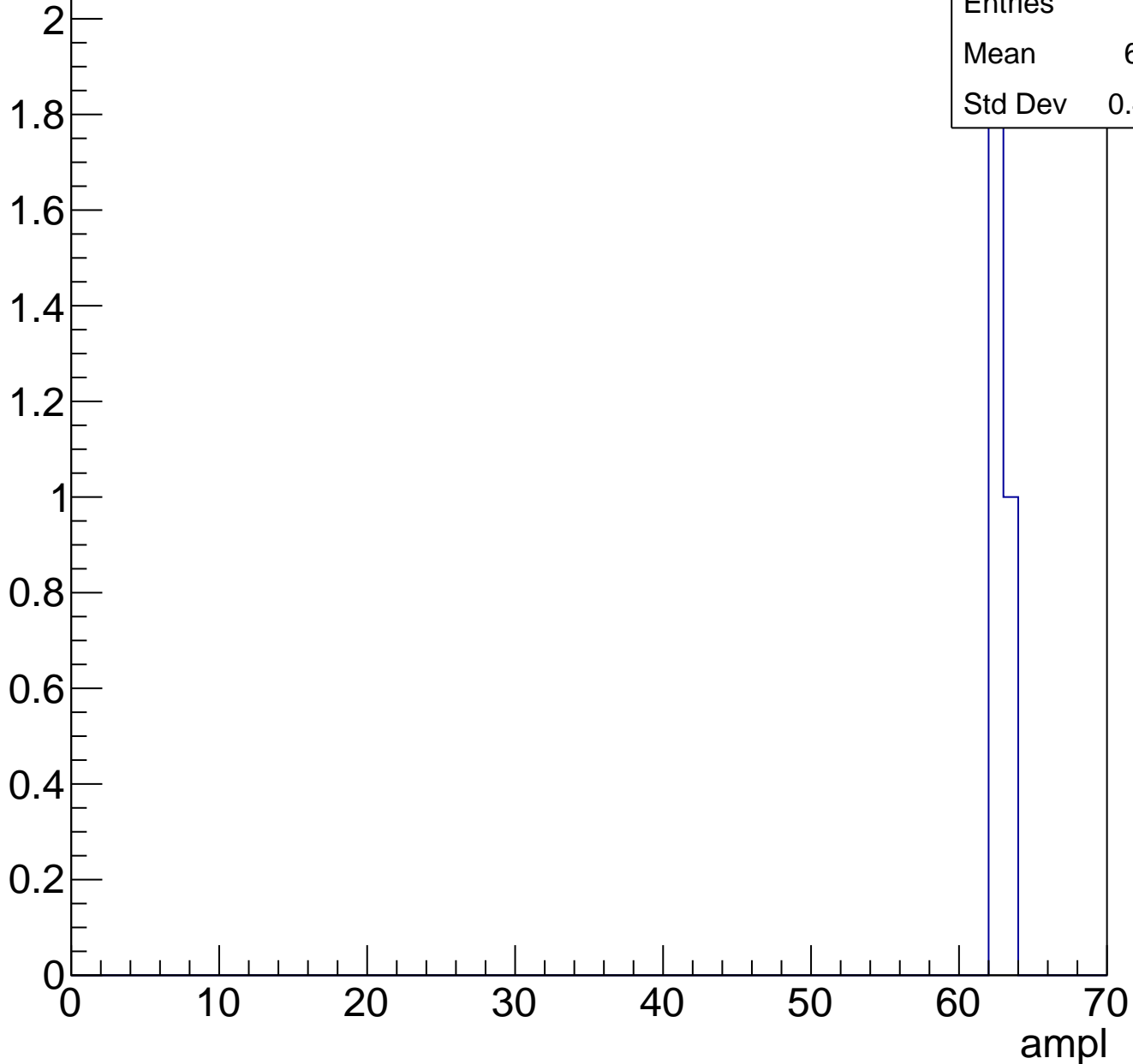
Entry



B1L101S, U15-ch124, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch125, adc0

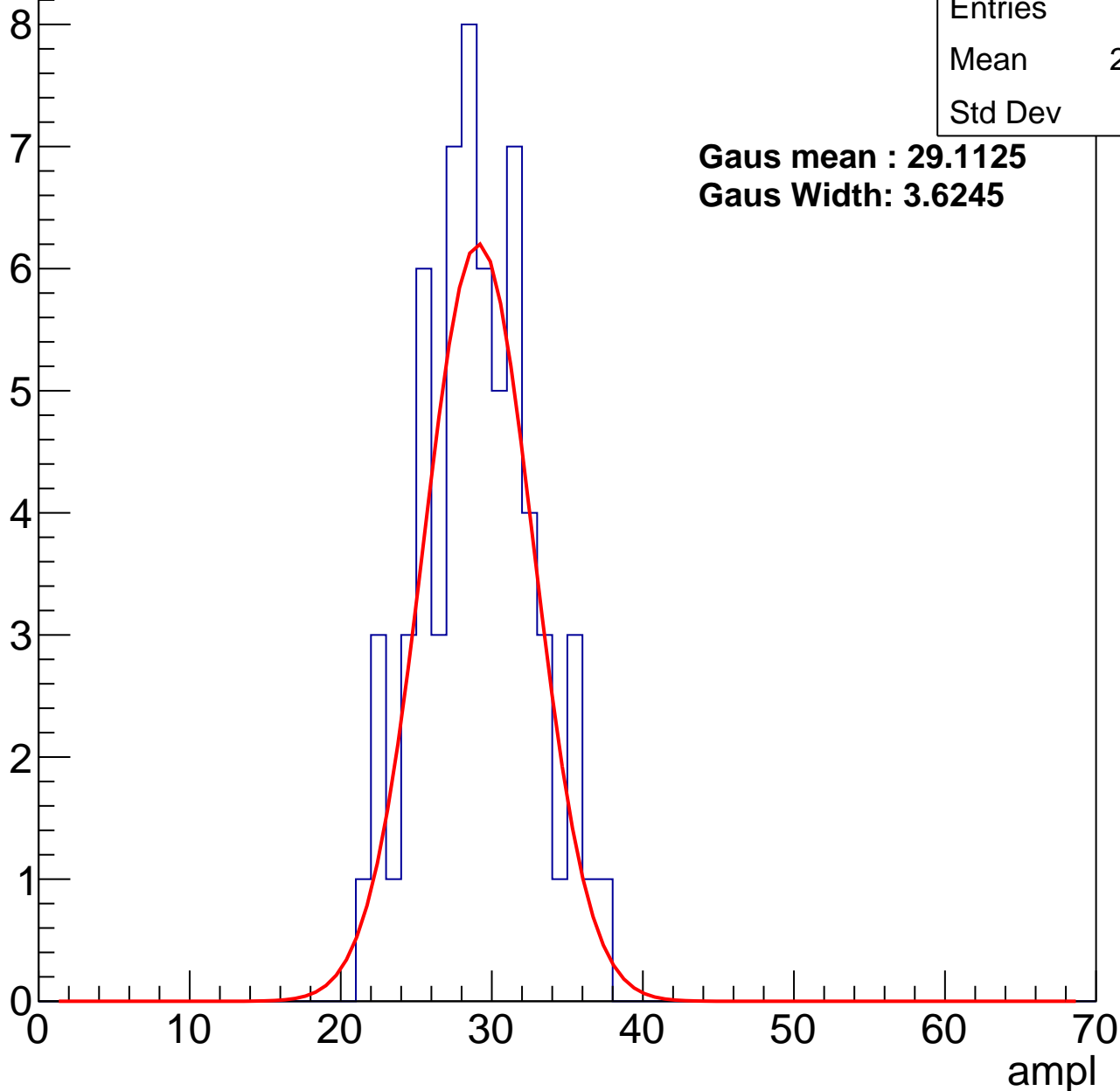
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	63
Mean	28.62
Std Dev	3.64

Gaus mean : 29.1125

Gaus Width: 3.6245



B1L101S, U15-ch125, adc1

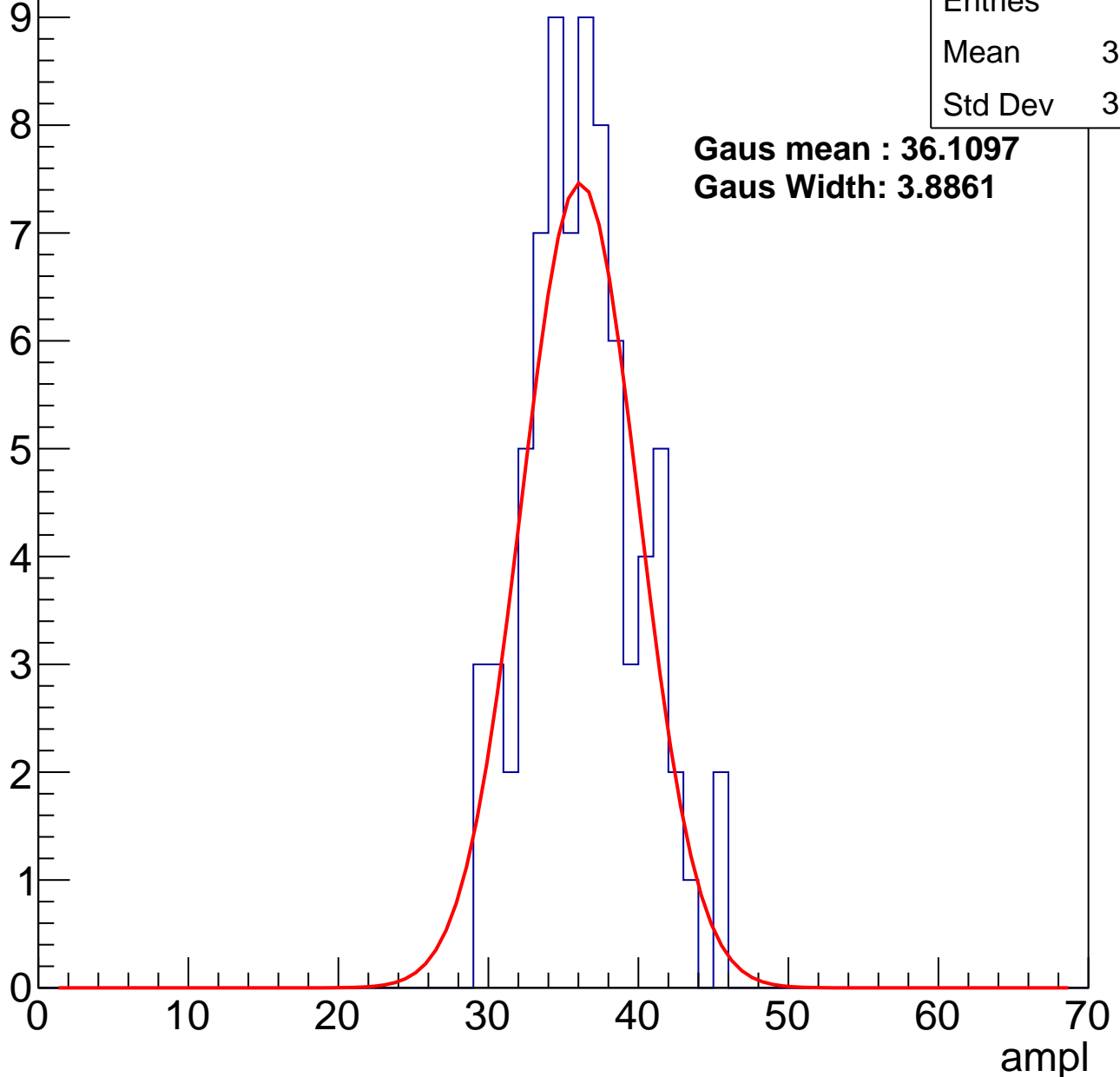
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	76
Mean	35.89
Std Dev	3.666

Gaus mean : 36.1097

Gaus Width: 3.8861



B1L101S, U15-ch125, adc2

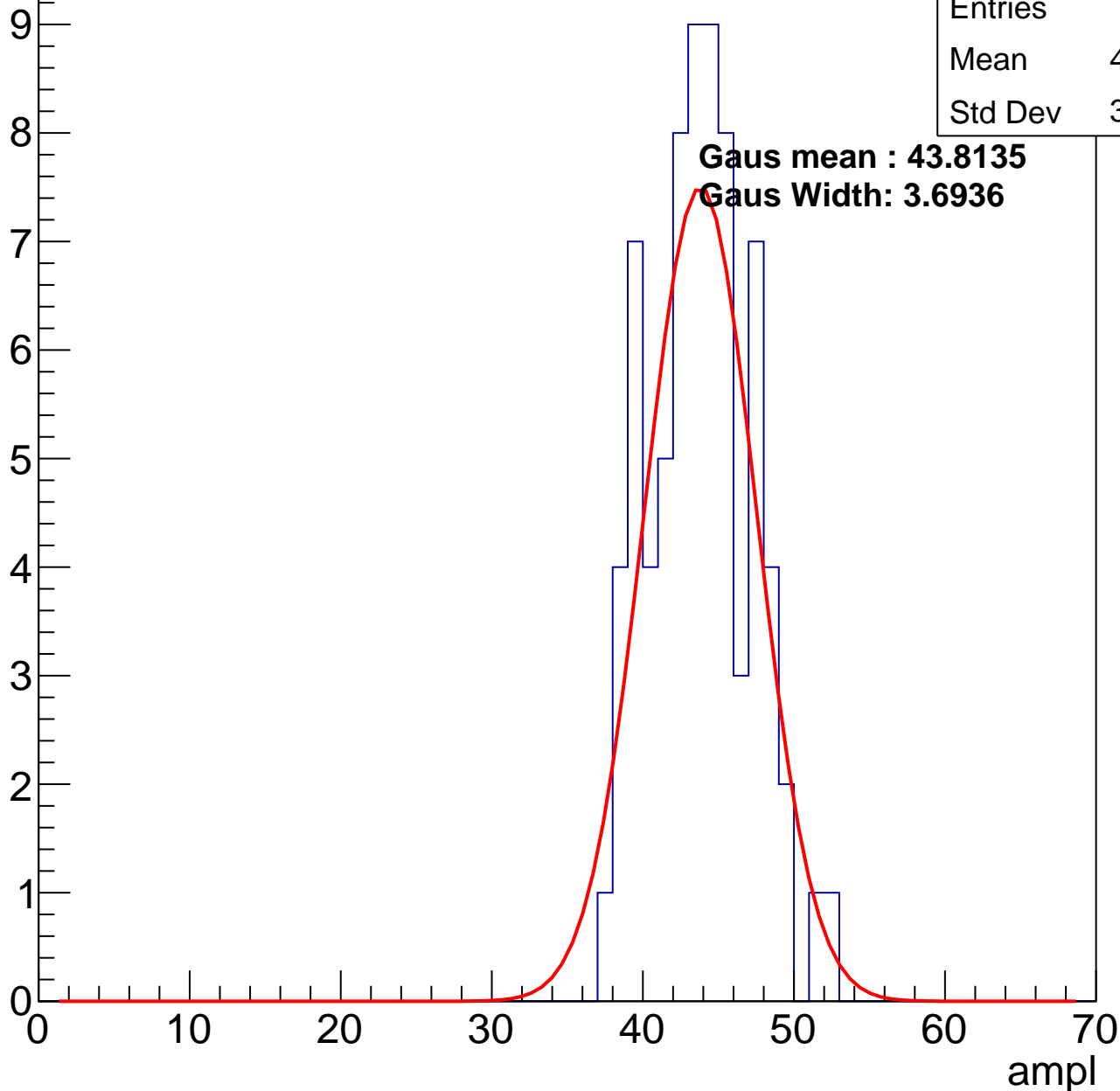
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	43.37
Std Dev	3.308

Gaus mean : 43.8135

Gaus Width: 3.6936

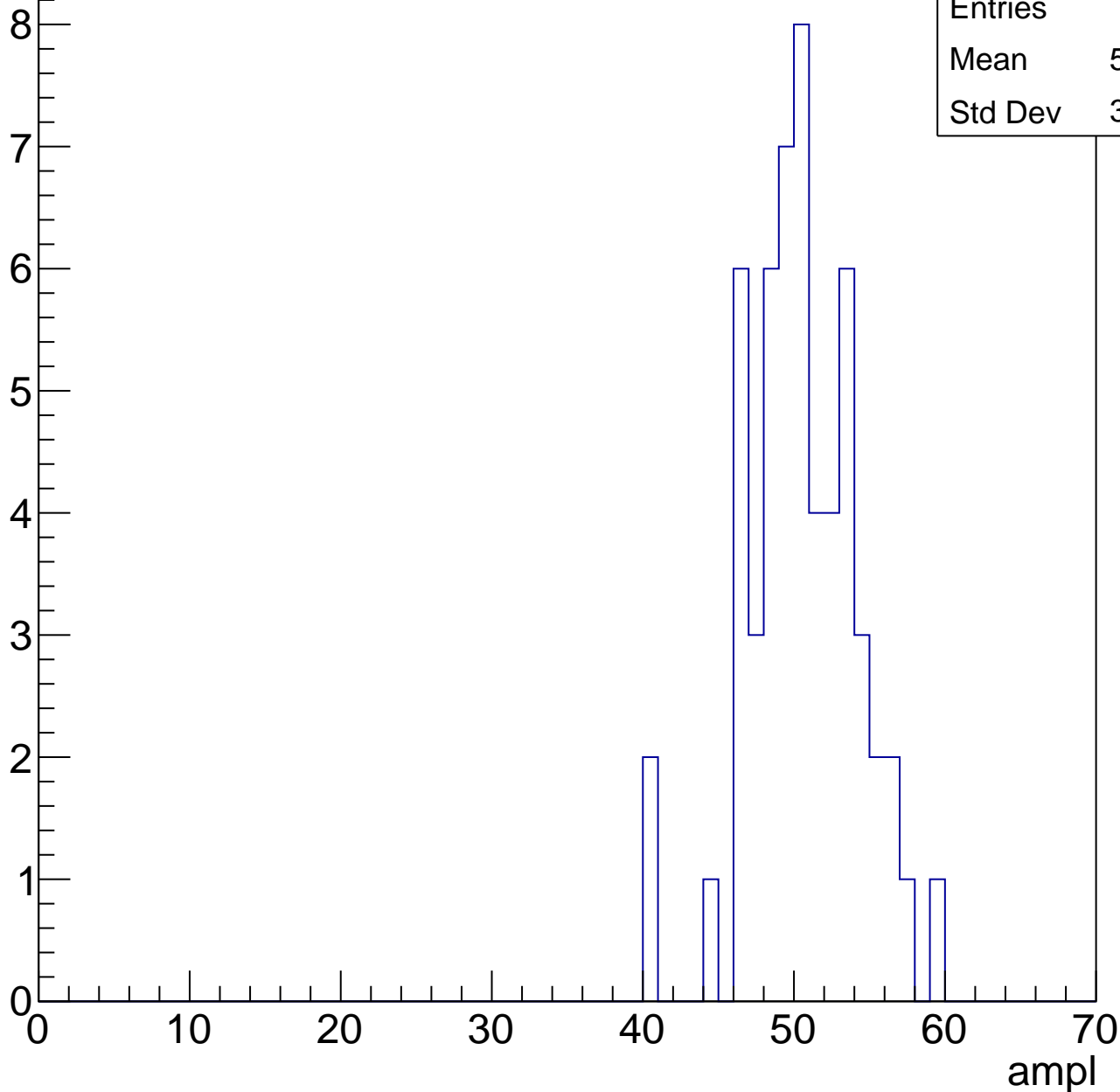


B1L101S, U15-ch125, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	56
Mean	50.04
Std Dev	3.693

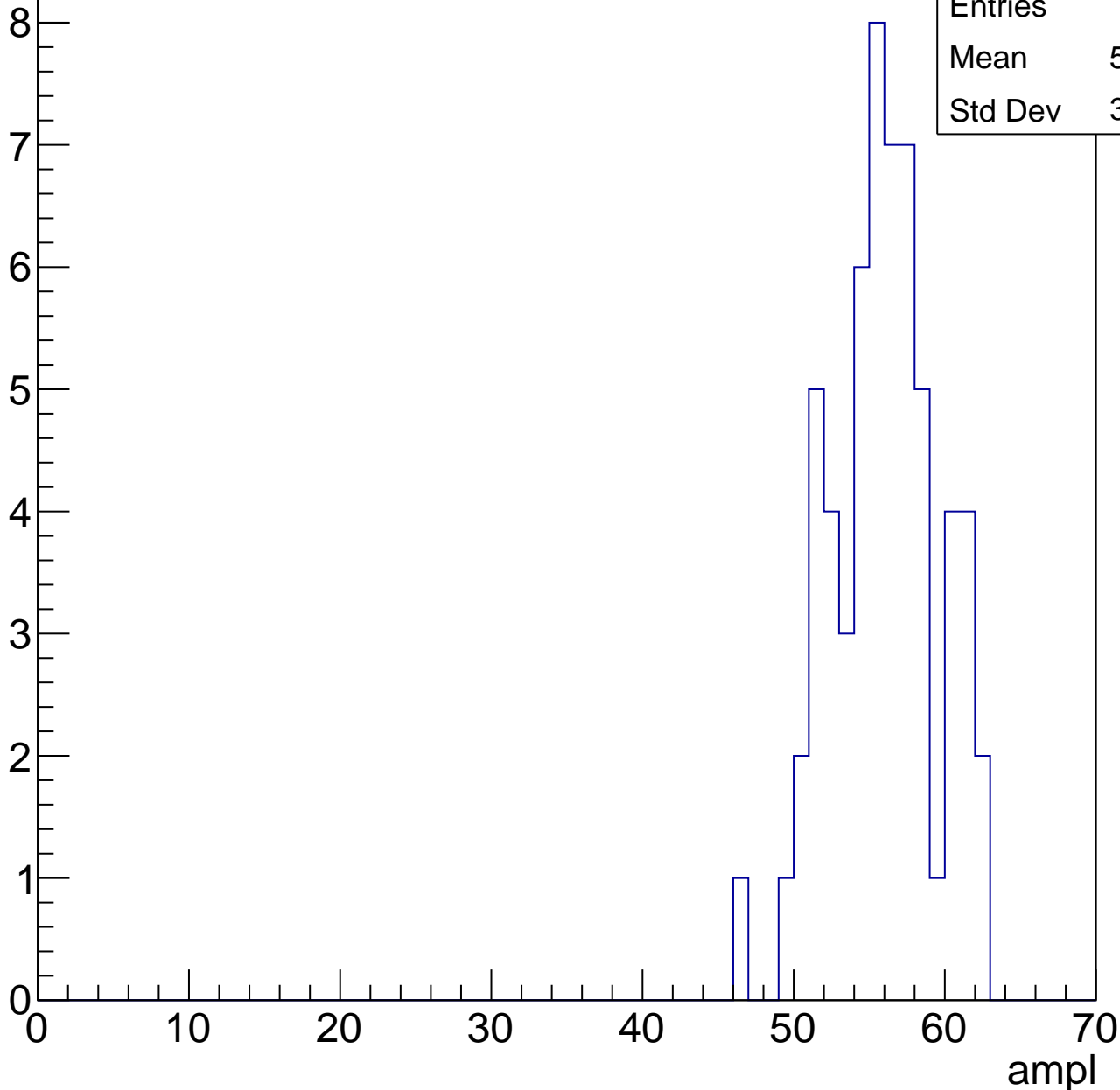


B1L101S, U15-ch125, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	60
Mean	55.48
Std Dev	3.476

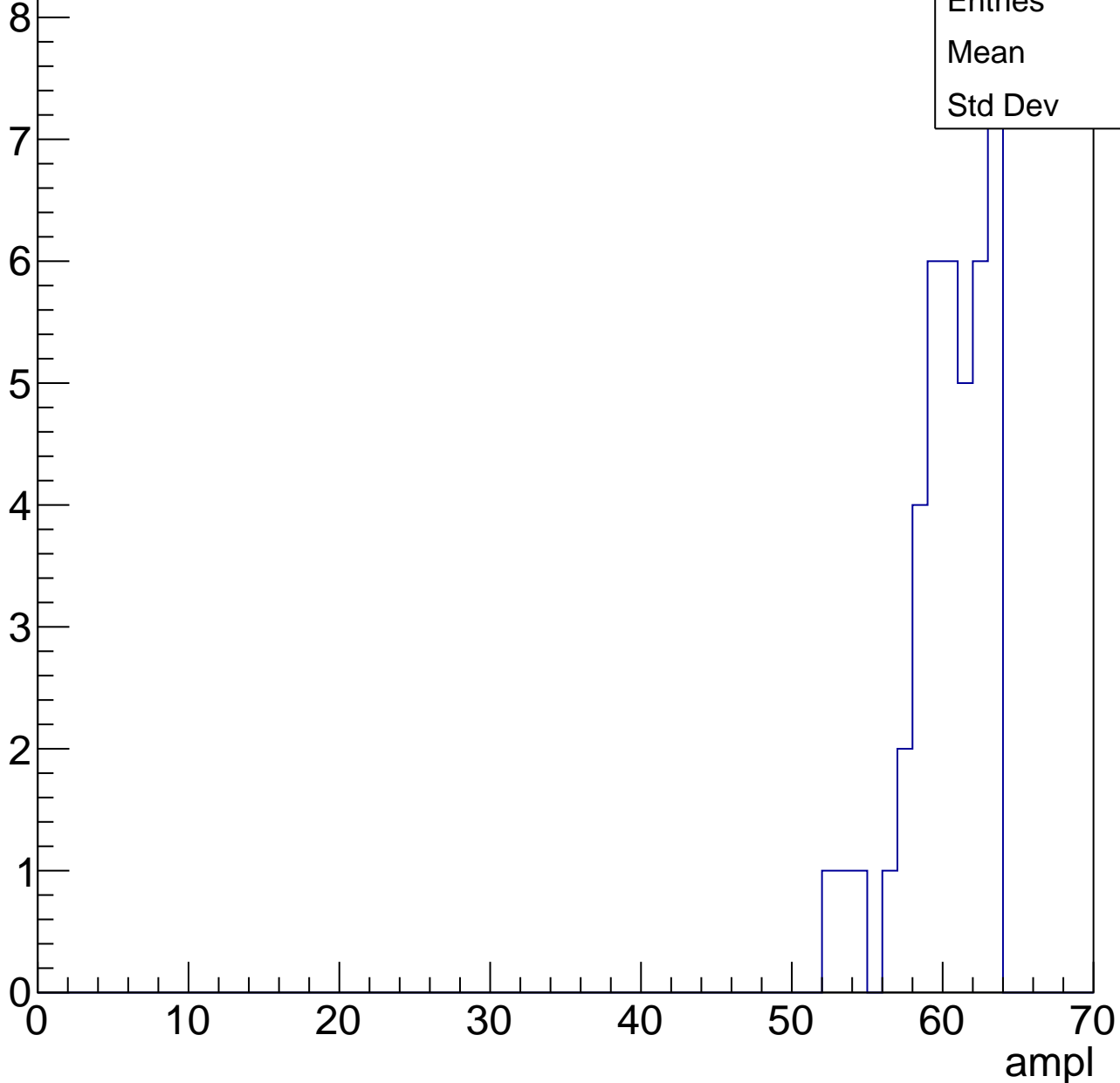


B1L101S, U15-ch125, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	59.9
Std Dev	2.73



B1L101S, U15-ch125, adc6

calib_packv5_042523_0143.root, FC#0, port D2

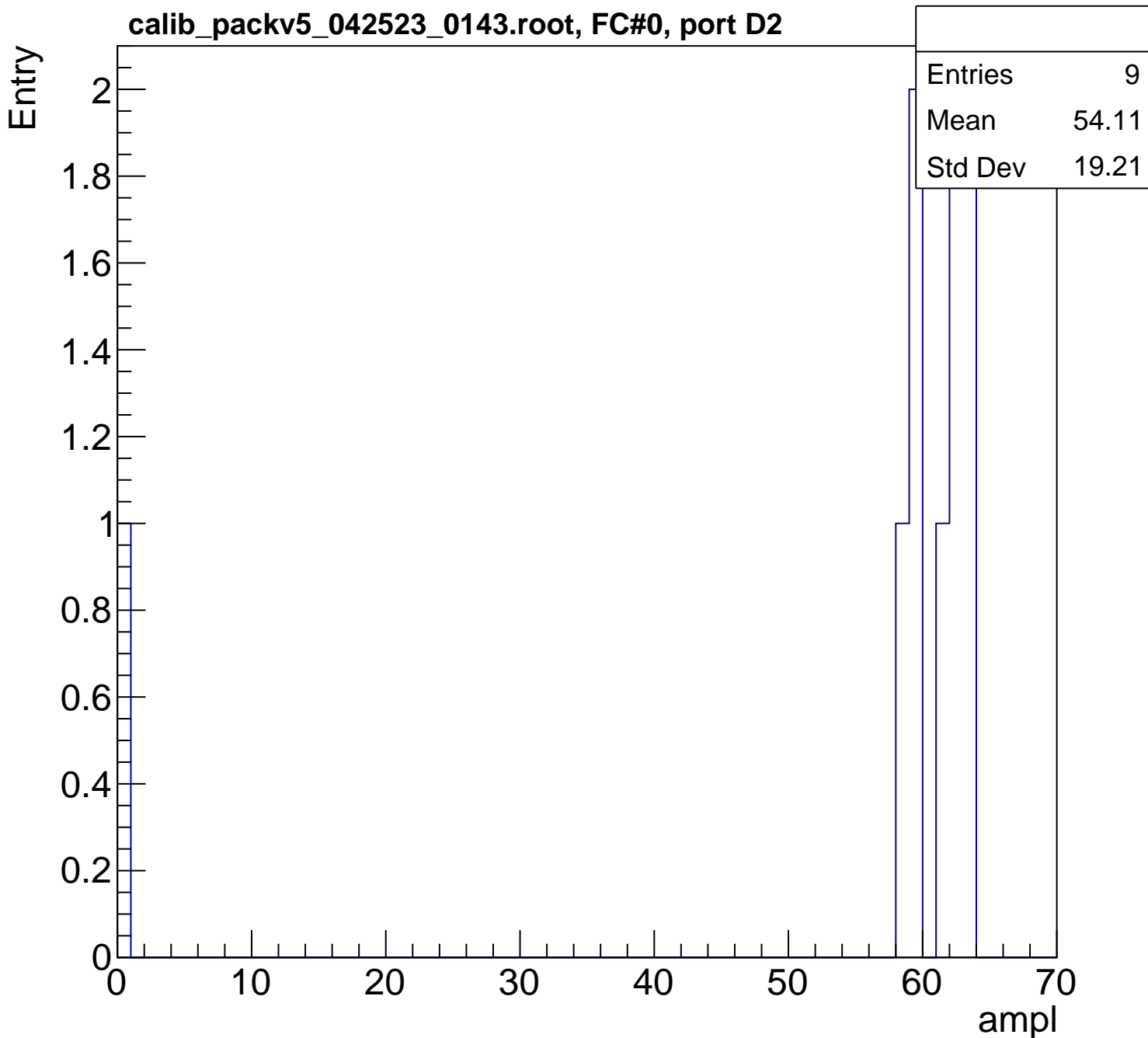
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	54.11
Std Dev	19.21

0 10 20 30 40 50 60 70

ampl



B1L101S, U15-ch125, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L101S, U15-ch126, adc0

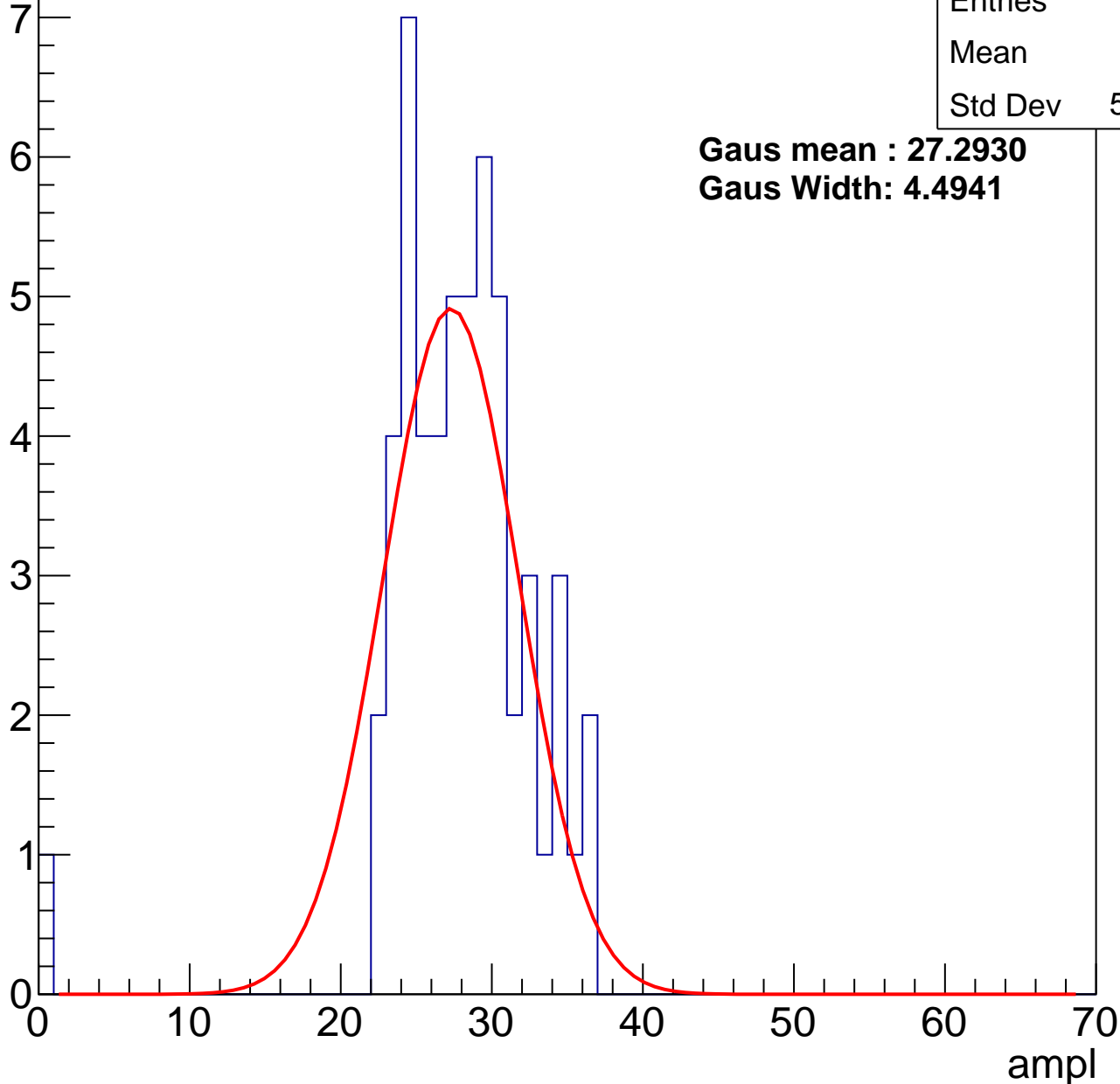
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	27.4
Std Dev	5.235

Gaus mean : 27.2930

Gaus Width: 4.4941



B1L101S, U15-ch126, adc1

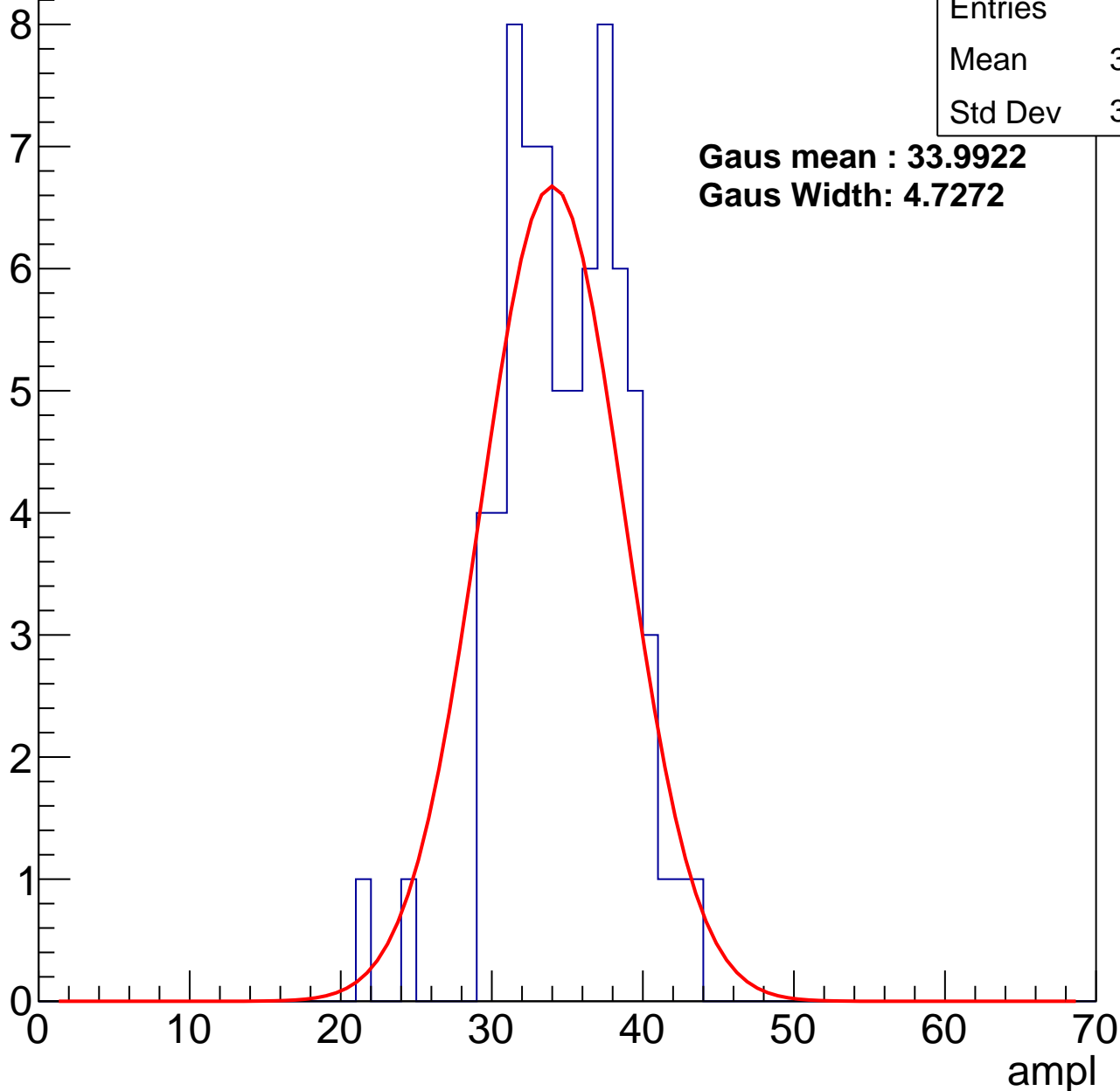
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	73
Mean	34.38
Std Dev	3.975

Gaus mean : 33.9922

Gaus Width: 4.7272



B1L101S, U15-ch126, adc2

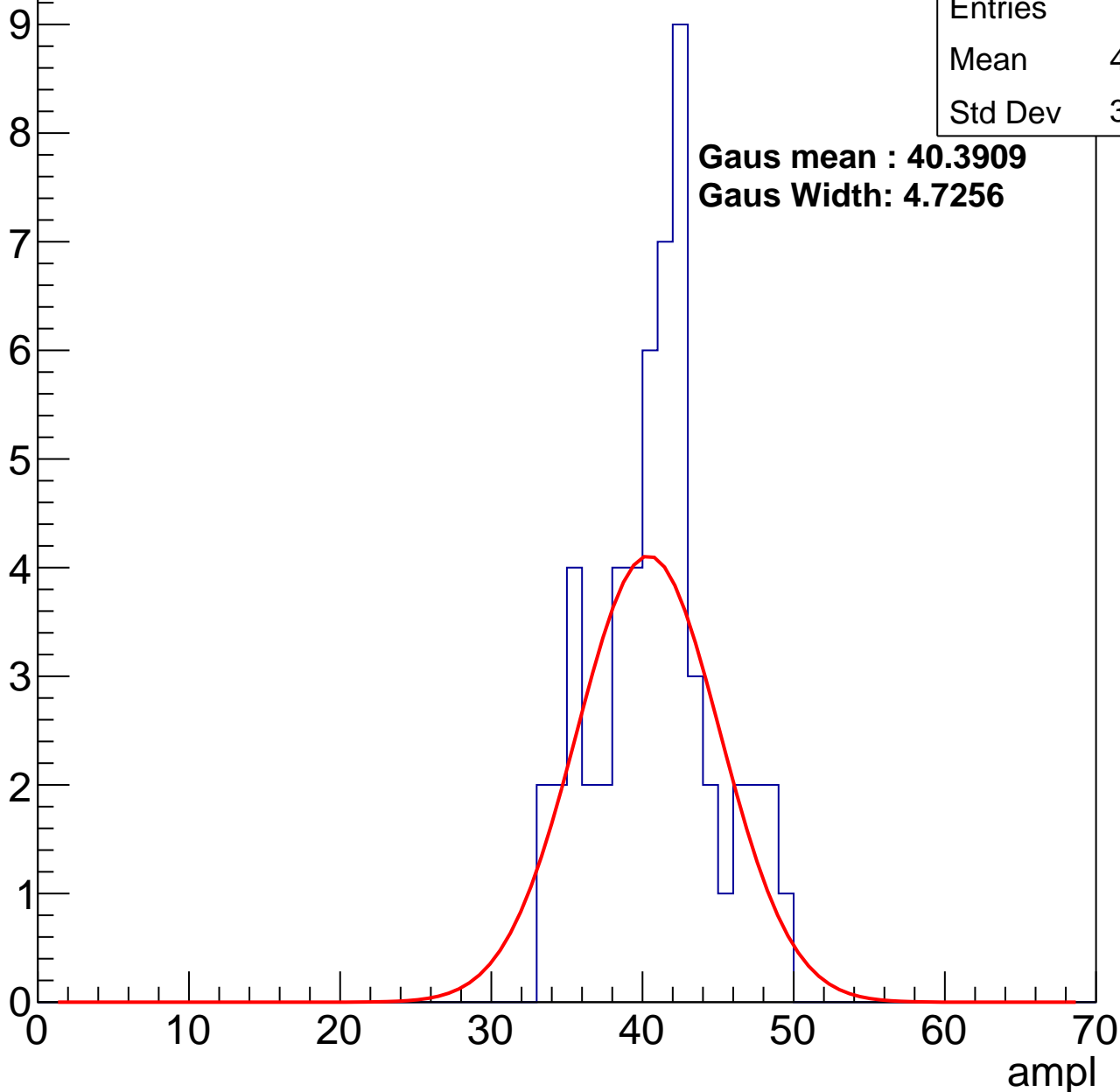
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	40.47
Std Dev	3.893

Gaus mean : 40.3909

Gaus Width: 4.7256

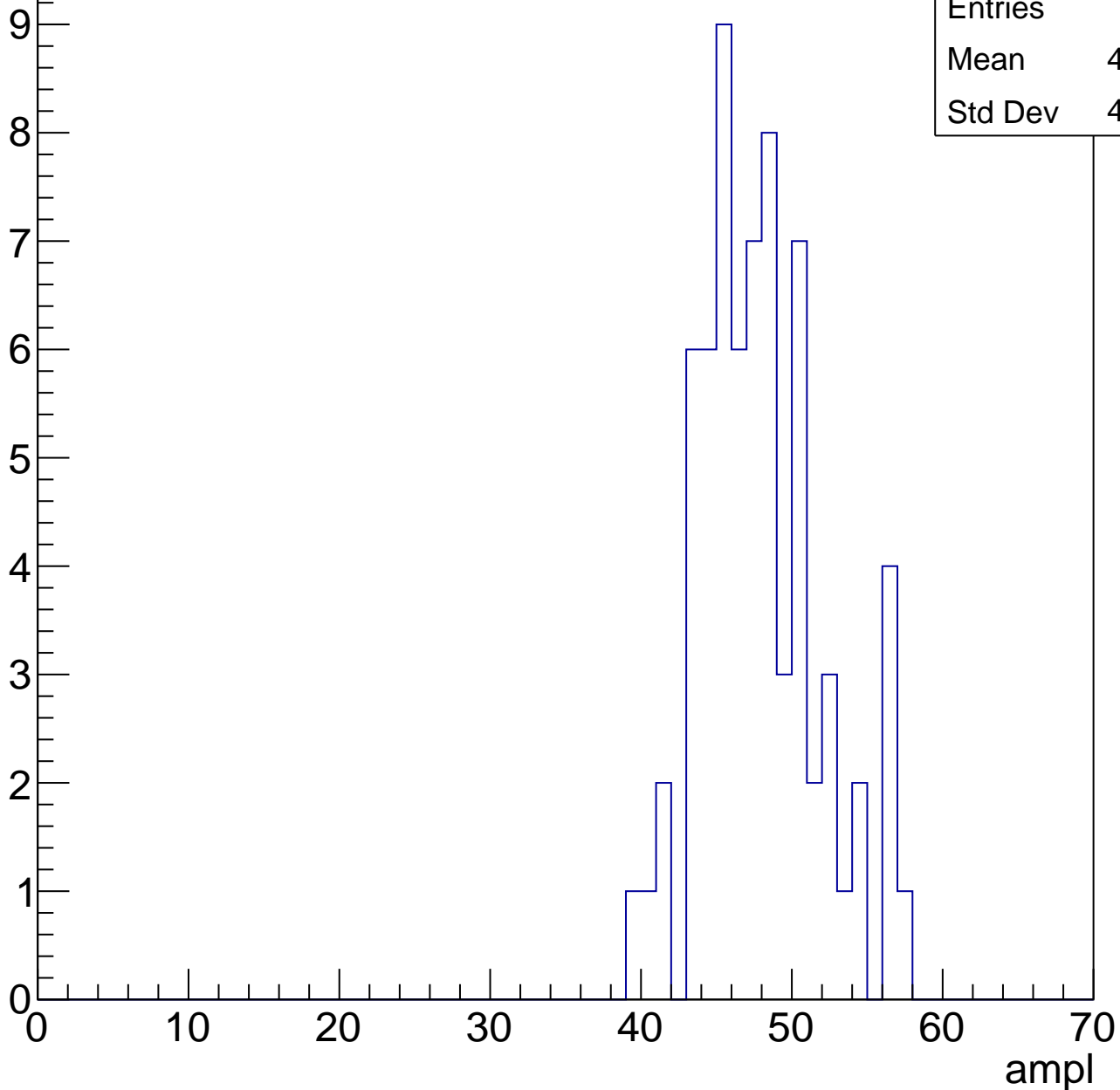


B1L101S, U15-ch126, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	69
Mean	47.45
Std Dev	4.013

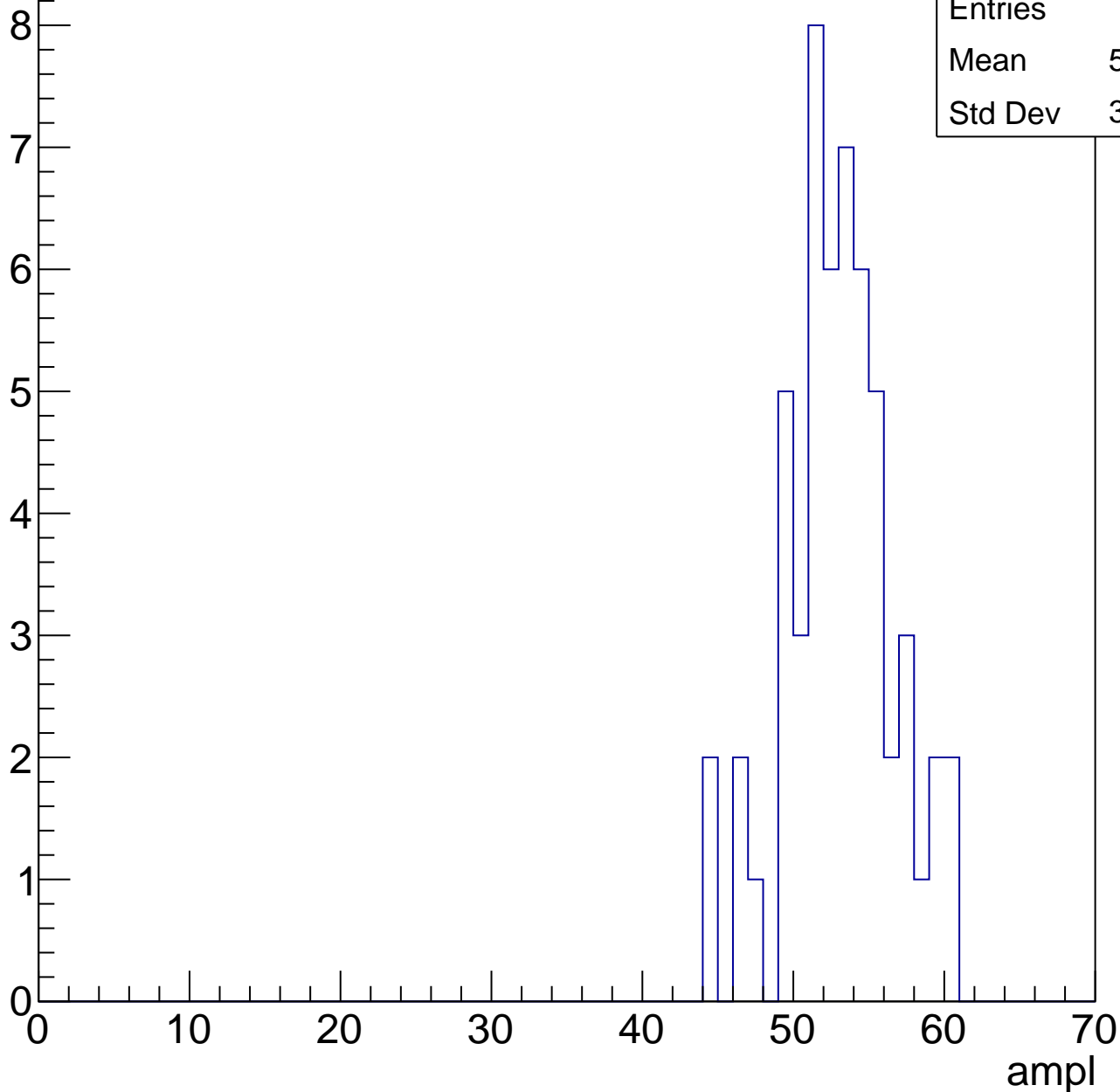


B1L101S, U15-ch126, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	55
Mean	52.56
Std Dev	3.612

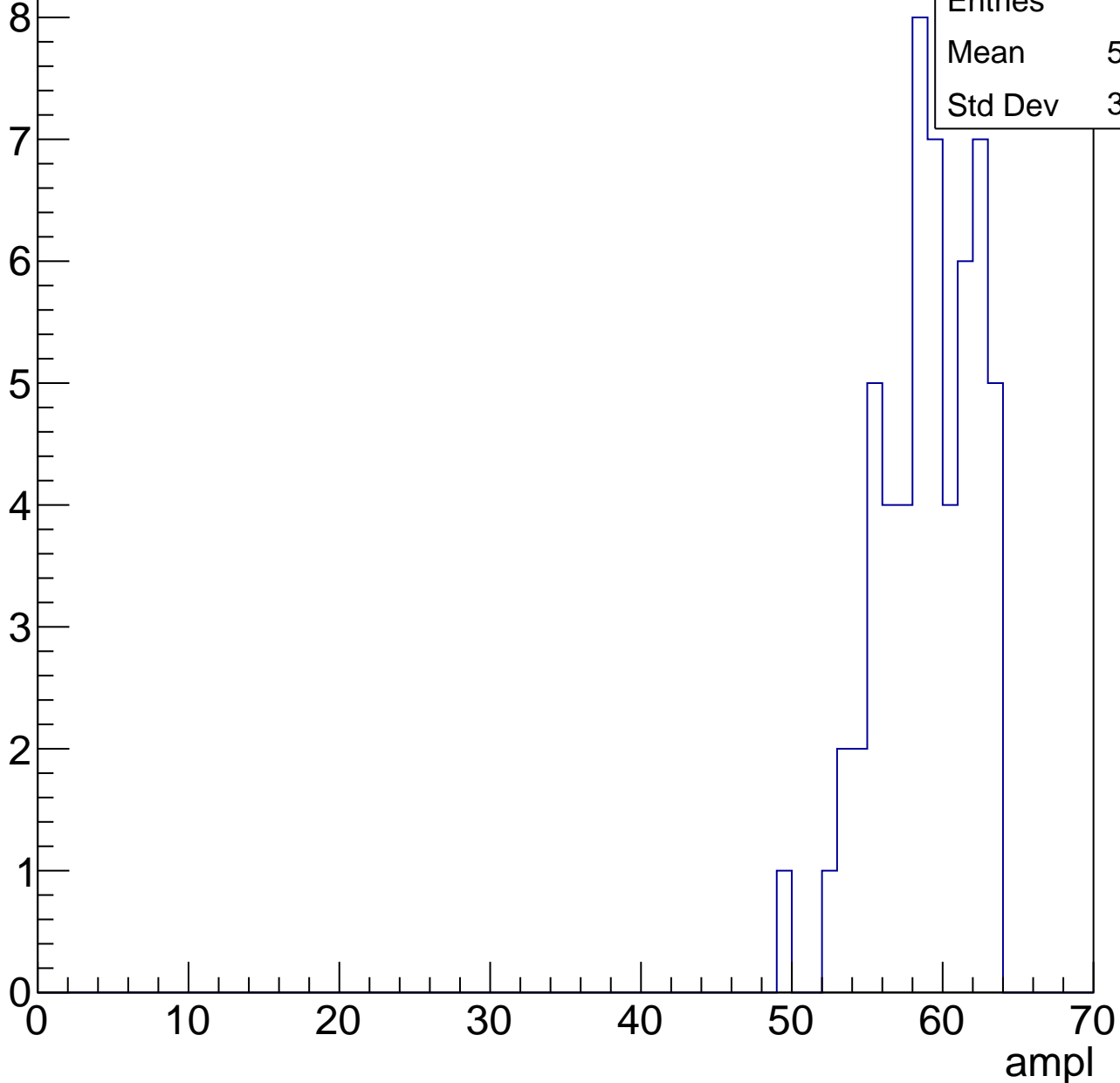


B1L101S, U15-ch126, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

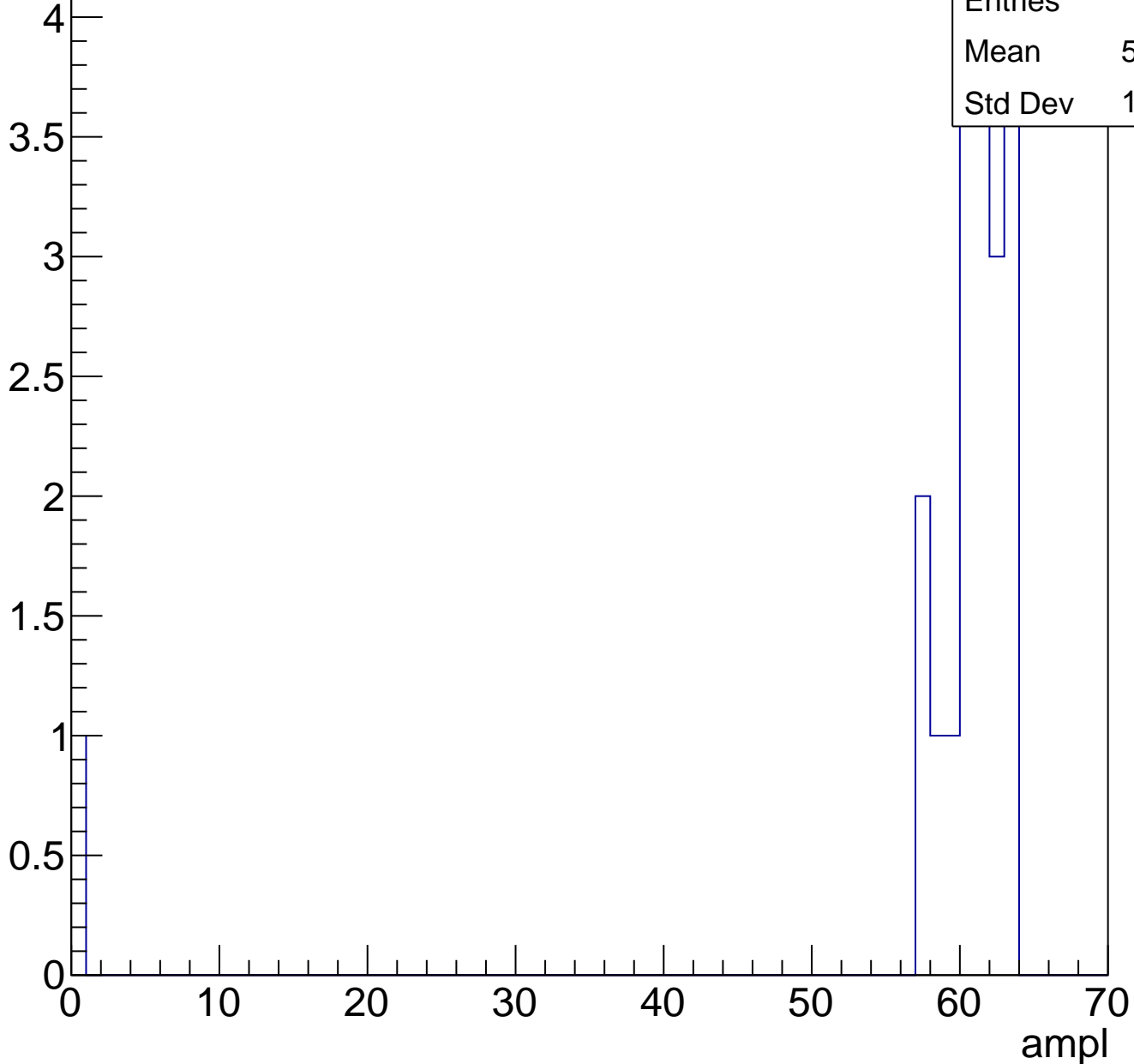
Entries	56
Mean	58.46
Std Dev	3.179



B1L101S, U15-ch126, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch126, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch127, adc0

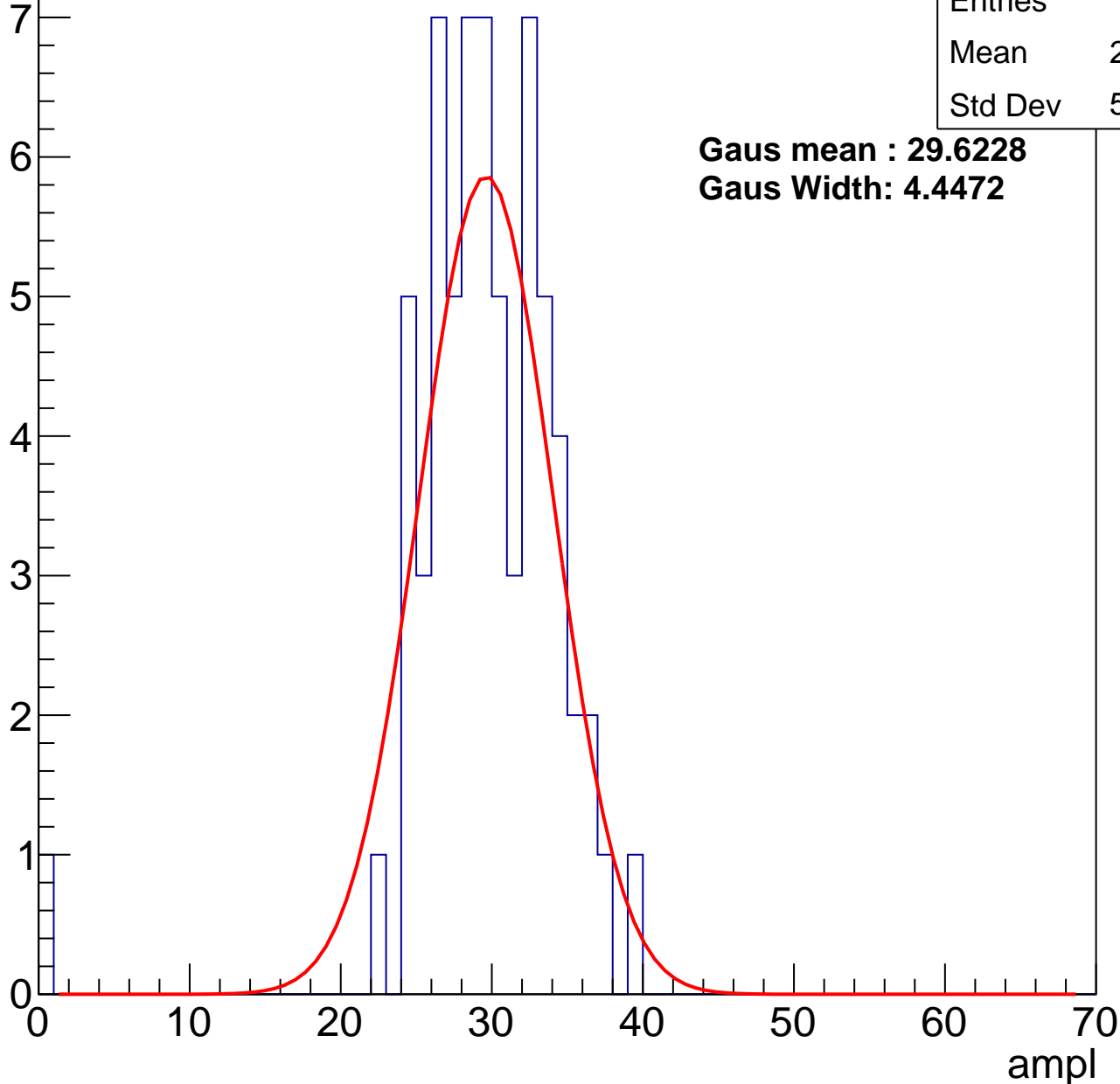
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	66
Mean	29.08
Std Dev	5.144

Gaus mean : 29.6228

Gaus Width: 4.4472



B1L101S, U15-ch127, adc1

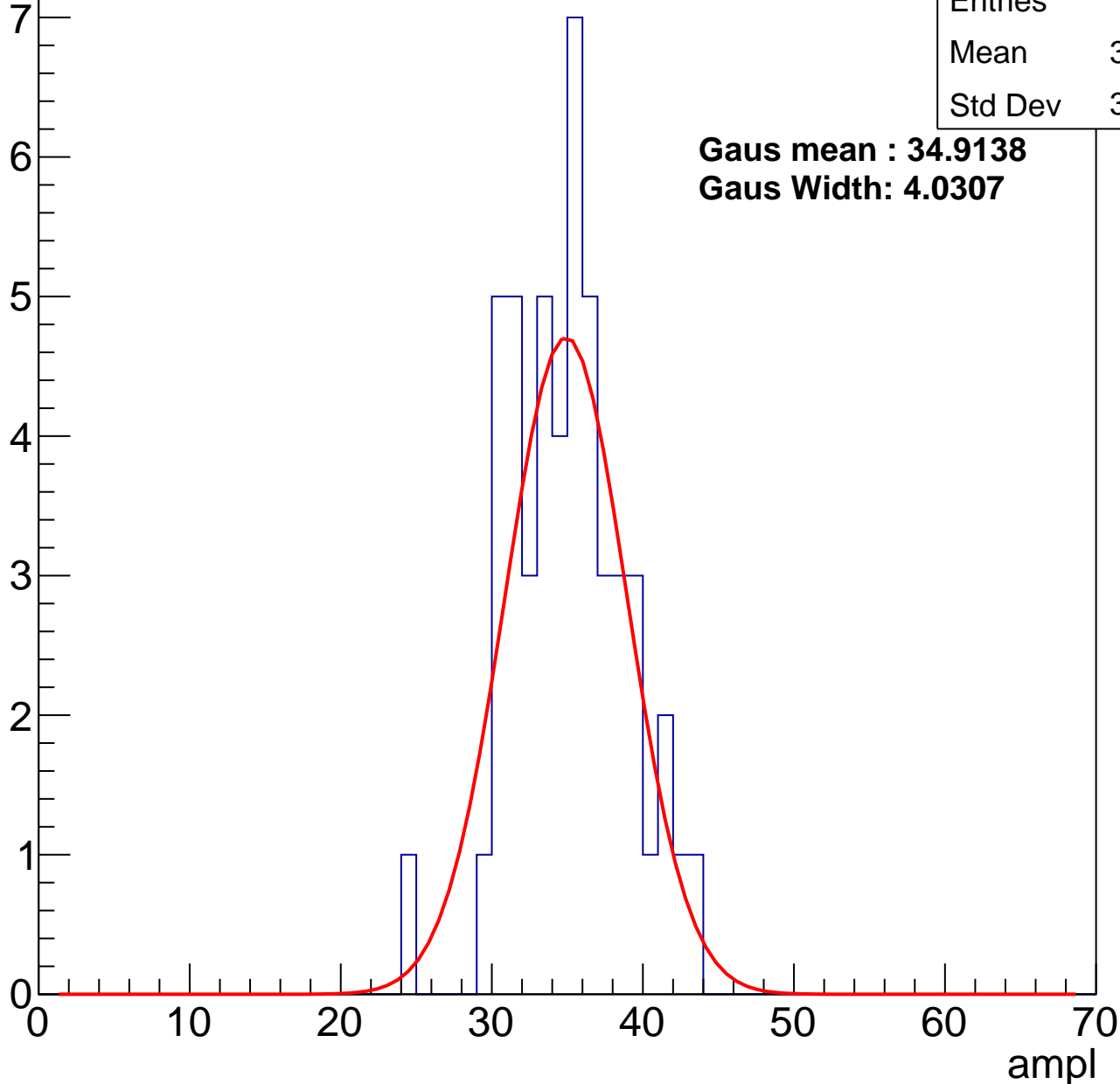
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	50
Mean	34.58
Std Dev	3.758

Gaus mean : 34.9138

Gaus Width: 4.0307



B1L101S, U15-ch127, adc2

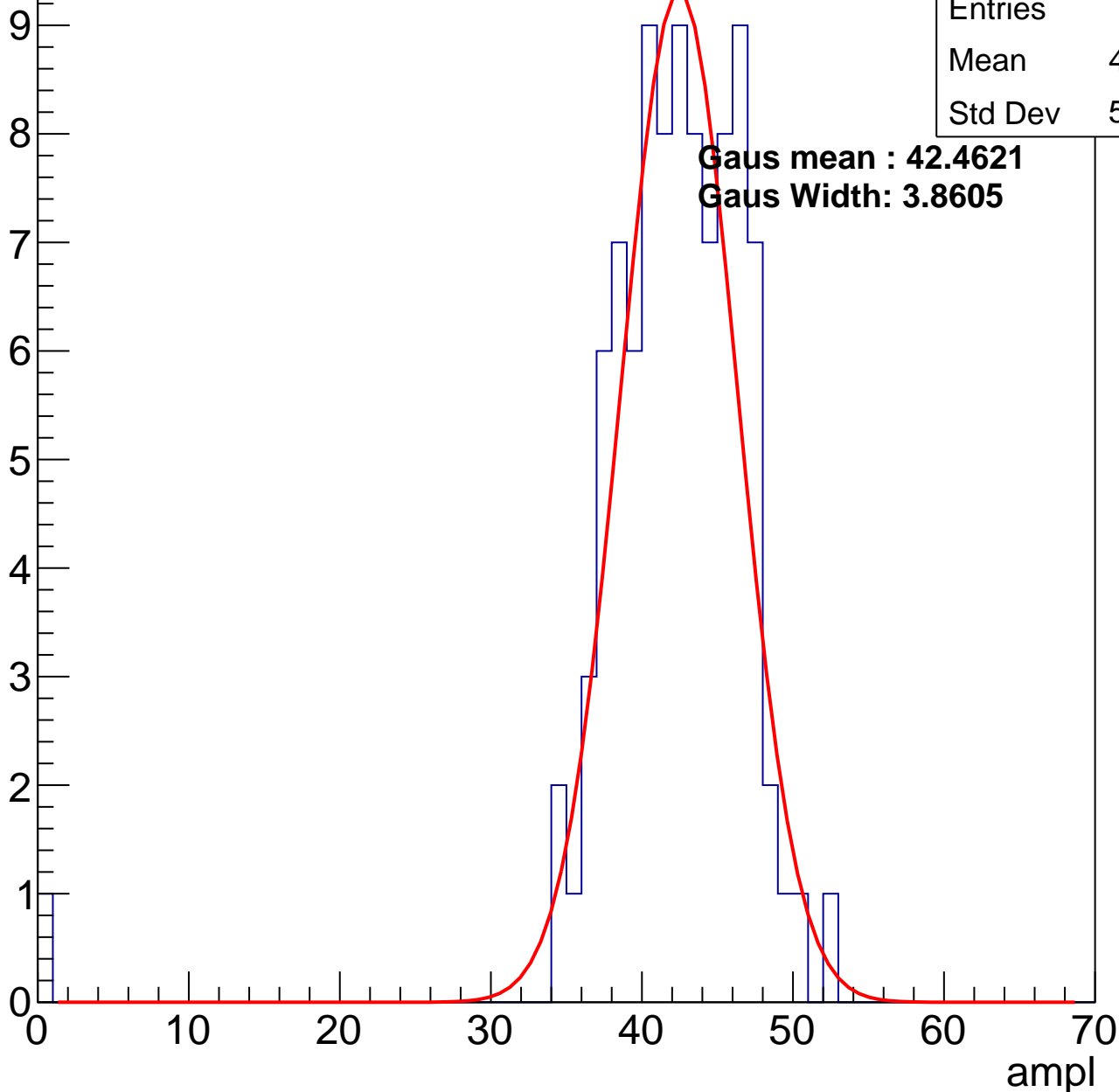
calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	96
Mean	41.68
Std Dev	5.693

Gaus mean : 42.4621

Gaus Width: 3.8605

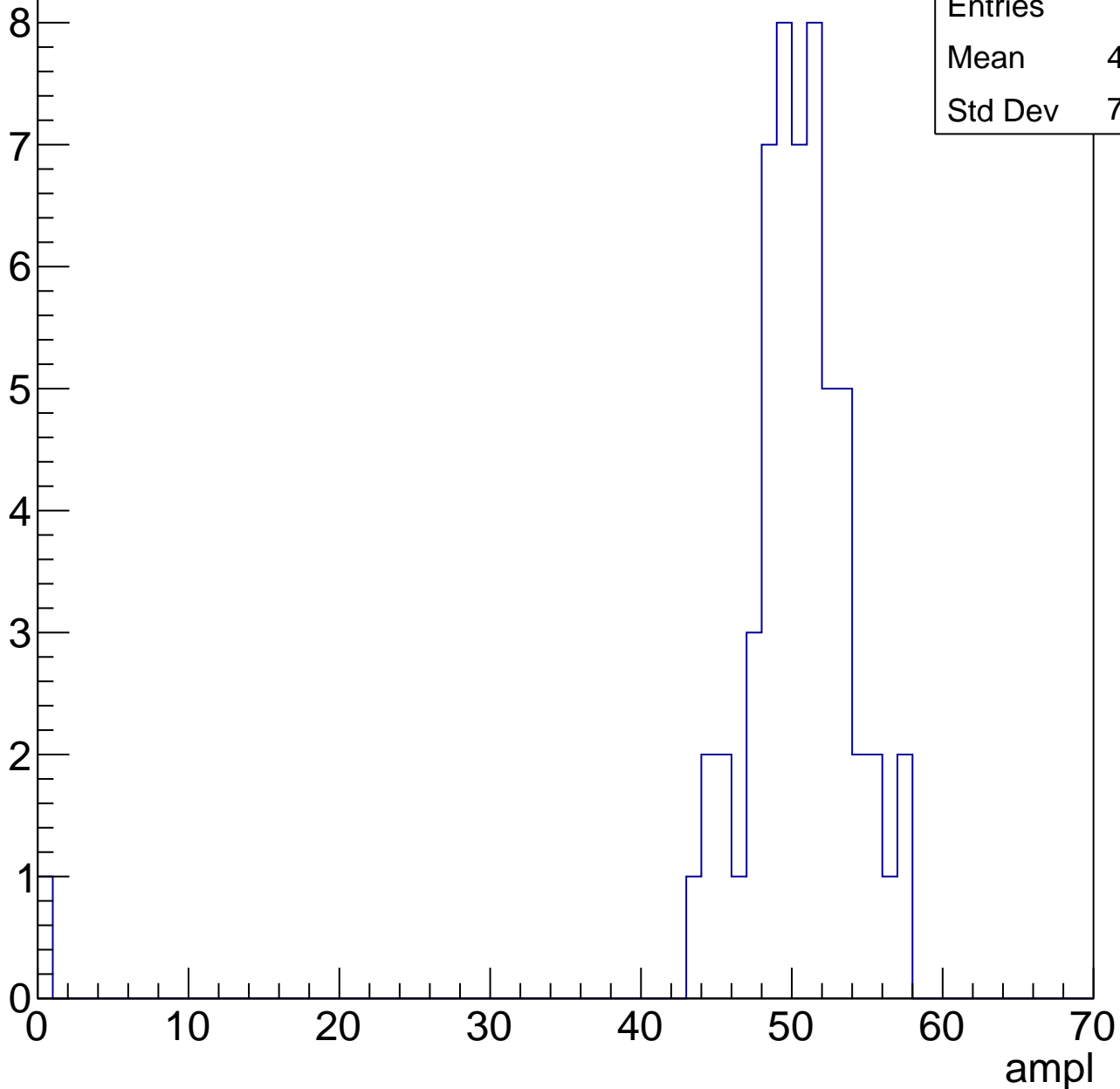


B1L101S, U15-ch127, adc3

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	57
Mean	49.25
Std Dev	7.263

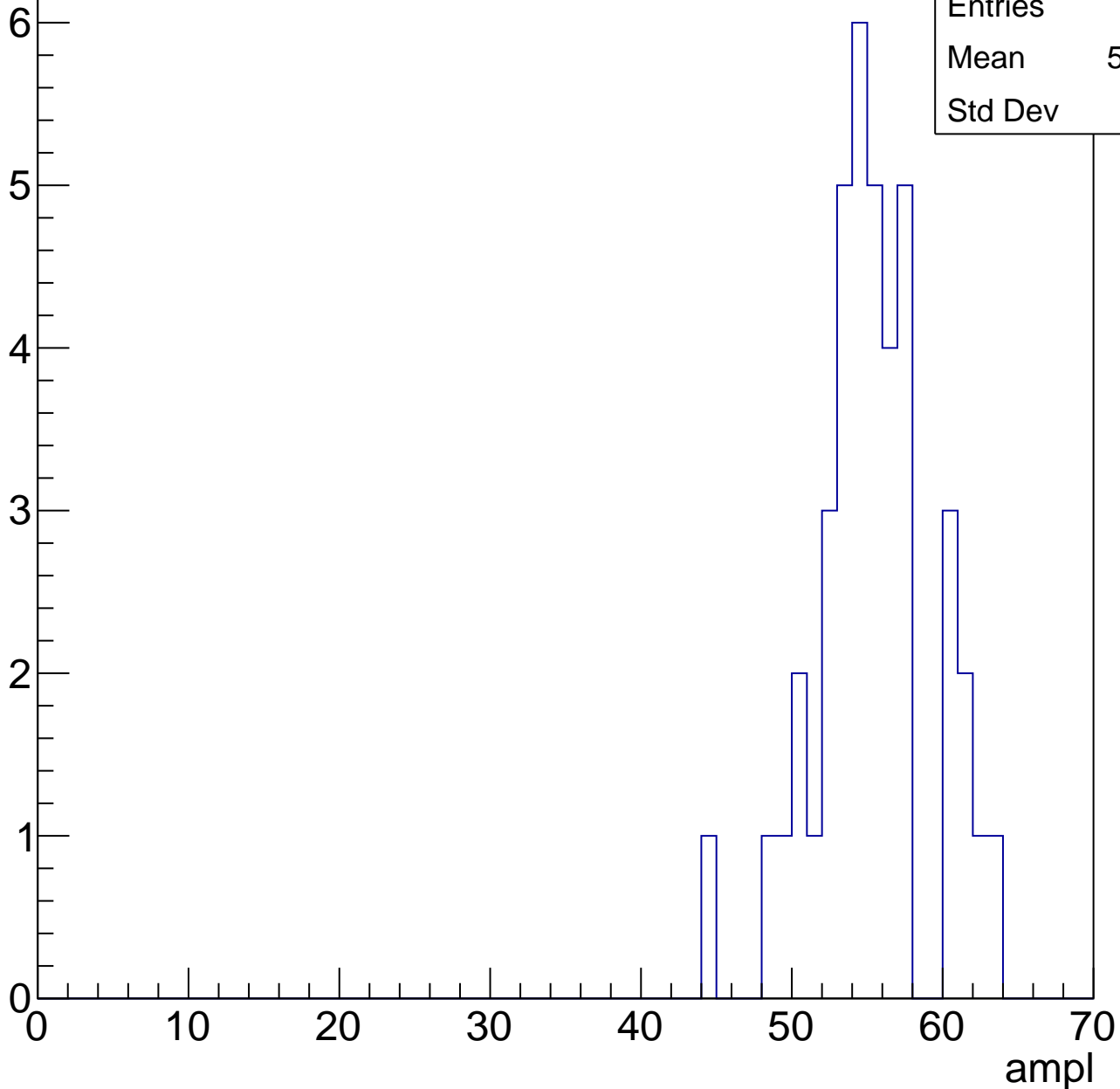


B1L101S, U15-ch127, adc4

calib_packv5_042523_0143.root, FC#0, port D2

Entry

Entries	41
Mean	54.83
Std Dev	3.85

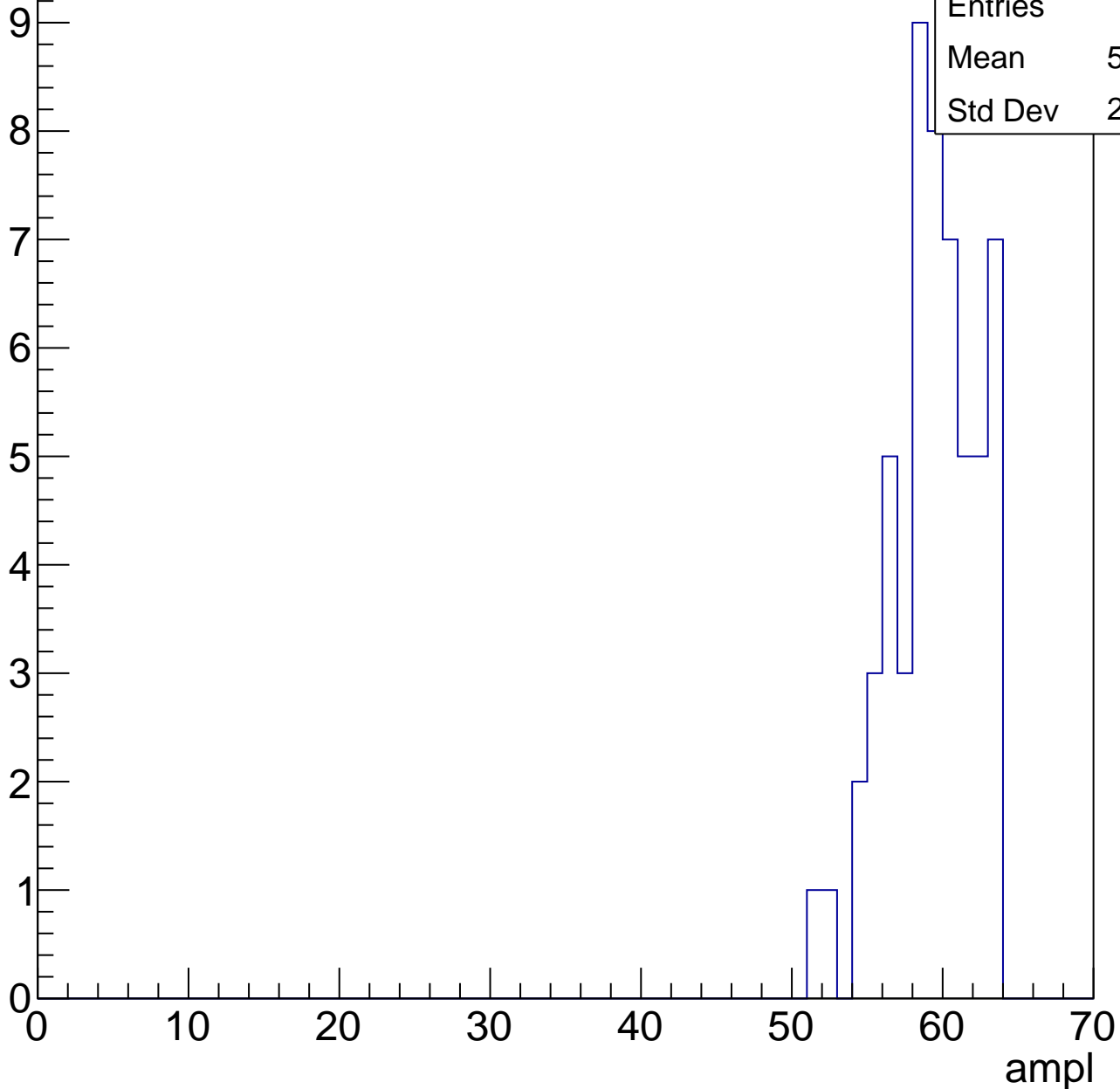


B1L101S, U15-ch127, adc5

calib_packv5_042523_0143.root, FC#0, port D2

Entry

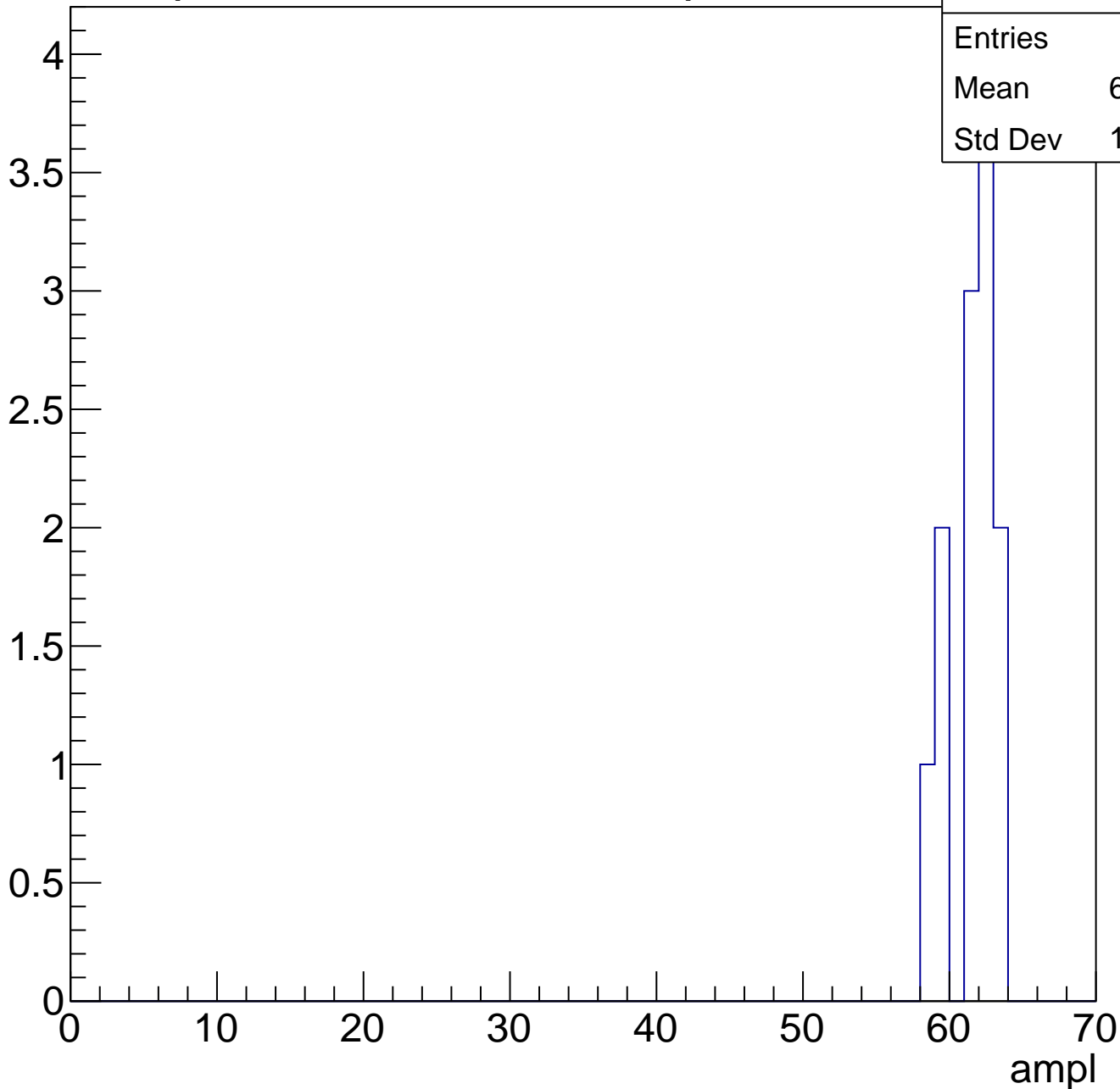
Entries	56
Mean	58.88
Std Dev	2.866



B1L101S, U15-ch127, adc6

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	12
Mean	61.08
Std Dev	1.552

B1L101S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



B1L101S, U15-ch127, adc7

calib_packv5_042523_0143.root, FC#0, port D2

Entry



Entries	1
Mean	0
Std Dev	0