



# B0L102S, U22-ch0

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.38
Std Dev	11.93

Turn on : 24.6456

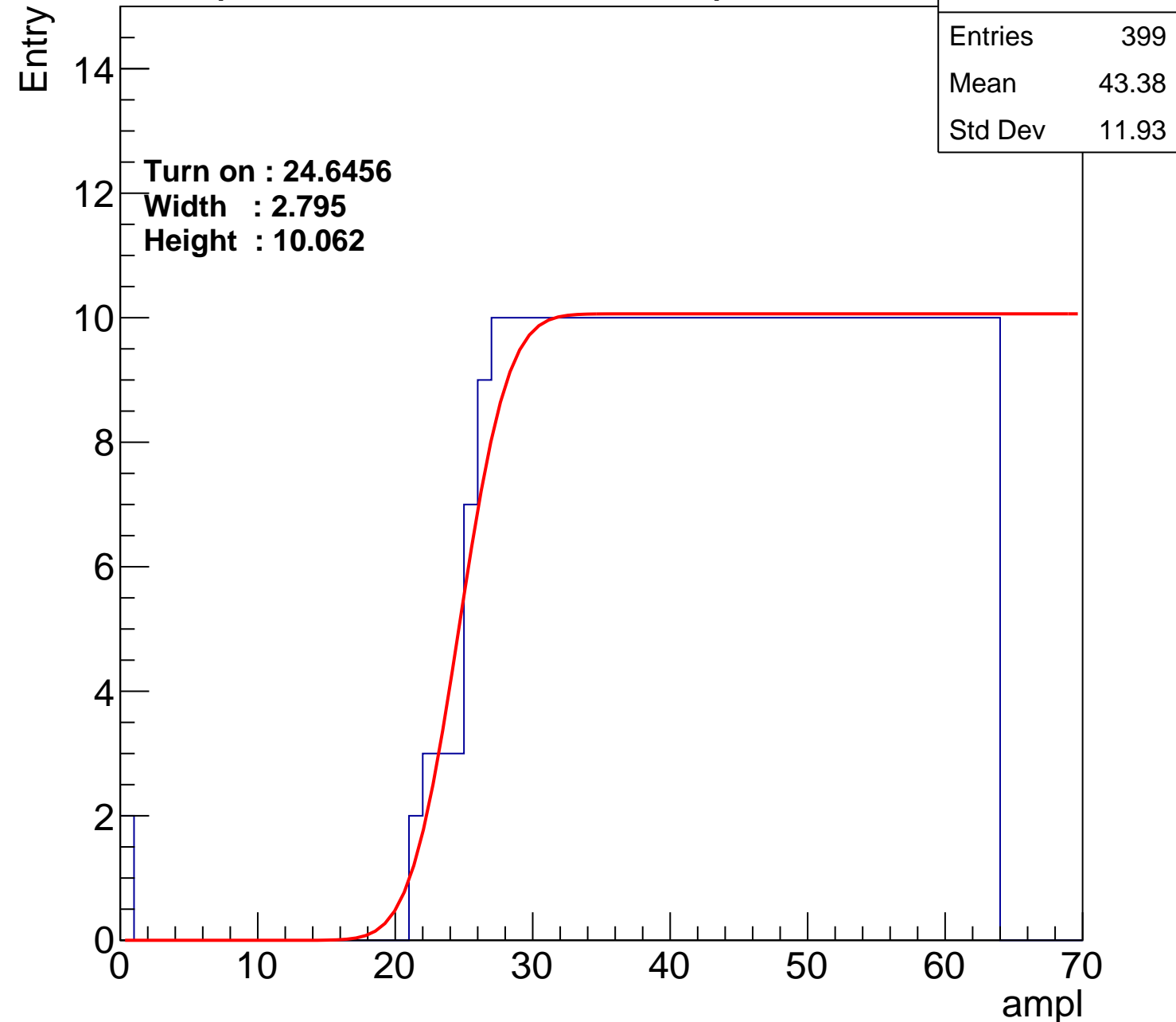
Width : 2.795

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch1

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.85
Std Dev	11.36

Turn on : 27.6394

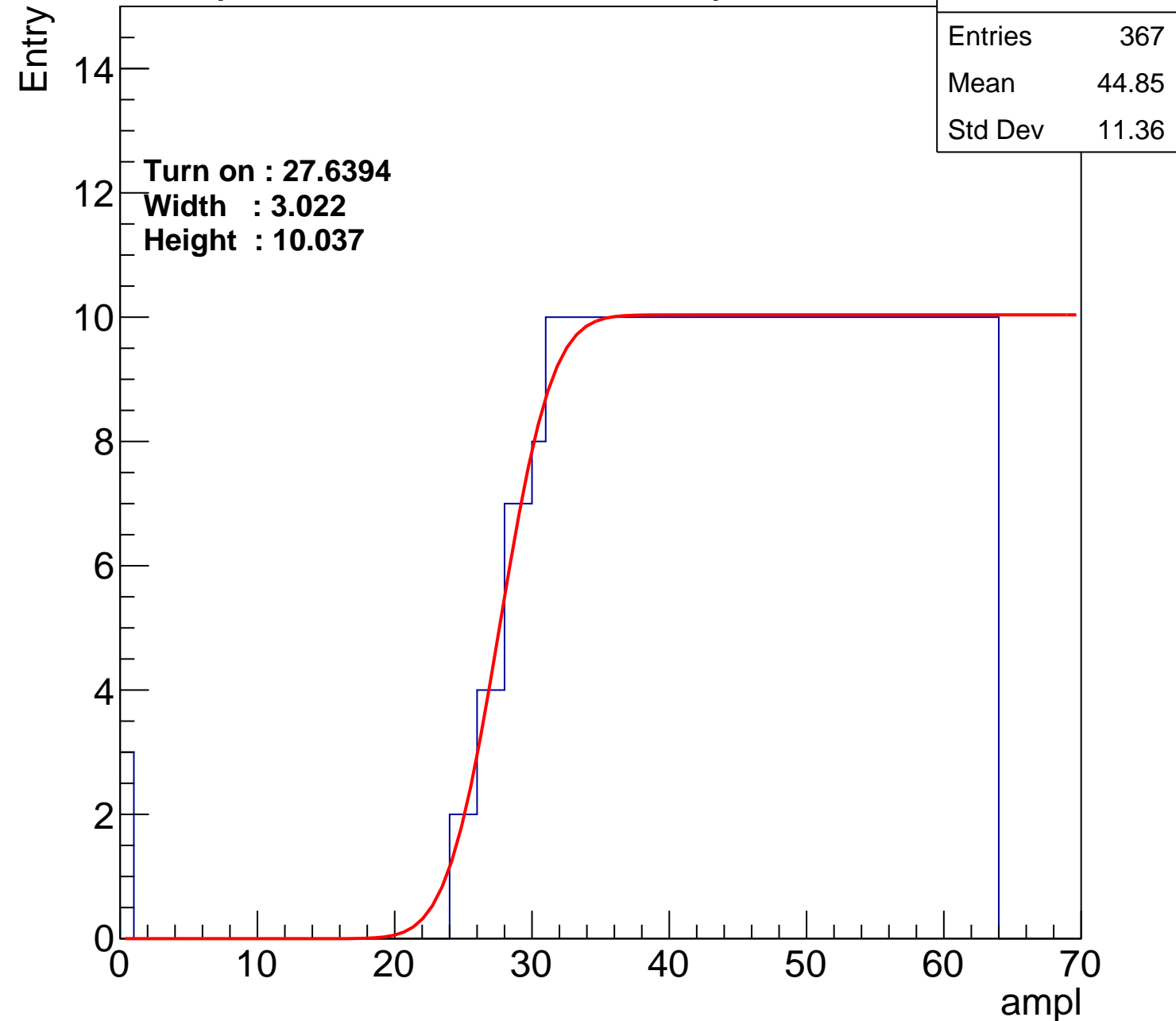
Width : 3.022

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch2

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.4334

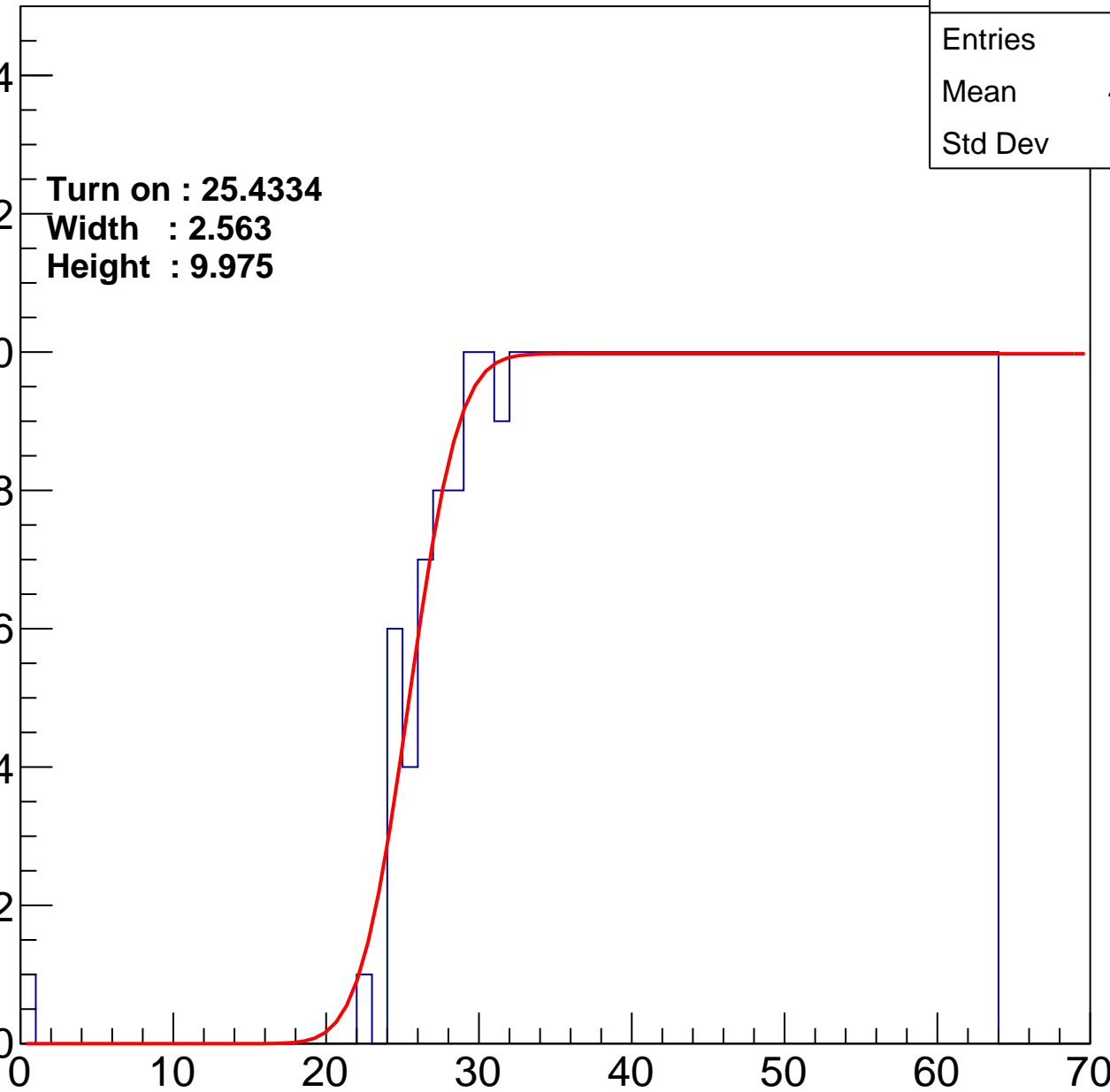
Width : 2.563

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch3

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.7079

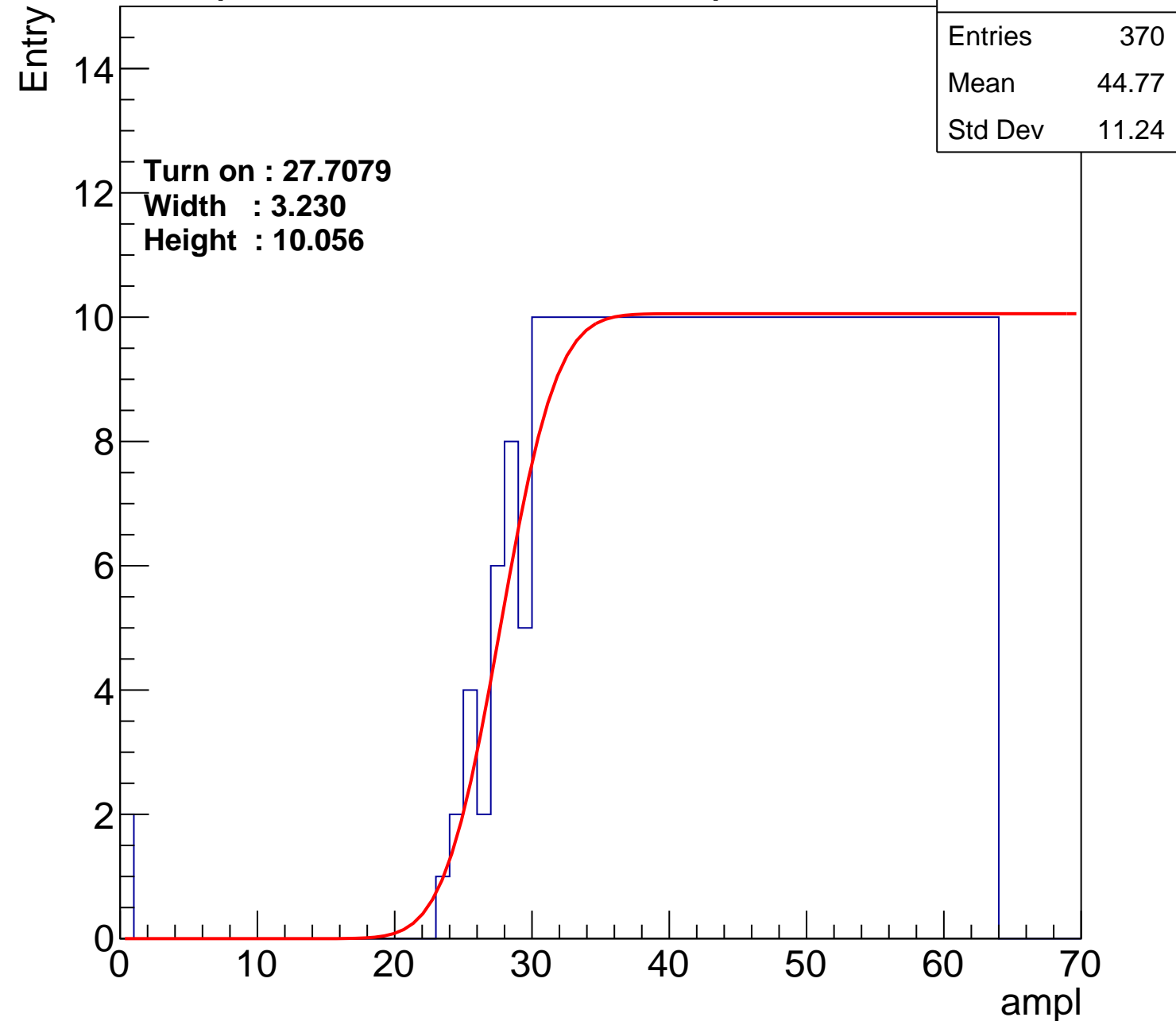
Width : 3.230

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch4

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.2193

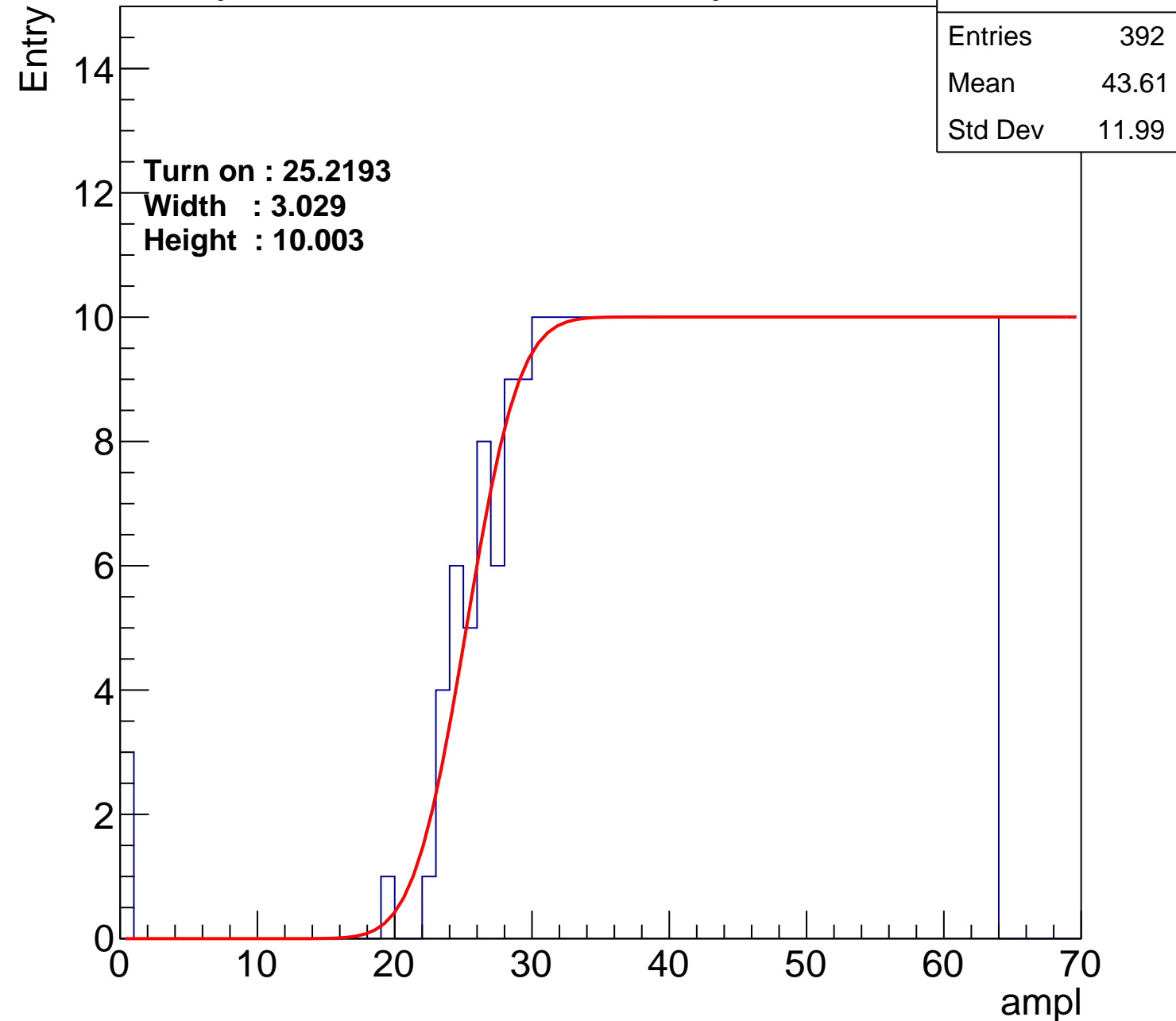
Width : 3.029

Height : 10.003

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch5

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.84
Std Dev	11.1

Turn on : 28.0055

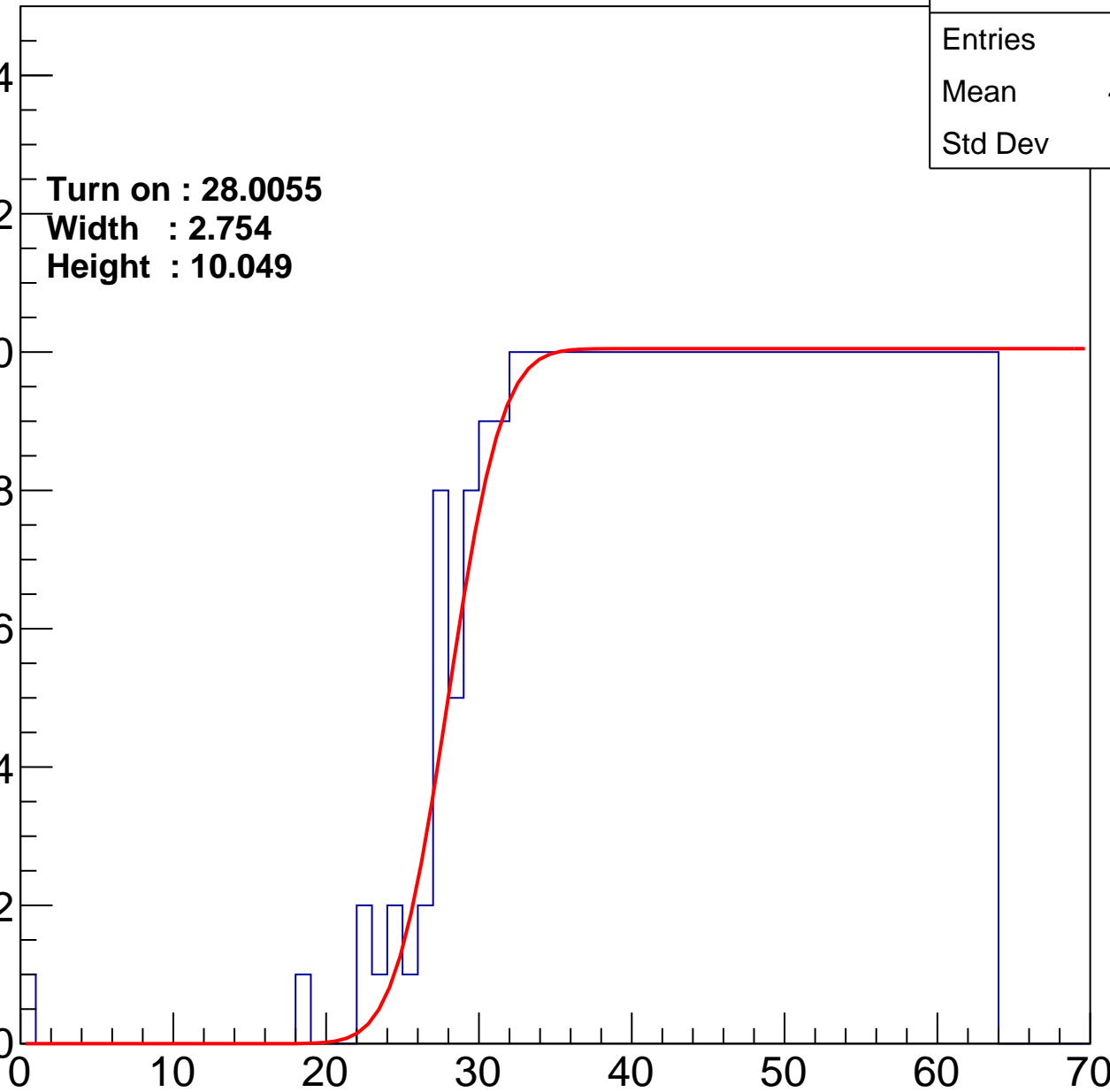
Width : 2.754

Height : 10.049

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch6

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.85
Std Dev	11.76

Turn on : 25.6279

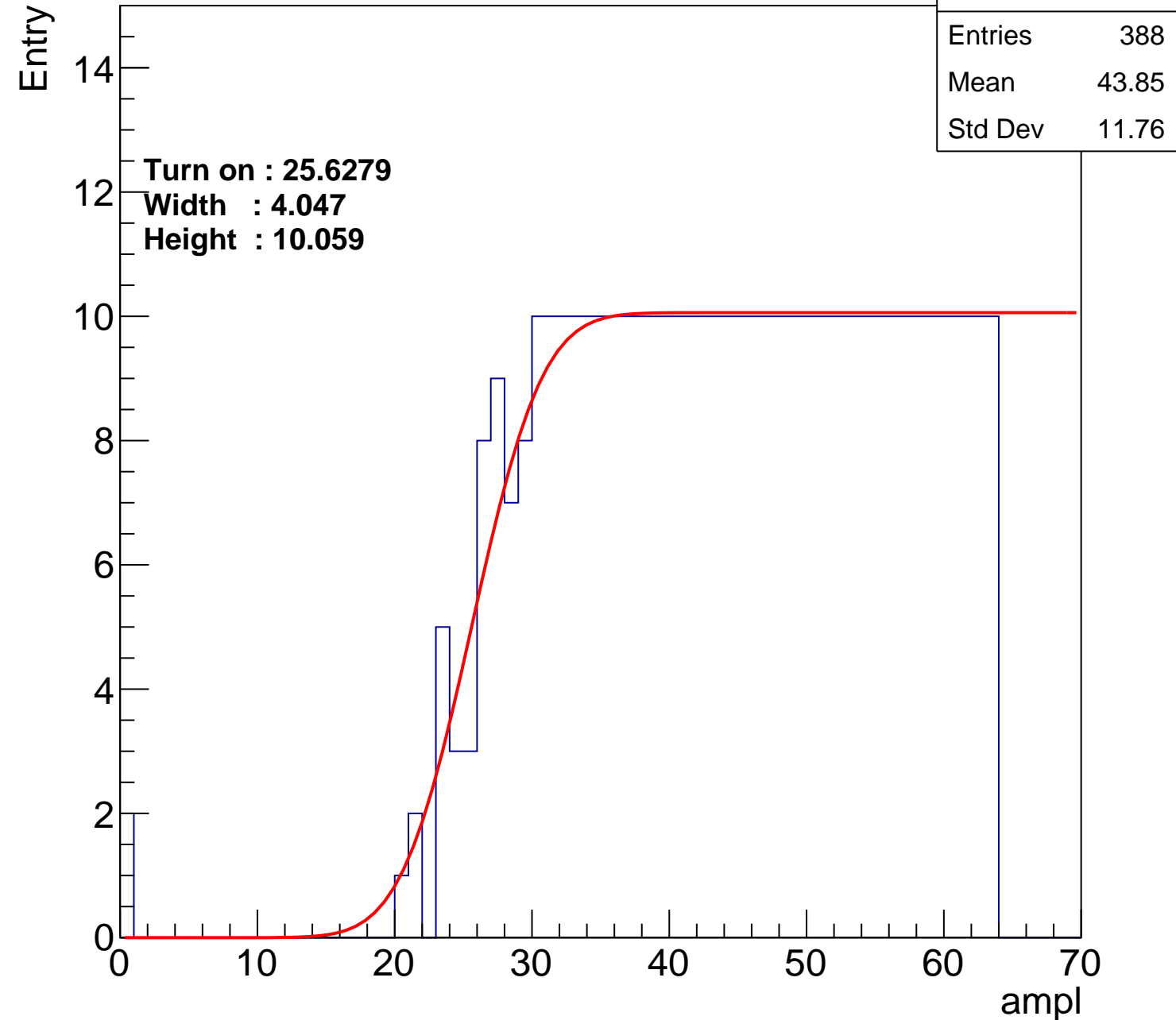
Width : 4.047

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch7

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.2203

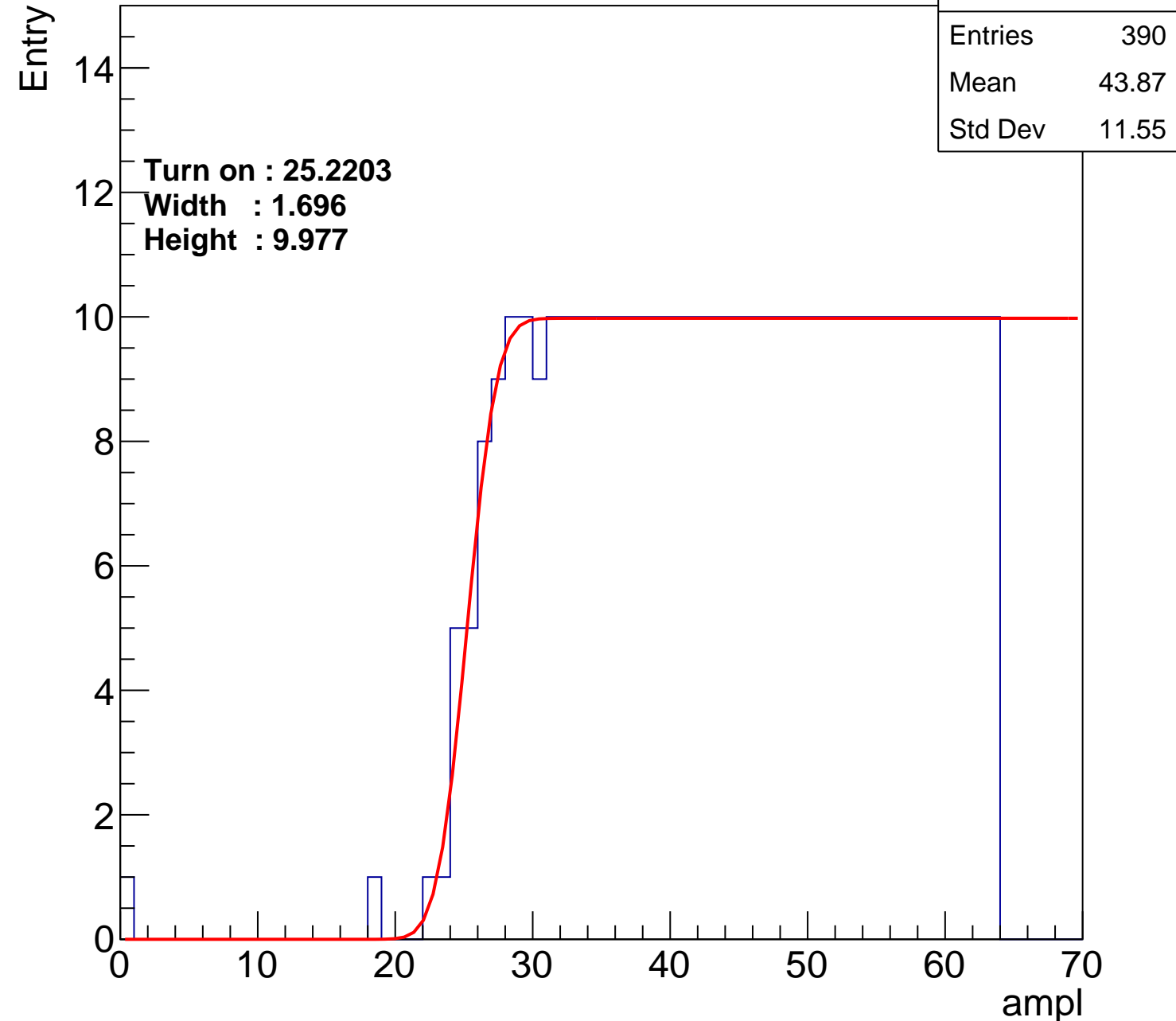
Width : 1.696

Height : 9.977

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch8

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.18
Std Dev	11.49

**Turn on : 26.1337**

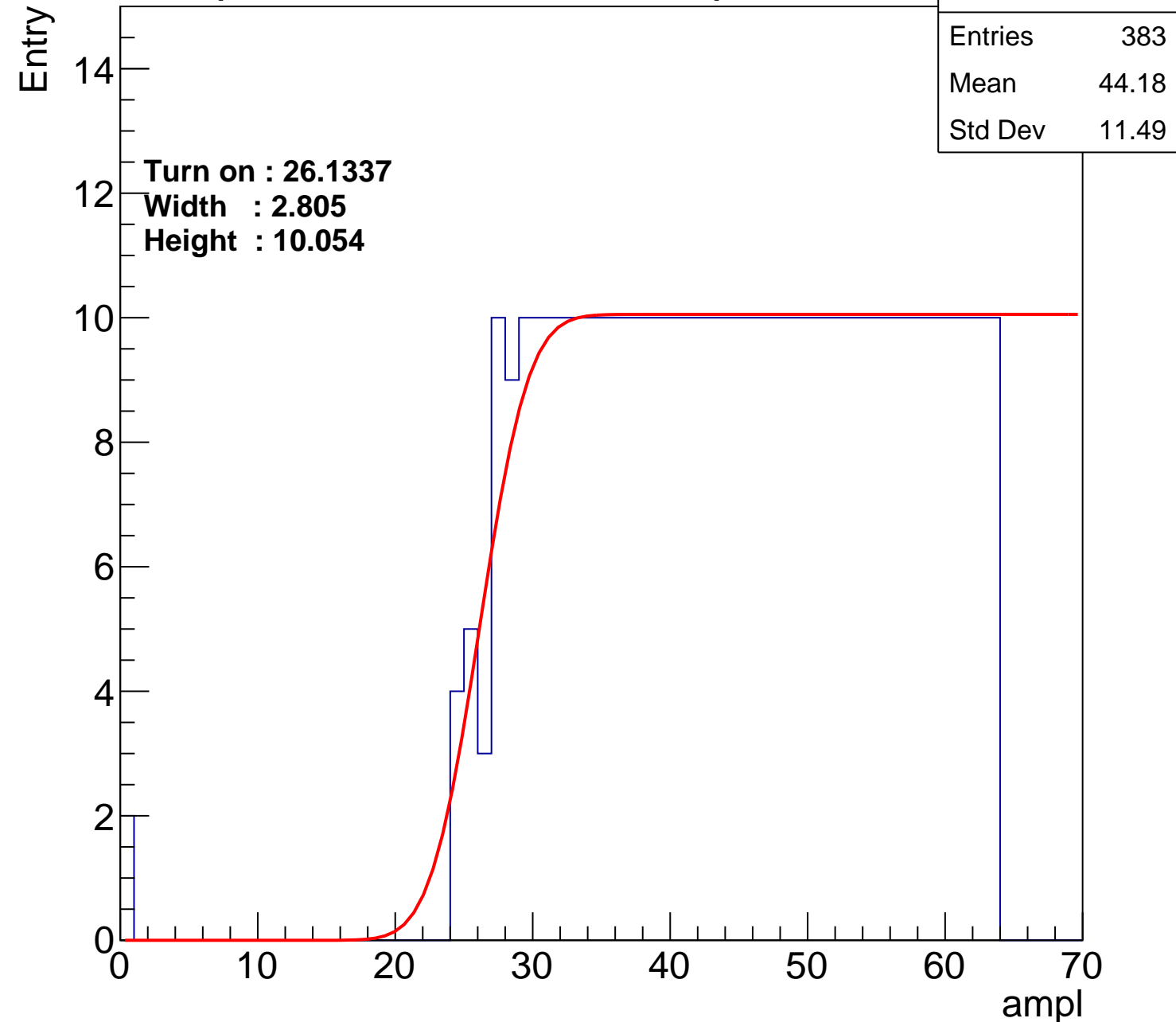
**Width : 2.805**

**Height : 10.054**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch9

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.47
Std Dev	12.16

Turn on : 25.3792

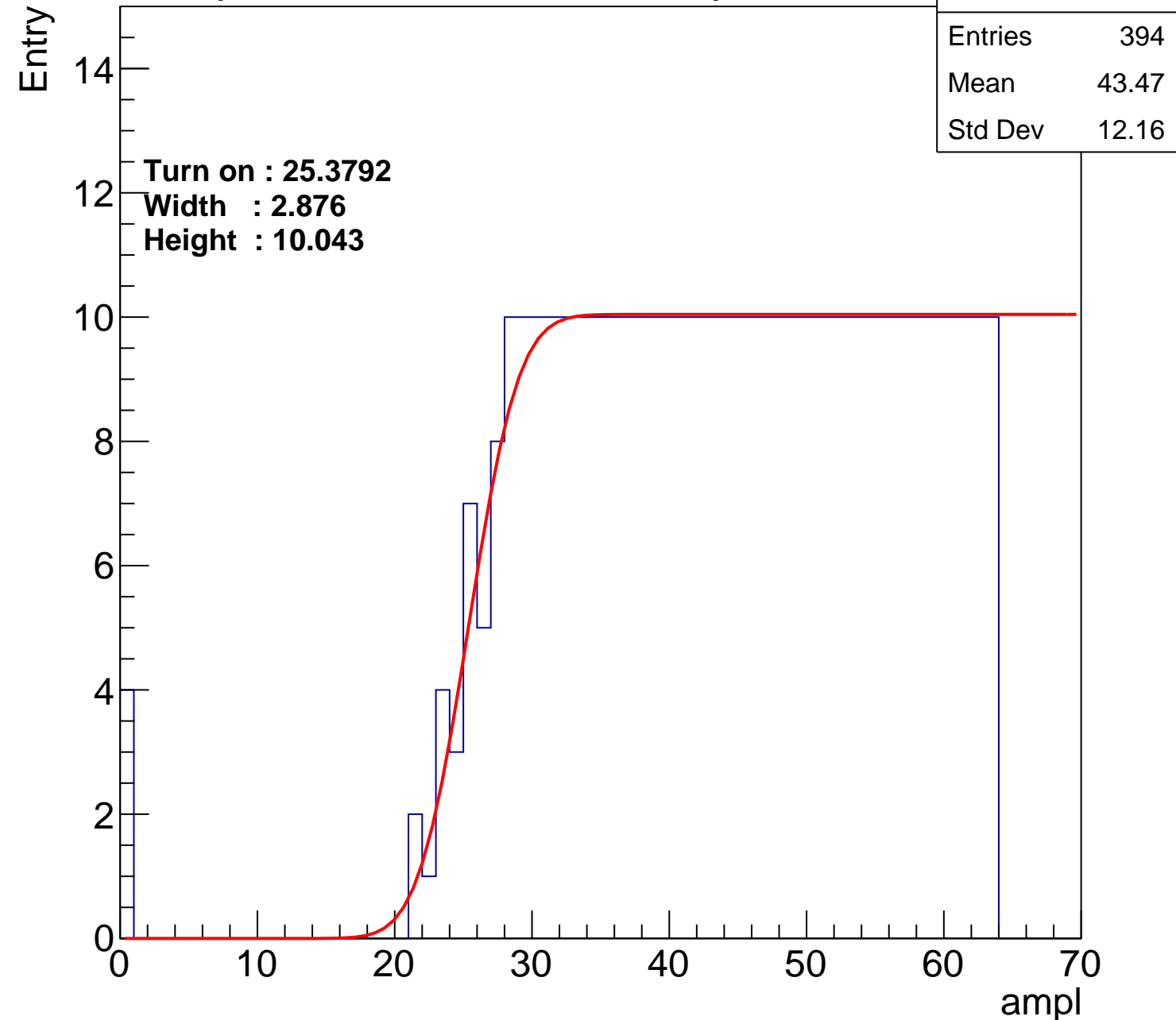
Width : 2.876

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch10

calib\_packv5\_042523\_0143.root, FC#12, port B1

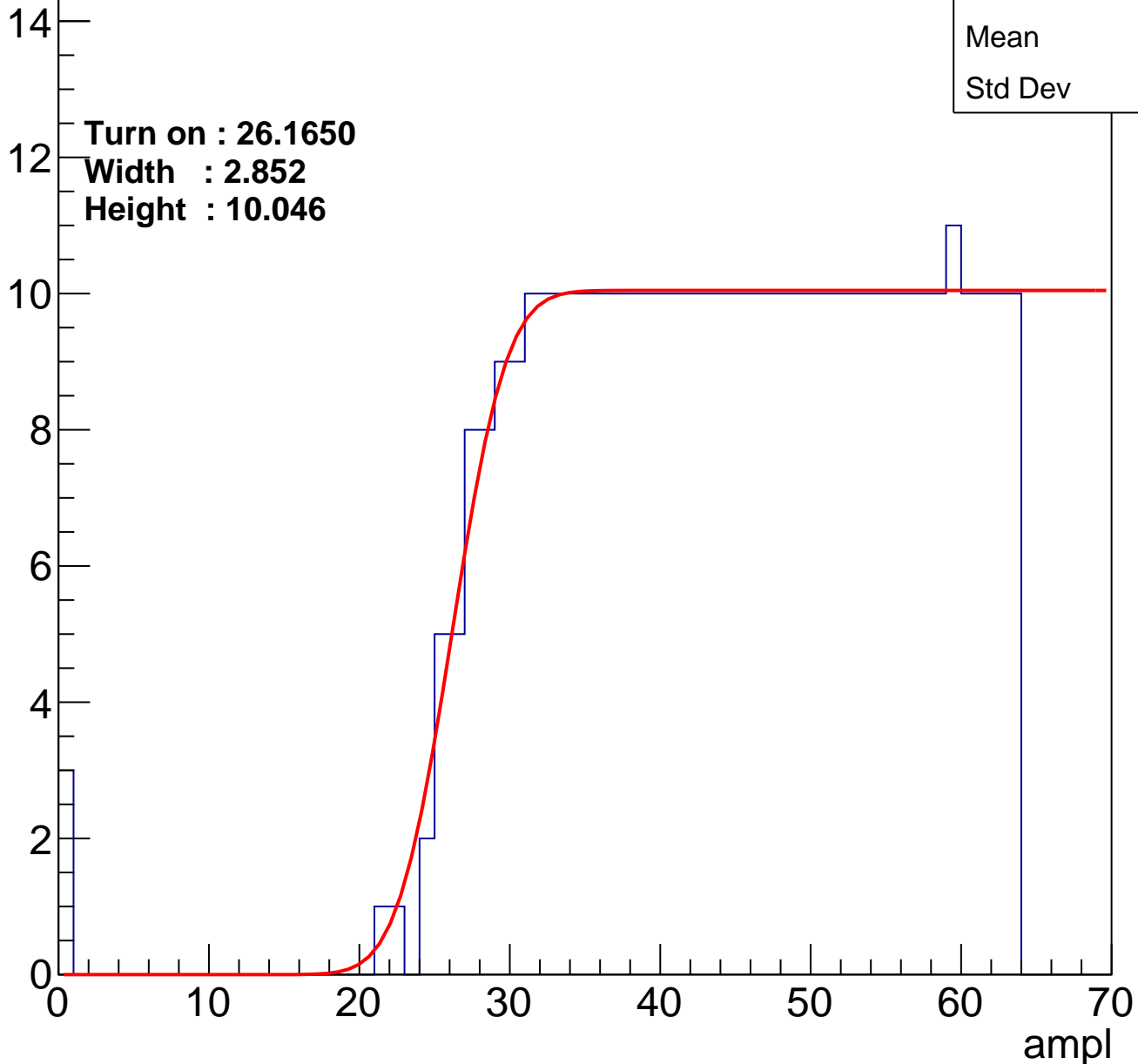
Entries	382
Mean	44.2
Std Dev	11.7

Turn on : 26.1650

Width : 2.852

Height : 10.046

Entry



# B0L102S, U22-ch11

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.78
Std Dev	11.21

Turn on : 27.1912

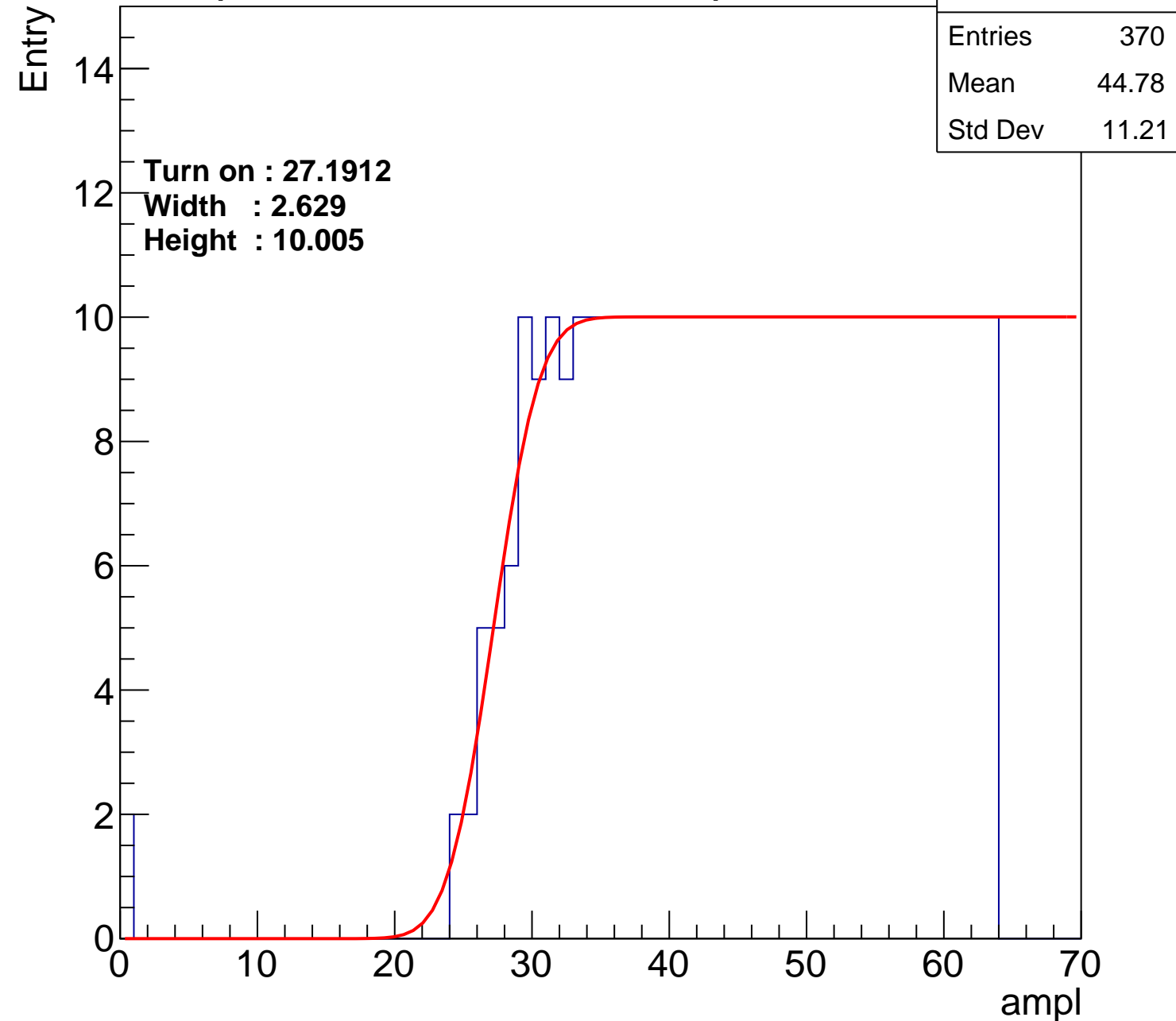
Width : 2.629

Height : 10.005

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch12

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.1724

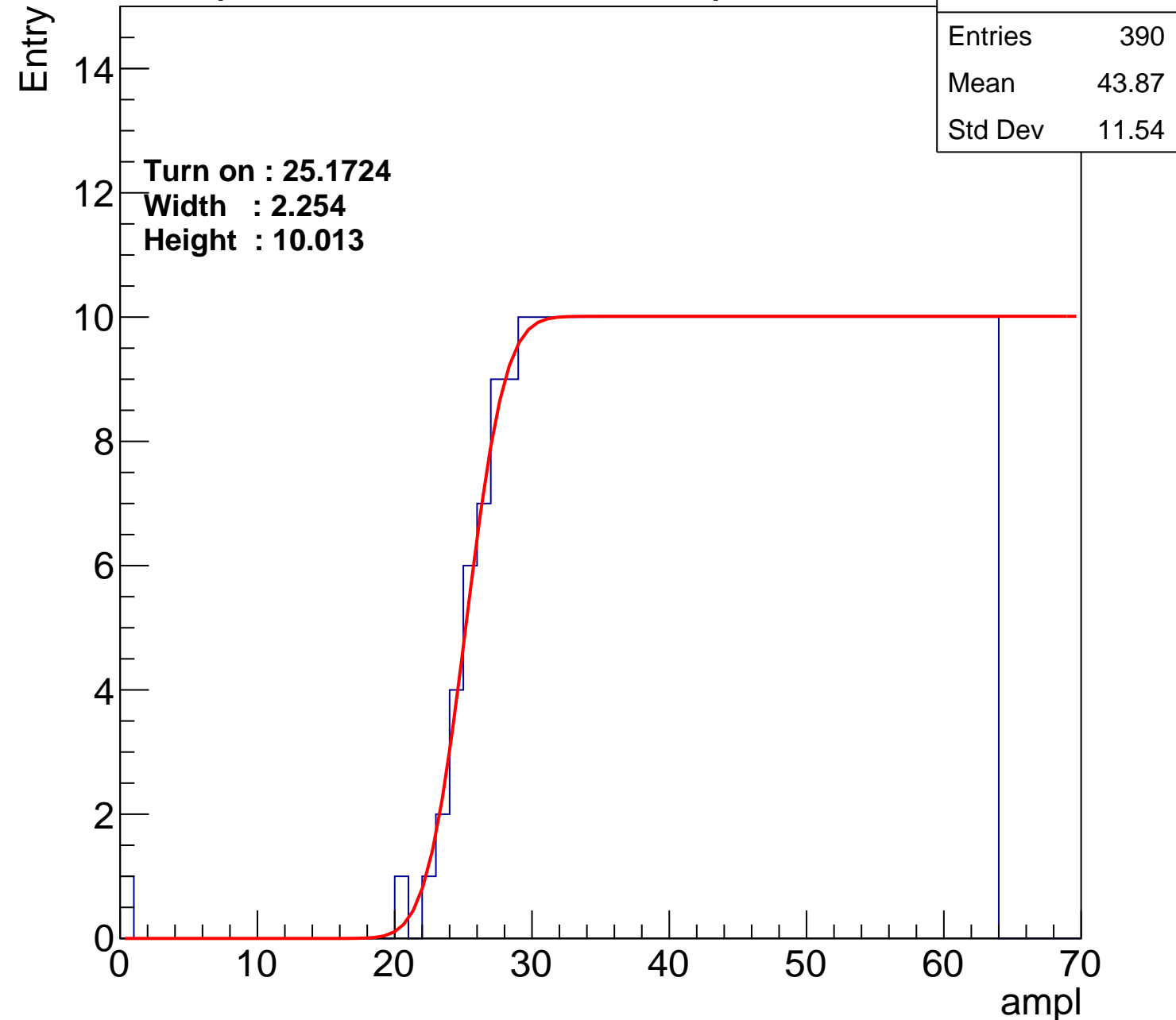
Width : 2.254

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch13

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	353
Mean	45.64
Std Dev	10.75

Turn on : 28.8976

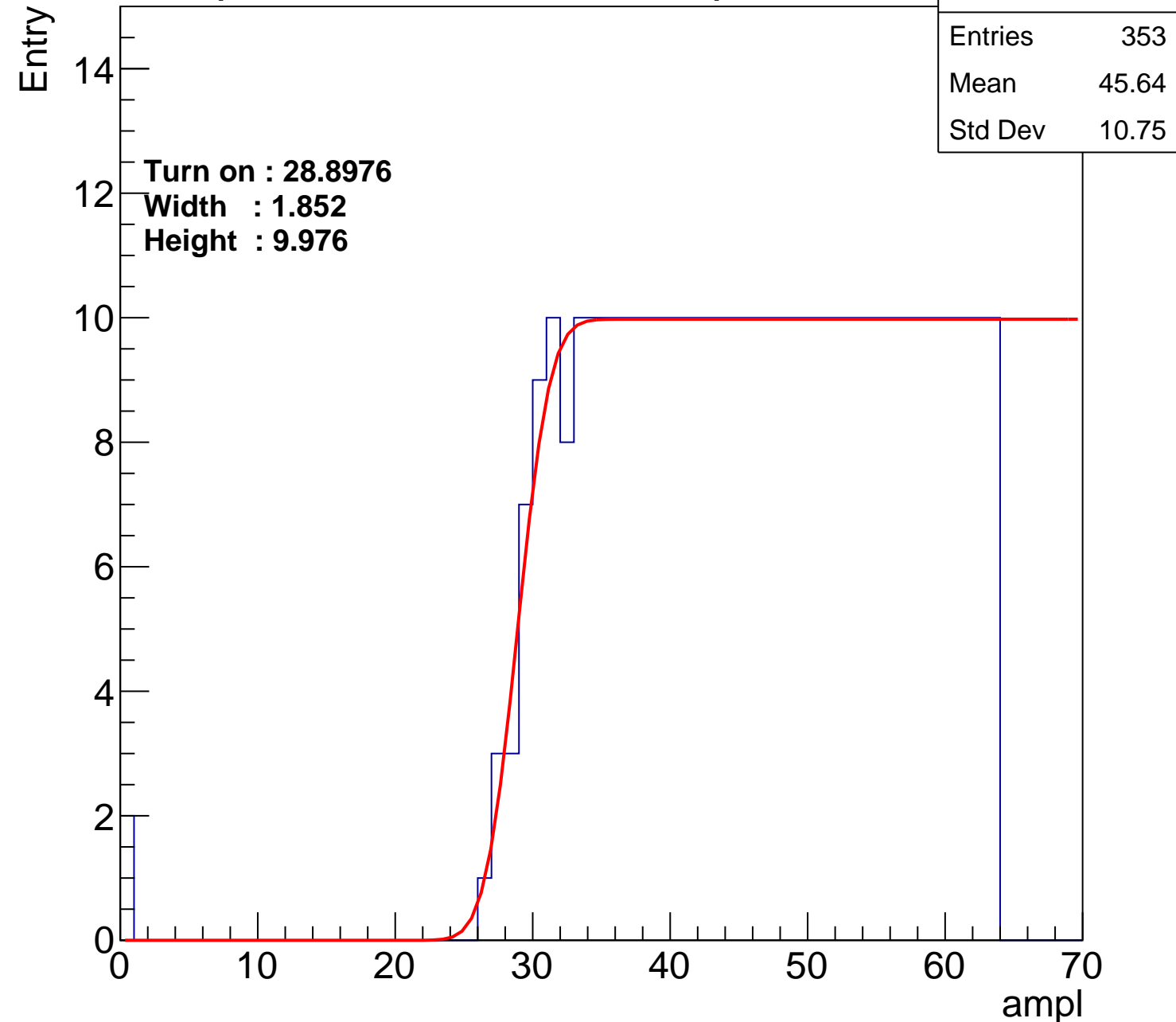
Width : 1.852

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch14

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	372
Mean	44.71
Std Dev	11.14

Turn on : 27.5149

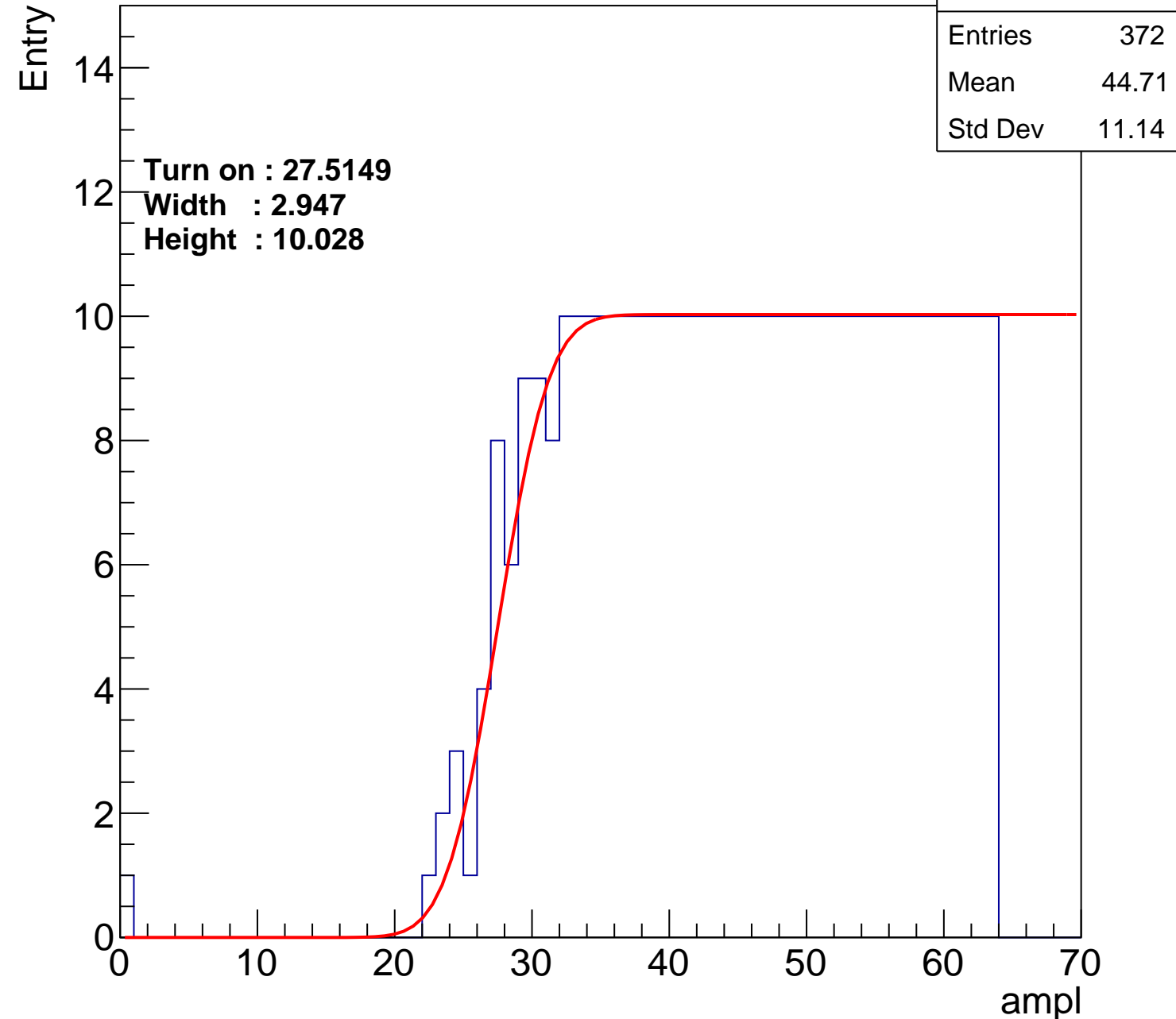
Width : 2.947

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch15

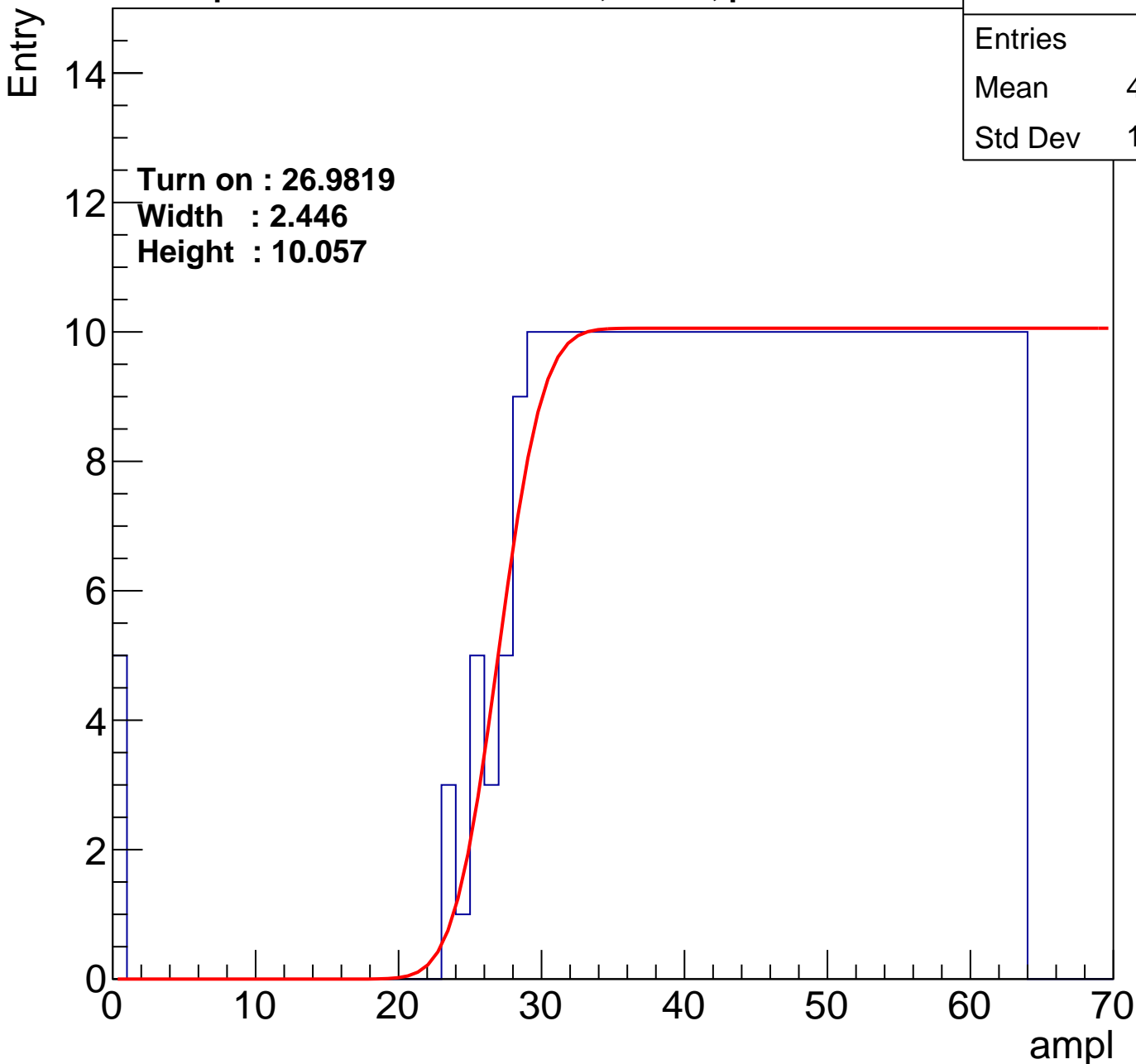
calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.05
Std Dev	12.02

Turn on : 26.9819

Width : 2.446

Height : 10.057



# B0L102S, U22-ch16

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.25
Std Dev	11.84

Turn on : 26.9600

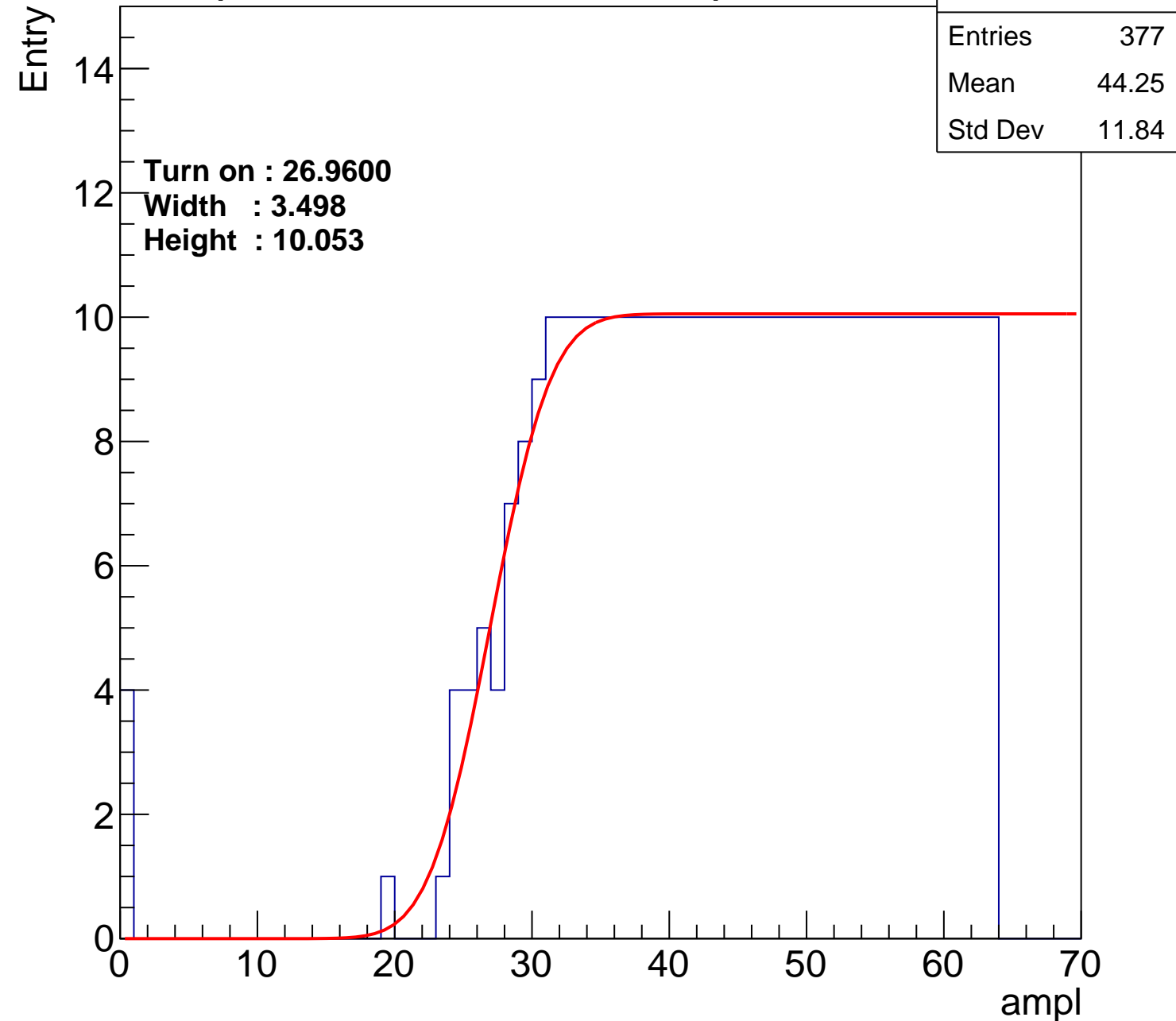
Width : 3.498

Height : 10.053

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch17

calib\_packv5\_042523\_0143.root, FC#12, port B1

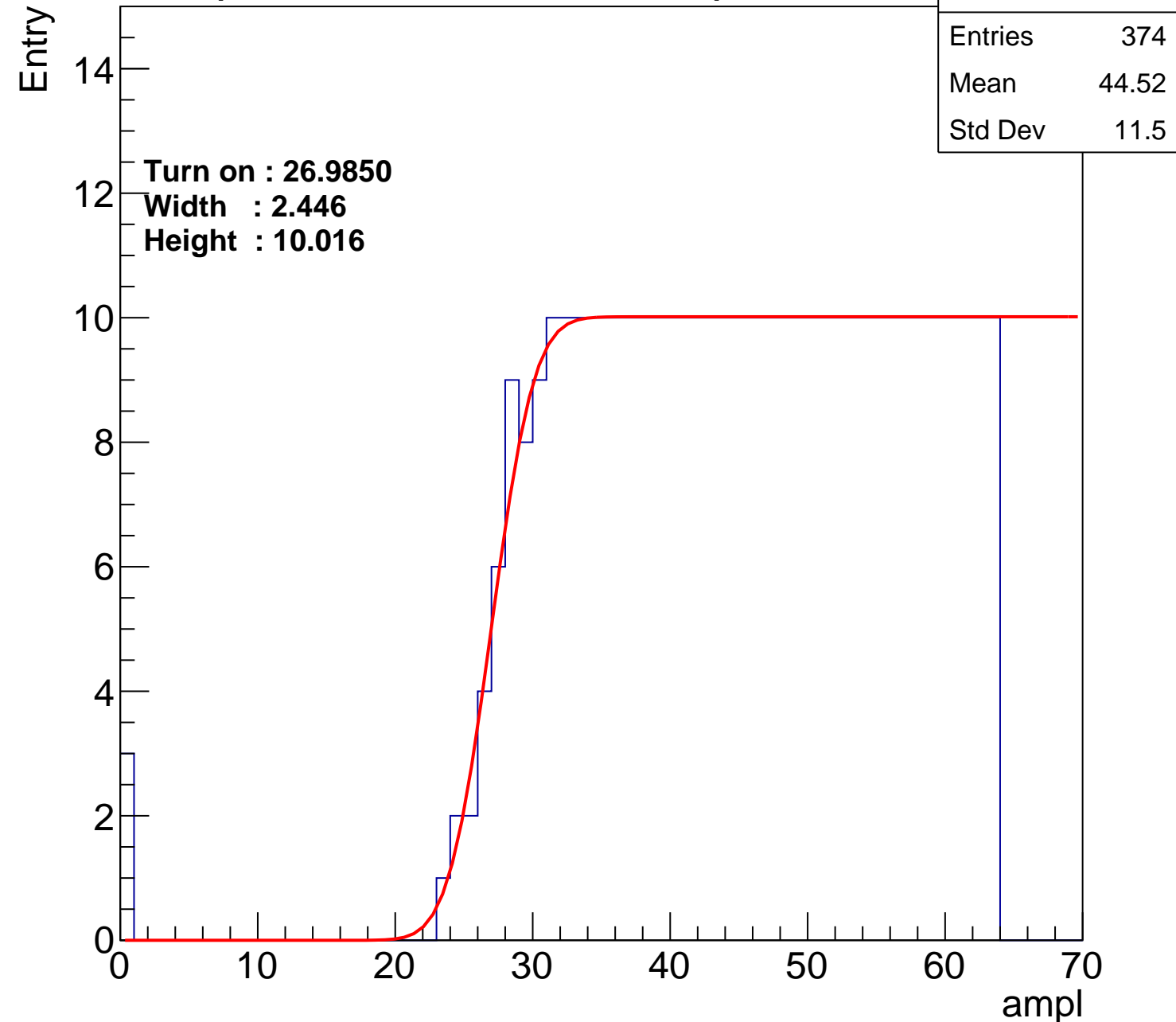
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.9850  
Width : 2.446  
Height : 10.016

Entries	374
Mean	44.52
Std Dev	11.5

ampl



# B0L102S, U22-ch18

calib\_packv5\_042523\_0143.root, FC#12, port B1

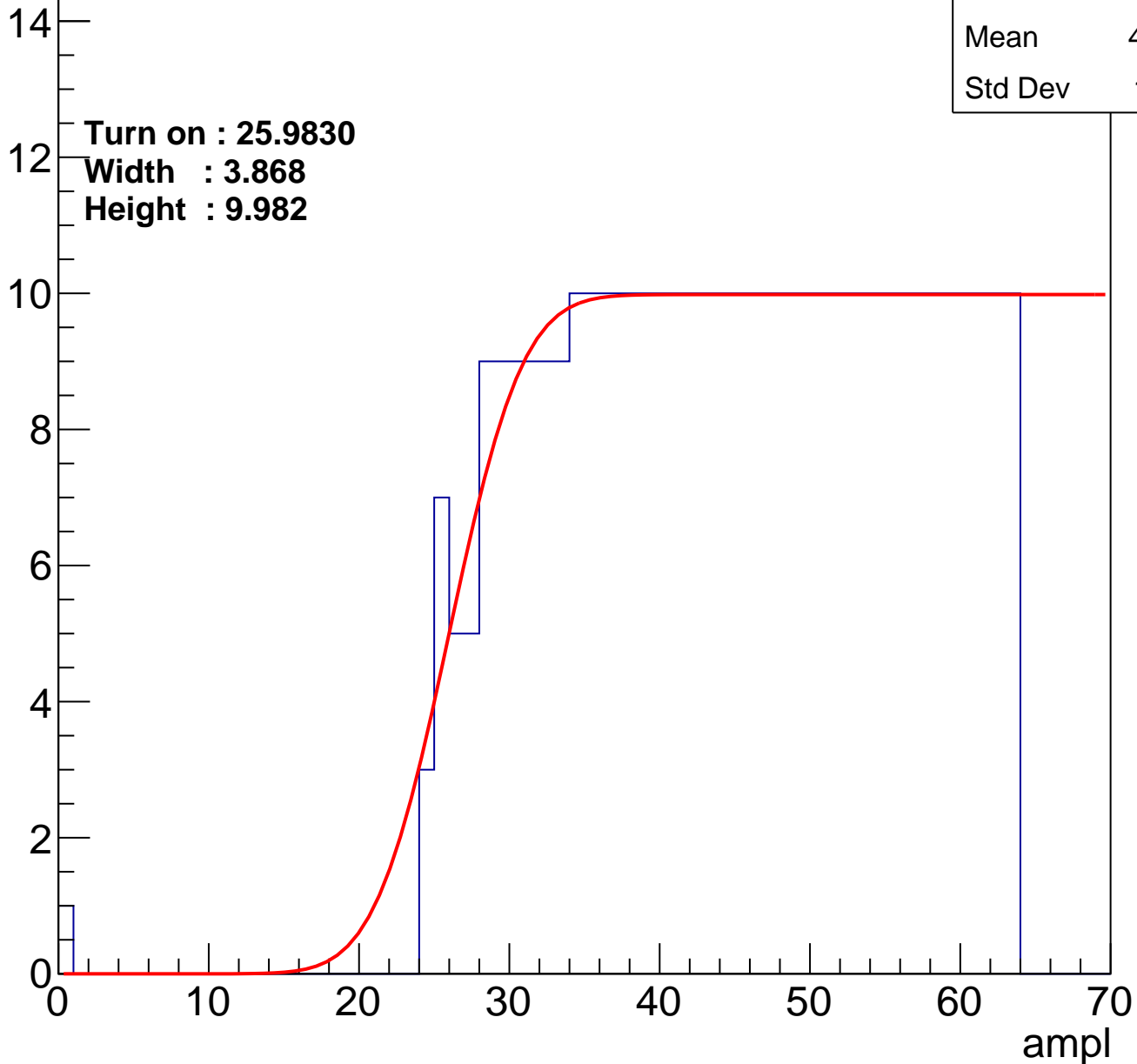
Entries	375
Mean	44.56
Std Dev	11.21

Turn on : 25.9830

Width : 3.868

Height : 9.982

Entry



# B0L102S, U22-ch19

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.38
Std Dev	11.62

Turn on : 27.4668

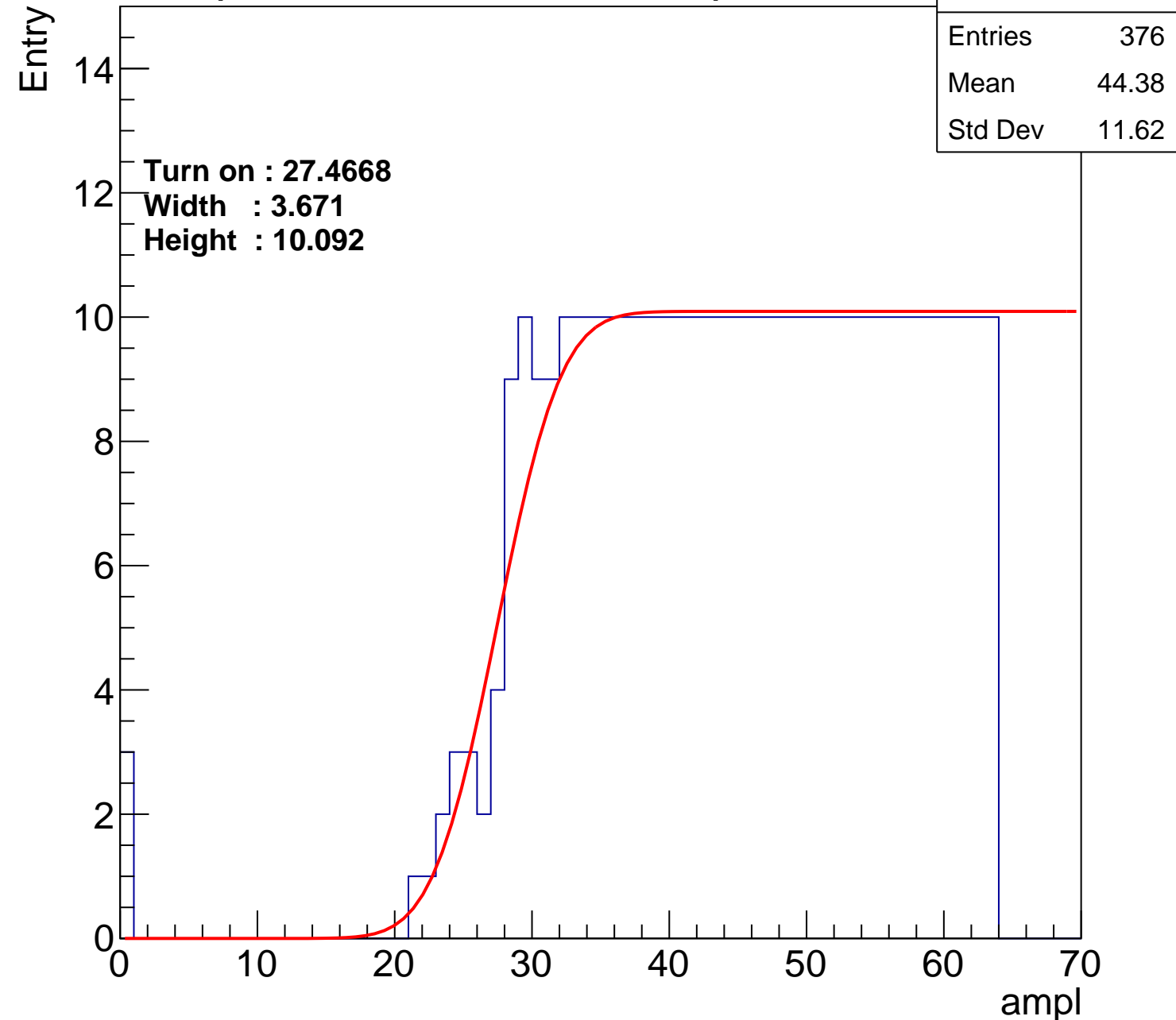
Width : 3.671

Height : 10.092

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch20

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.91
Std Dev	11.31

Turn on : 28.0434

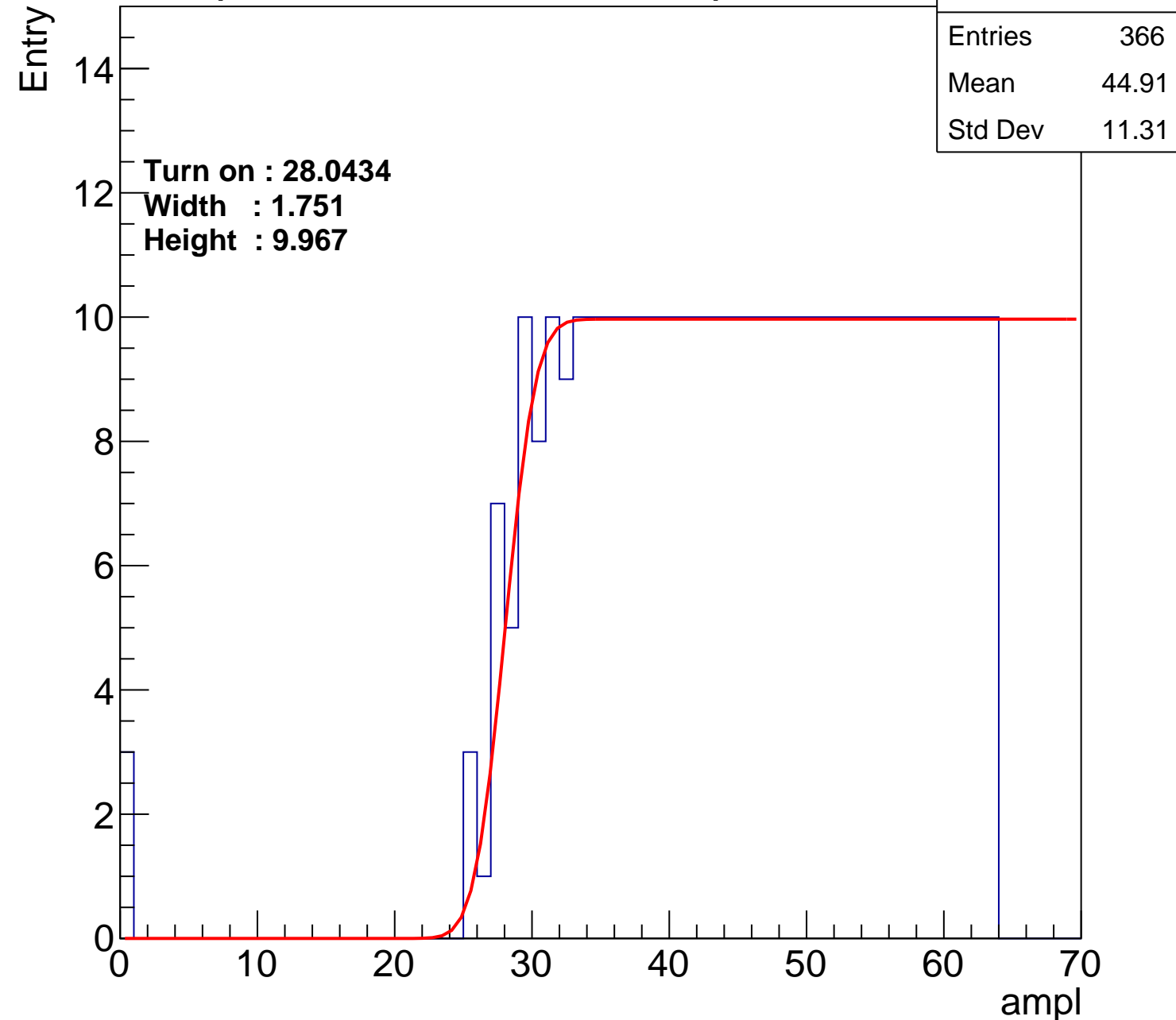
Width : 1.751

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch21

calib\_packv5\_042523\_0143.root, FC#12, port B1

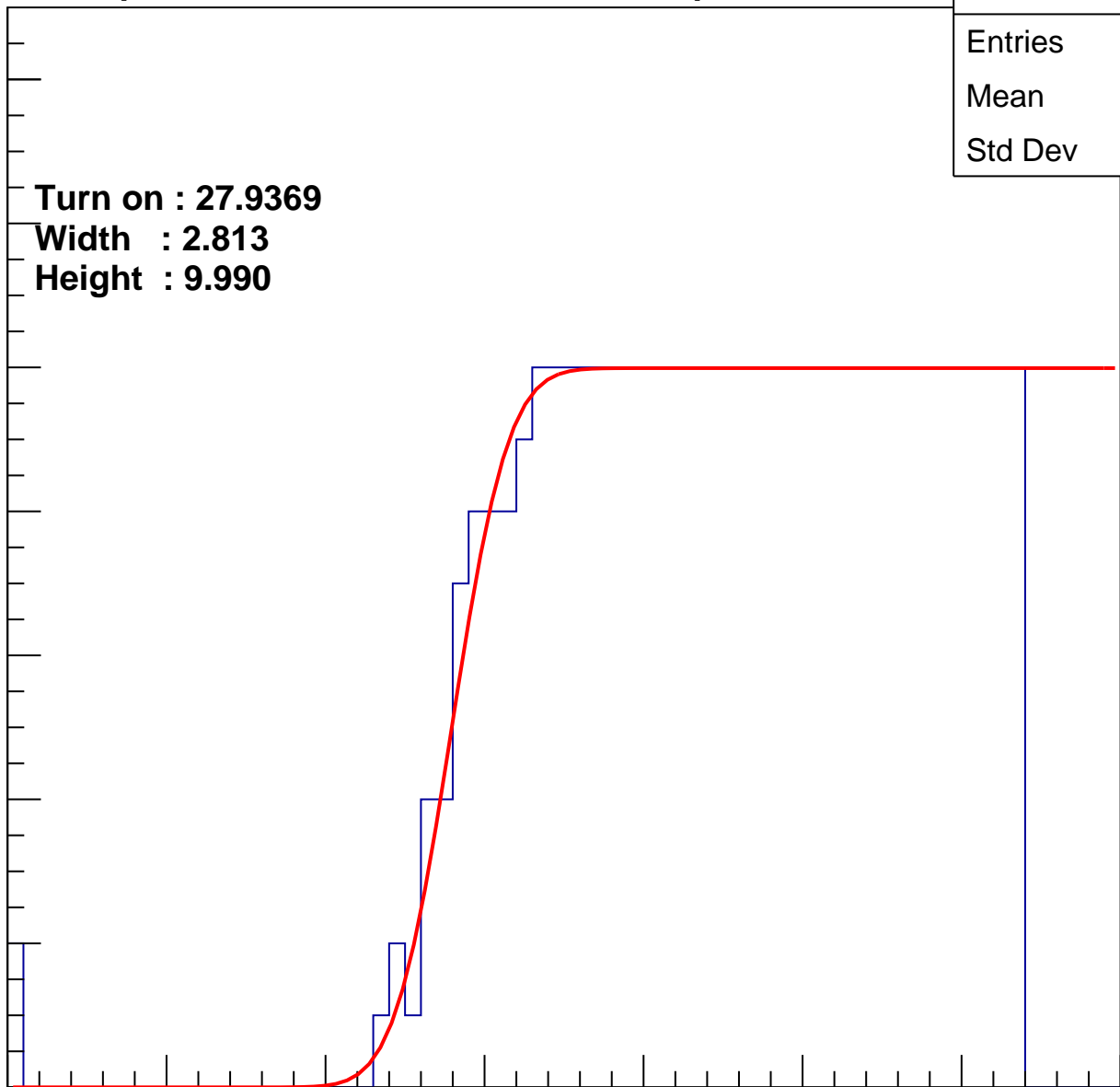
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.9369**  
**Width : 2.813**  
**Height : 9.990**

Entries	364
Mean	45.03
Std Dev	11.13

ampl



# B0L102S, U22-ch22

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.98
Std Dev	11.78

Turn on : 25.7078

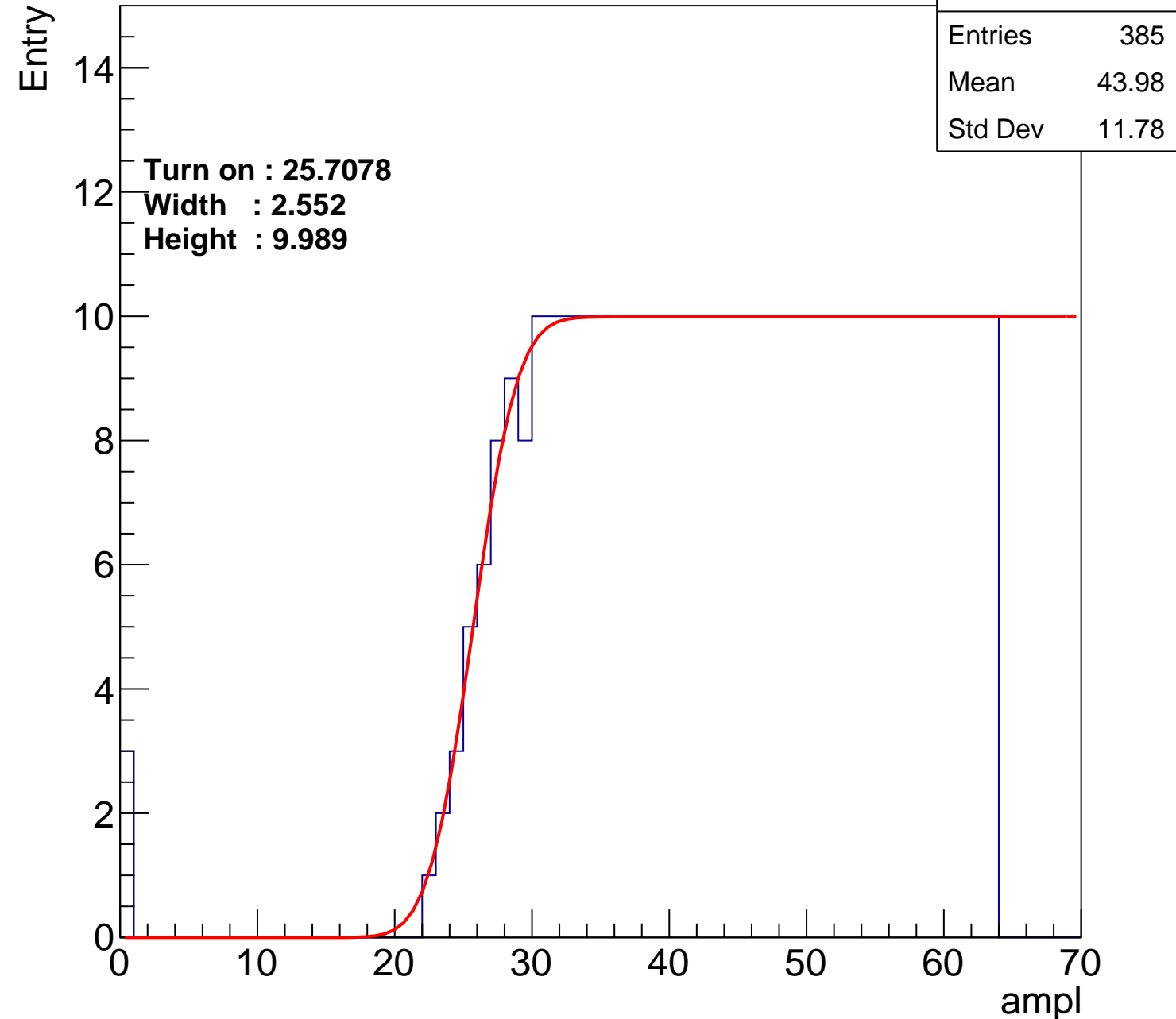
Width : 2.552

Height : 9.989

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch23

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.77
Std Dev	11.11

Turn on : 27.3531

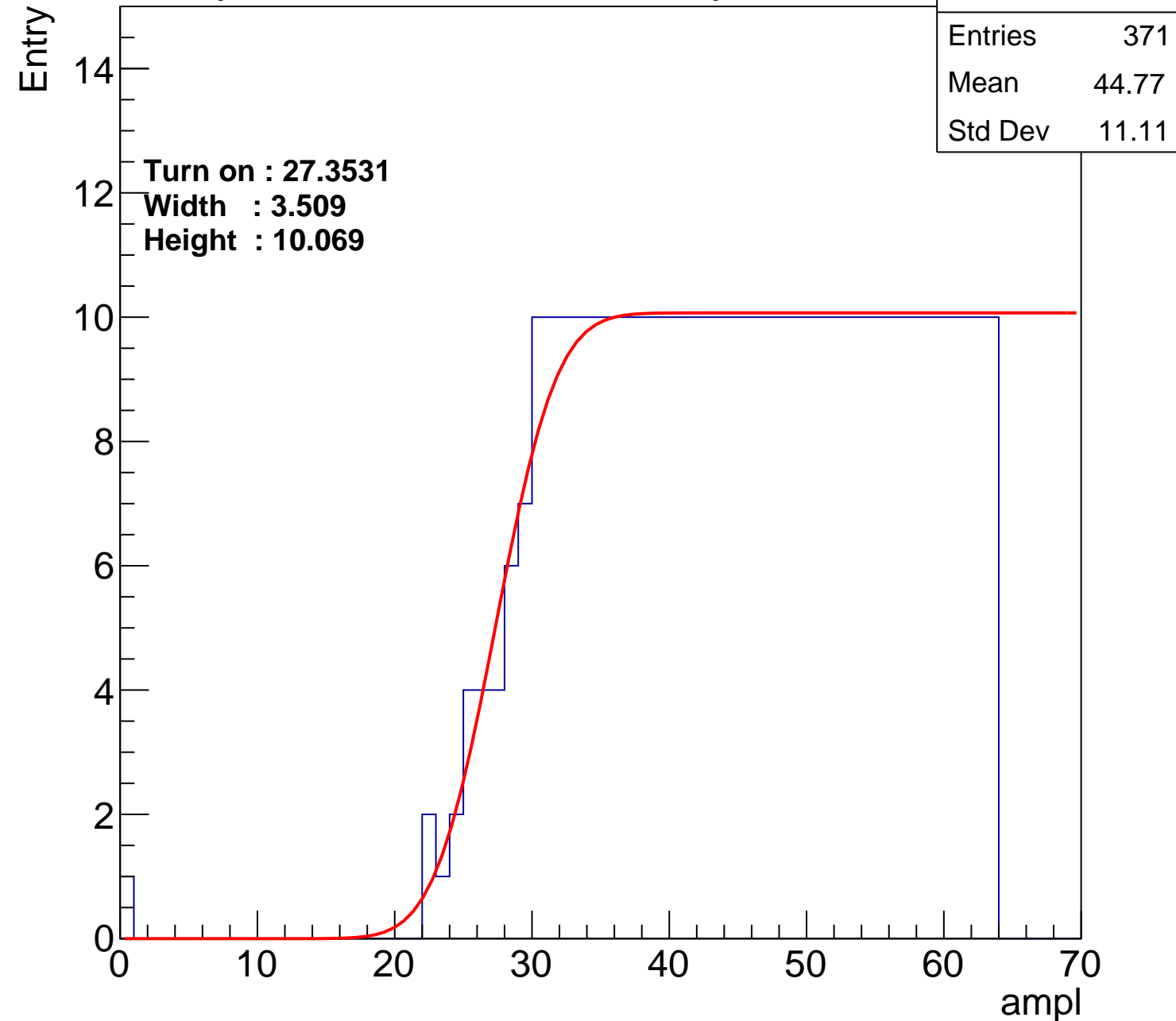
Width : 3.509

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch24

calib\_packv5\_042523\_0143.root, FC#12, port B1

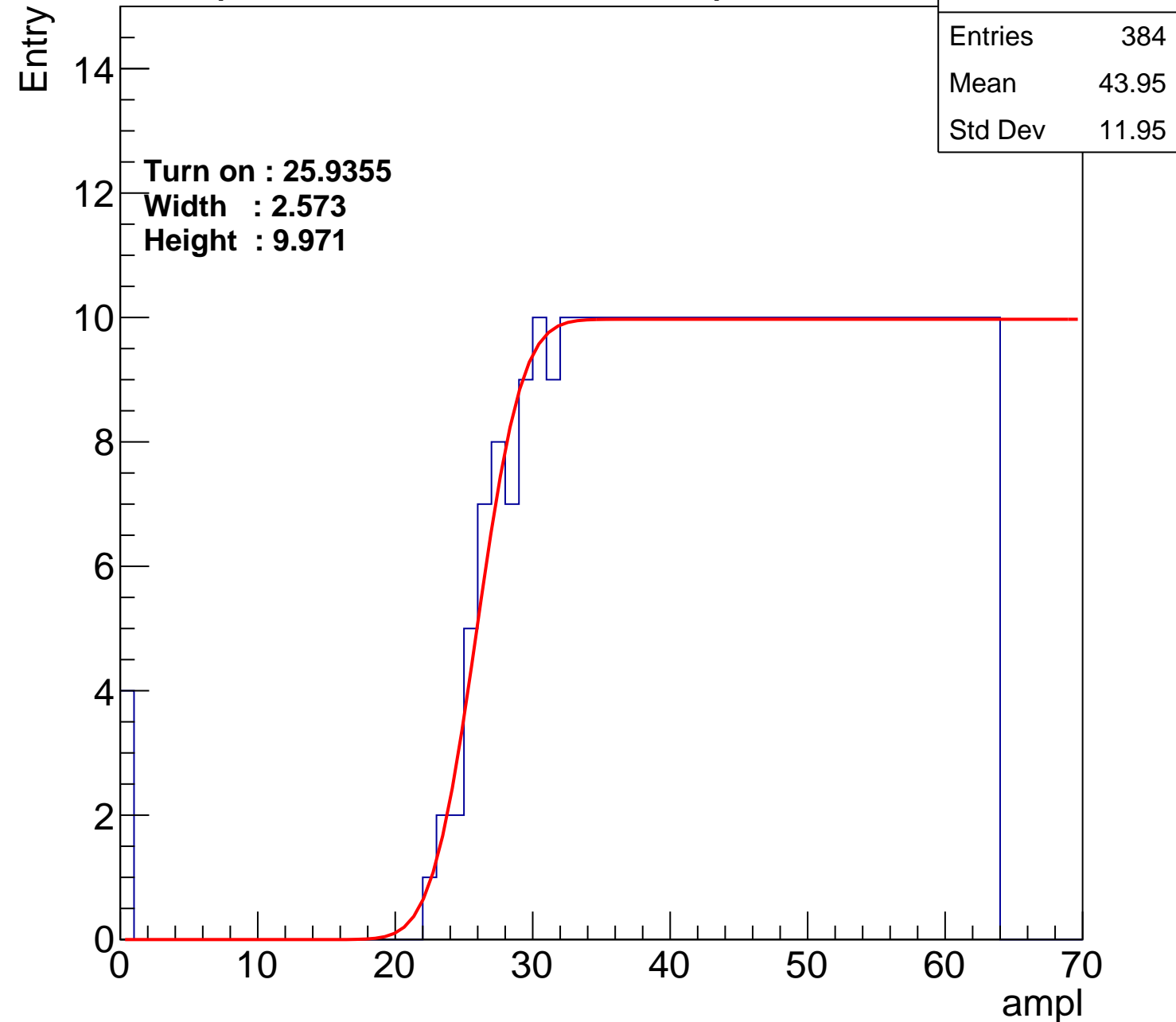
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.9355  
Width : 2.573  
Height : 9.971

Entries	384
Mean	43.95
Std Dev	11.95

ampl



# B0L102S, U22-ch25

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.23
Std Dev	11.7

Turn on : 26.7863

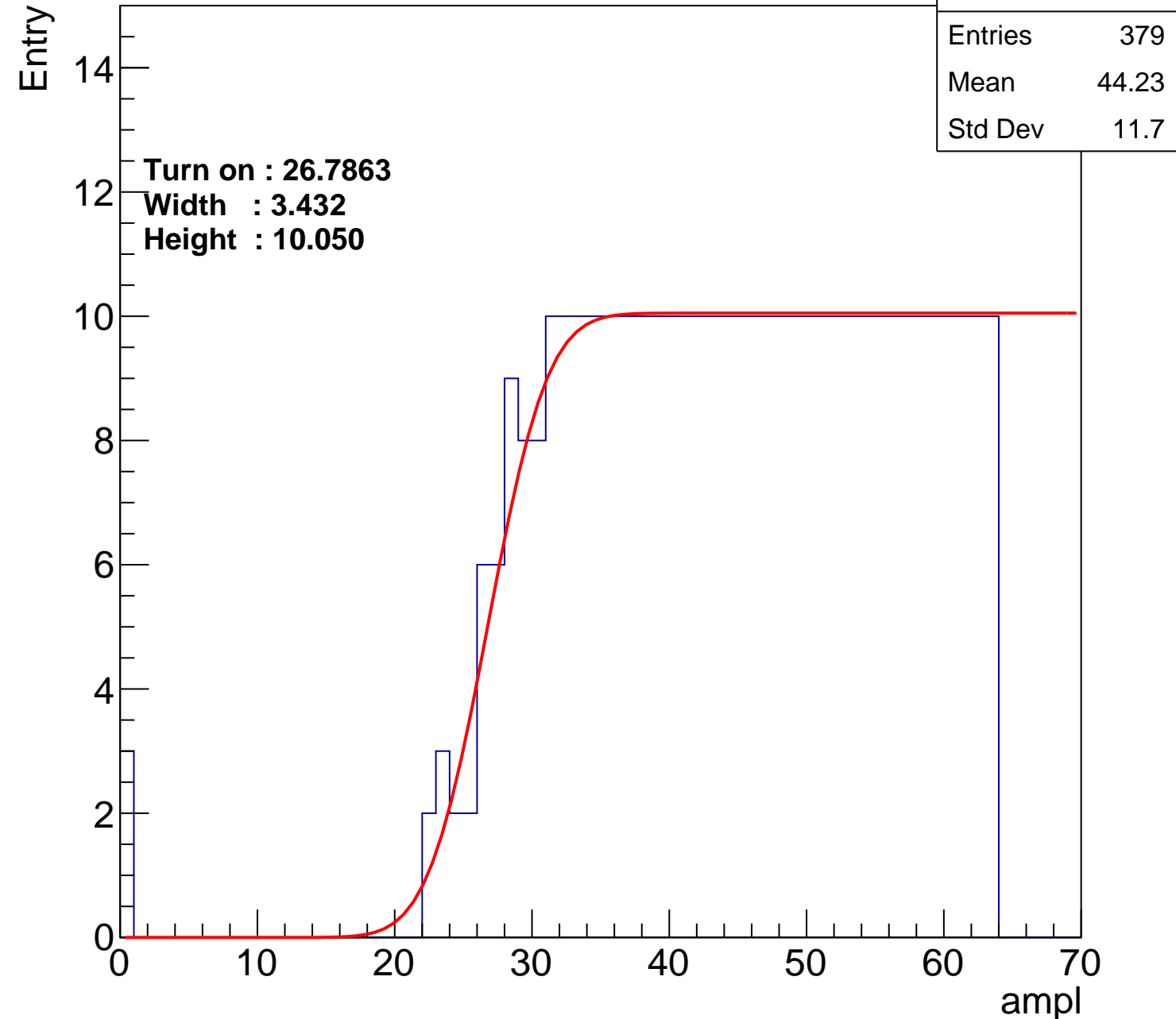
Width : 3.432

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch26

calib\_packv5\_042523\_0143.root, FC#12, port B1

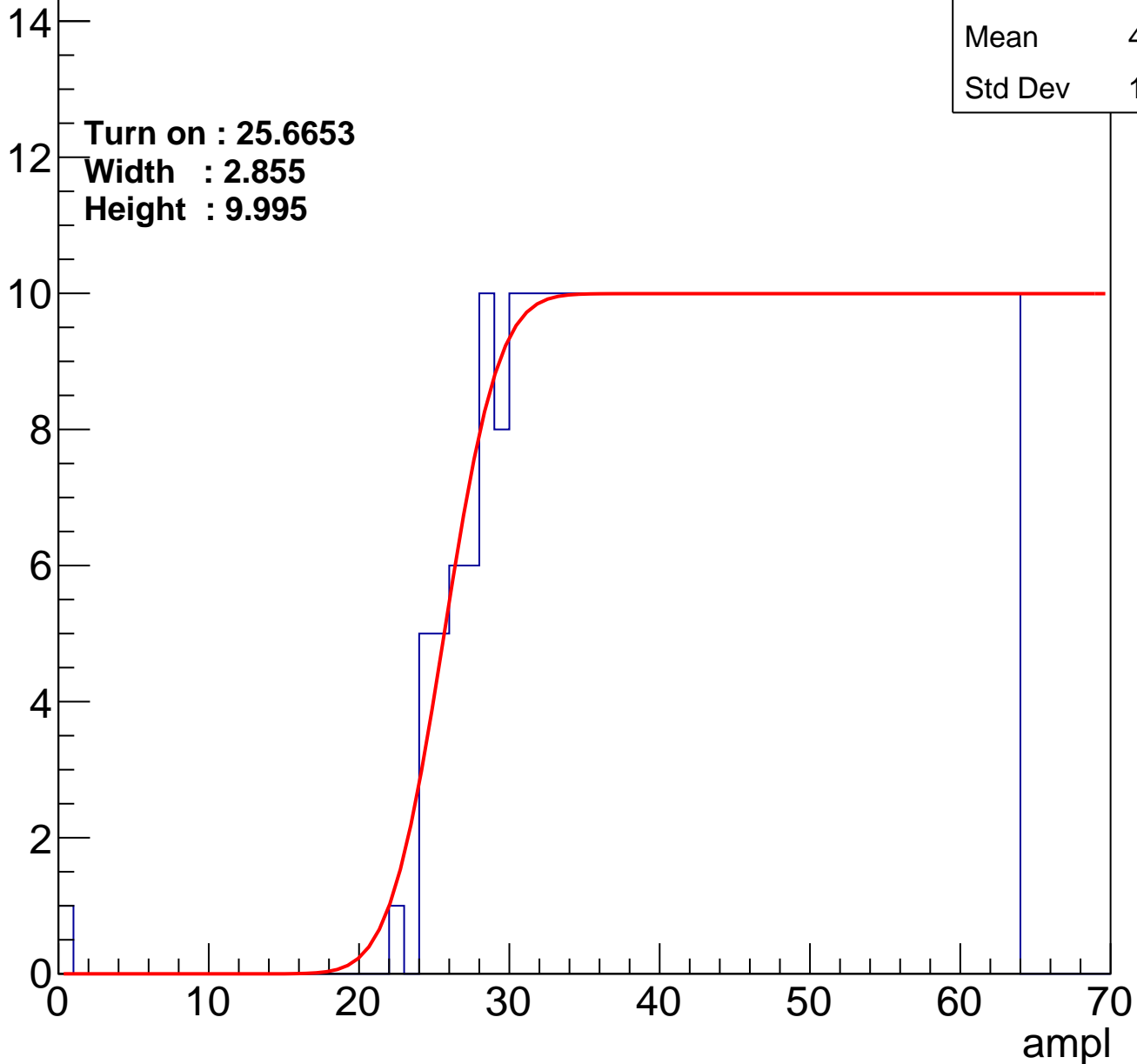
Entries	382
Mean	44.26
Std Dev	11.34

Turn on : 25.6653

Width : 2.855

Height : 9.995

Entry



# B0L102S, U22-ch27

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 25.3729

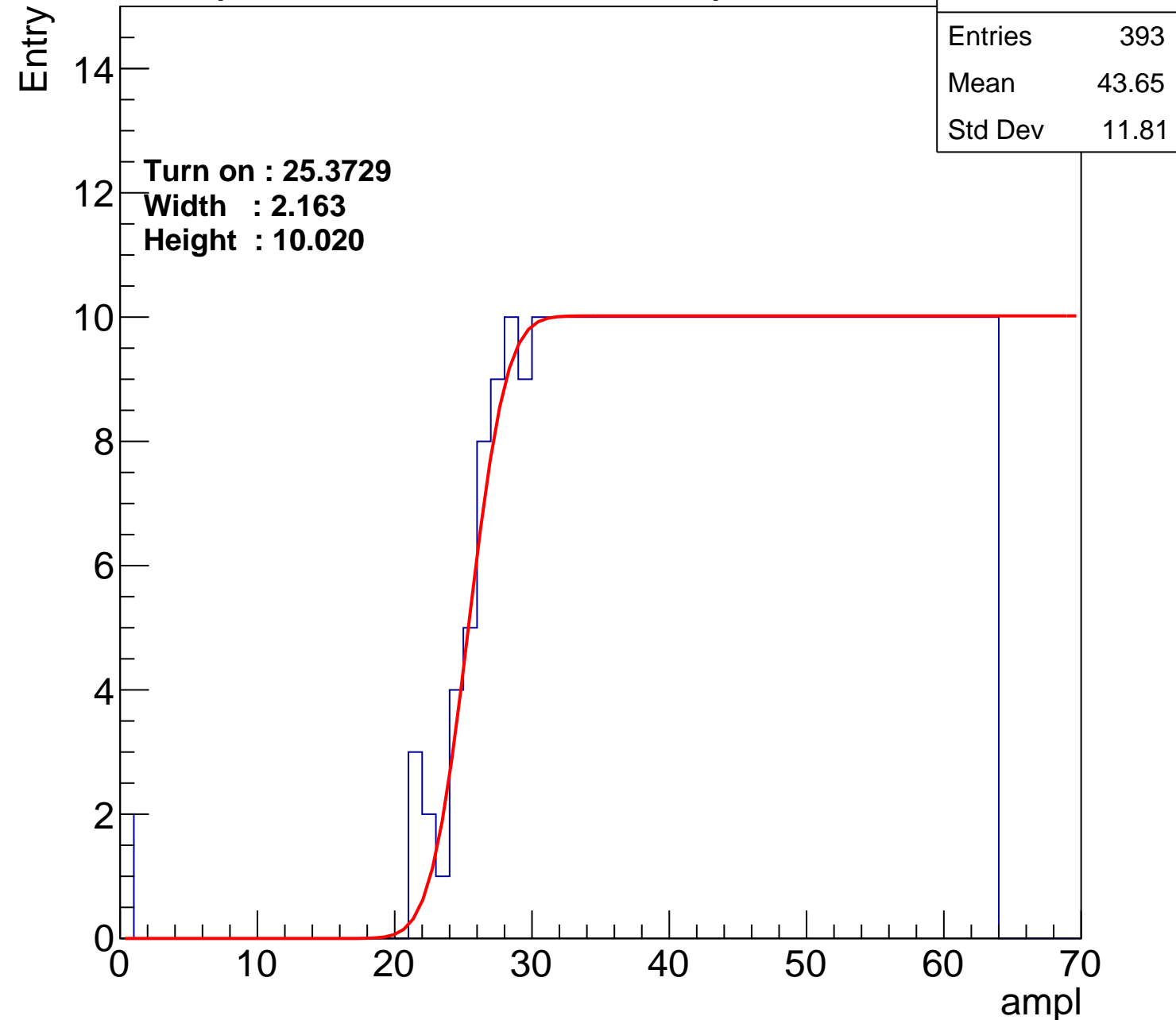
Width : 2.163

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch28

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 26.2386

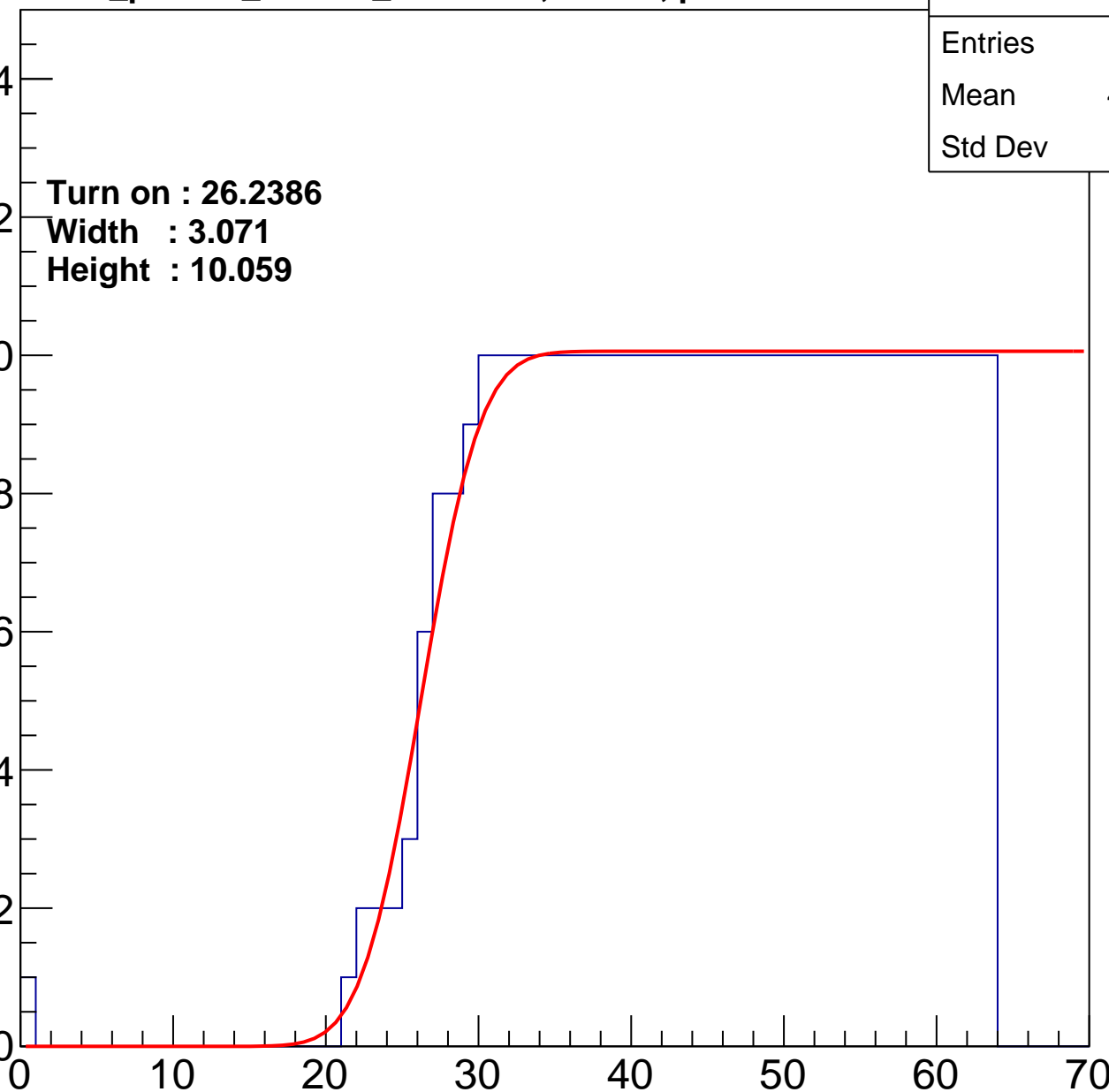
Width : 3.071

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch29

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	361
Mean	45.2
Std Dev	11.03

Turn on : 28.4561

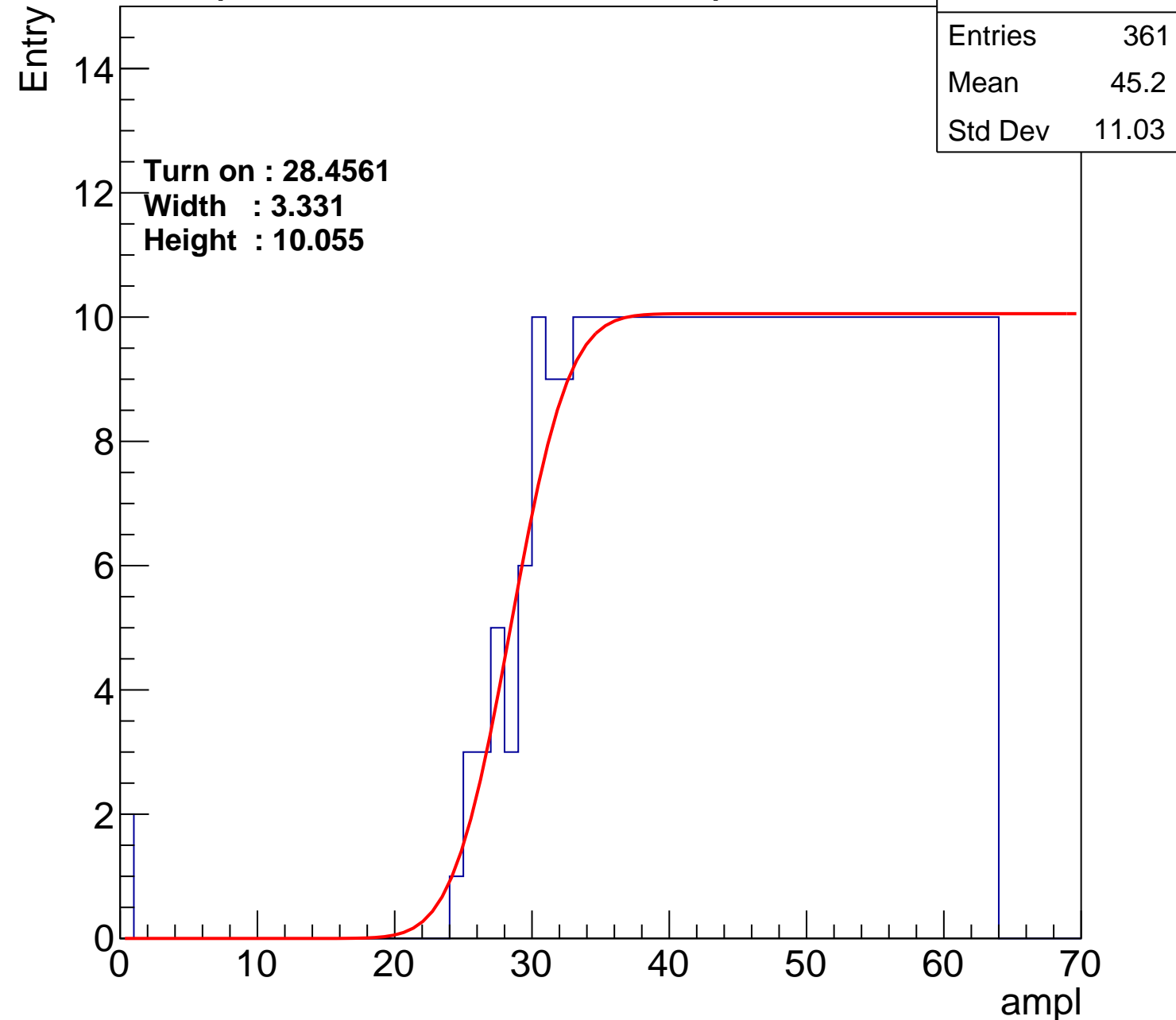
Width : 3.331

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch30

calib\_packv5\_042523\_0143.root, FC#12, port B1

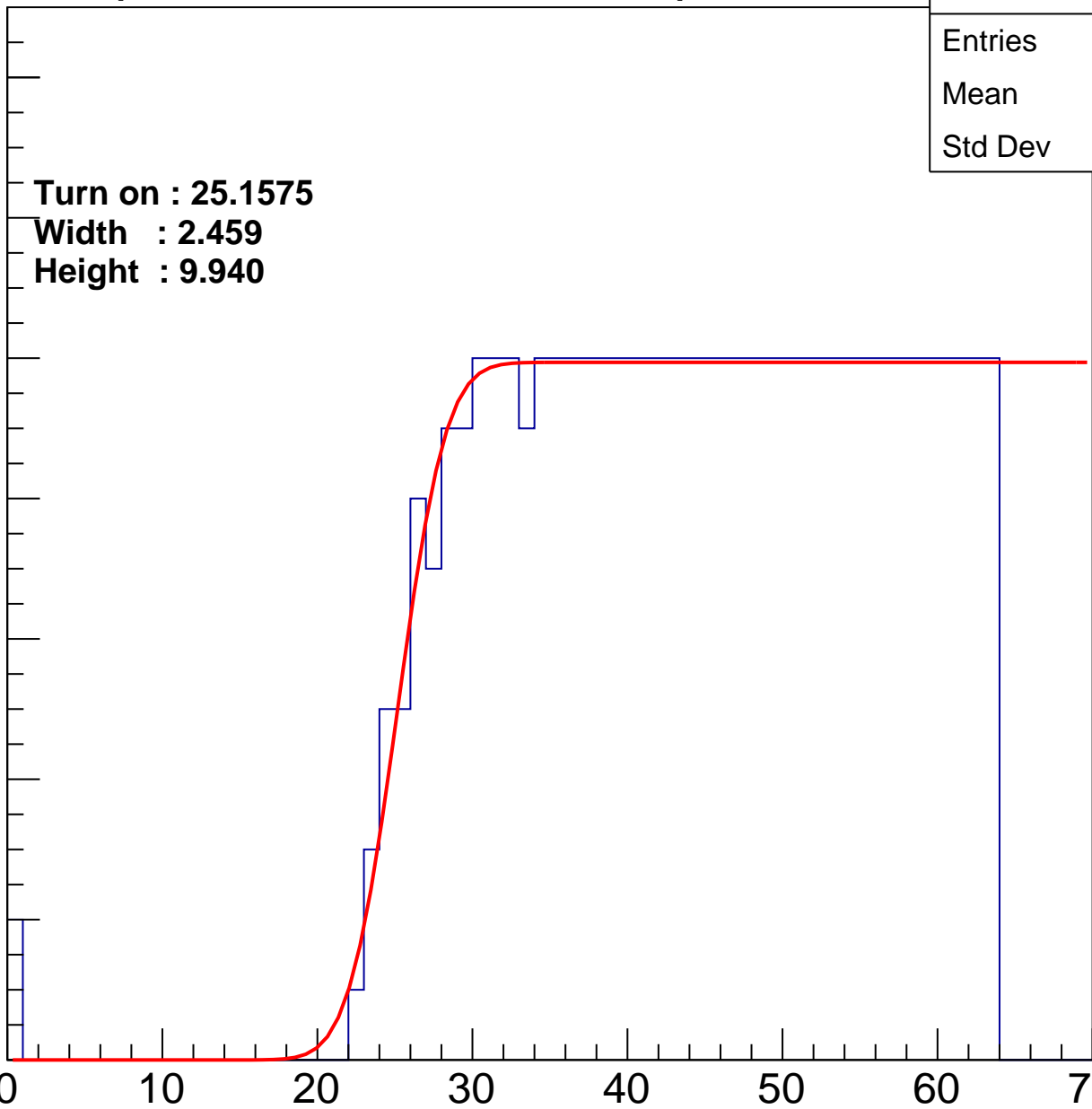
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.1575  
Width : 2.459  
Height : 9.940

Entries	388
Mean	43.87
Std Dev	11.7

ampl





# B0L102S, U22-ch31

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	369
Mean	44.91
Std Dev	10.98

**Turn on : 27.3412**

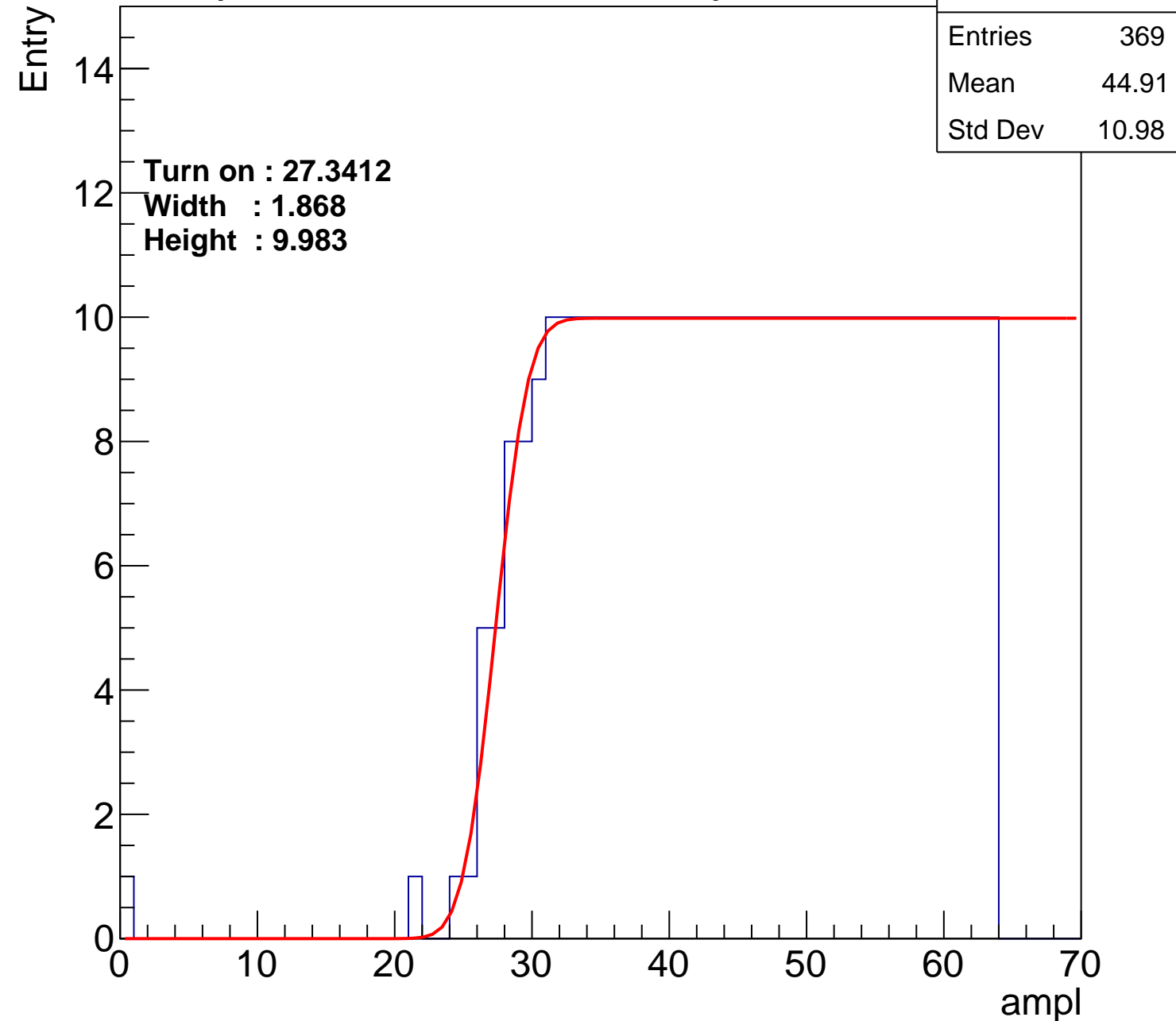
**Width : 1.868**

**Height : 9.983**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch32

calib\_packv5\_042523\_0143.root, FC#12, port B1

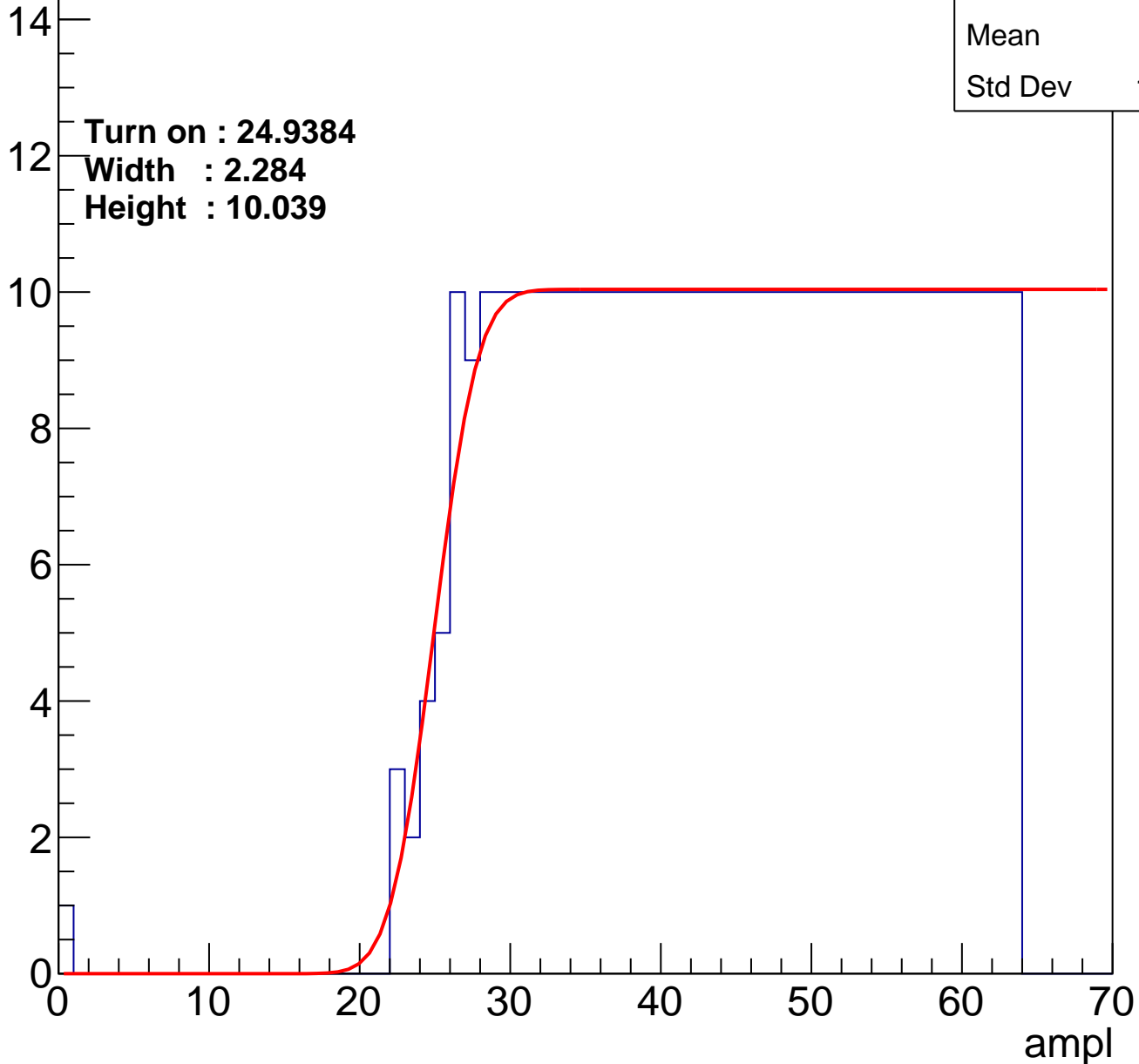
Entries	394
Mean	43.7
Std Dev	11.61

Turn on : 24.9384

Width : 2.284

Height : 10.039

Entry



# B0L102S, U22-ch33

calib\_packv5\_042523\_0143.root, FC#12, port B1

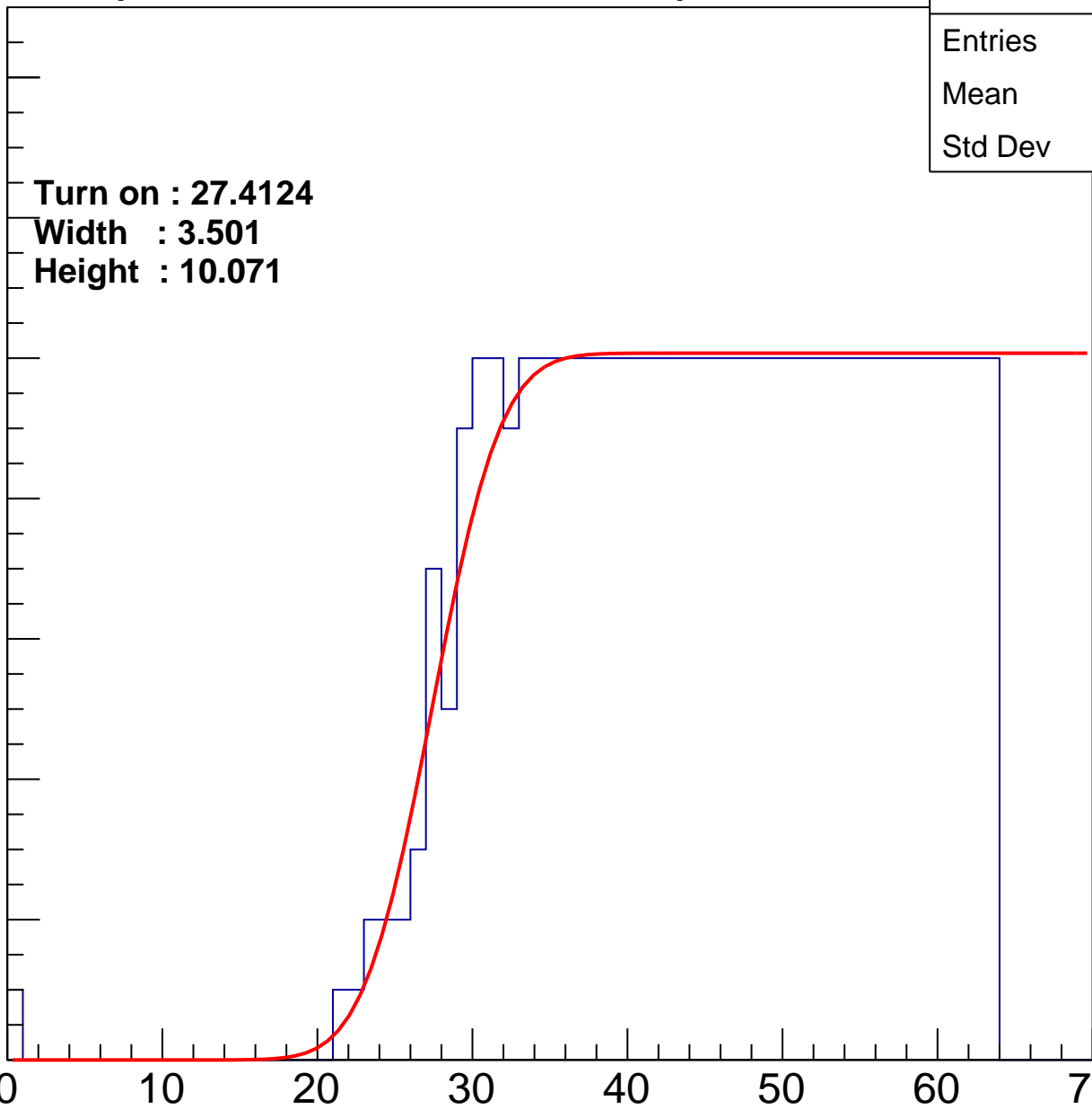
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4124  
Width : 3.501  
Height : 10.071

Entries	372
Mean	44.71
Std Dev	11.14

ampl



# B0L102S, U22-ch34

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	372
Mean	44.46
Std Dev	11.78

Turn on : 27.4635

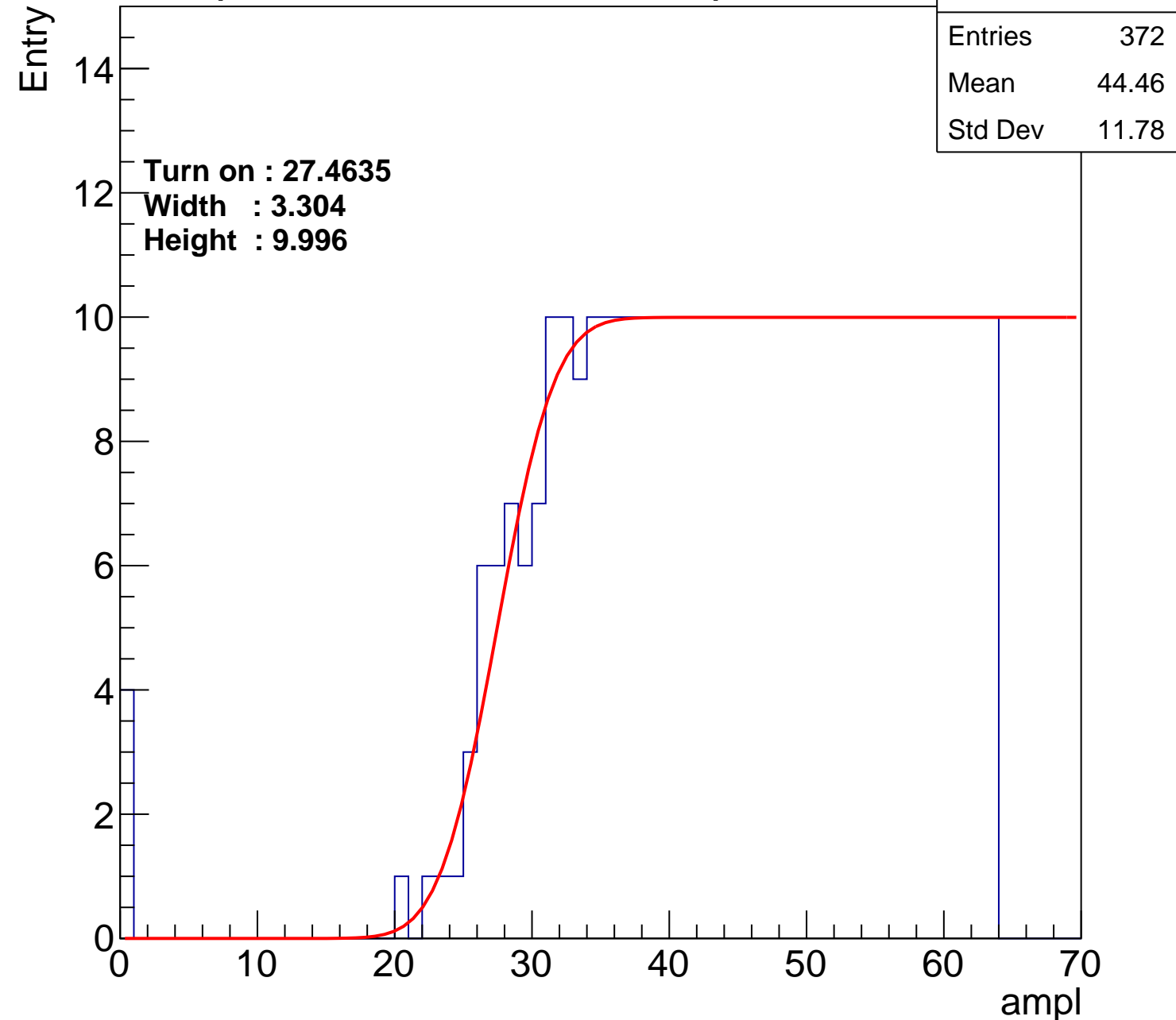
Width : 3.304

Height : 9.996

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch35

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 26.9340

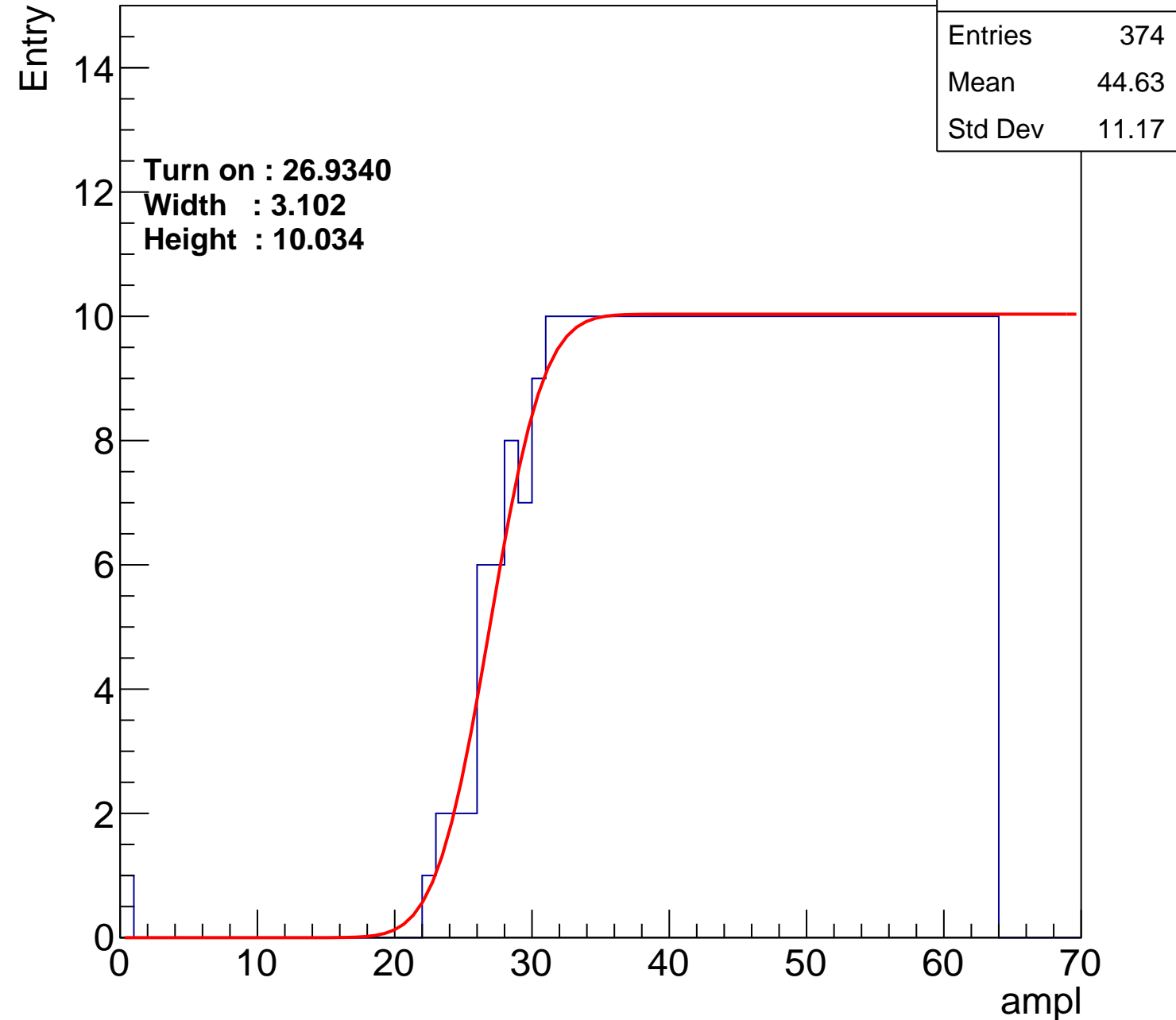
Width : 3.102

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch36

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.27
Std Dev	12.16

Turn on : 26.0263

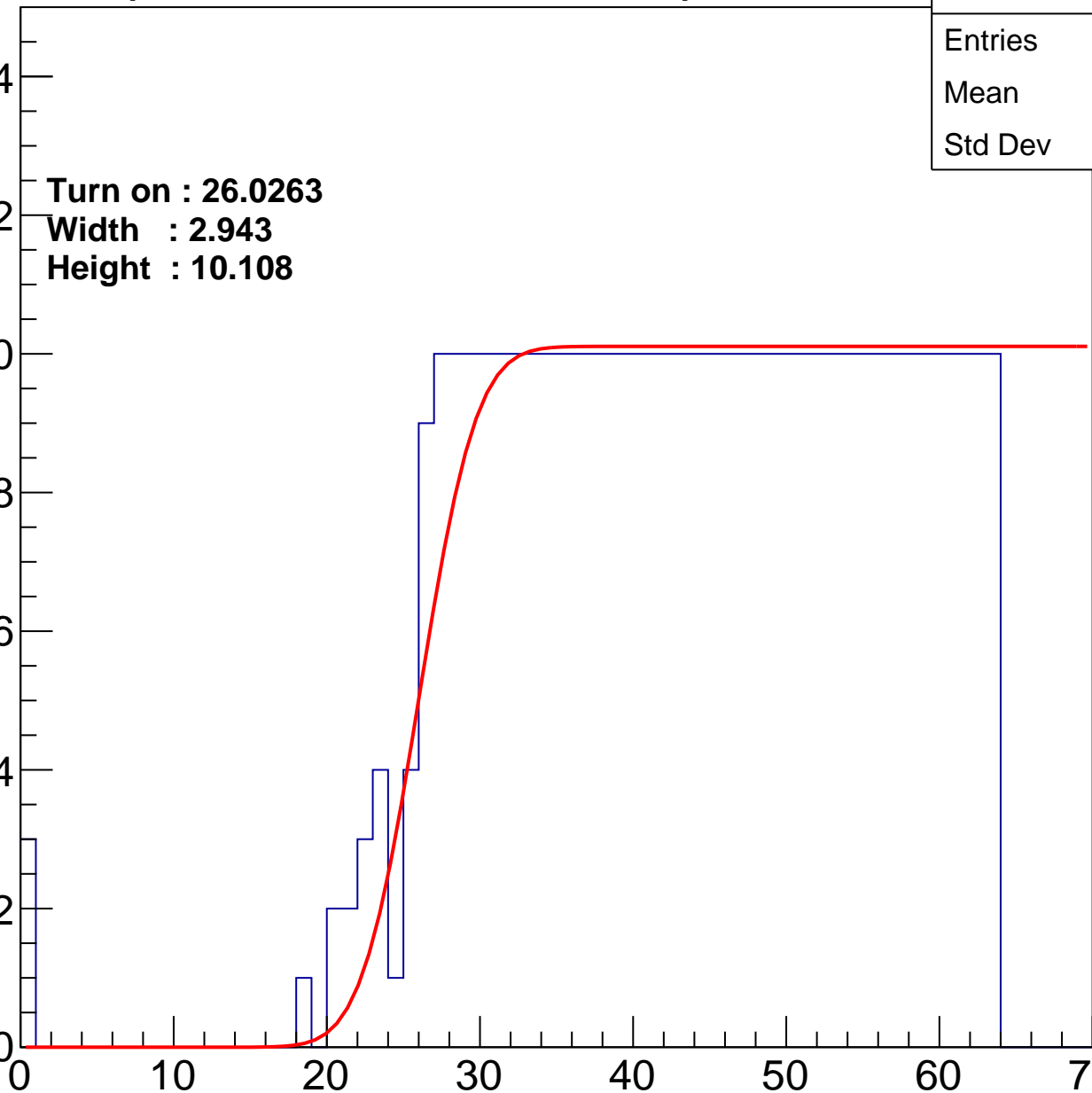
Width : 2.943

Height : 10.108

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch37

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	377
Mean	44.5
Std Dev	11.23

Turn on : 25.6472

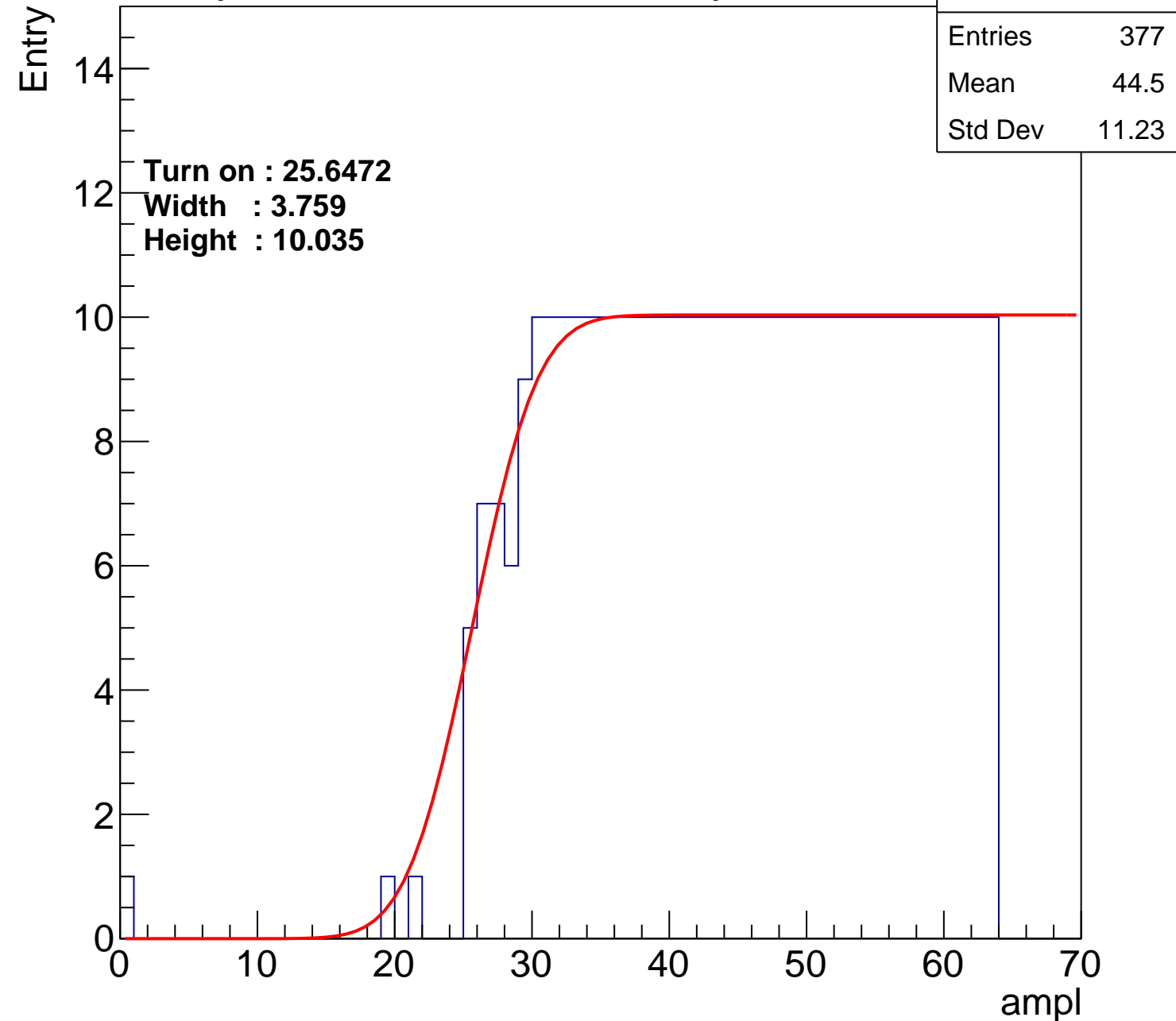
Width : 3.759

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch38

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.87
Std Dev	11.73

Turn on : 25.7472

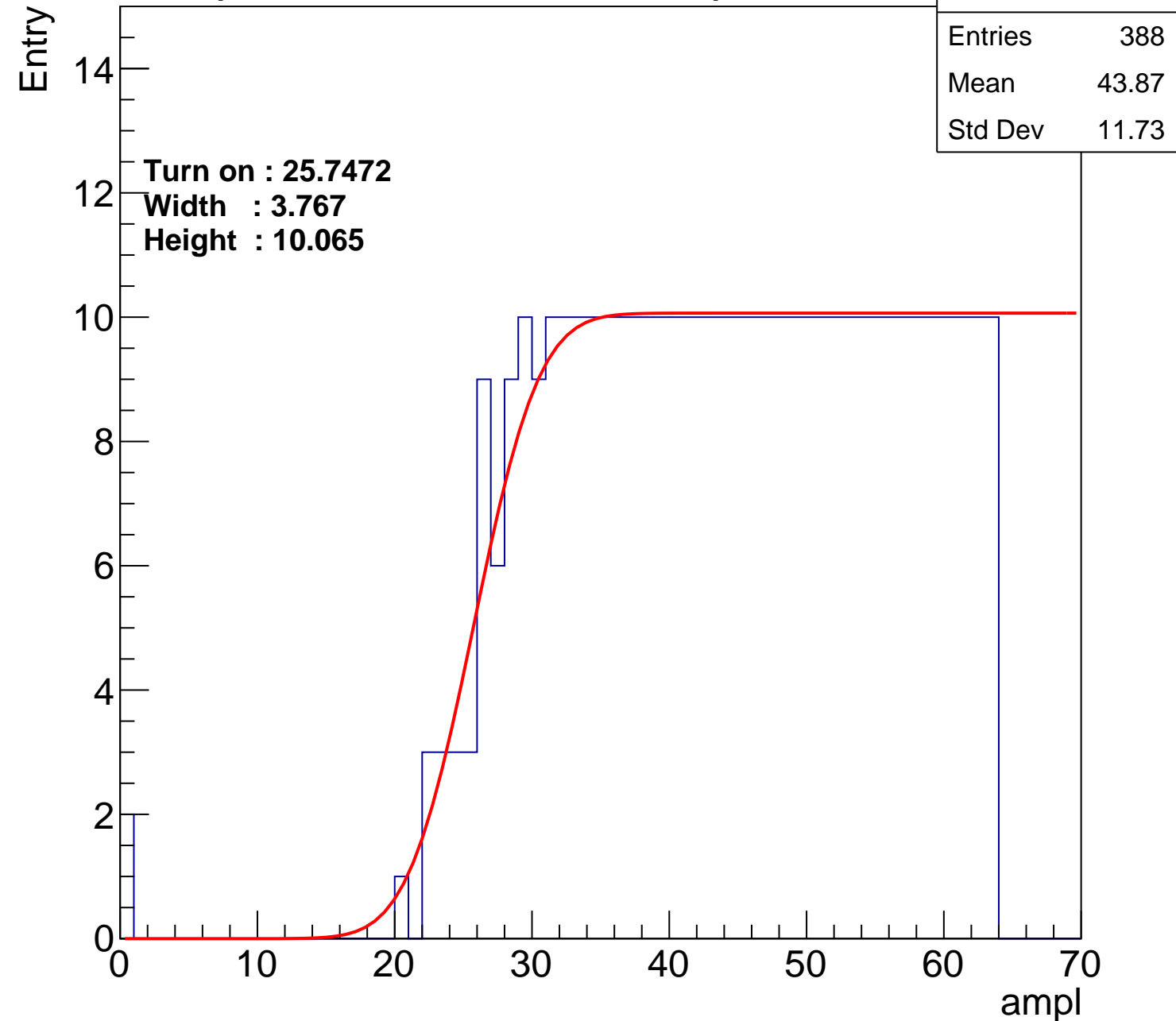
Width : 3.767

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch39

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.65
Std Dev	11.26

Turn on : 27.1025

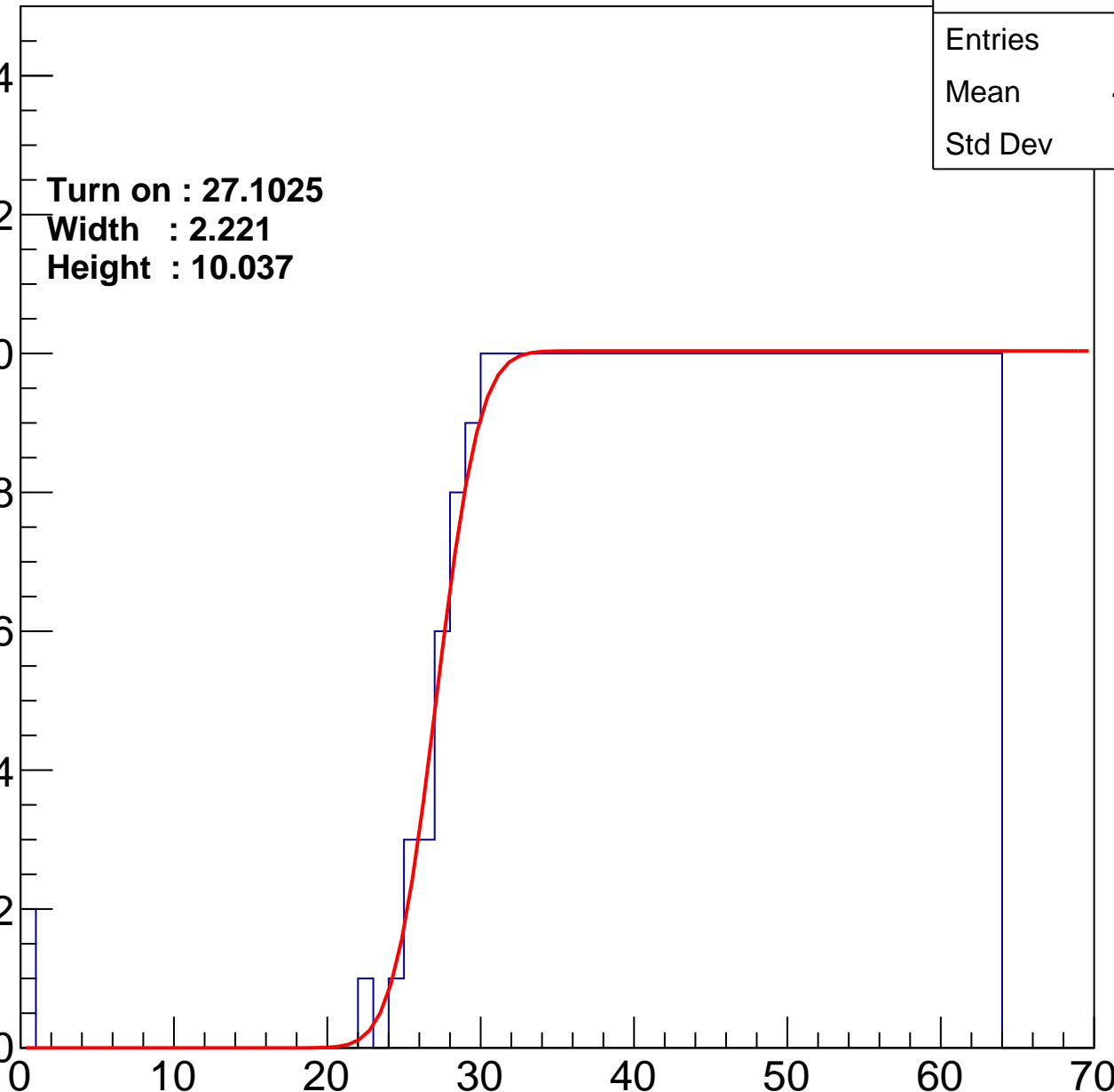
Width : 2.221

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch40

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.12
Std Dev	11.46

Turn on : 26.5384

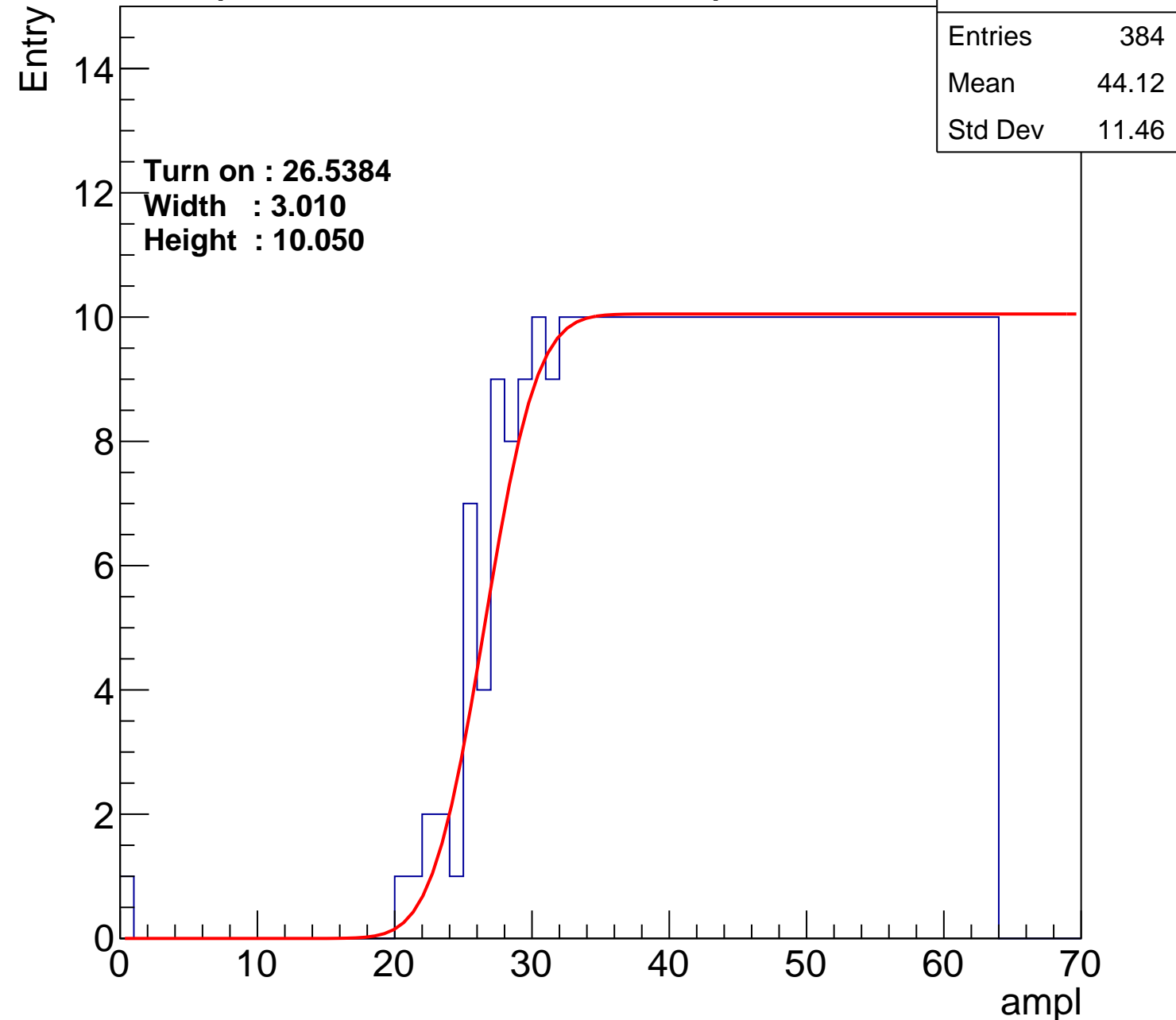
Width : 3.010

Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch41

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 25.9859

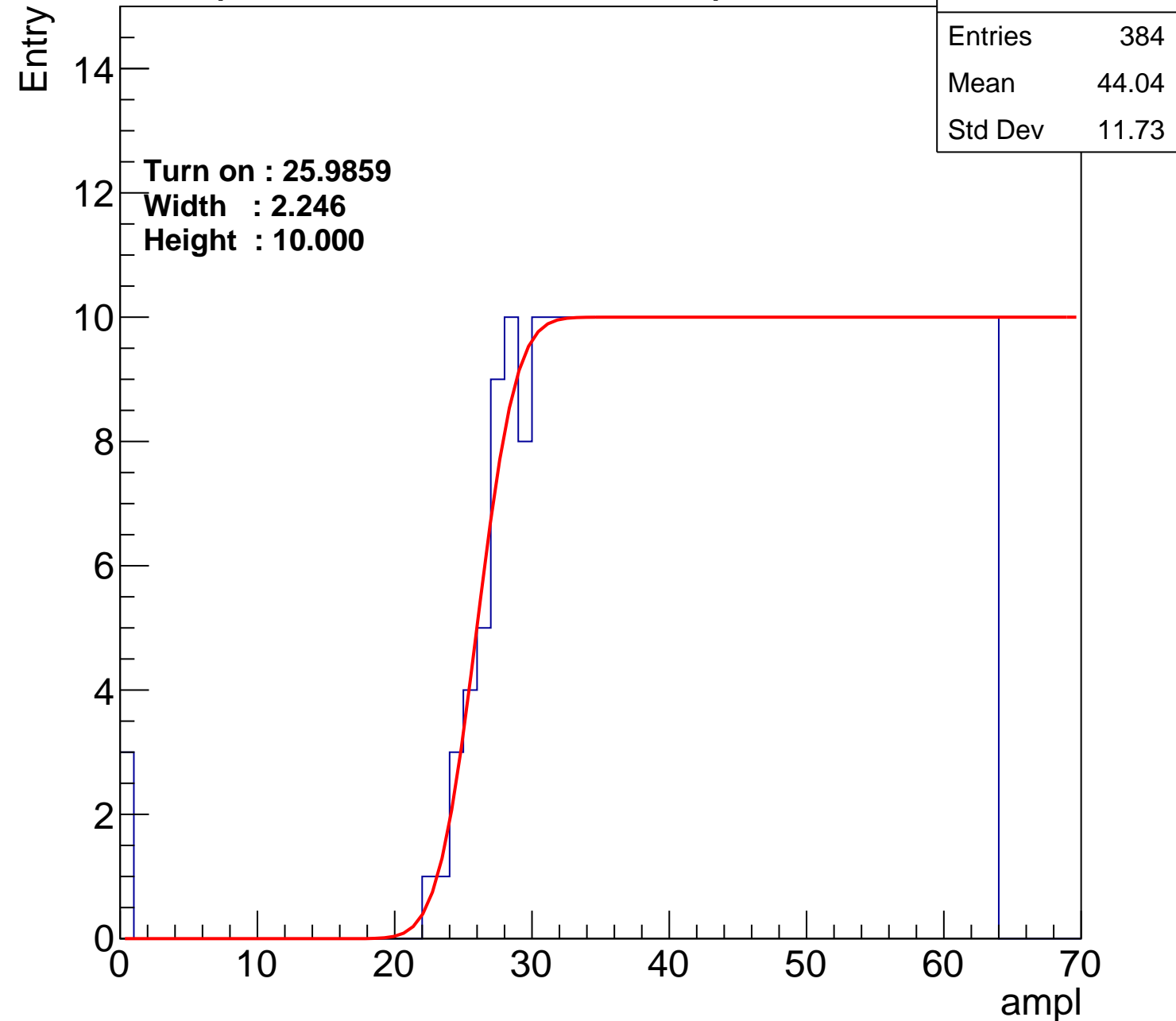
Width : 2.246

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch42

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.79
Std Dev	12.04

Turn on : 26.2469

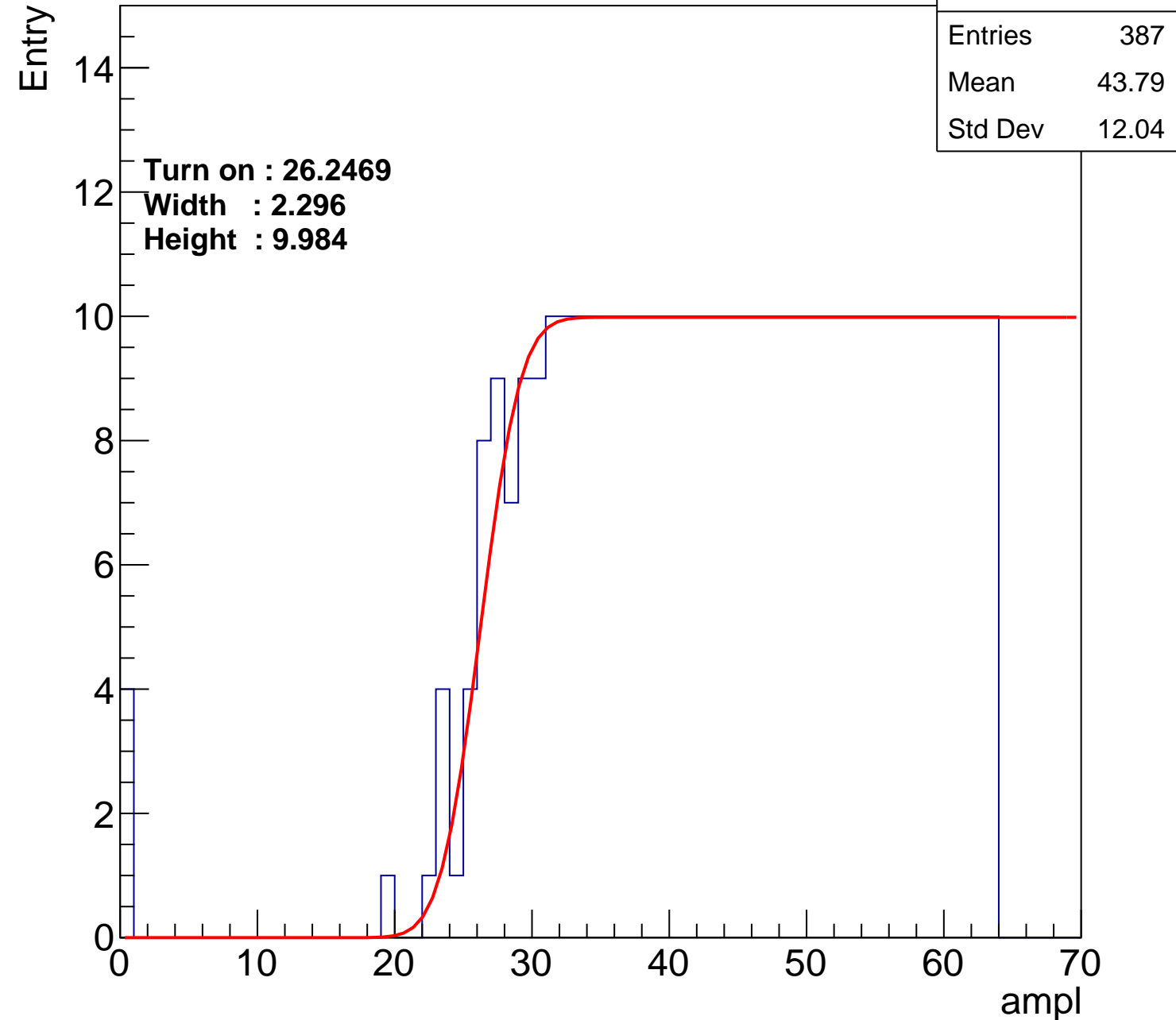
Width : 2.296

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch43

calib\_packv5\_042523\_0143.root, FC#12, port B1

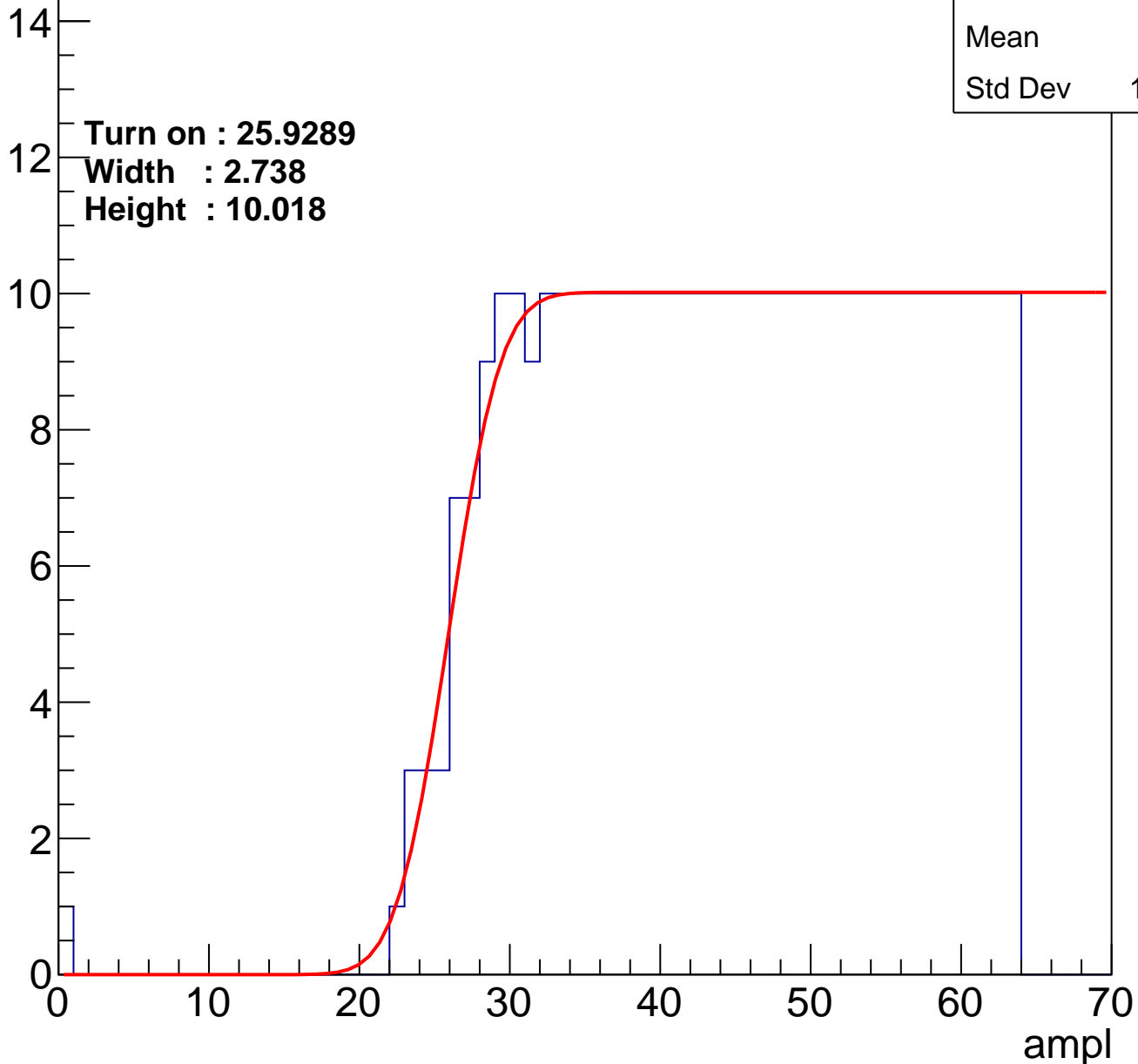
Entries	383
Mean	44.2
Std Dev	11.37

Turn on : 25.9289

Width : 2.738

Height : 10.018

Entry



# B0L102S, U22-ch44

calib\_packv5\_042523\_0143.root, FC#12, port B1

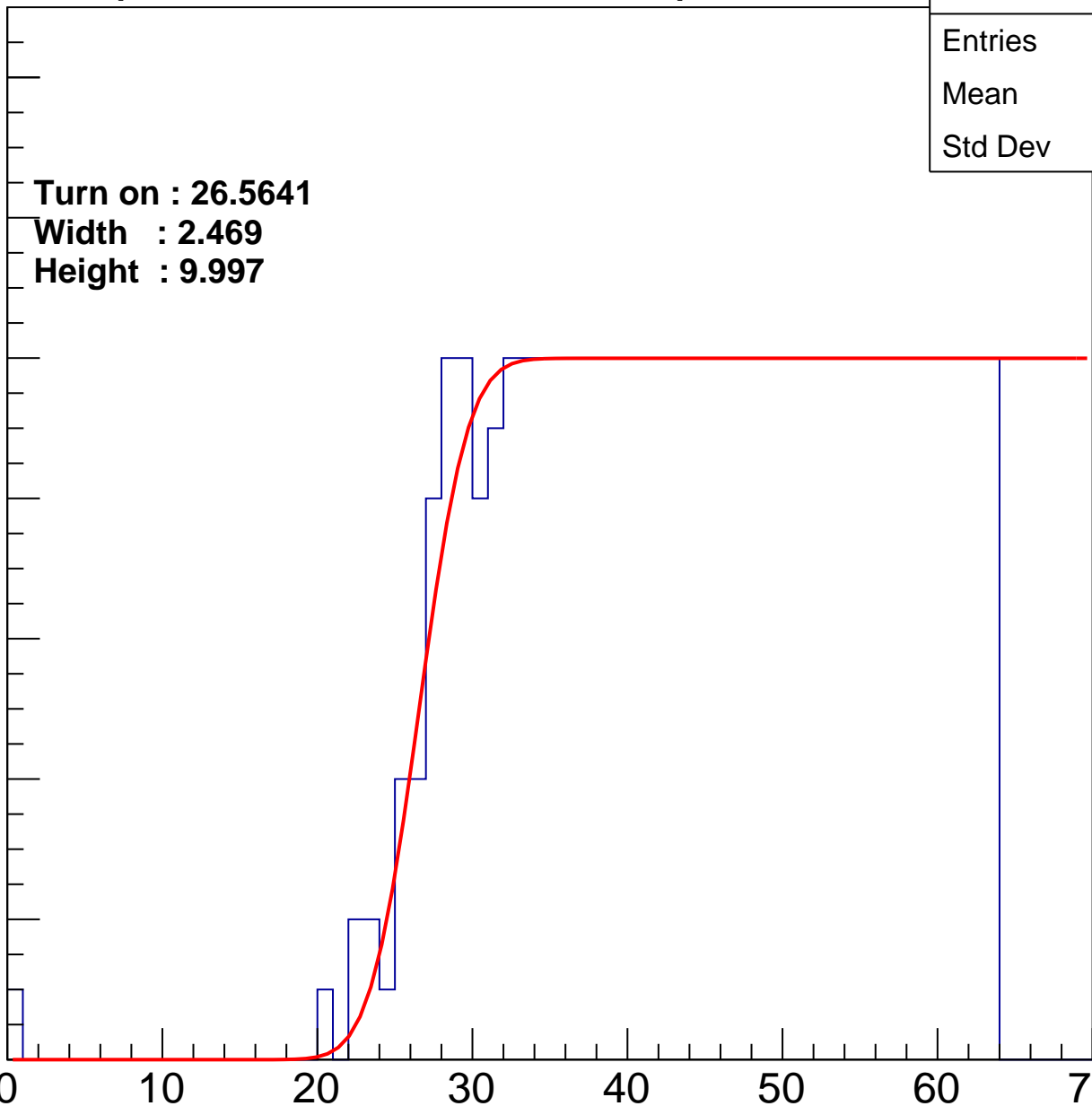
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.5641  
Width : 2.469  
Height : 9.997

Entries	380
Mean	44.32
Std Dev	11.34

ampl



# B0L102S, U22-ch45

calib\_packv5\_042523\_0143.root, FC#12, port B1

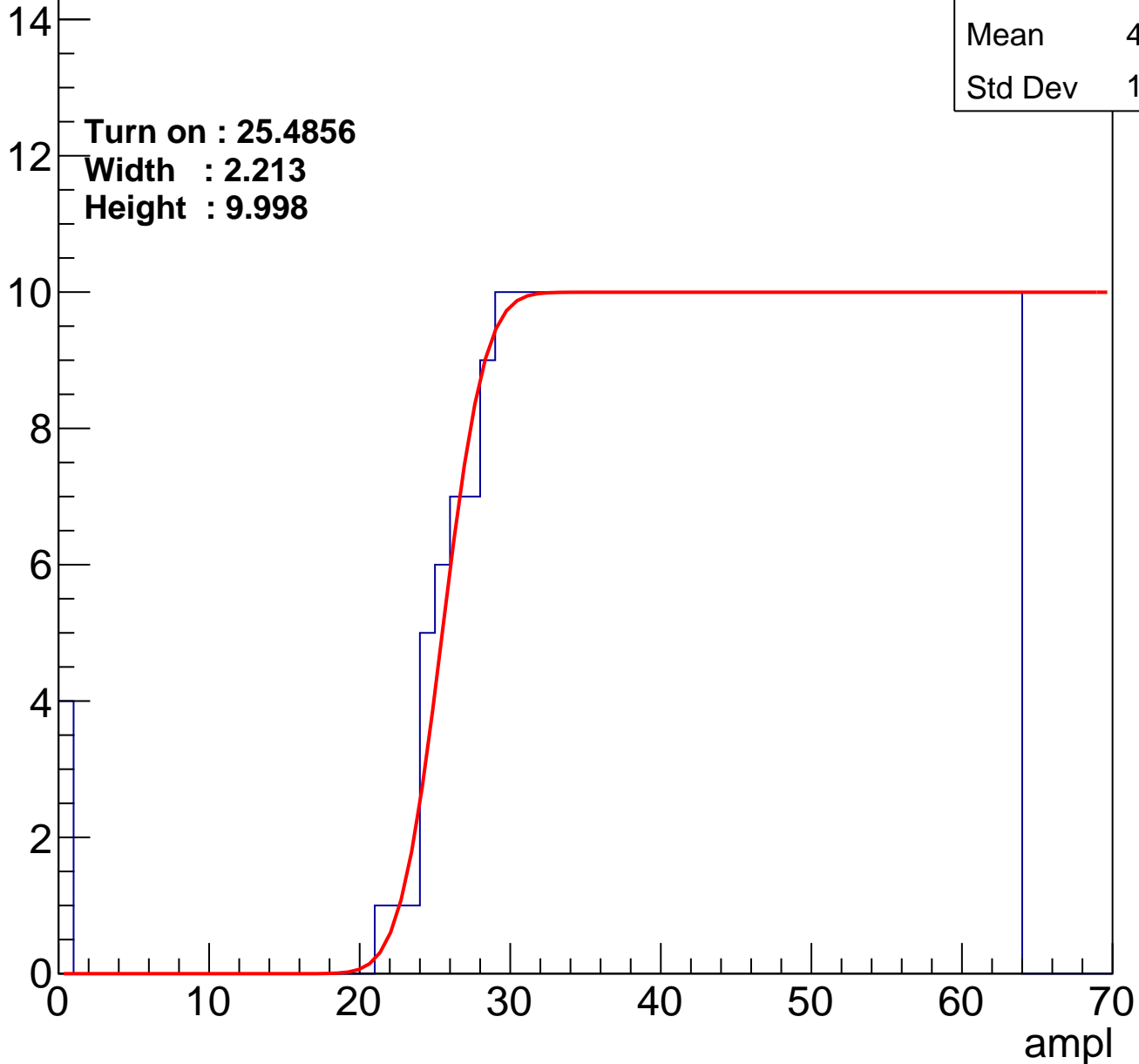
Entries	391
Mean	43.63
Std Dev	12.07

Turn on : 25.4856

Width : 2.213

Height : 9.998

Entry



# B0L102S, U22-ch46

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 25.1667

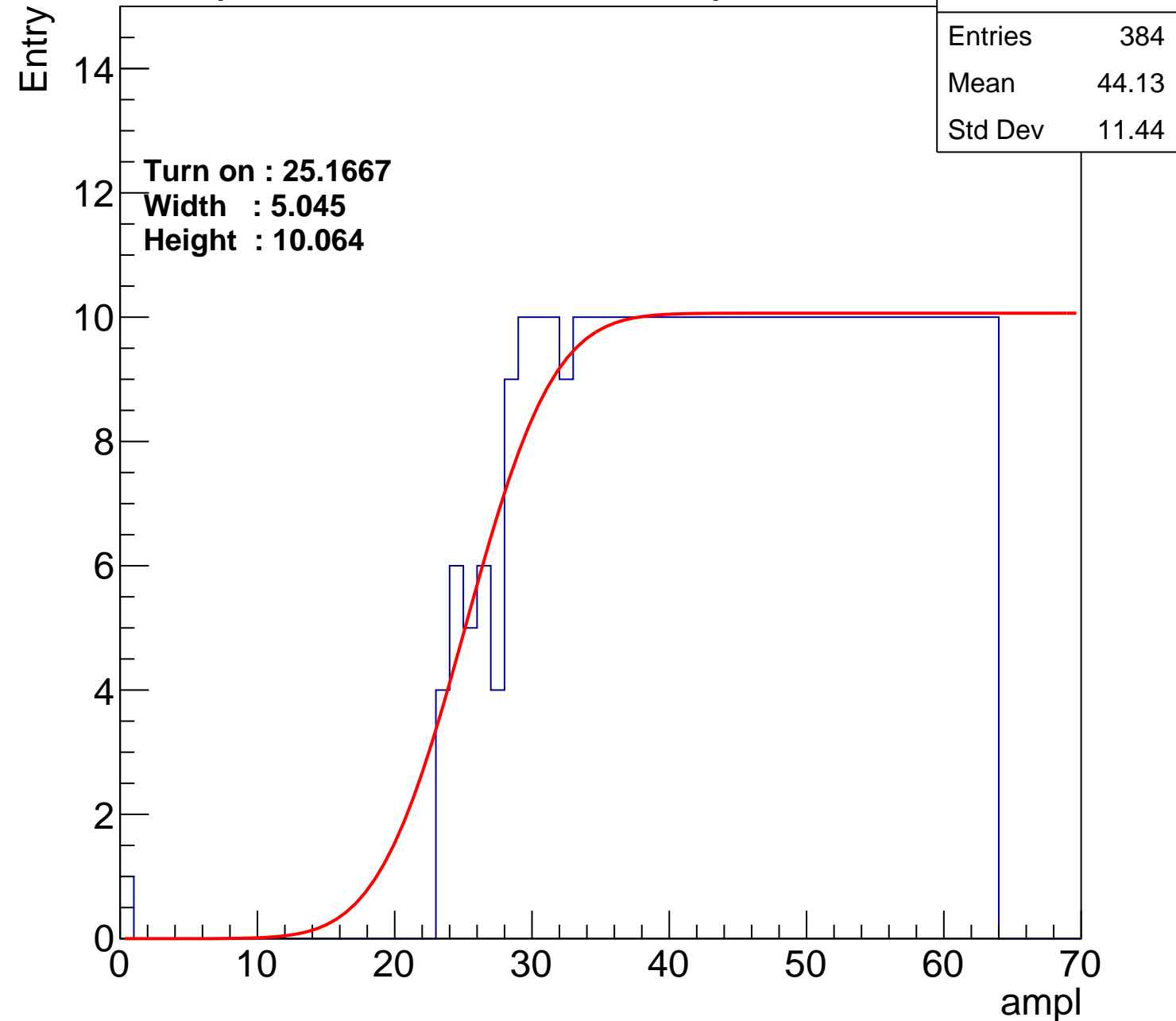
Width : 5.045

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch47

calib\_packv5\_042523\_0143.root, FC#12, port B1

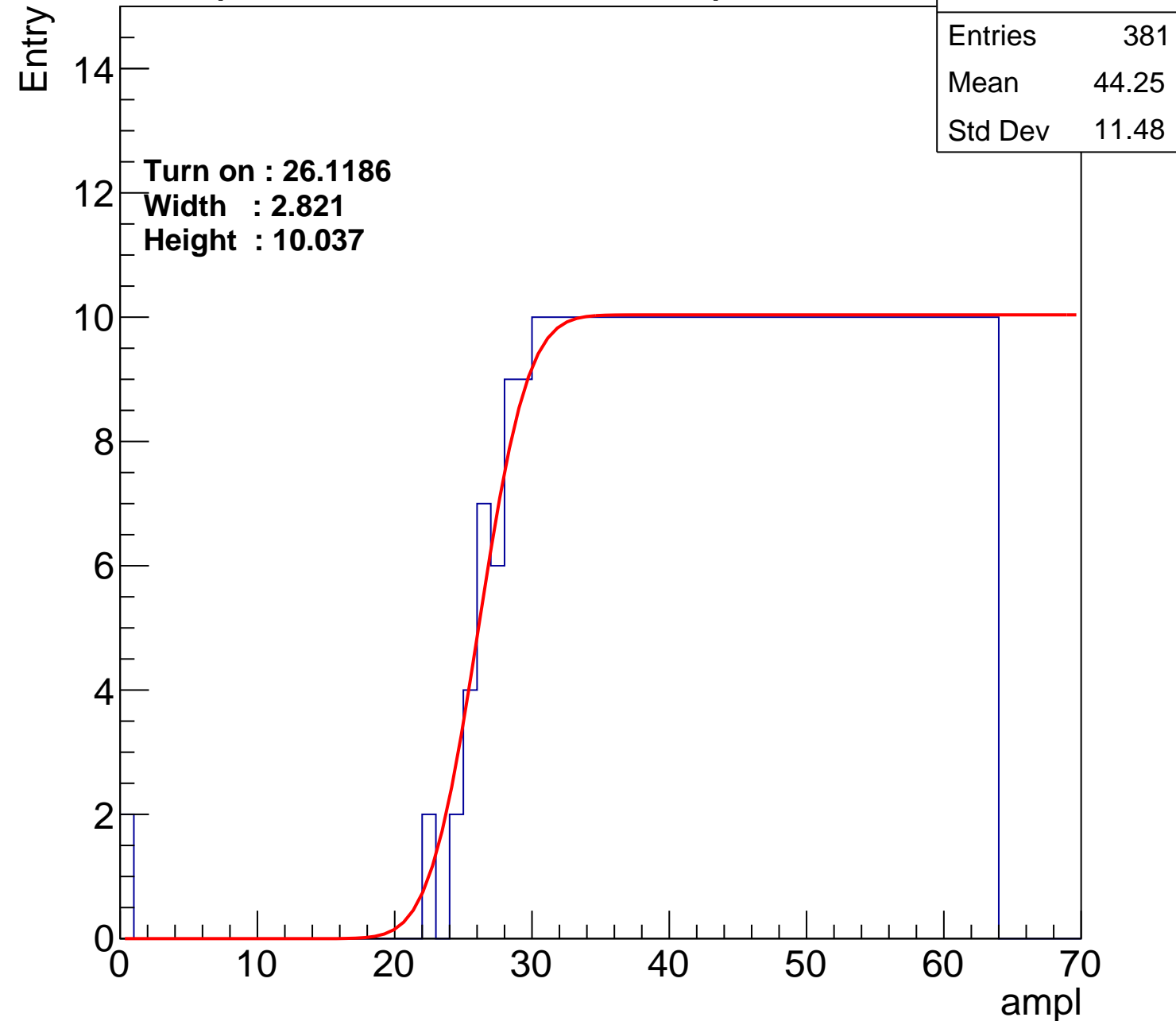
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.1186  
Width : 2.821  
Height : 10.037

Entries	381
Mean	44.25
Std Dev	11.48

ampl



# B0L102S, U22-ch48

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.88

Turn on : 24.9365

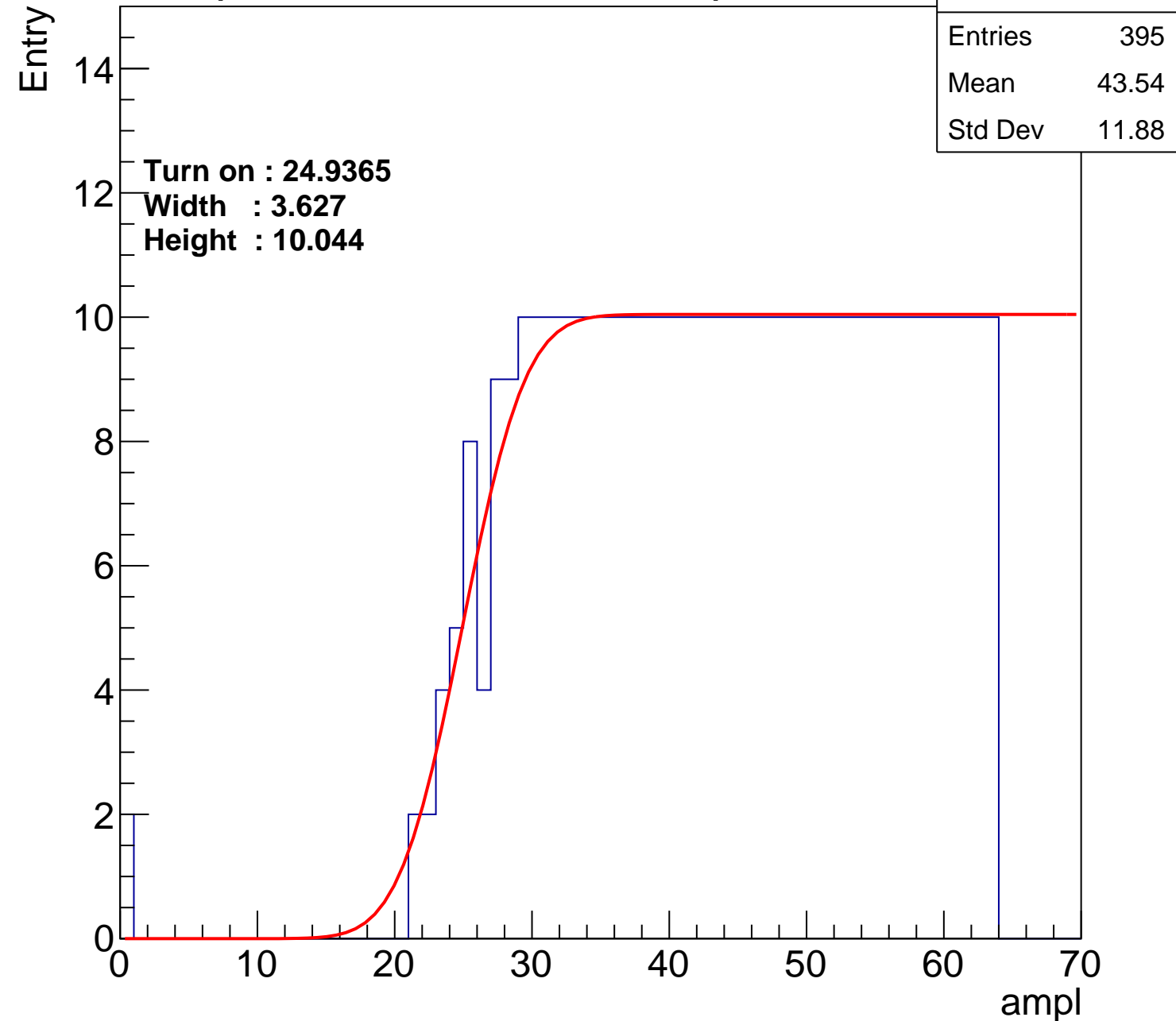
Width : 3.627

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch49

calib\_packv5\_042523\_0143.root, FC#12, port B1

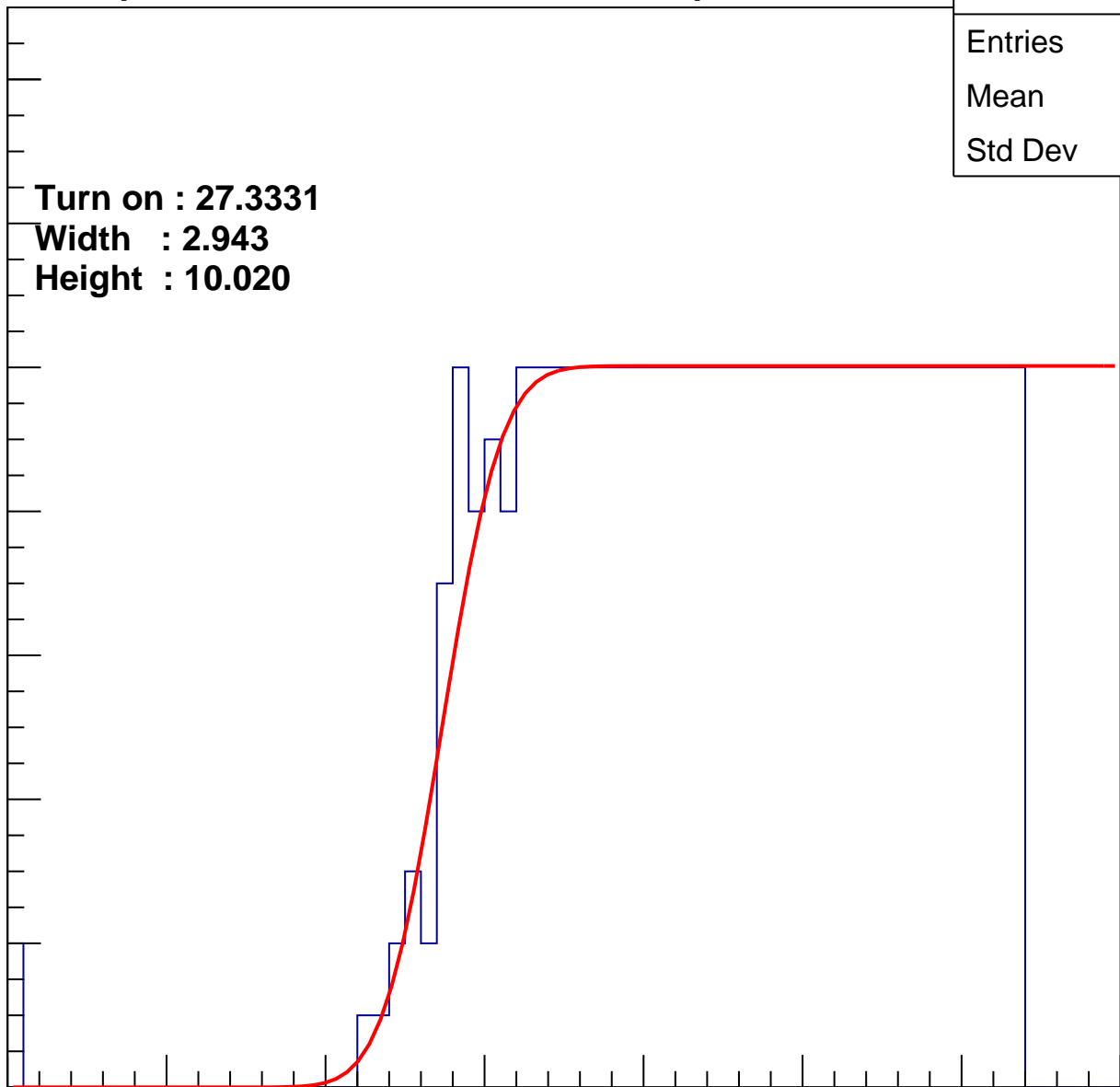
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.3331  
Width : 2.943  
Height : 10.020

Entries	373
Mean	44.61
Std Dev	11.33

ampl



# B0L102S, U22-ch50

calib\_packv5\_042523\_0143.root, FC#12, port B1

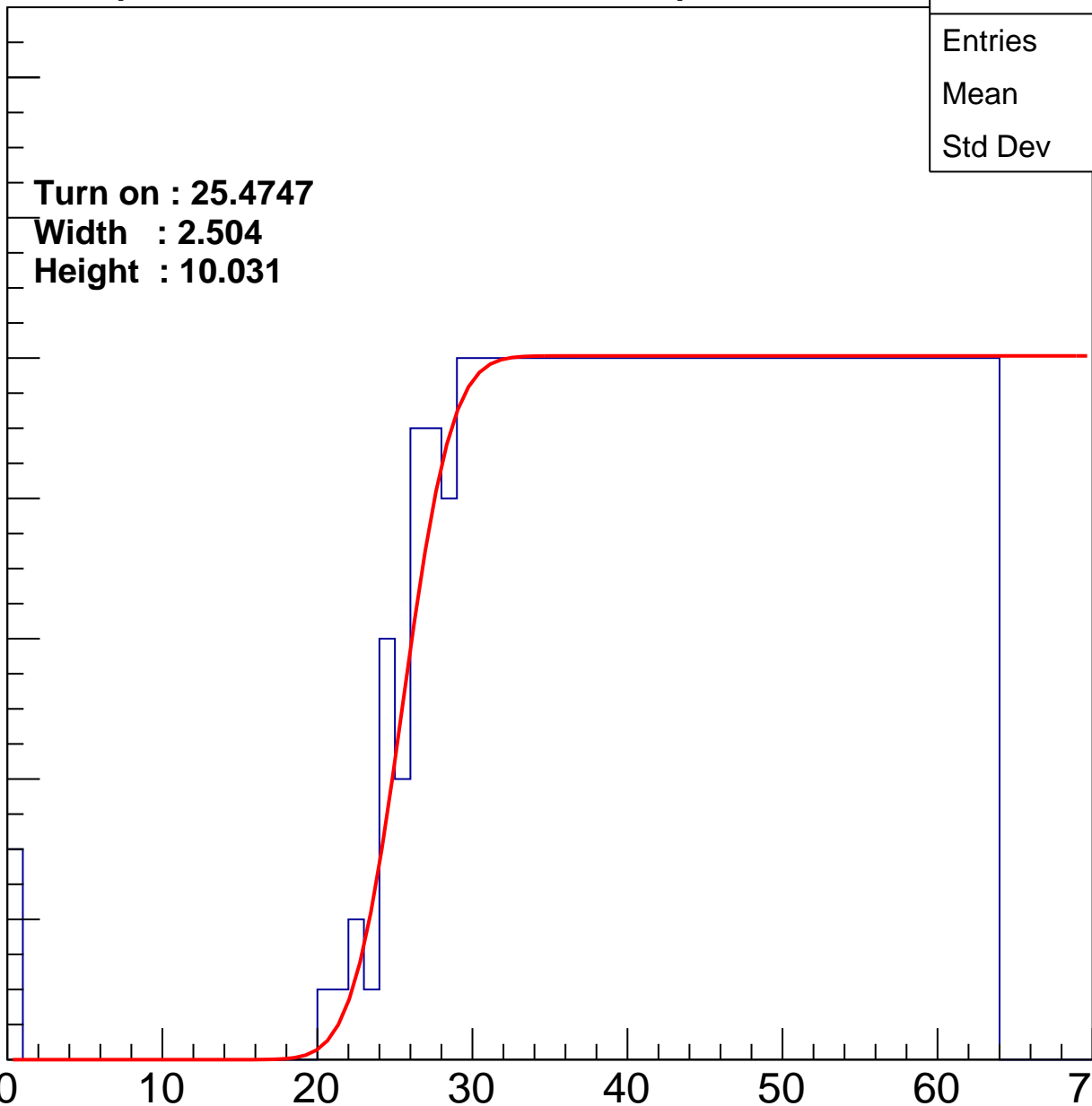
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.4747  
Width : 2.504  
Height : 10.031

Entries	394
Mean	43.54
Std Dev	12

ampl



# B0L102S, U22-ch51

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.15
Std Dev	11.71

Turn on : 25.5238

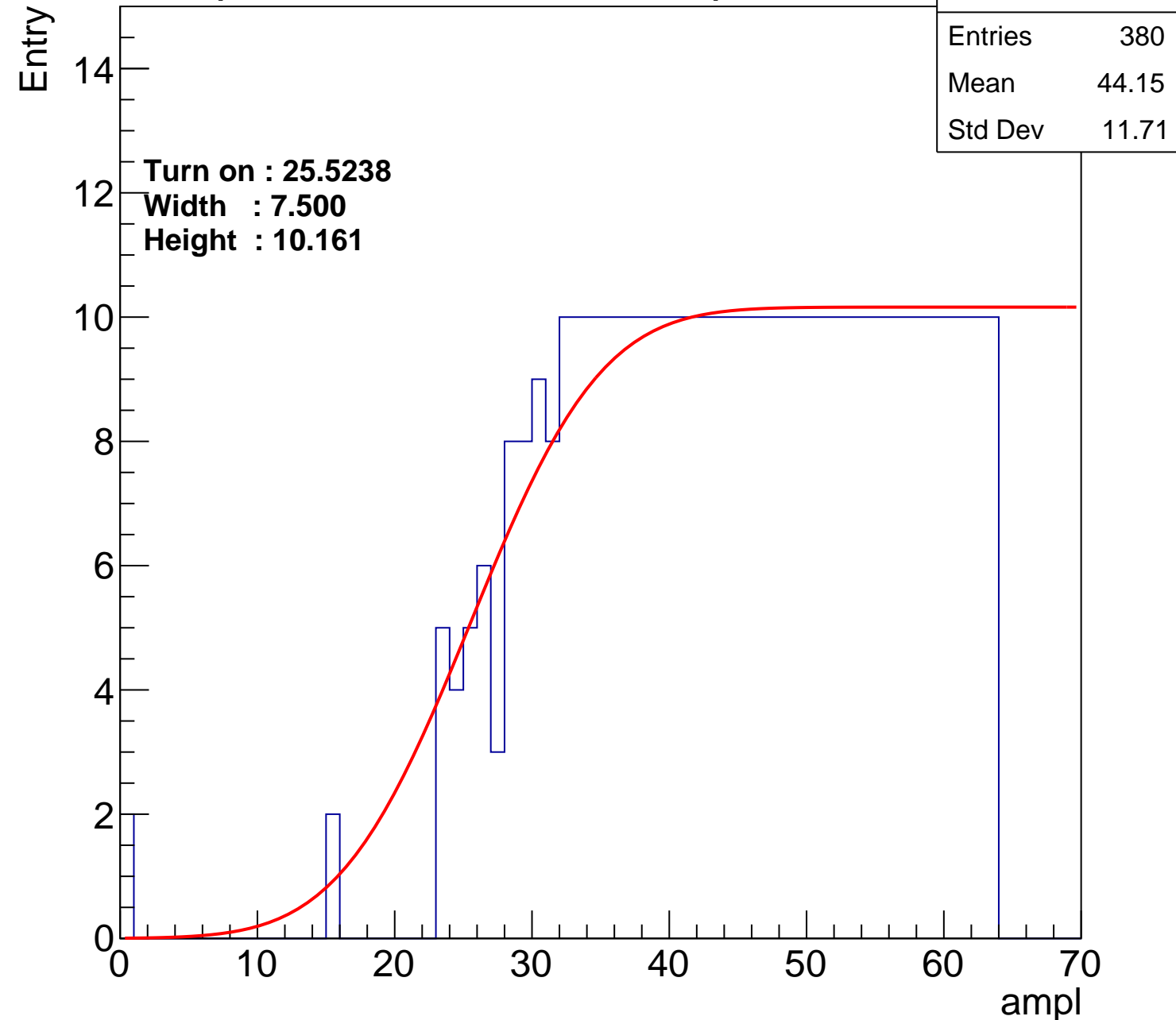
Width : 7.500

Height : 10.161

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch52

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	357
Mean	45.14
Std Dev	11.6

Turn on : 28.5283

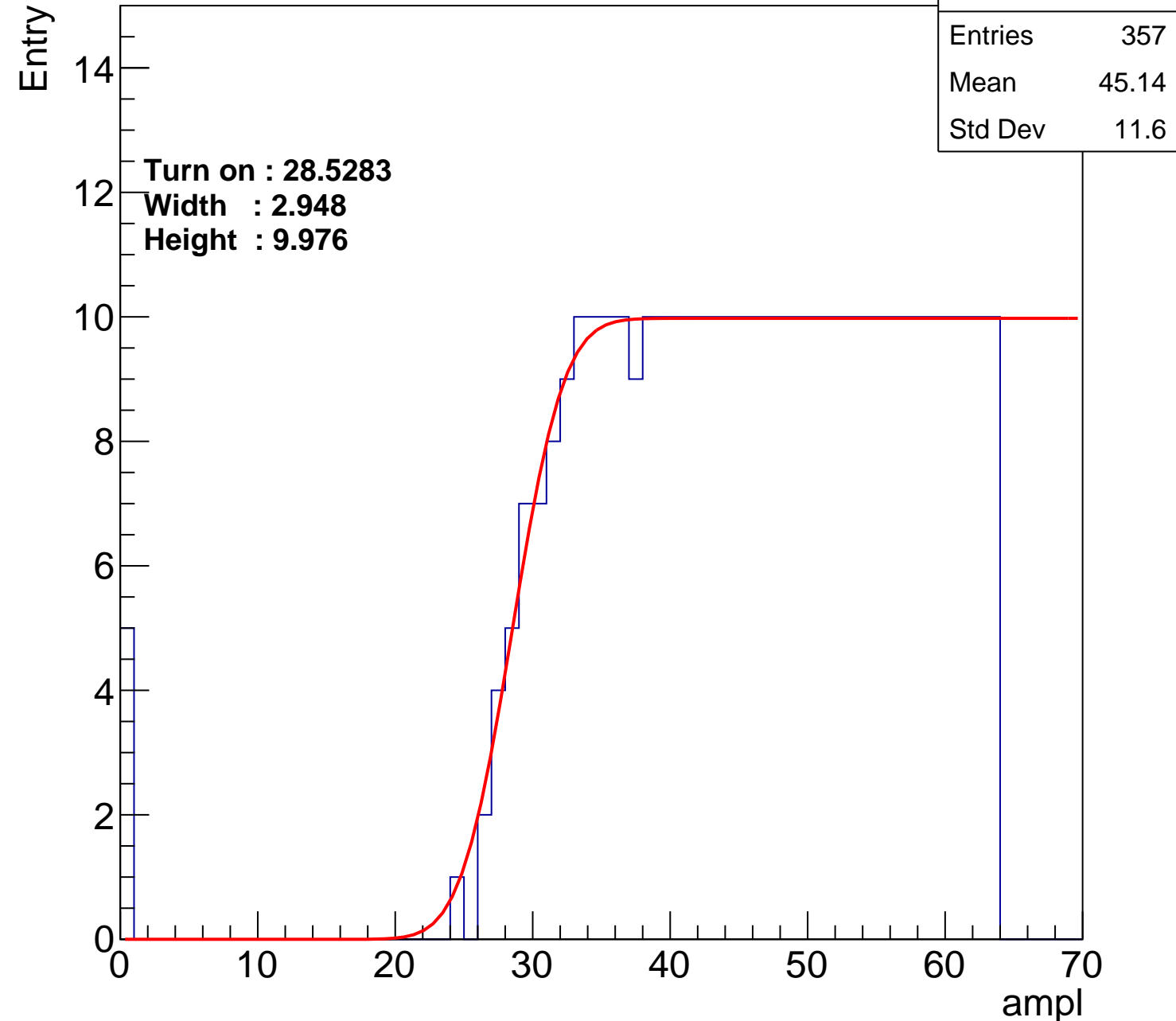
Width : 2.948

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch53

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.44
Std Dev	11.83

Turn on : 24.7965

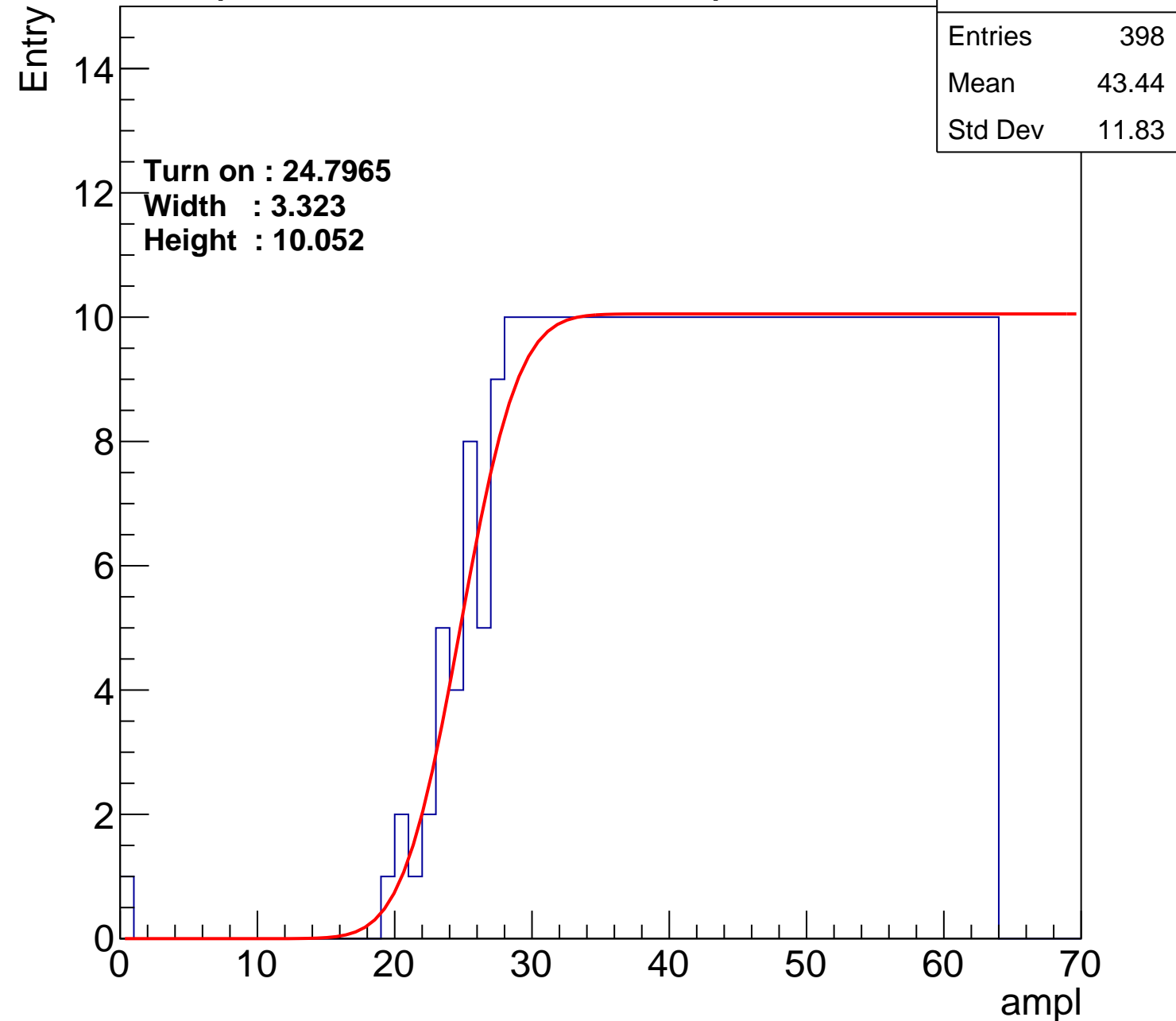
Width : 3.323

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch54

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.5771

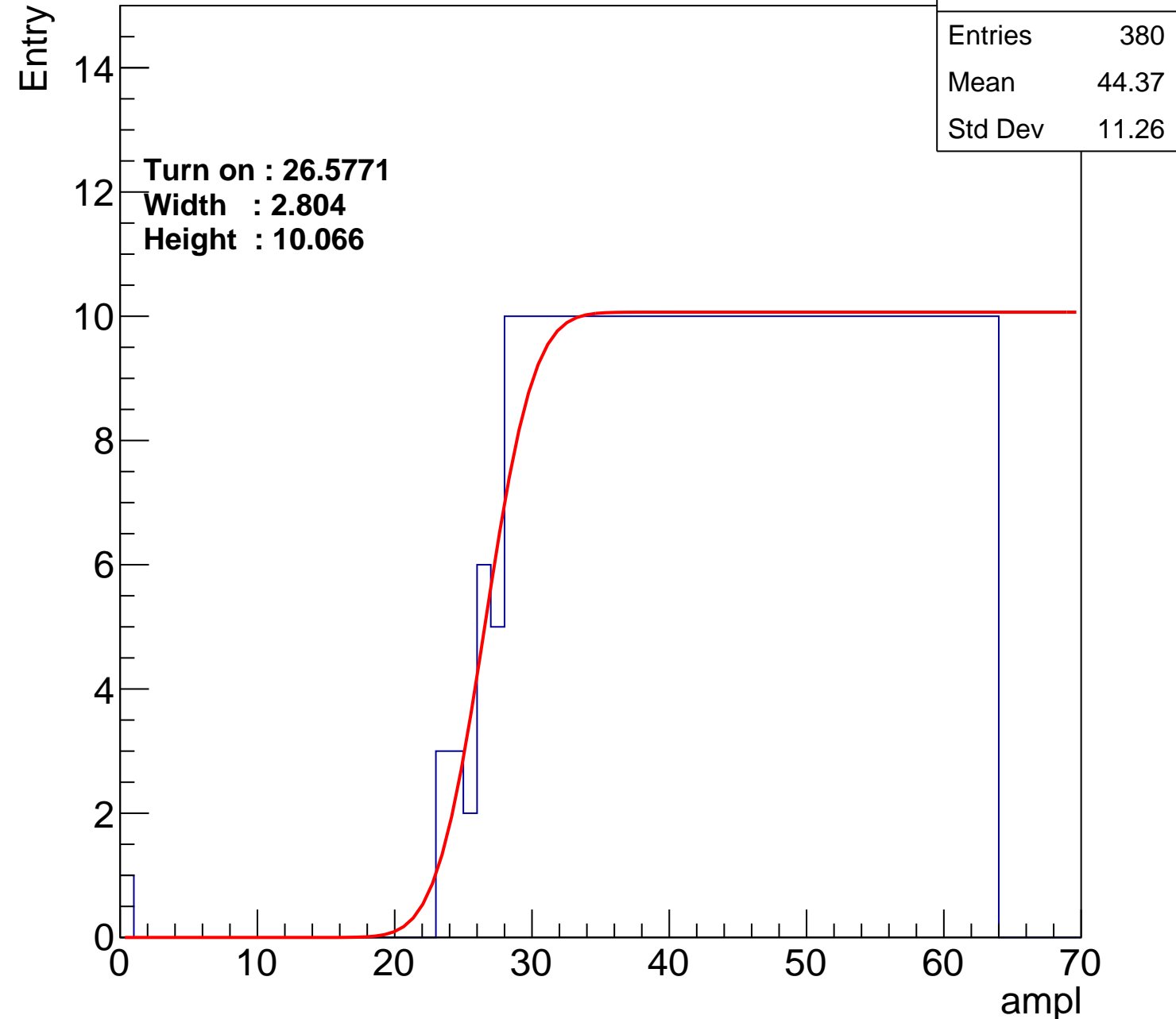
Width : 2.804

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch55

calib\_packv5\_042523\_0143.root, FC#12, port B1

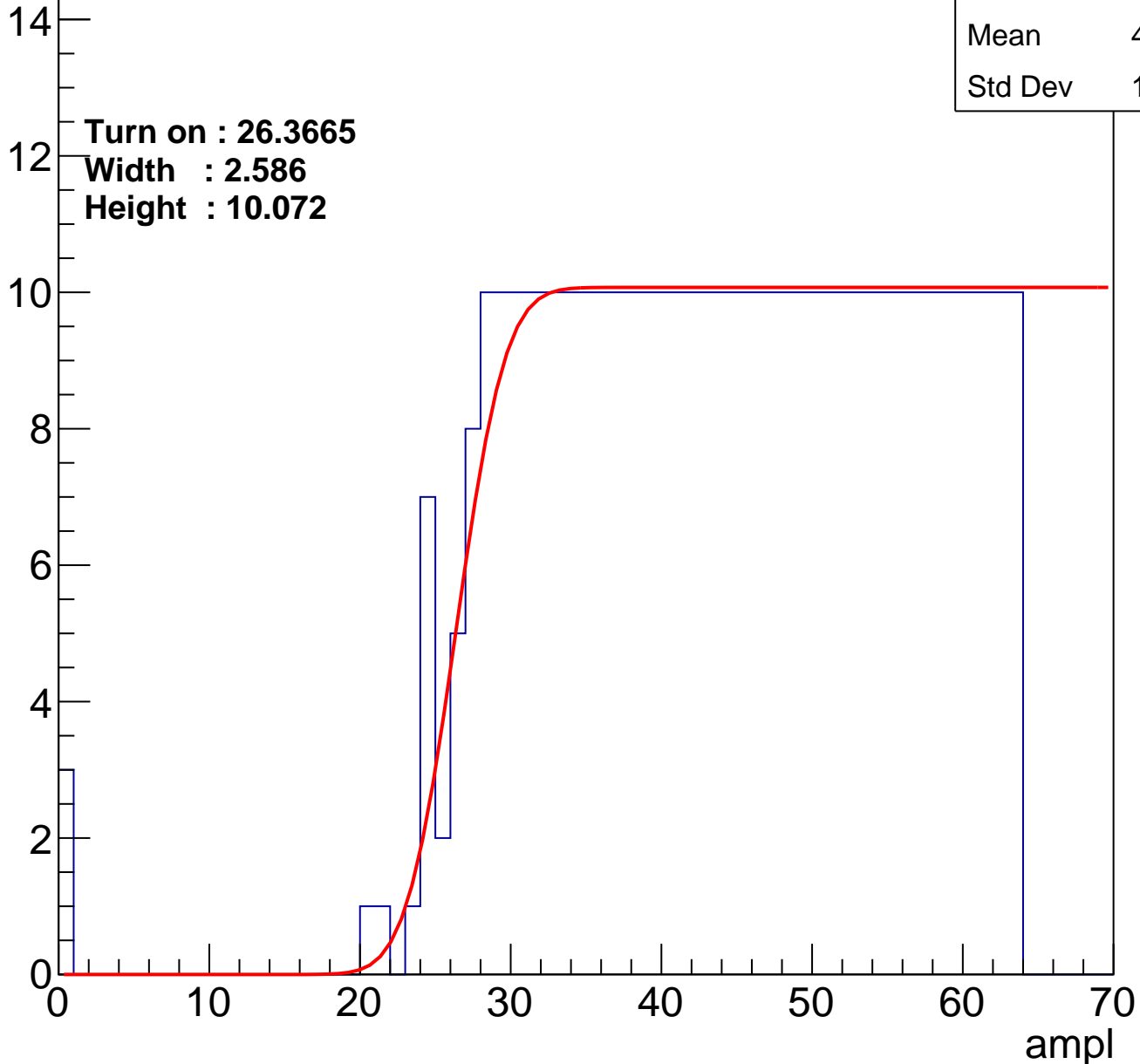
Entries	388
Mean	43.84
Std Dev	11.85

Turn on : 26.3665

Width : 2.586

Height : 10.072

Entry



# B0L102S, U22-ch56

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.24
Std Dev	11.8

Turn on : 26.6818

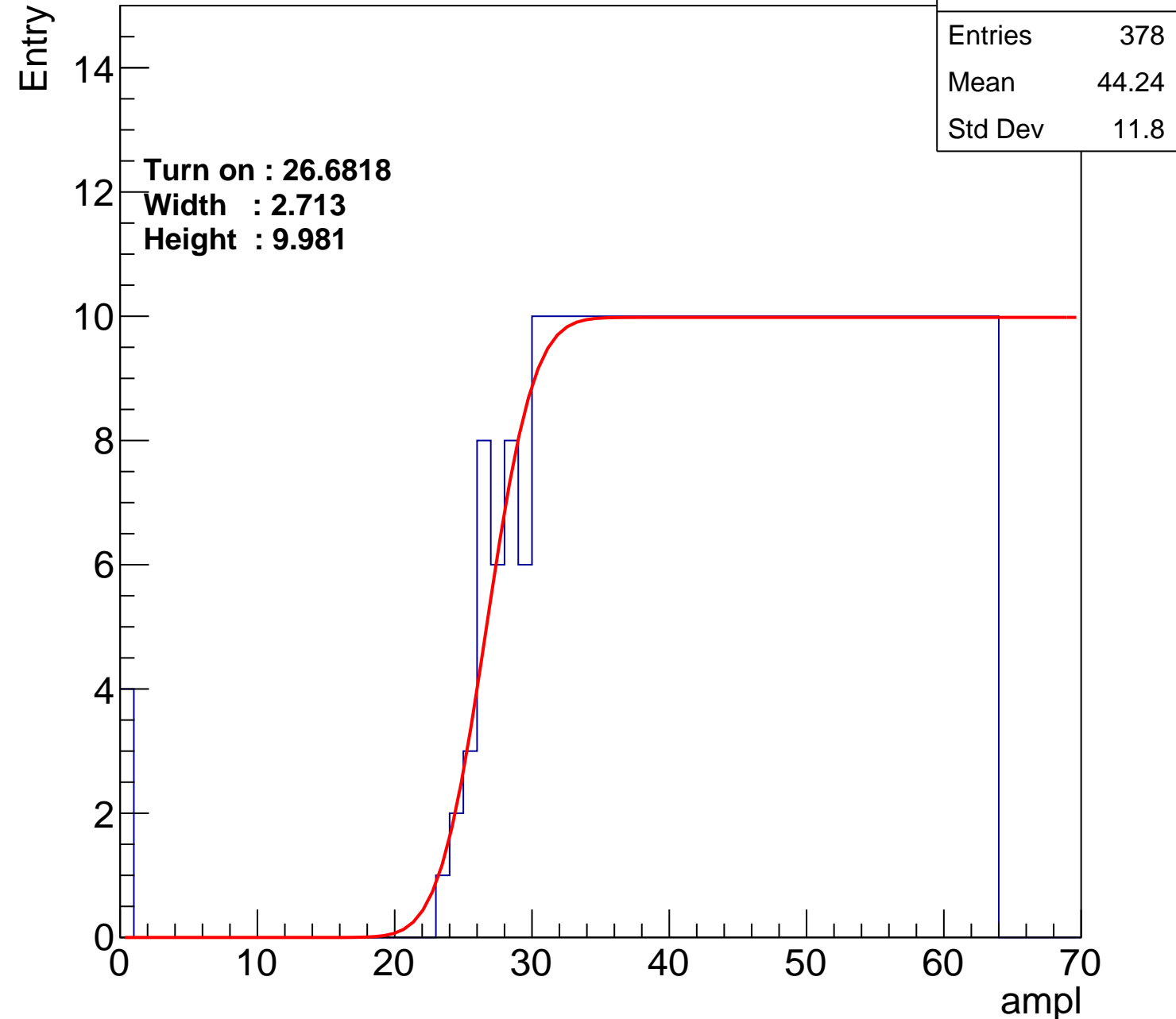
Width : 2.713

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch57

calib\_packv5\_042523\_0143.root, FC#12, port B1

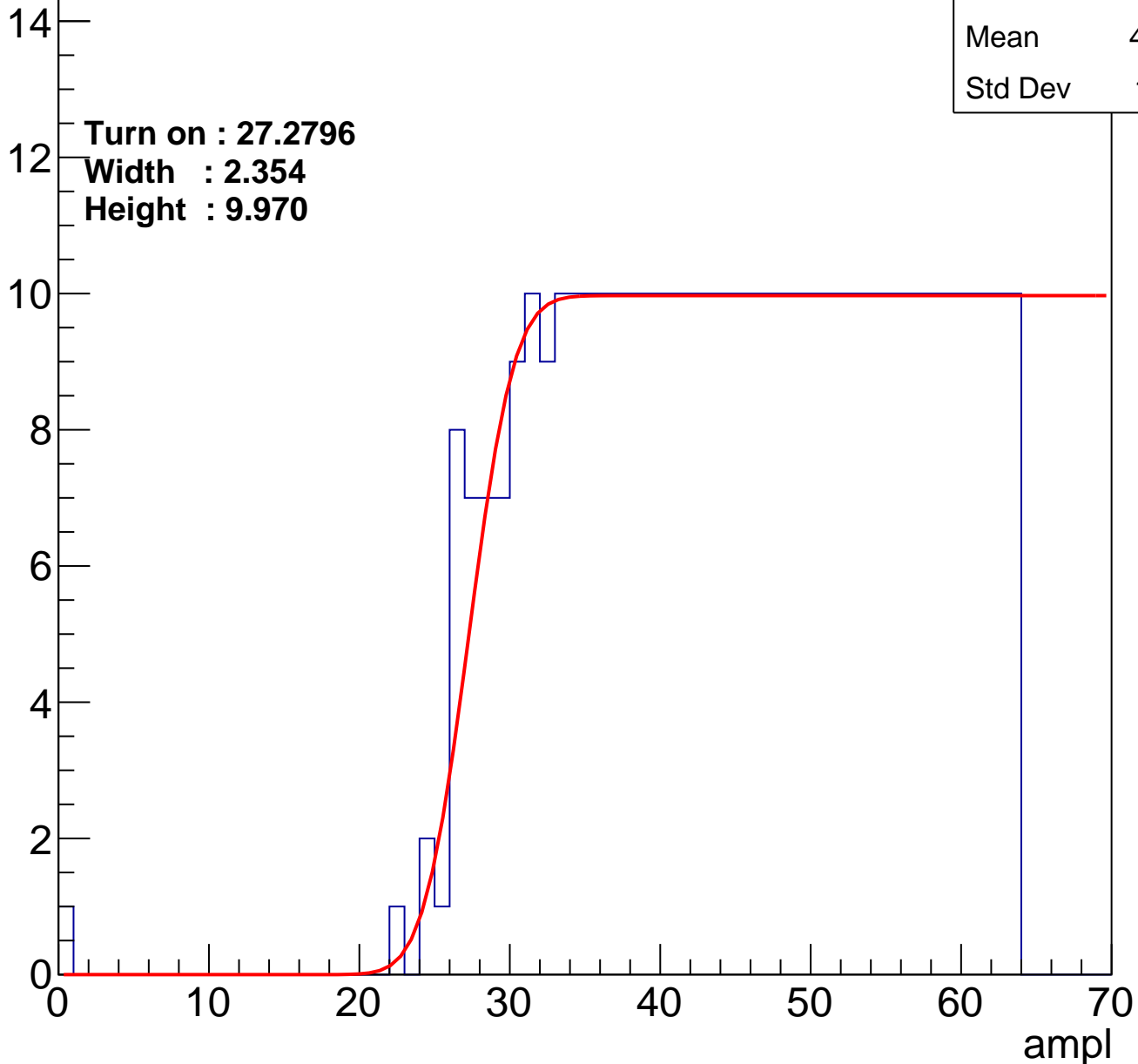
Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 27.2796

Width : 2.354

Height : 9.970

Entry



# B0L102S, U22-ch58

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.79
Std Dev	11.4

Turn on : 27.6774

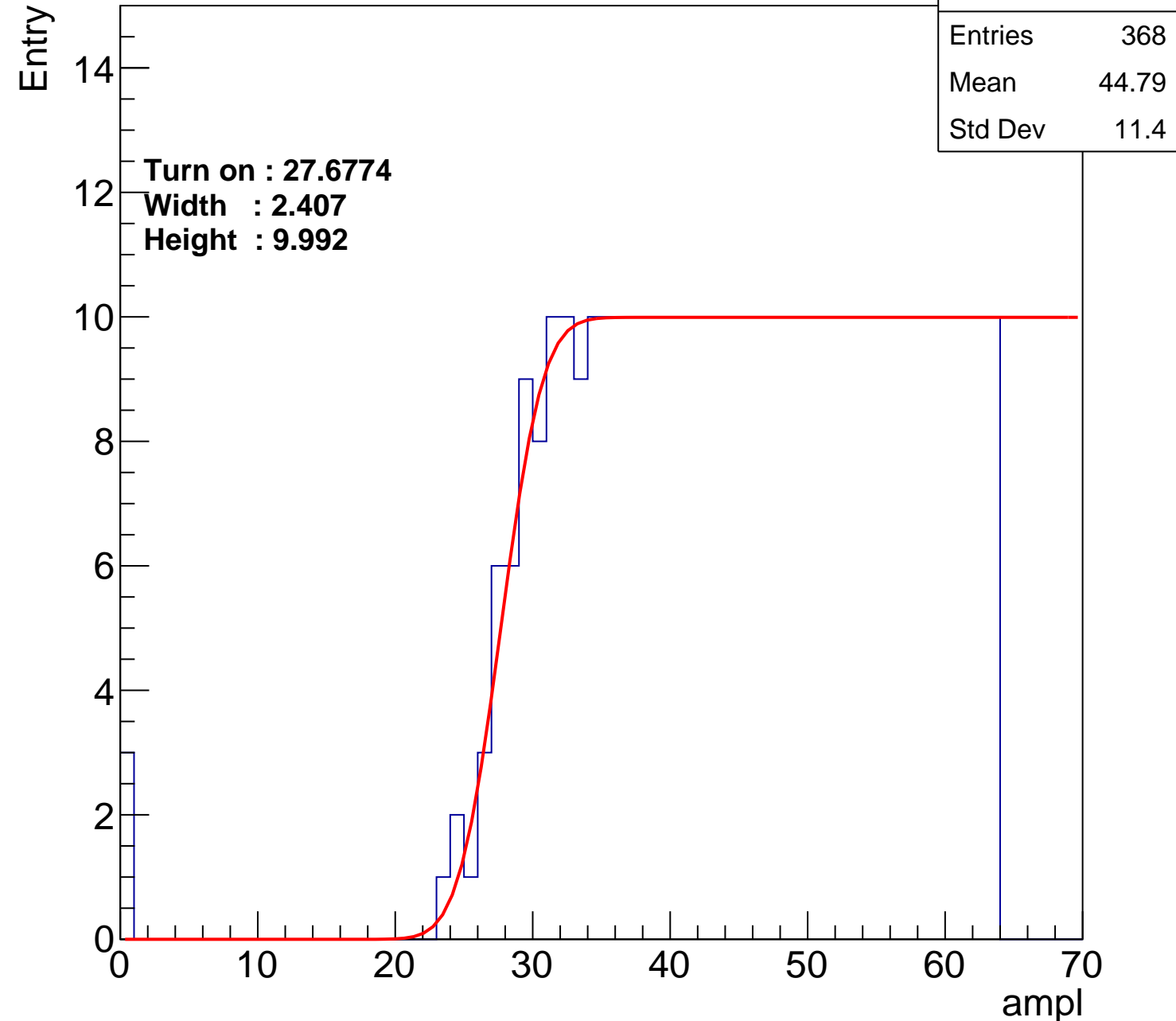
Width : 2.407

Height : 9.992

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch59

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.2848

Width : 2.879

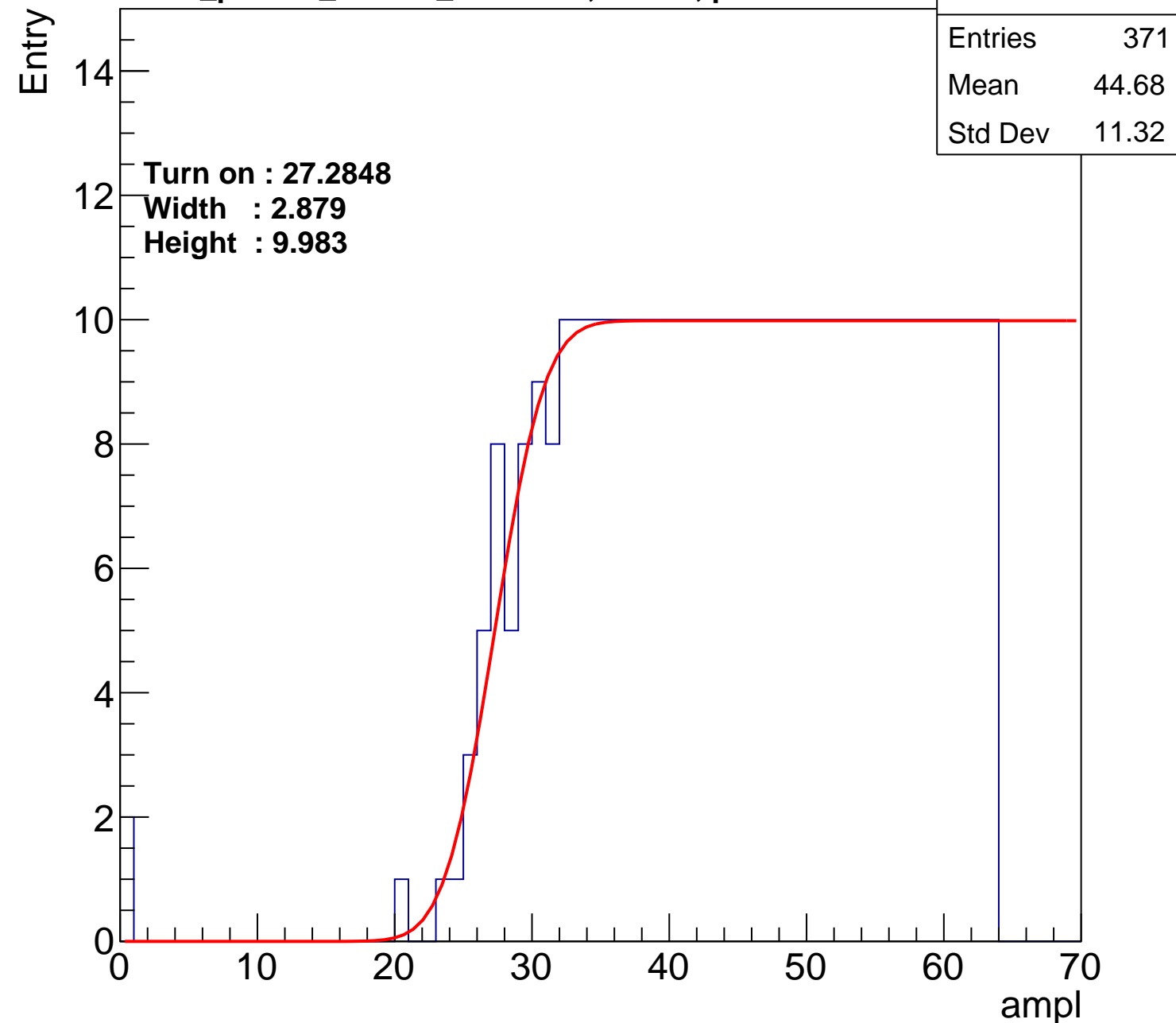
Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U22-ch60

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.55
Std Dev	12.15

Turn on : 25.7849

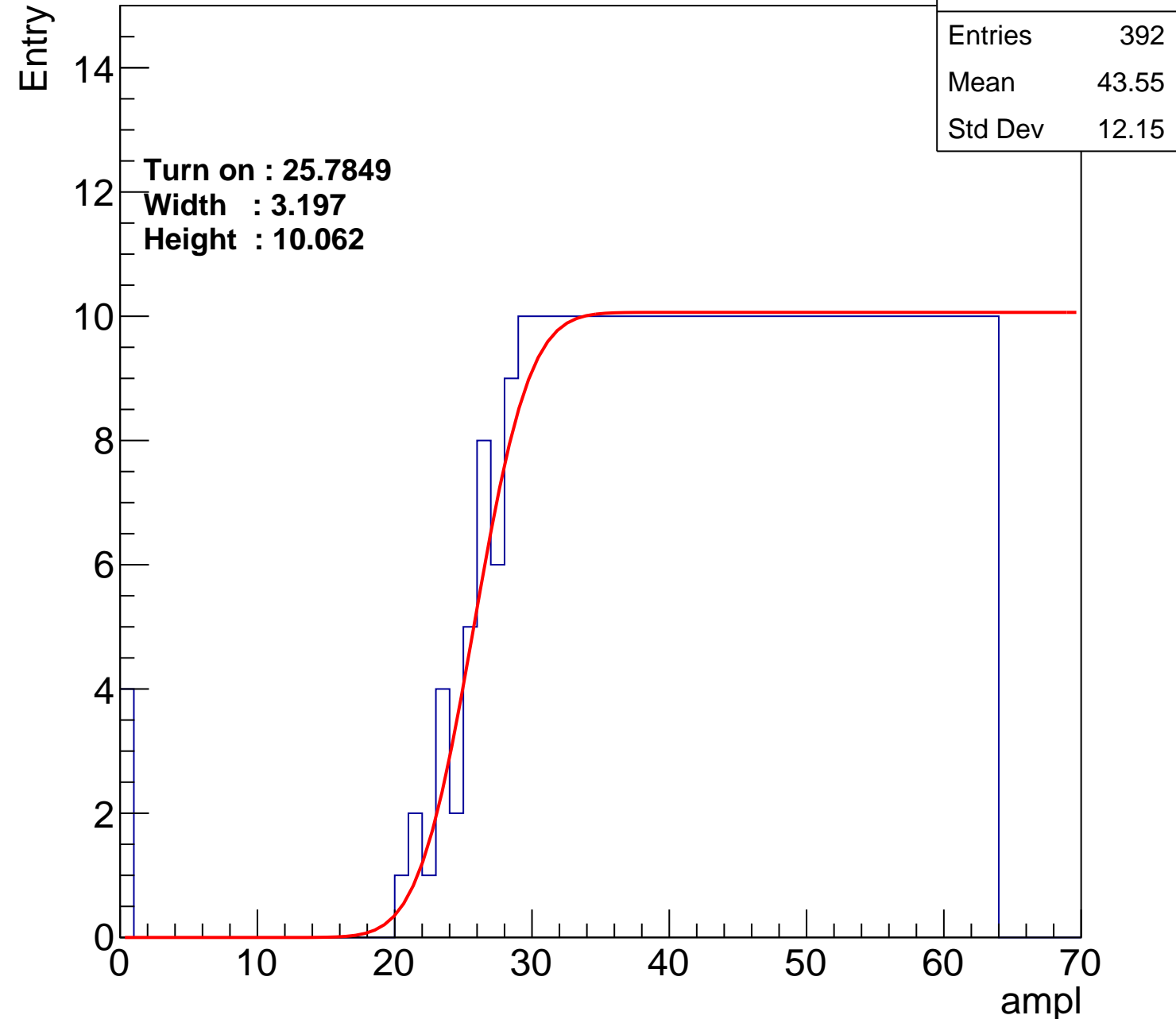
Width : 3.197

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch61

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	362
Mean	45.25
Std Dev	10.8

**Turn on : 28.0433**

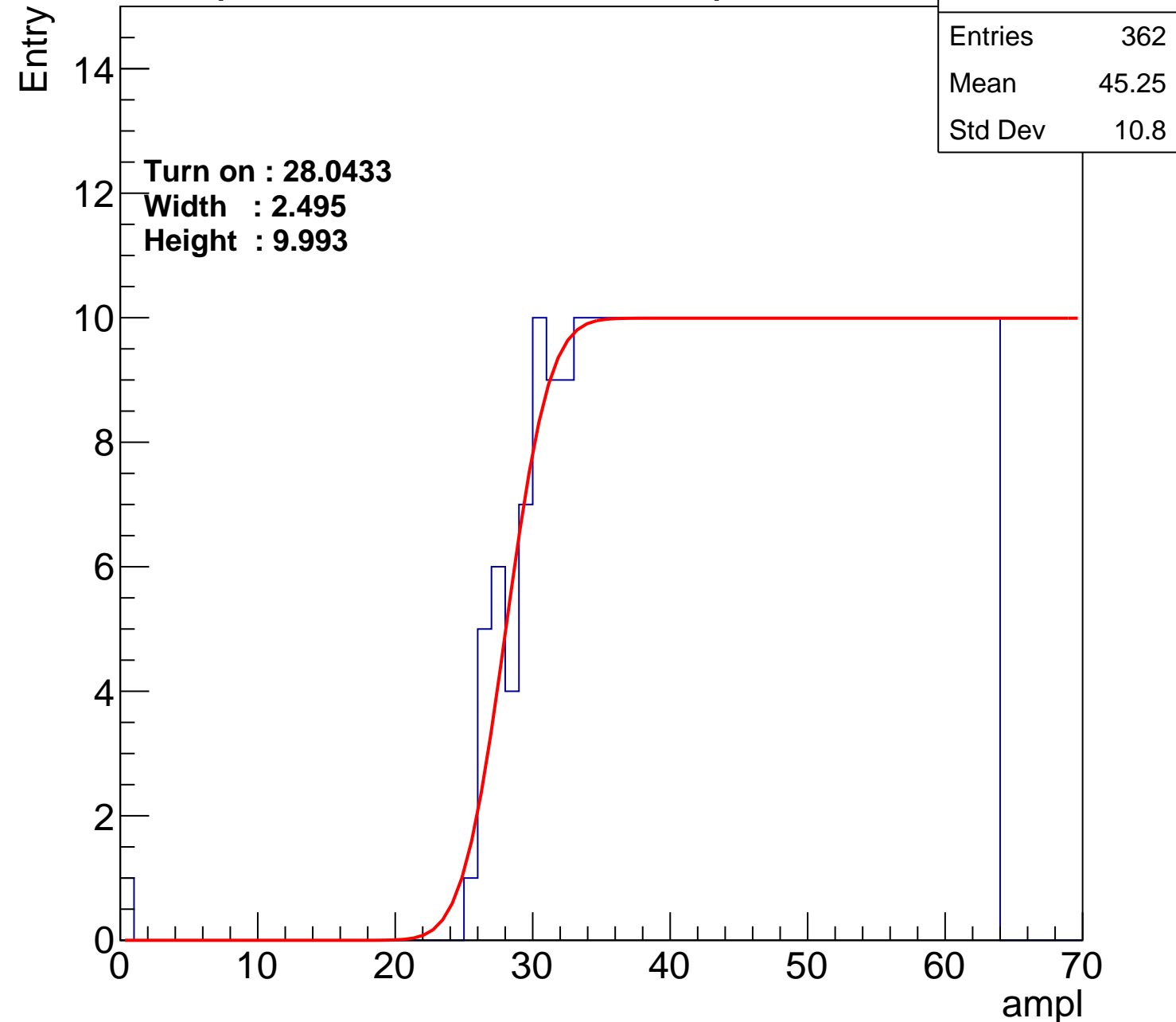
**Width : 2.495**

**Height : 9.993**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch62

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	374
Mean	44.48
Std Dev	11.57

Turn on : 27.1010

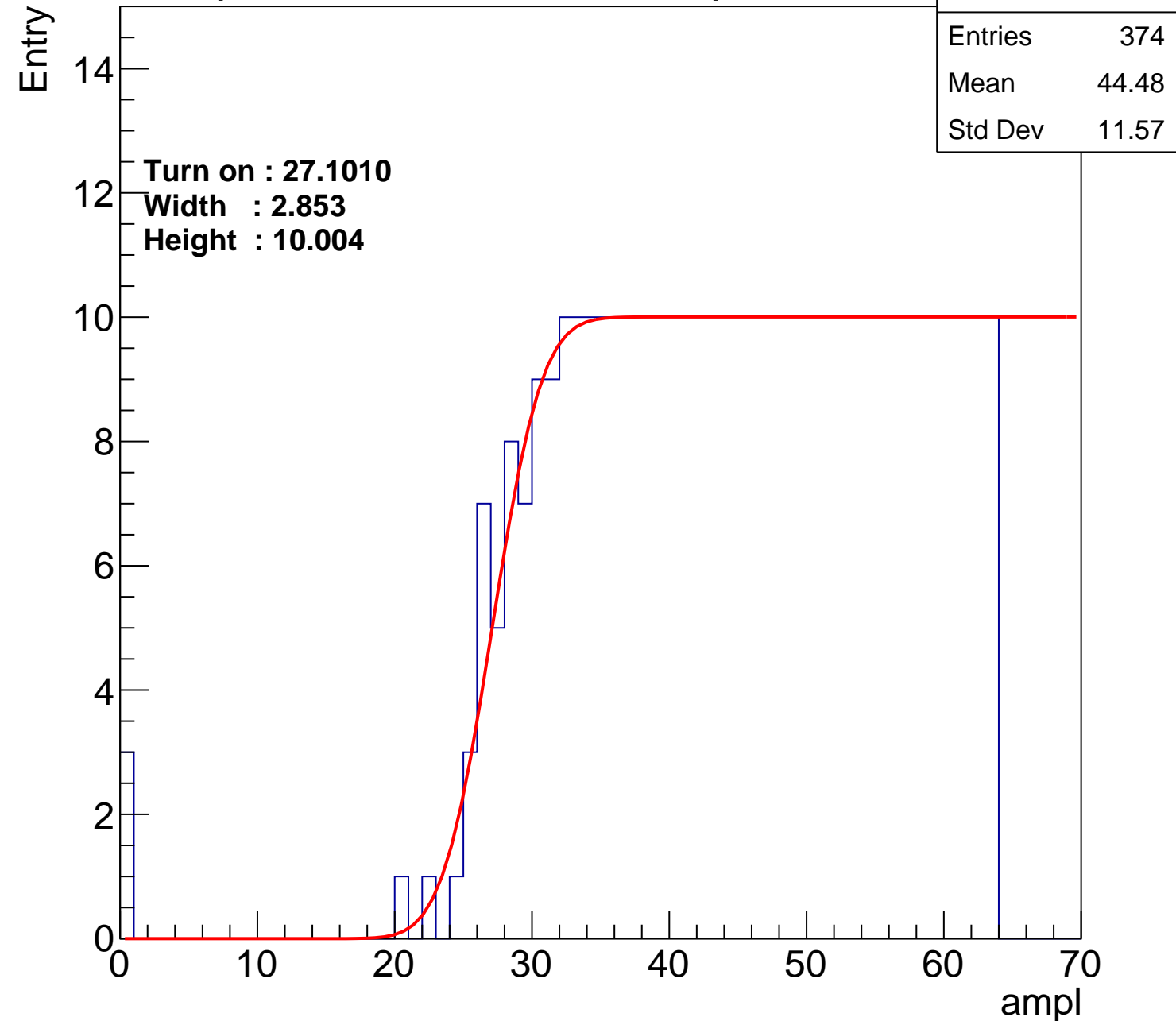
Width : 2.853

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch63

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.2848

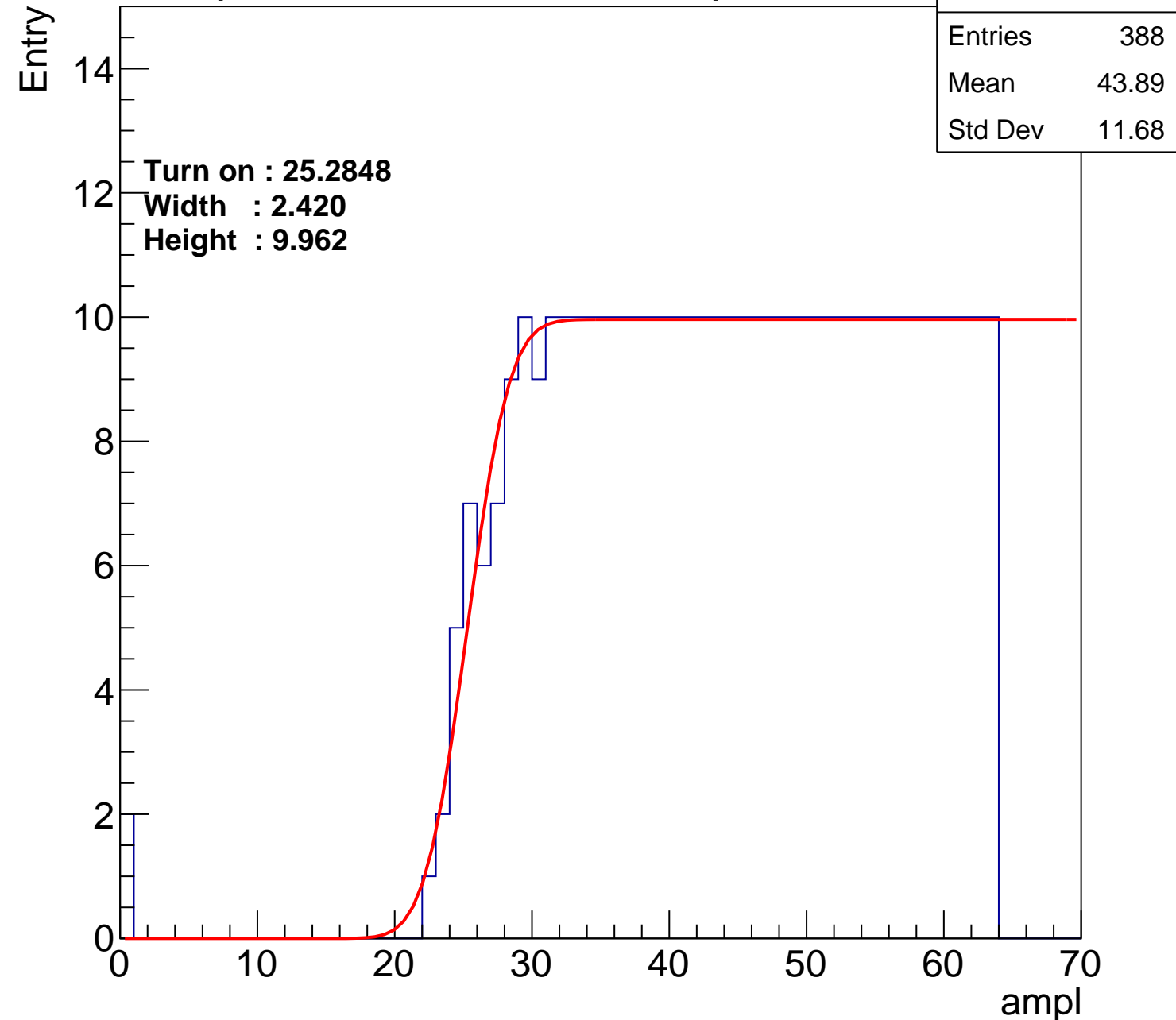
Width : 2.420

Height : 9.962

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch64

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.36
Std Dev	11.46

Turn on : 26.6743

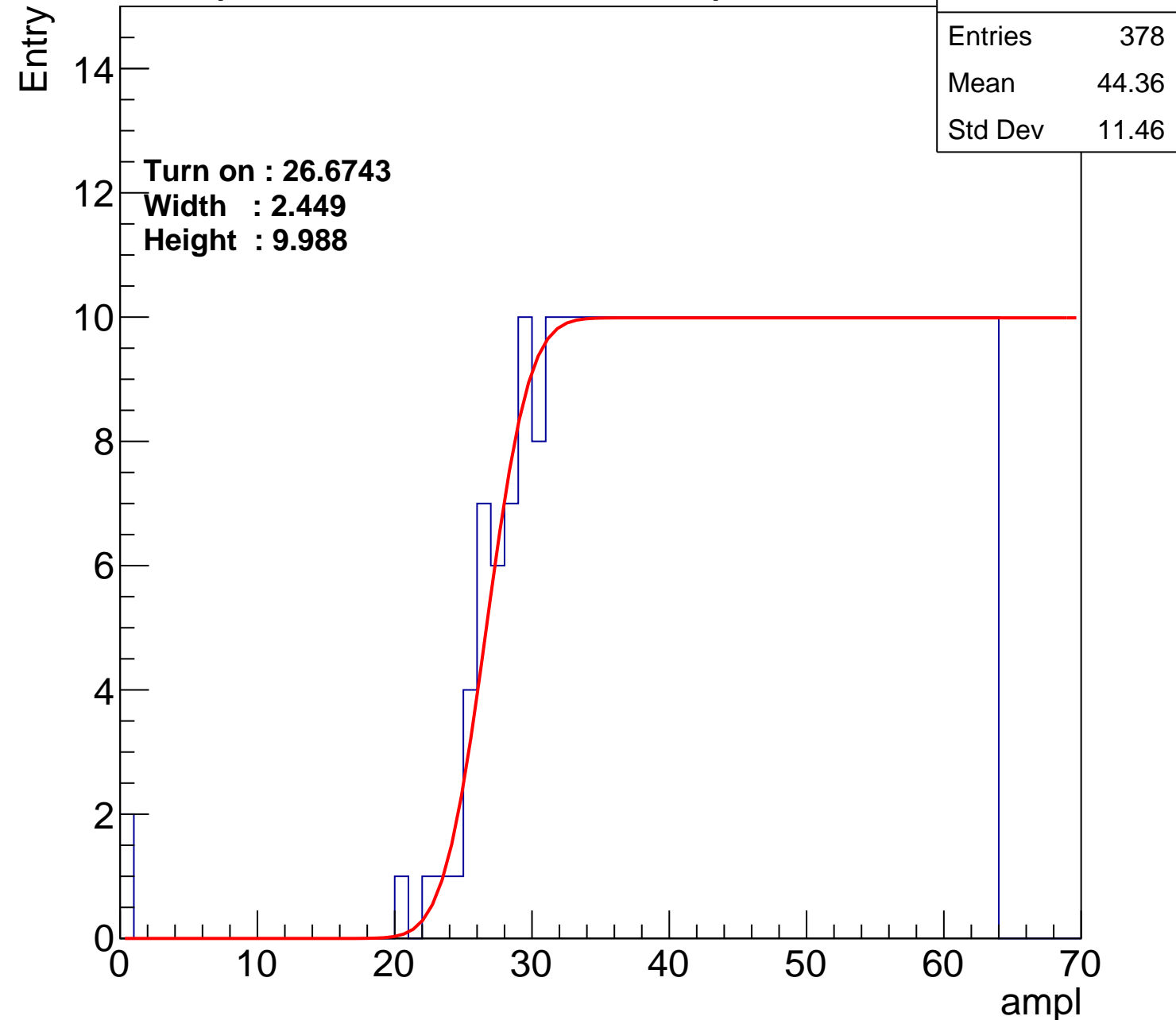
Width : 2.449

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch65

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 26.2664

Width : 2.907

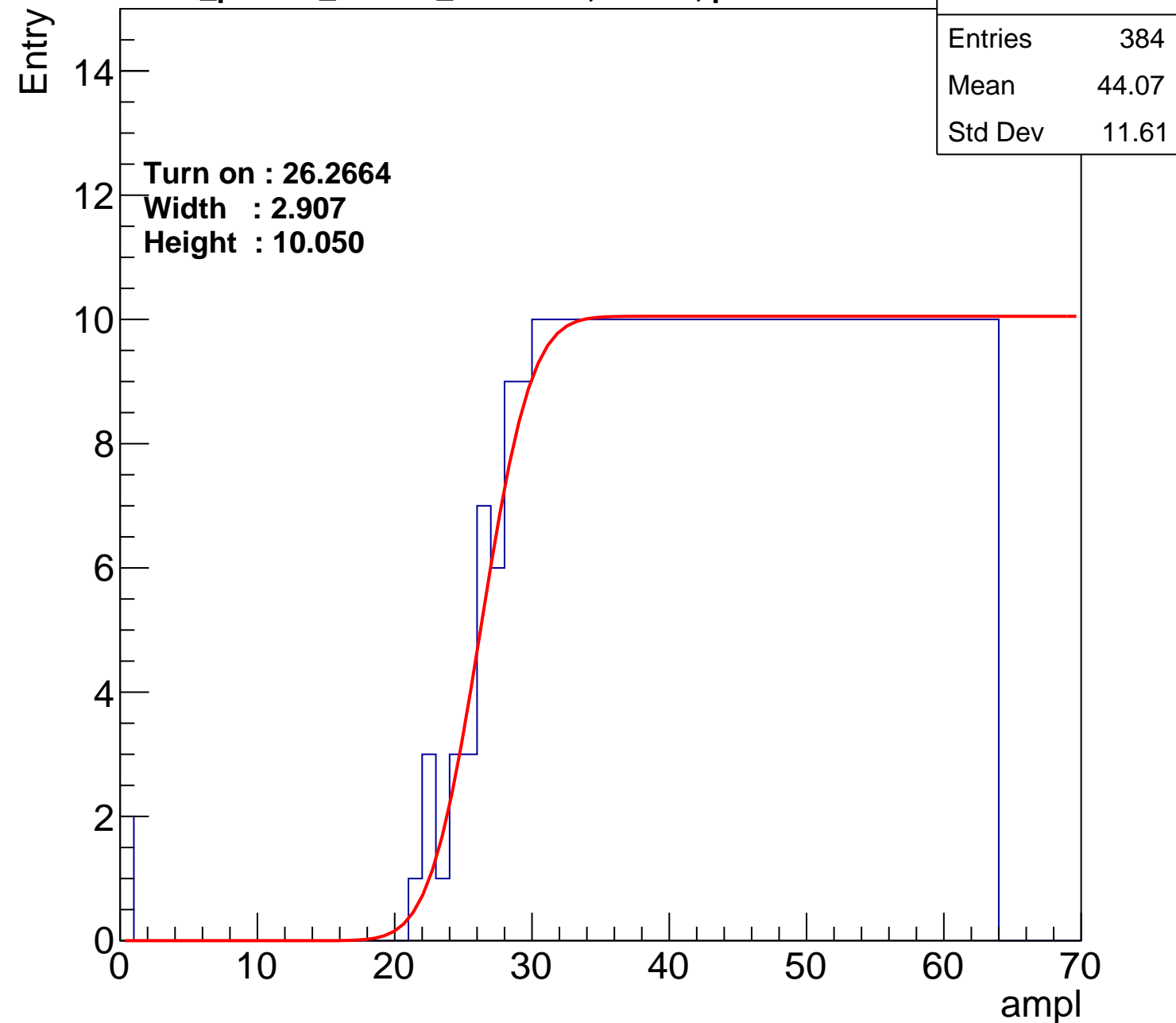
Height : 10.050

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U22-ch66

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	389
Mean	43.79
Std Dev	11.86

Turn on : 25.0265

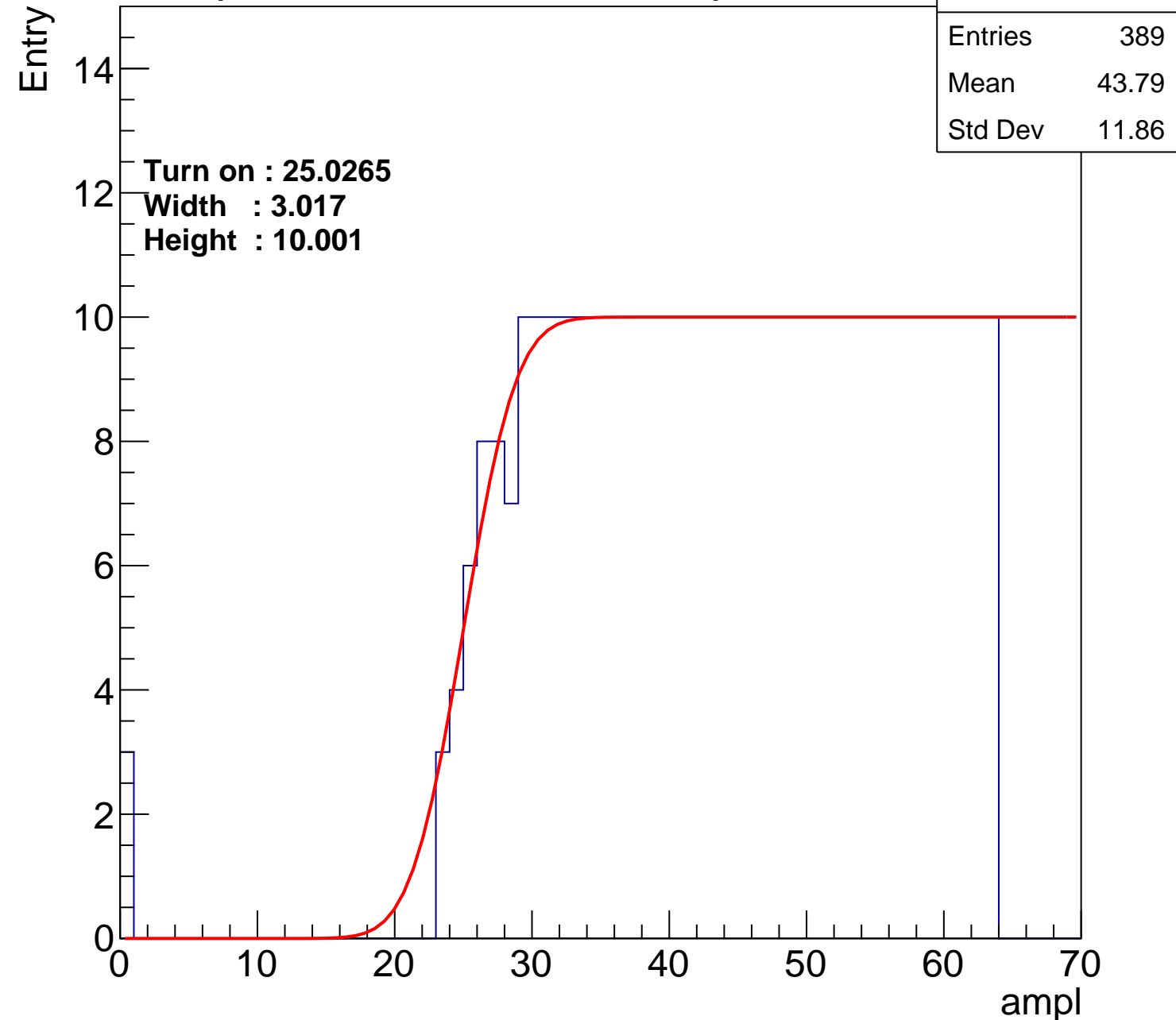
Width : 3.017

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch67

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.9
Std Dev	11.17

Turn on : 27.2606

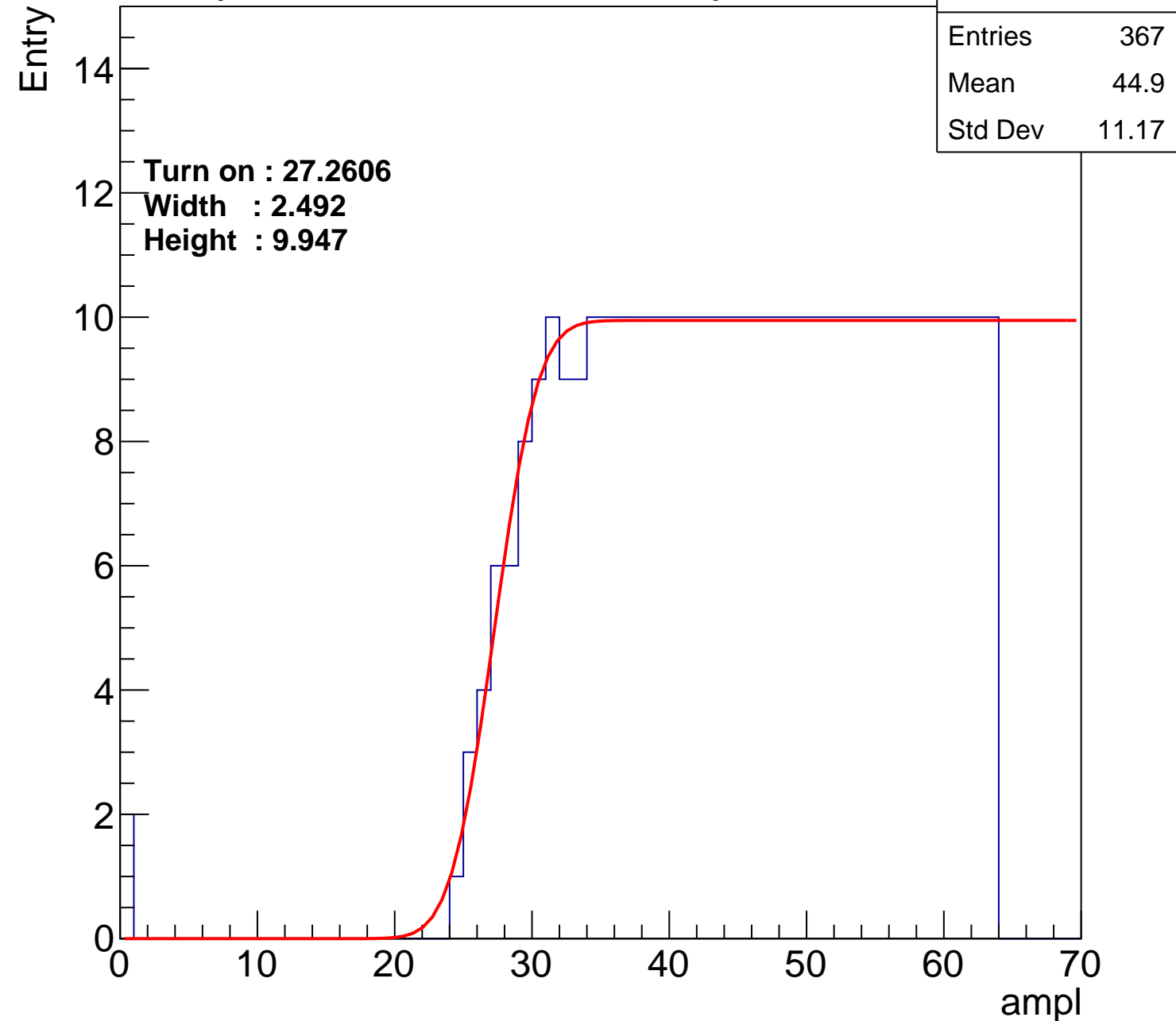
Width : 2.492

Height : 9.947

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch68

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	43.97
Std Dev	11.78

Turn on : 25.8141

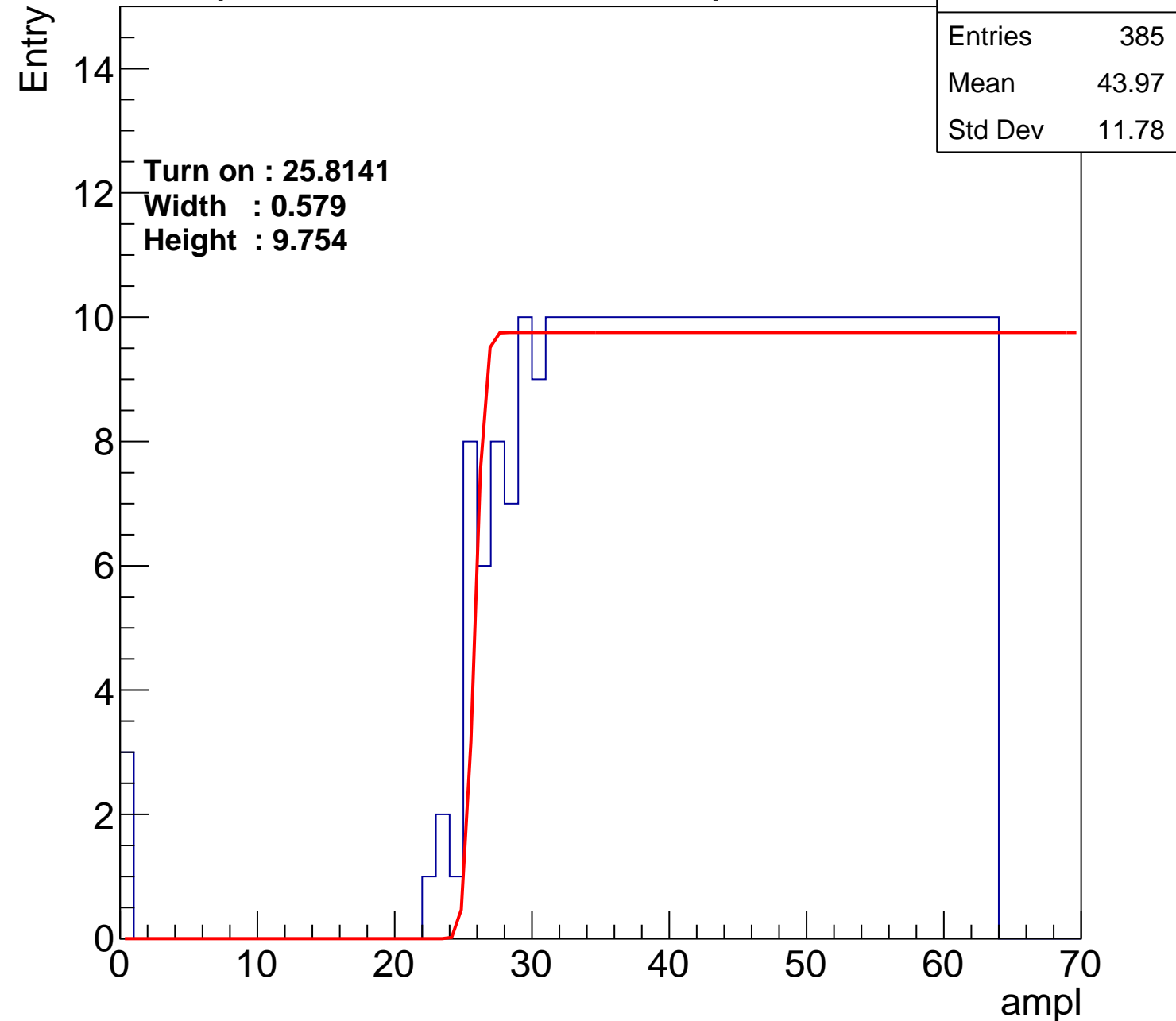
Width : 0.579

Height : 9.754

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch69

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	370
Mean	44.74
Std Dev	11.27

Turn on : 27.5387

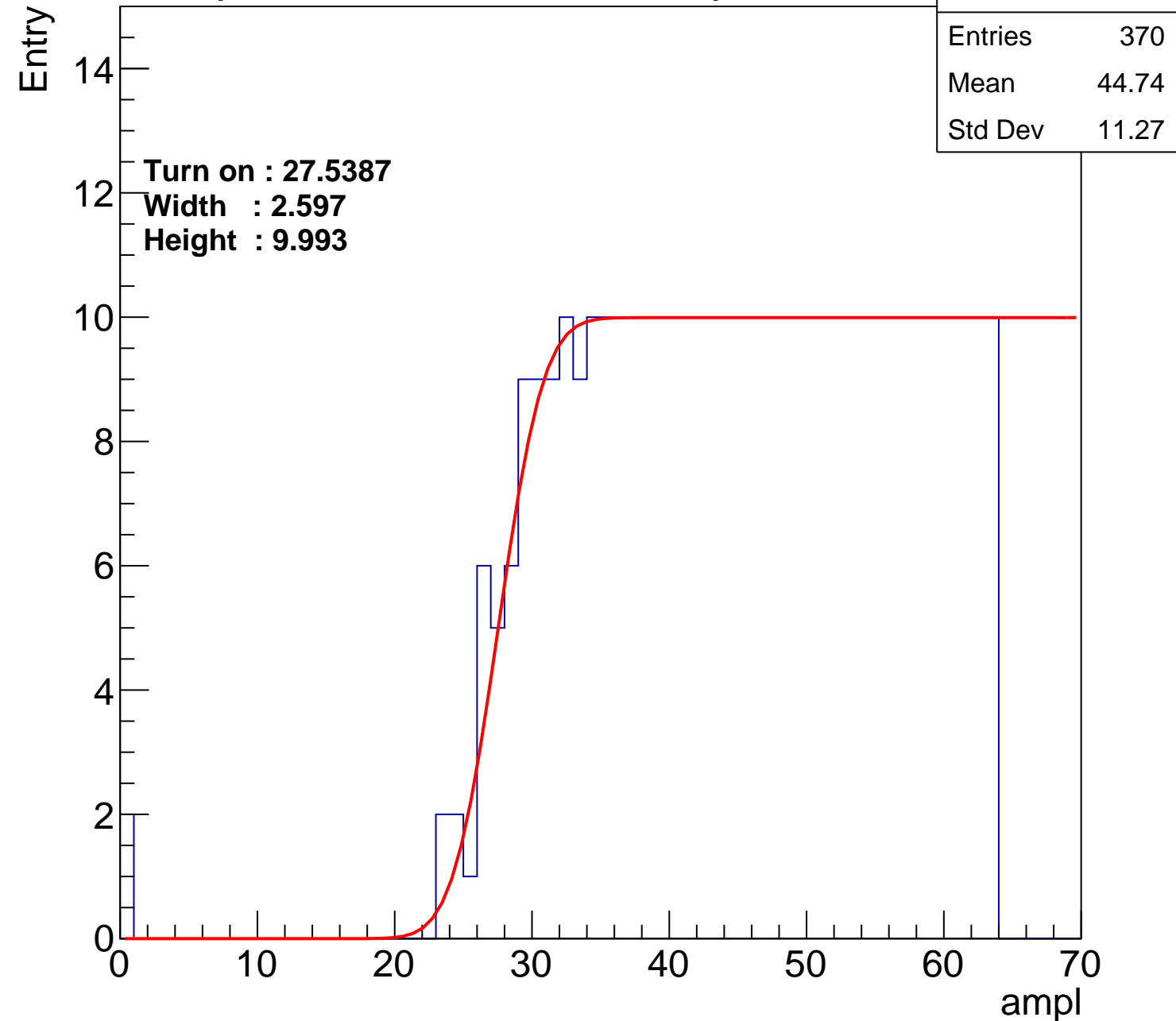
Width : 2.597

Height : 9.993

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch70

calib\_packv5\_042523\_0143.root, FC#12, port B1

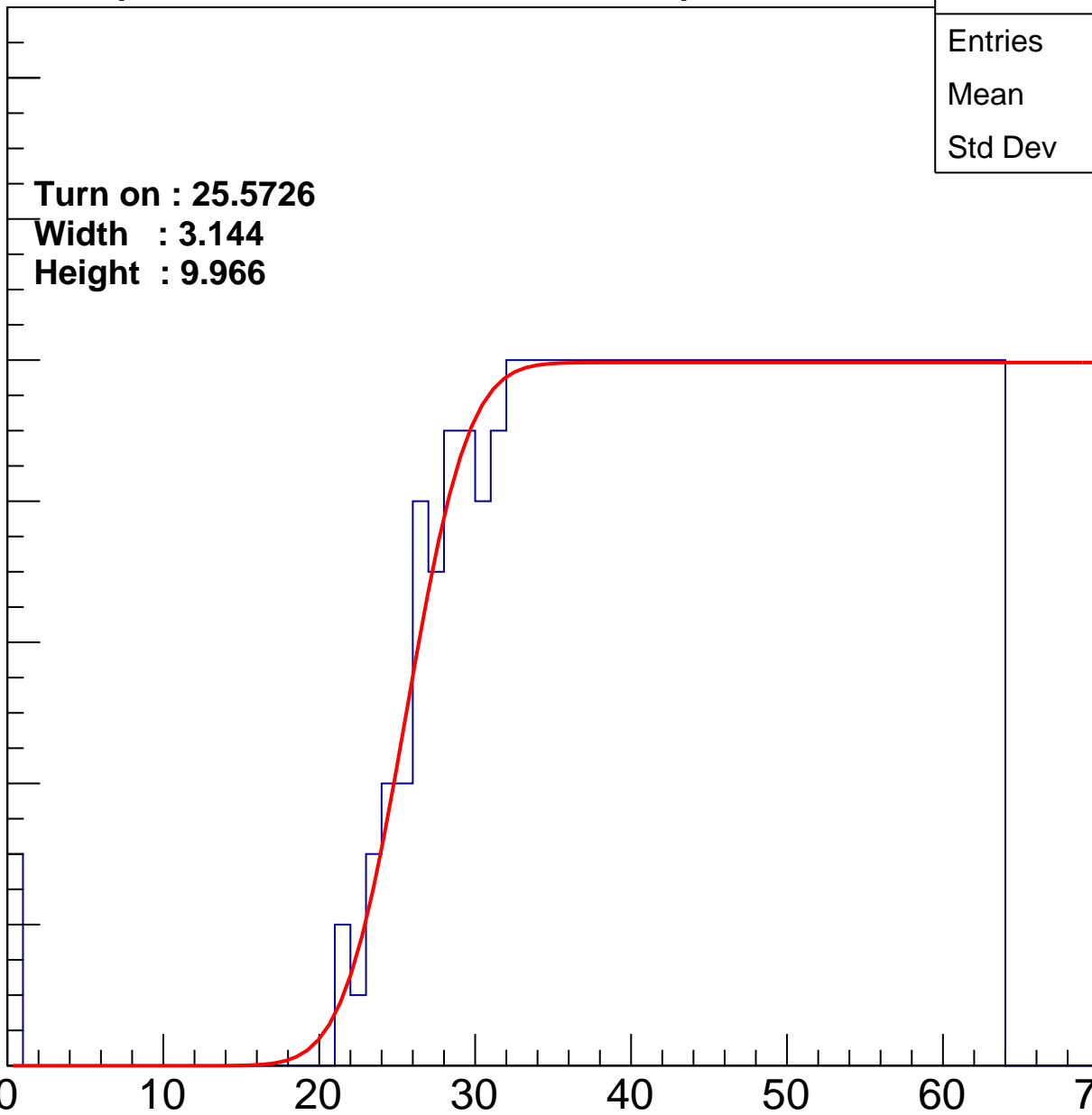
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.5726  
Width : 3.144  
Height : 9.966

Entries	387
Mean	43.82
Std Dev	11.91

ampl





# B0L102S, U22-ch71

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	351
Mean	45.53
Std Dev	11.18

Turn on : 29.6201

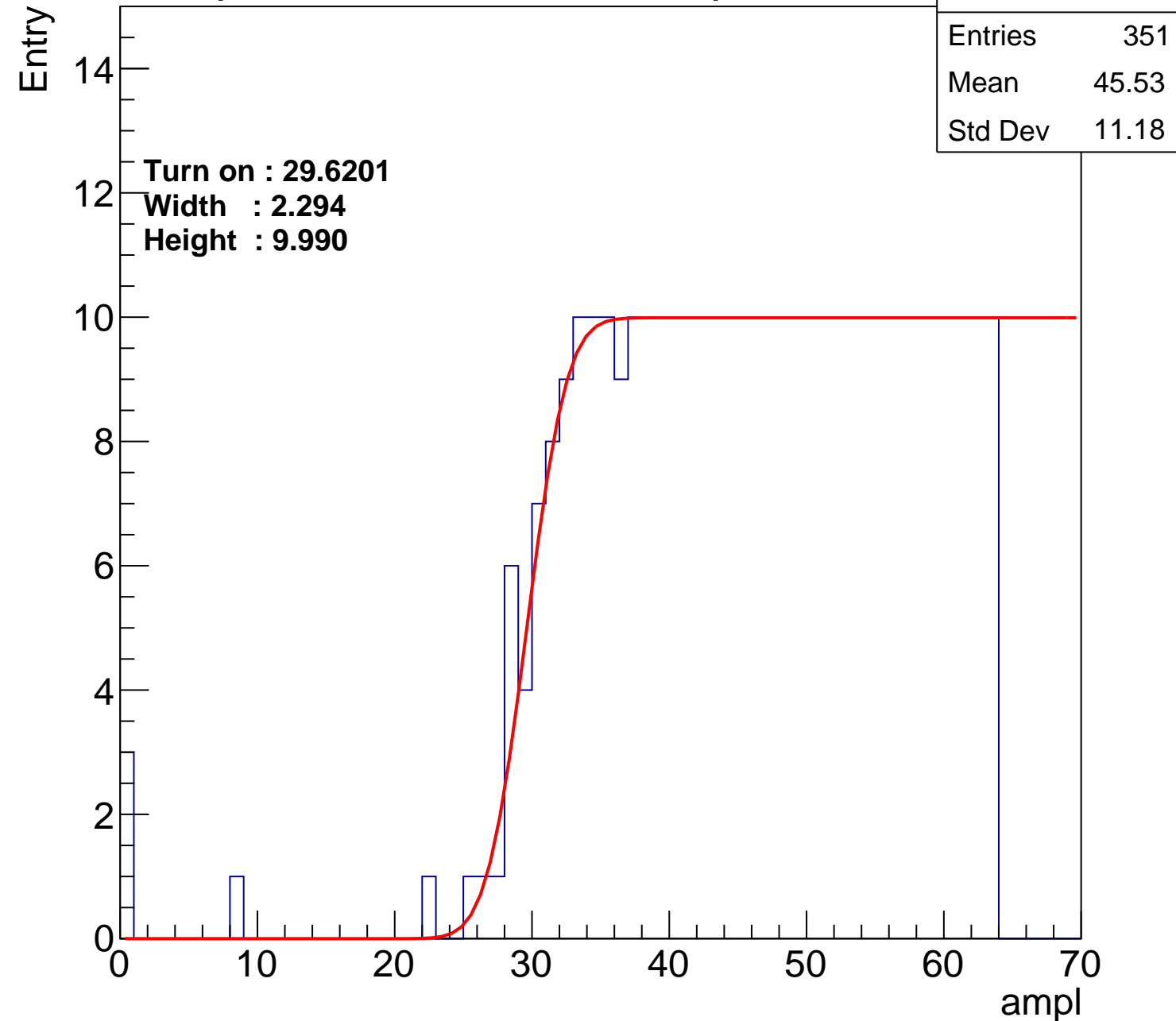
Width : 2.294

Height : 9.990

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch72

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.15
Std Dev	11.47

Turn on : 26.5521

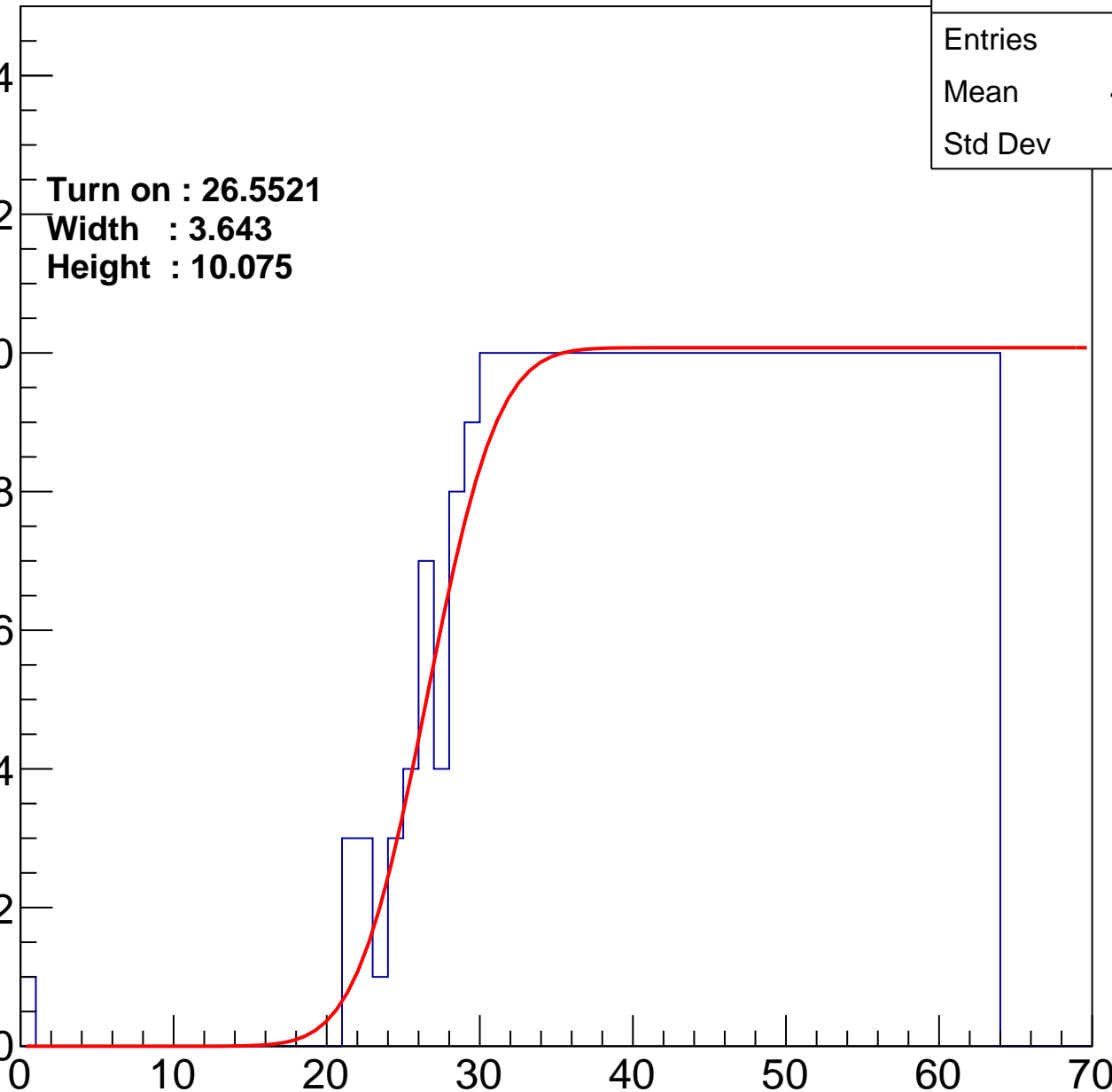
Width : 3.643

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch73

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.86
Std Dev	11.2

Turn on : 28.2098

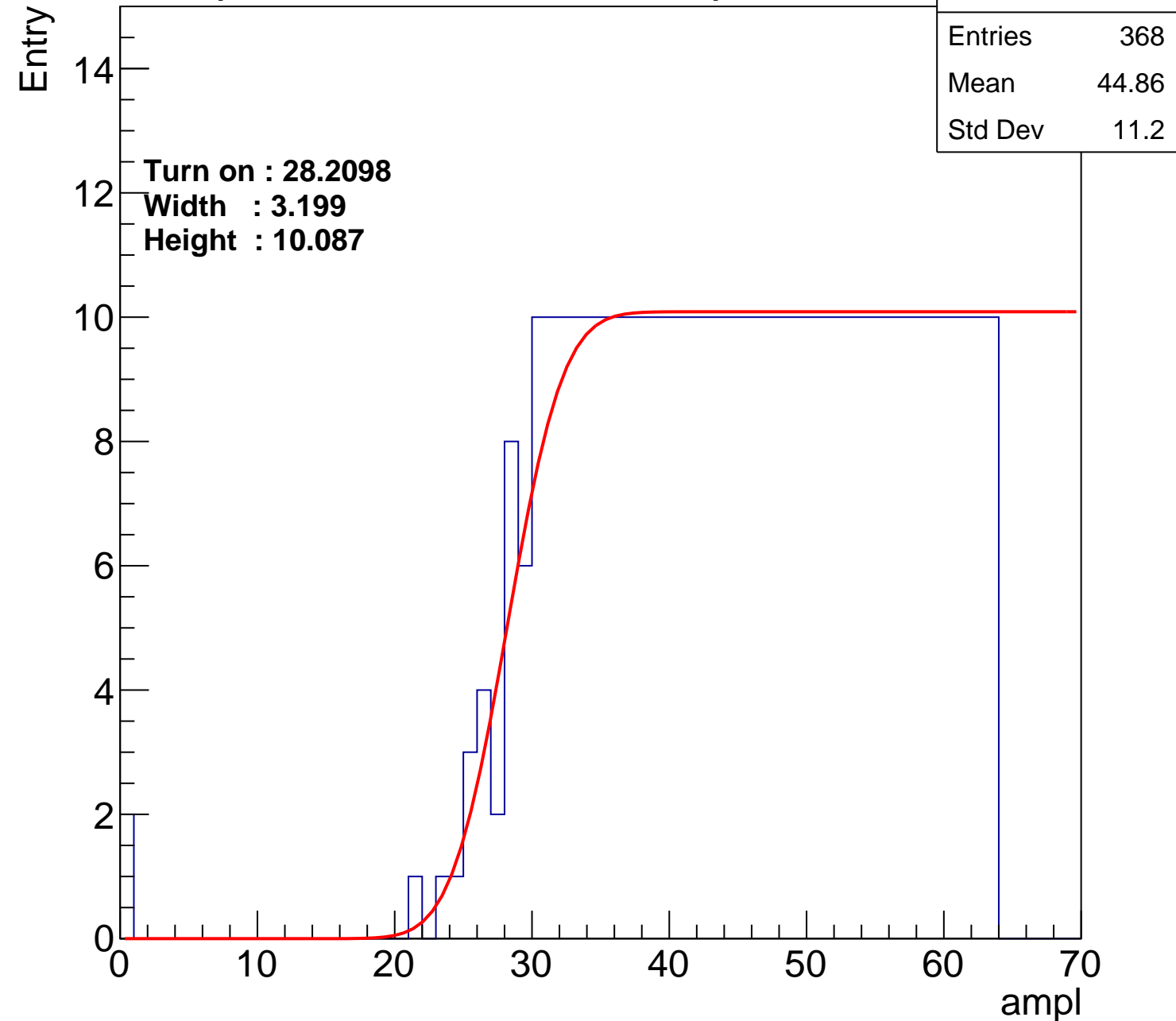
Width : 3.199

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch74

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 27.1004

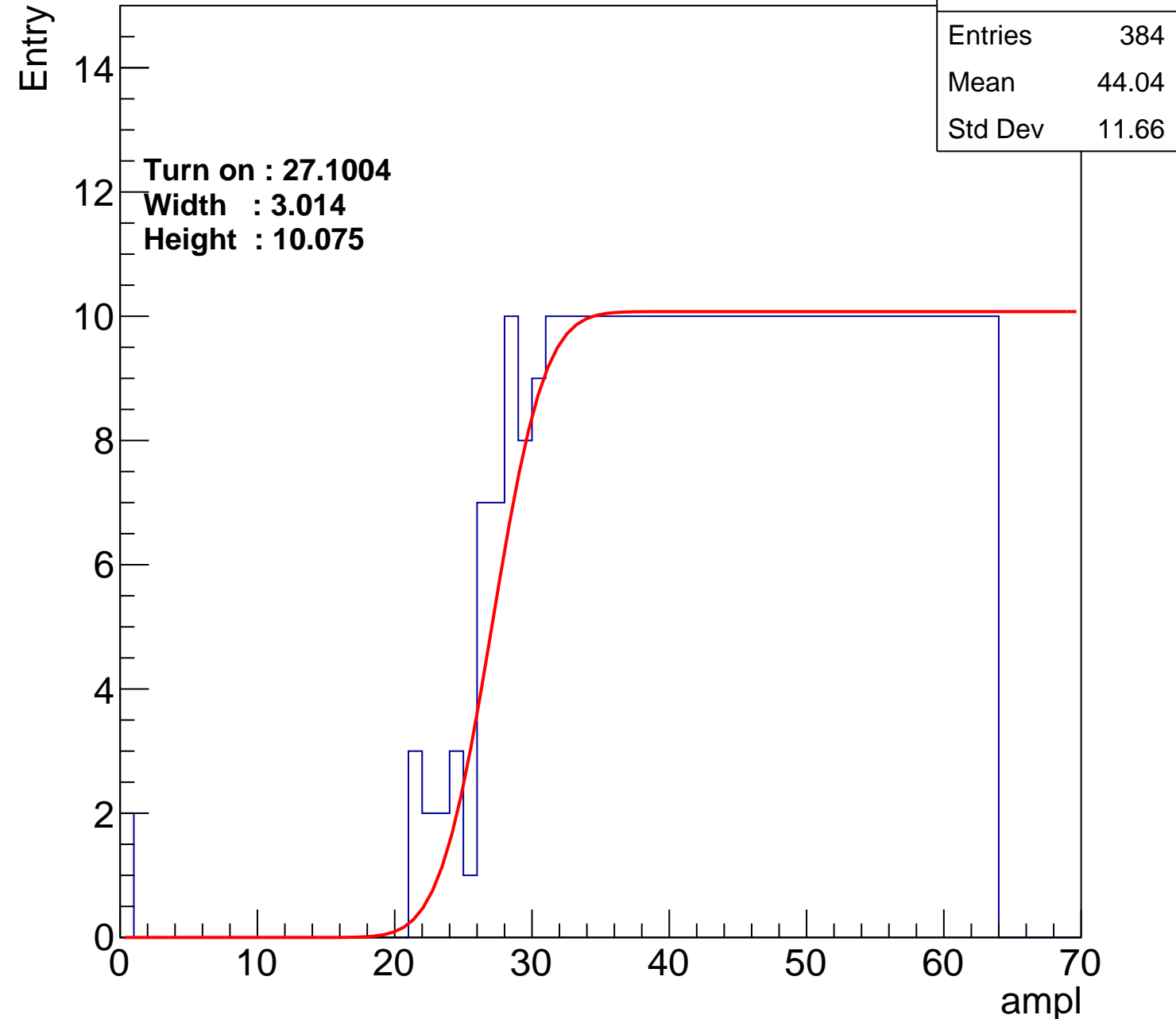
Width : 3.014

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch75

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	366
Mean	44.93
Std Dev	11.19

Turn on : 27.7177

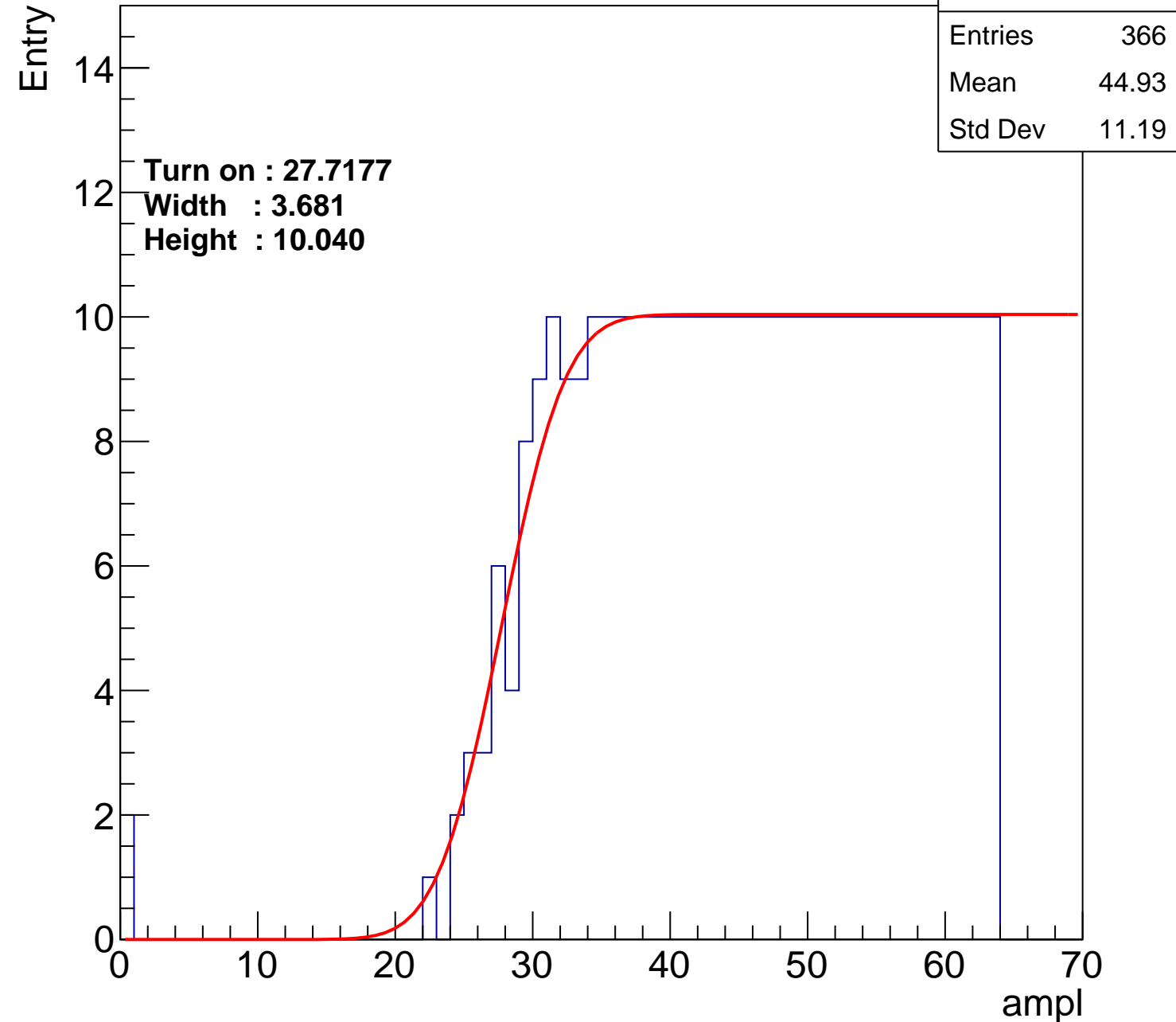
Width : 3.681

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch76

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.91
Std Dev	11.02

Turn on : 27.4527

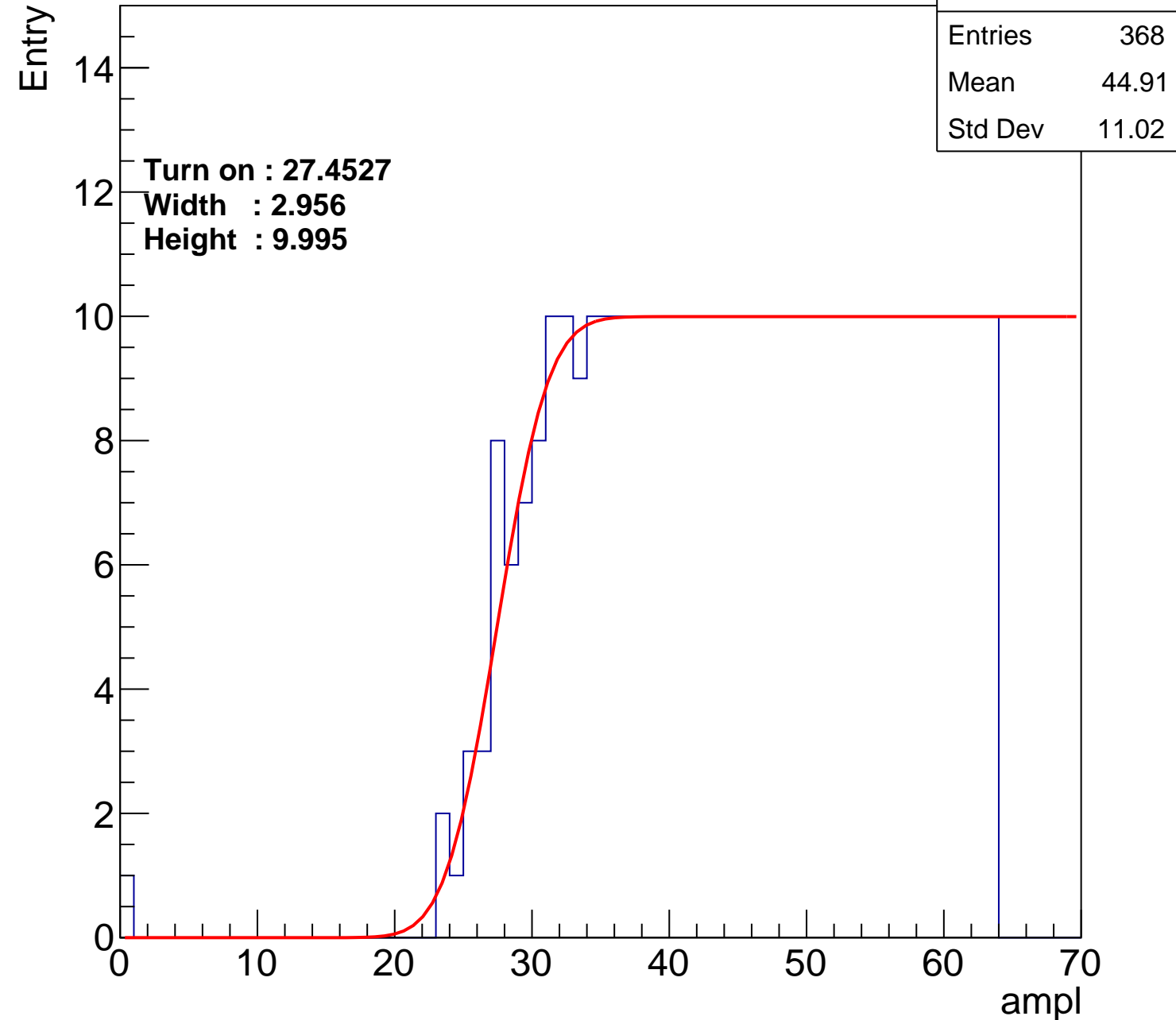
Width : 2.956

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch77

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	360
Mean	45.11
Std Dev	11.33

Turn on : 29.5746

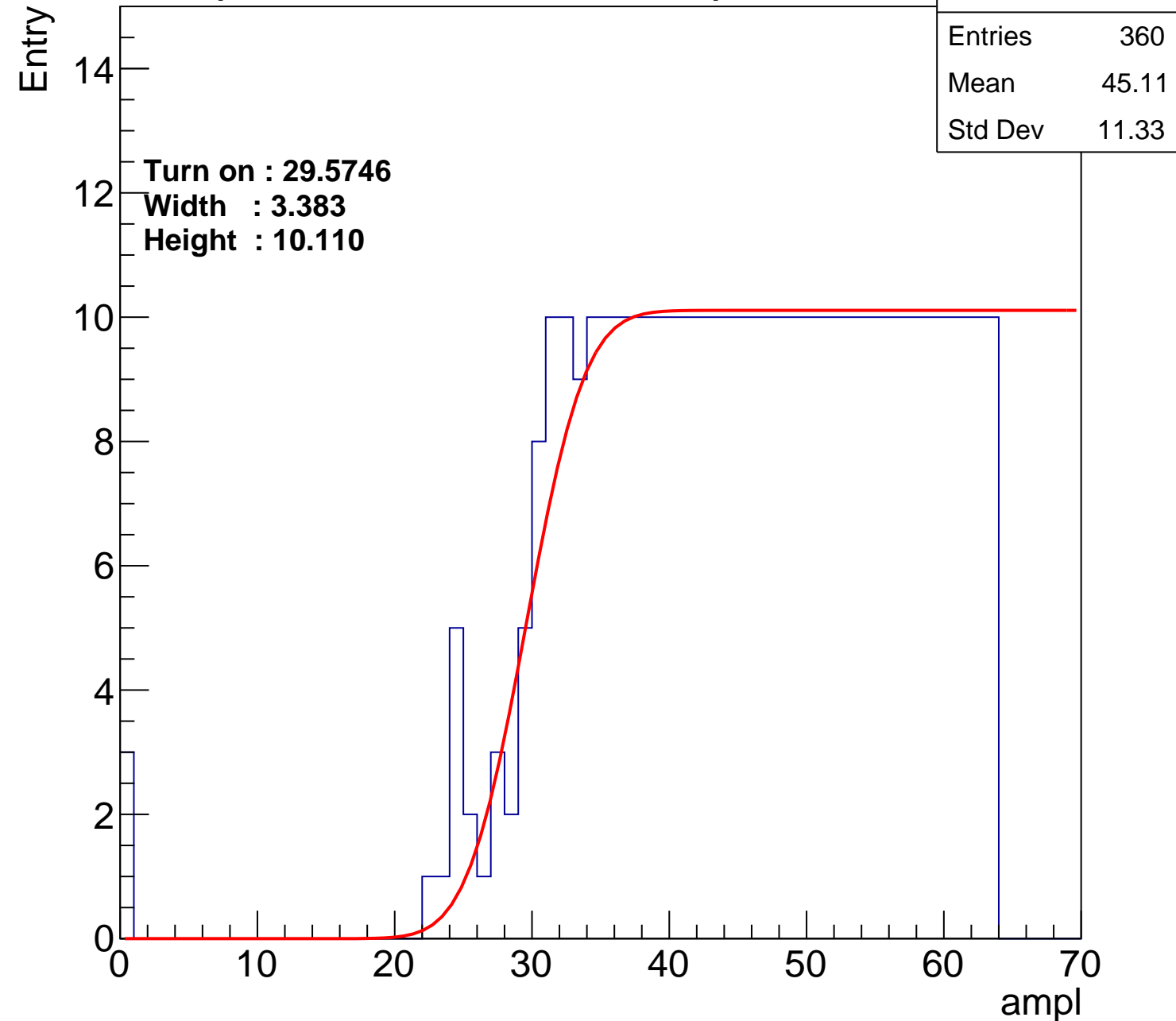
Width : 3.383

Height : 10.110

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch78

calib\_packv5\_042523\_0143.root, FC#12, port B1

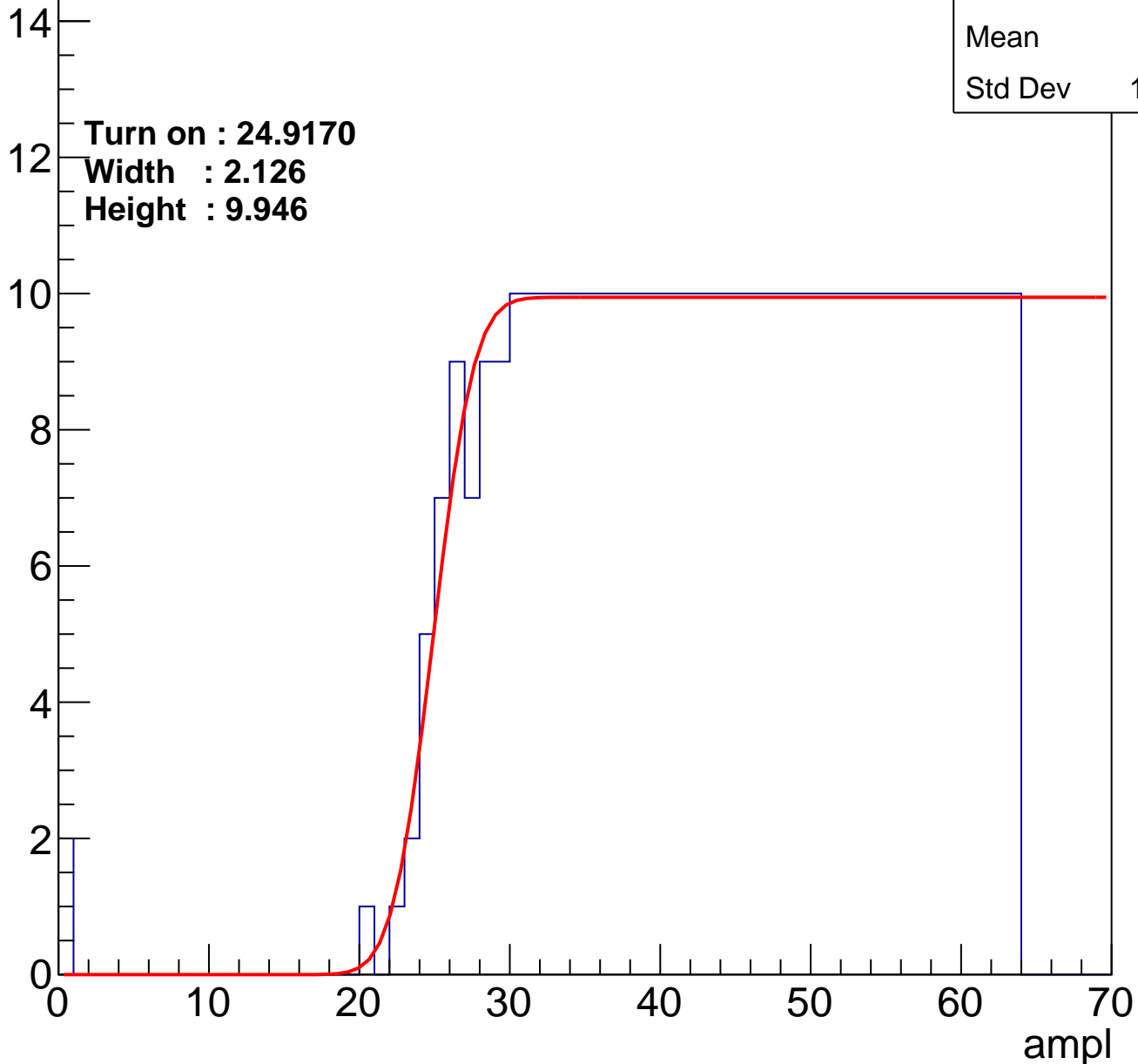
Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 24.9170

Width : 2.126

Height : 9.946

Entry





# B0L102S, U22-ch79

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.51
Std Dev	11.24

**Turn on : 26.7899**

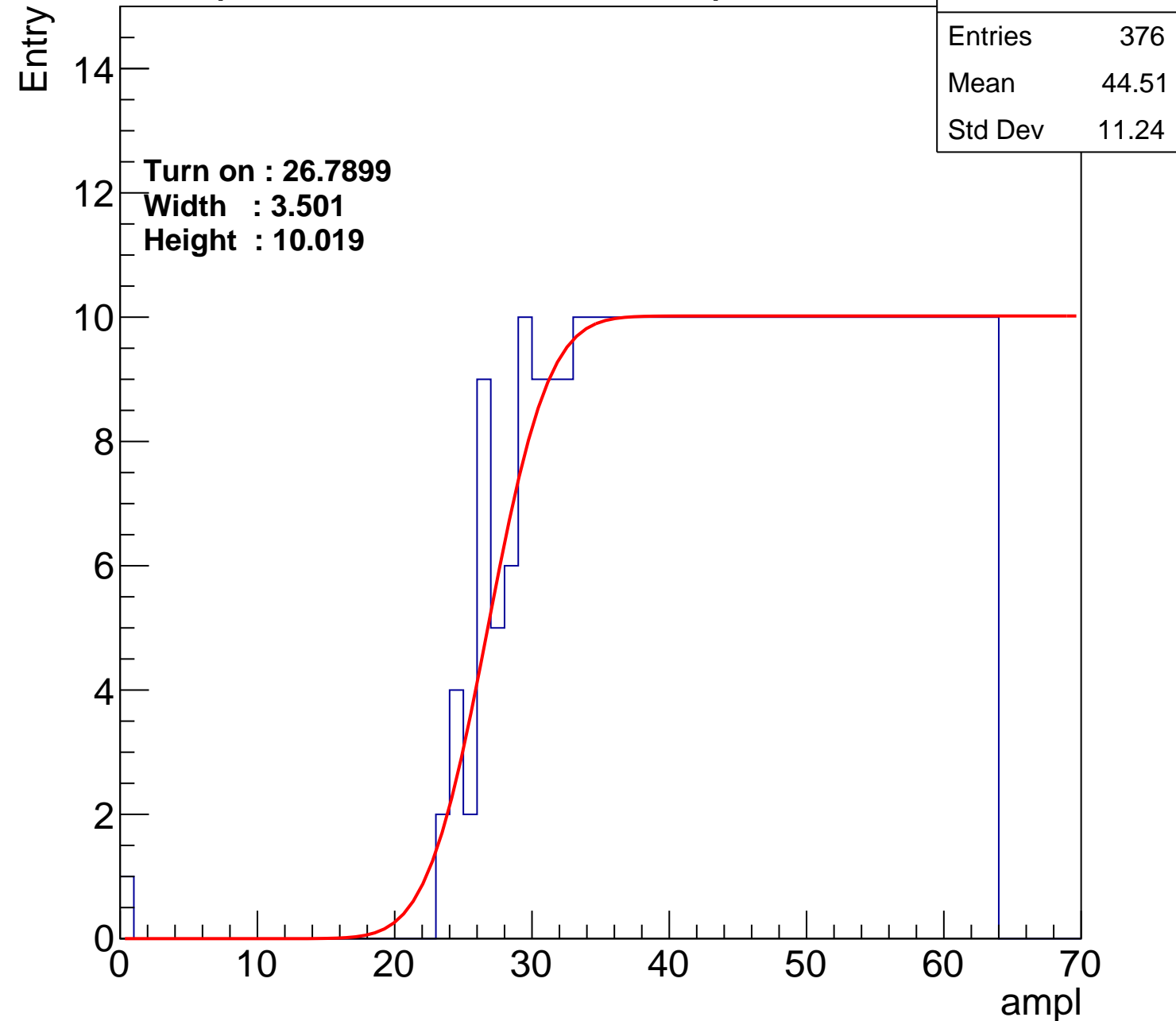
**Width : 3.501**

**Height : 10.019**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch80

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.6010

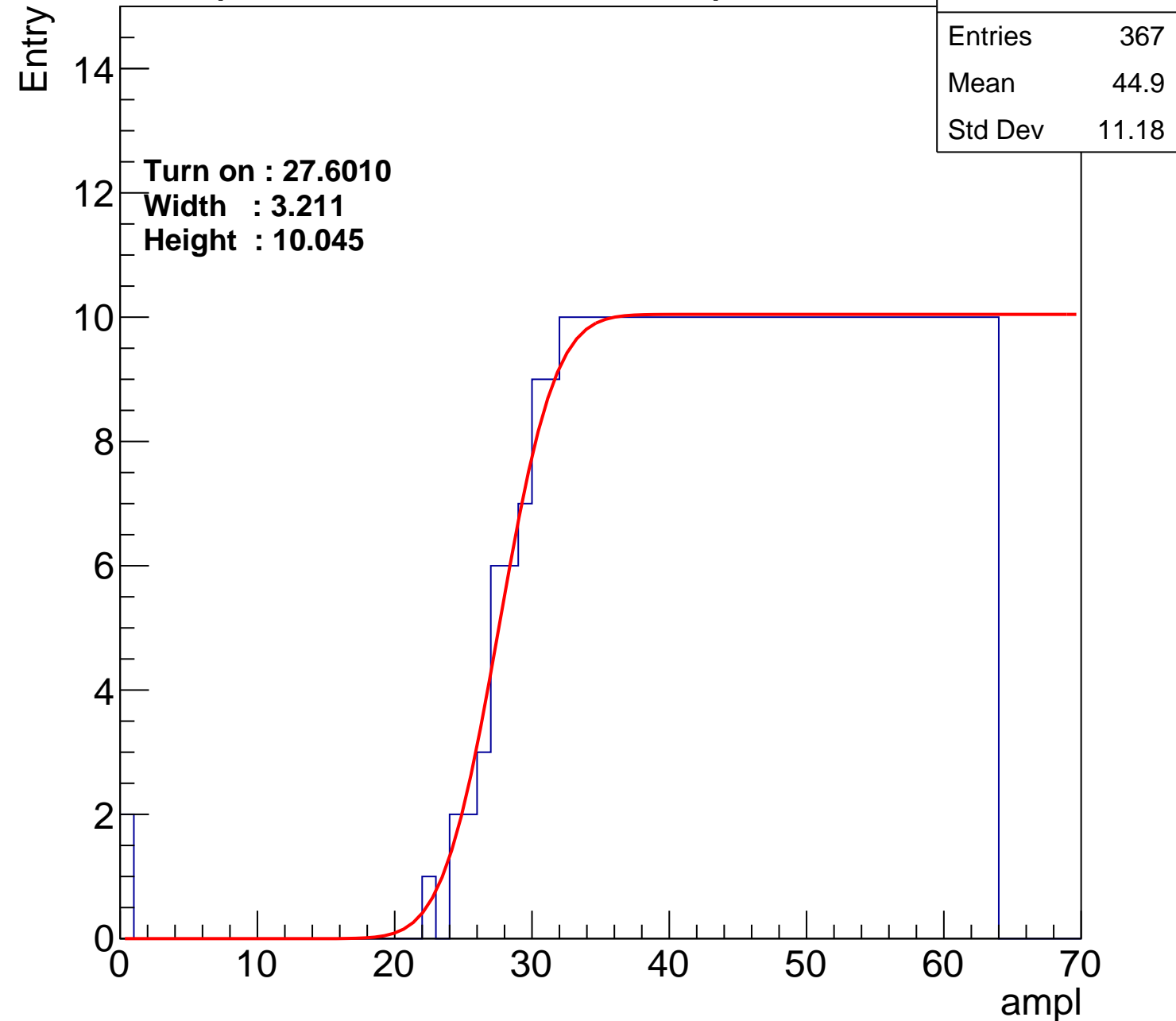
Width : 3.211

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch81

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.72
Std Dev	12.21

Turn on : 25.5146

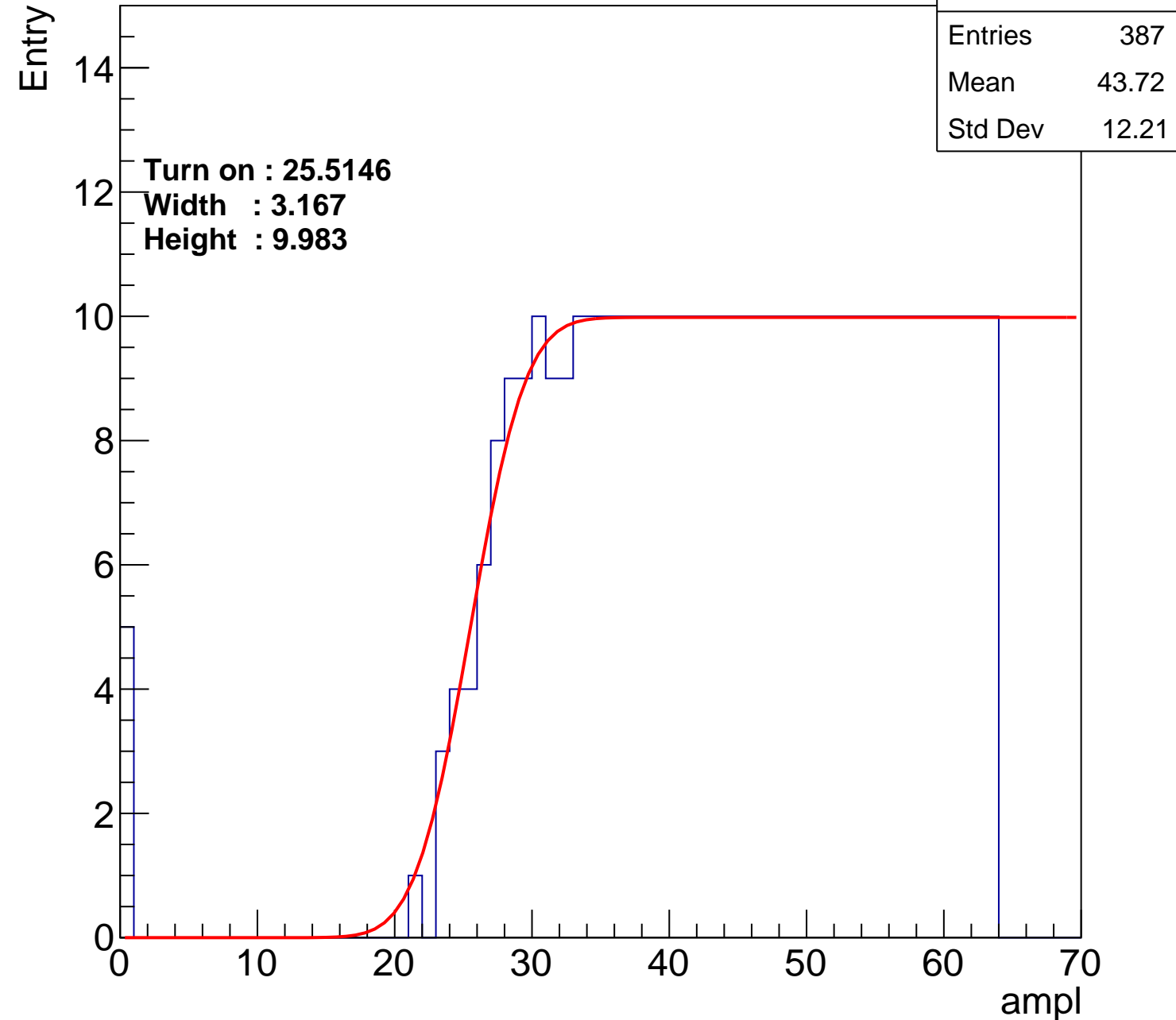
Width : 3.167

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch82

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	373
Mean	44.55
Std Dev	11.42

Turn on : 27.2358

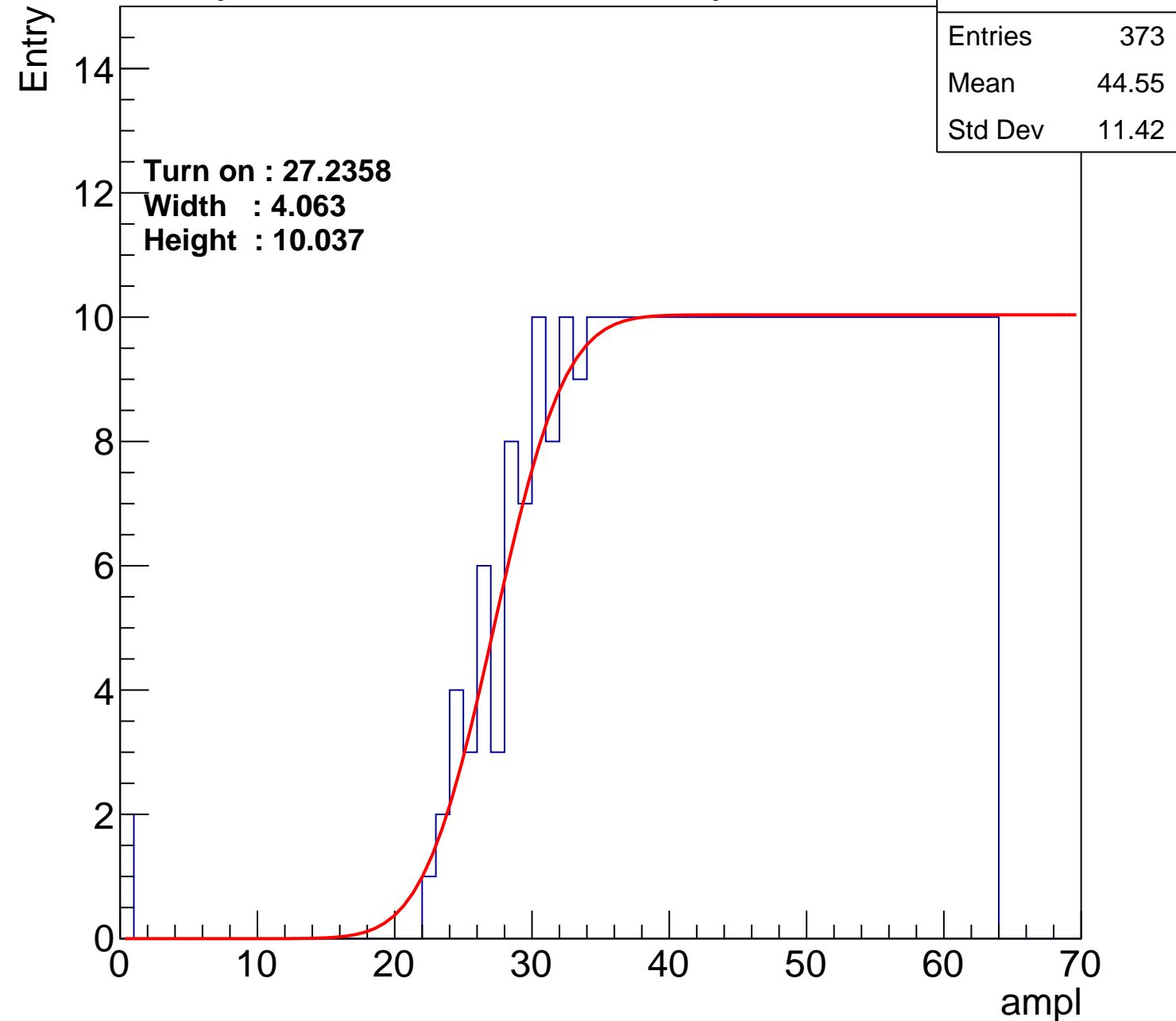
Width : 4.063

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch83

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	371
Mean	44.73
Std Dev	11.18

Turn on : 27.6450

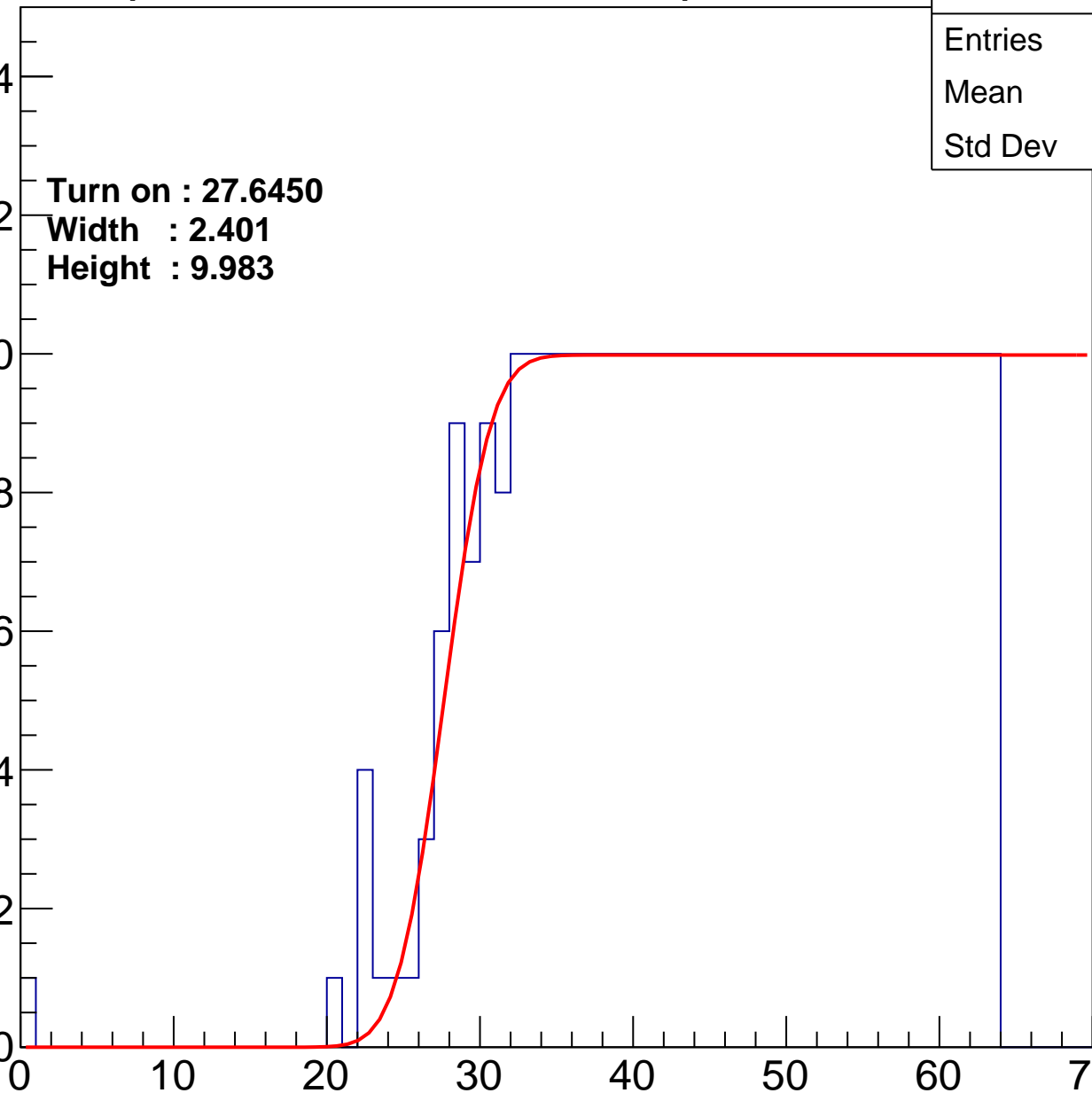
Width : 2.401

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch84

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	395
Mean	43.54
Std Dev	11.82

Turn on : 25.0523

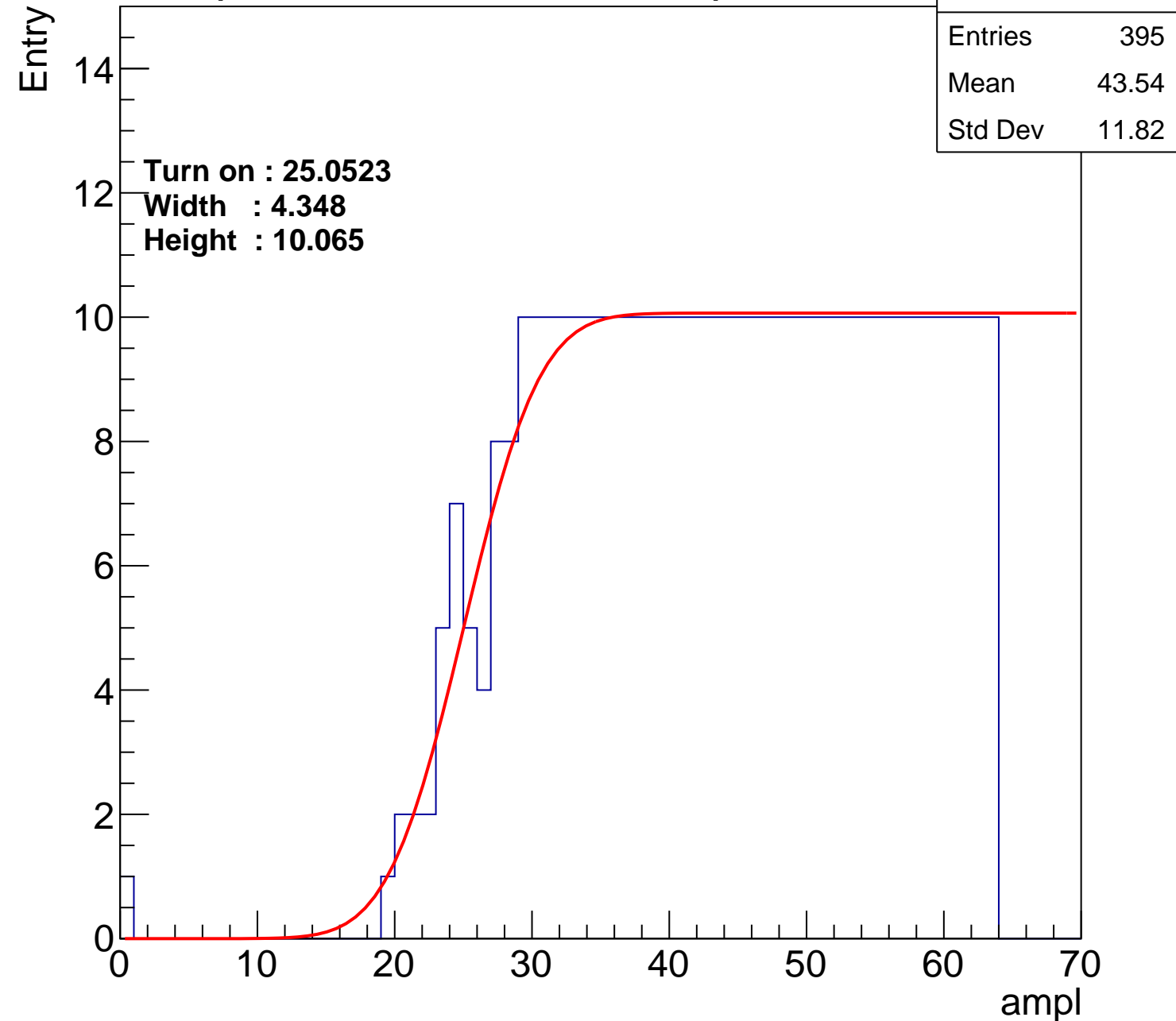
Width : 4.348

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch85

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.49
Std Dev	11.28

Turn on : 26.6574

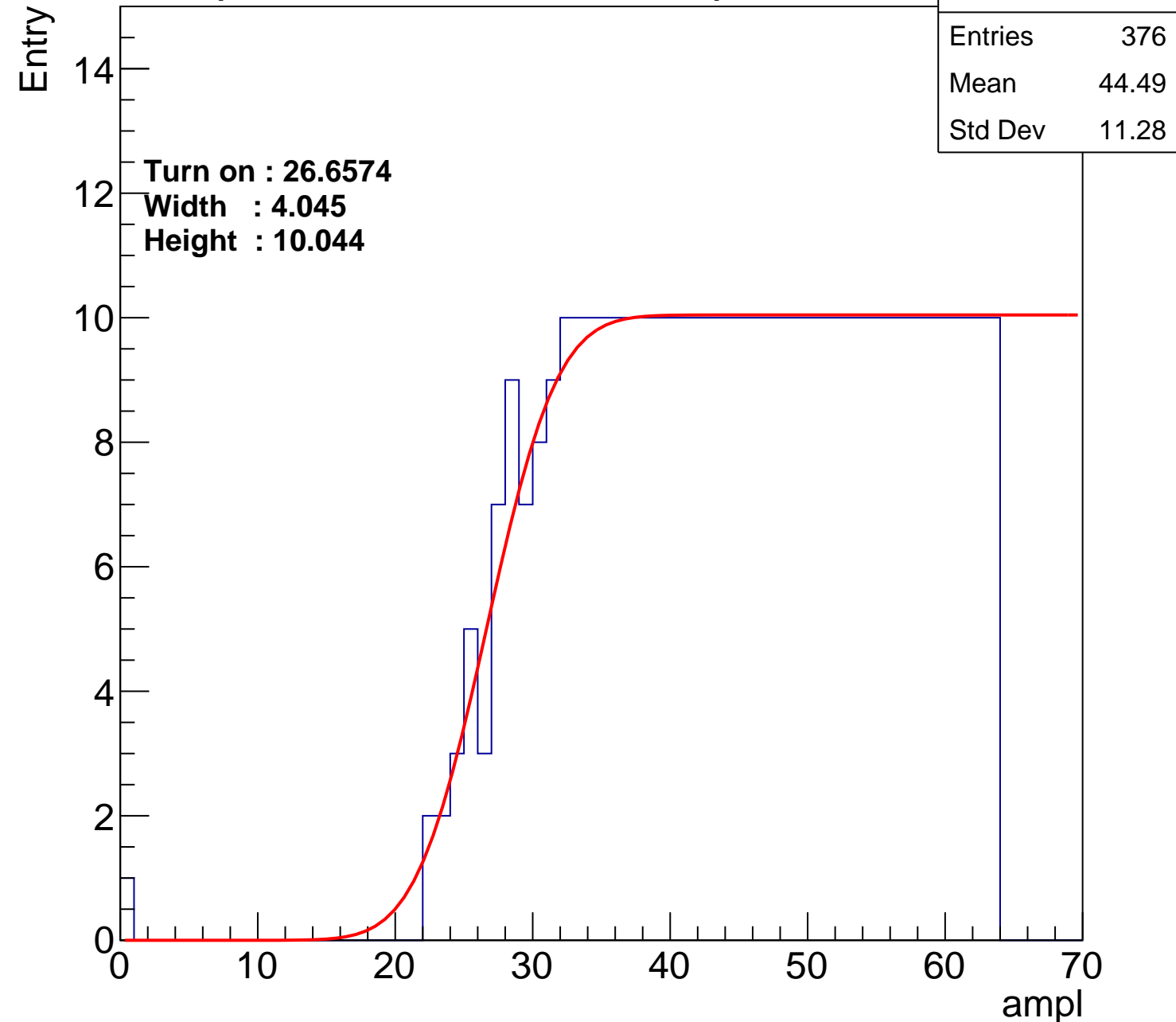
Width : 4.045

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch86

calib\_packv5\_042523\_0143.root, FC#12, port B1

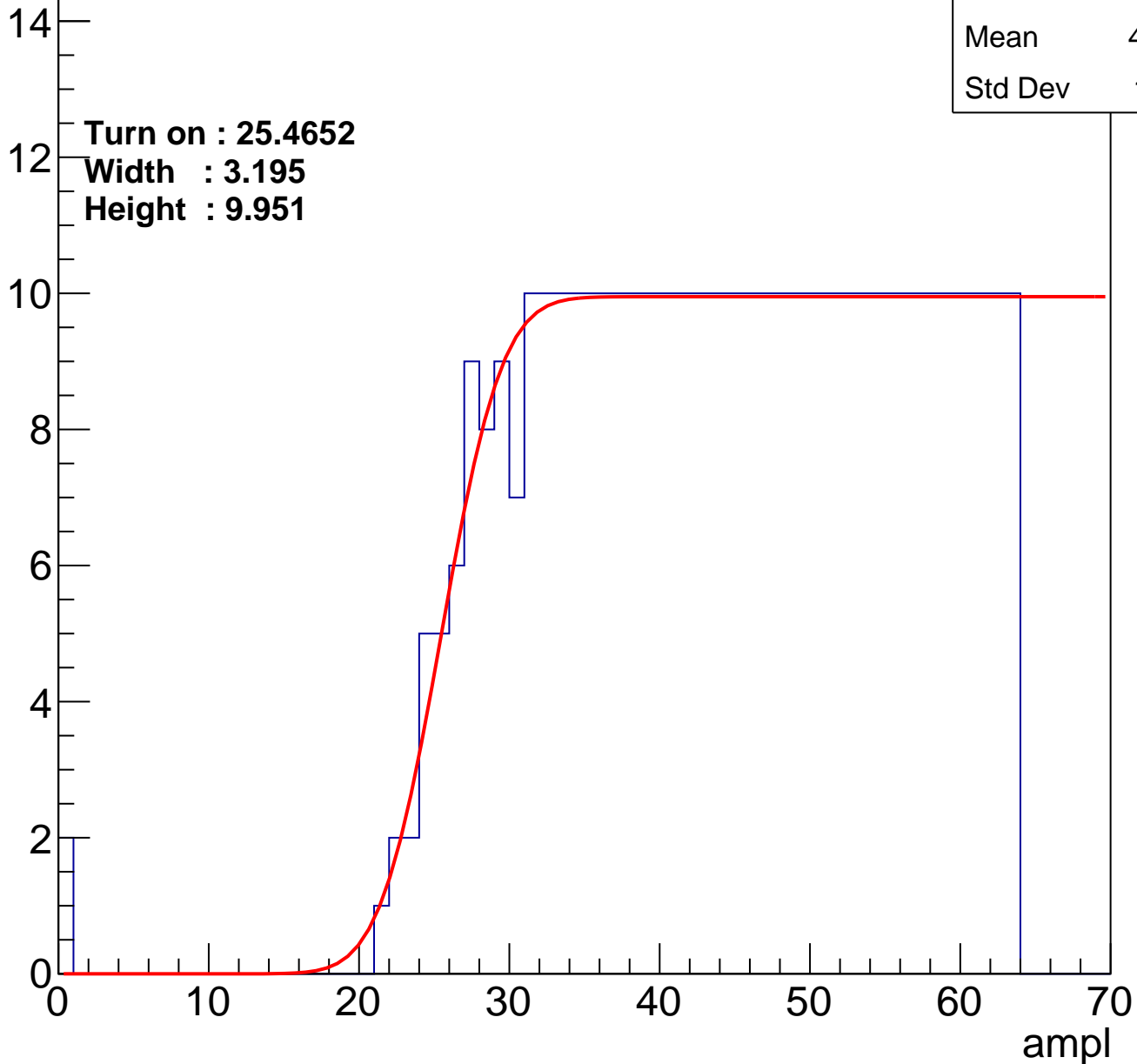
Entries	386
Mean	43.94
Std Dev	11.71

Turn on : 25.4652

Width : 3.195

Height : 9.951

Entry





# B0L102S, U22-ch87

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	43.96
Std Dev	11.92

Turn on : 25.5643

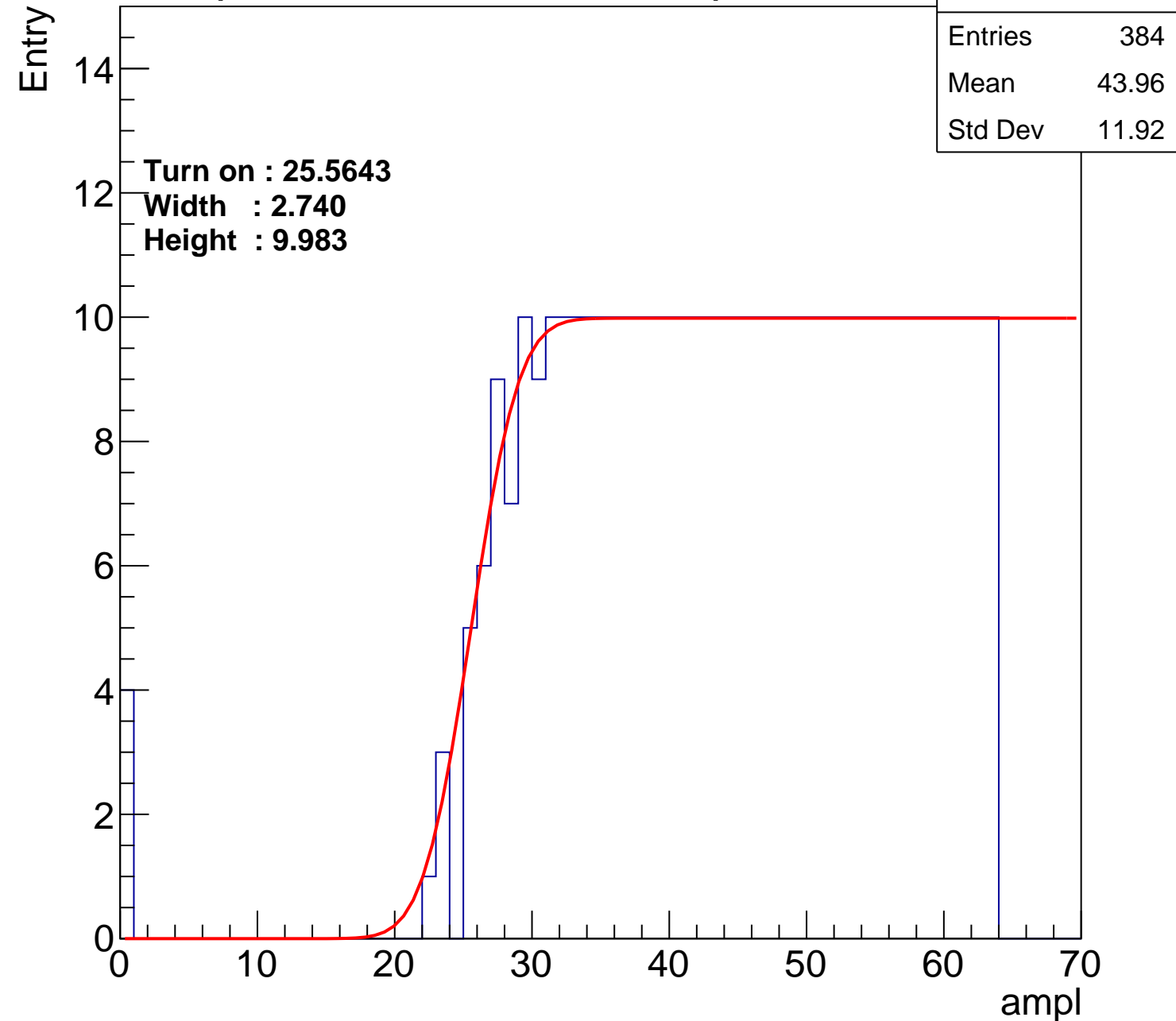
Width : 2.740

Height : 9.983

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch88

calib\_packv5\_042523\_0143.root, FC#12, port B1

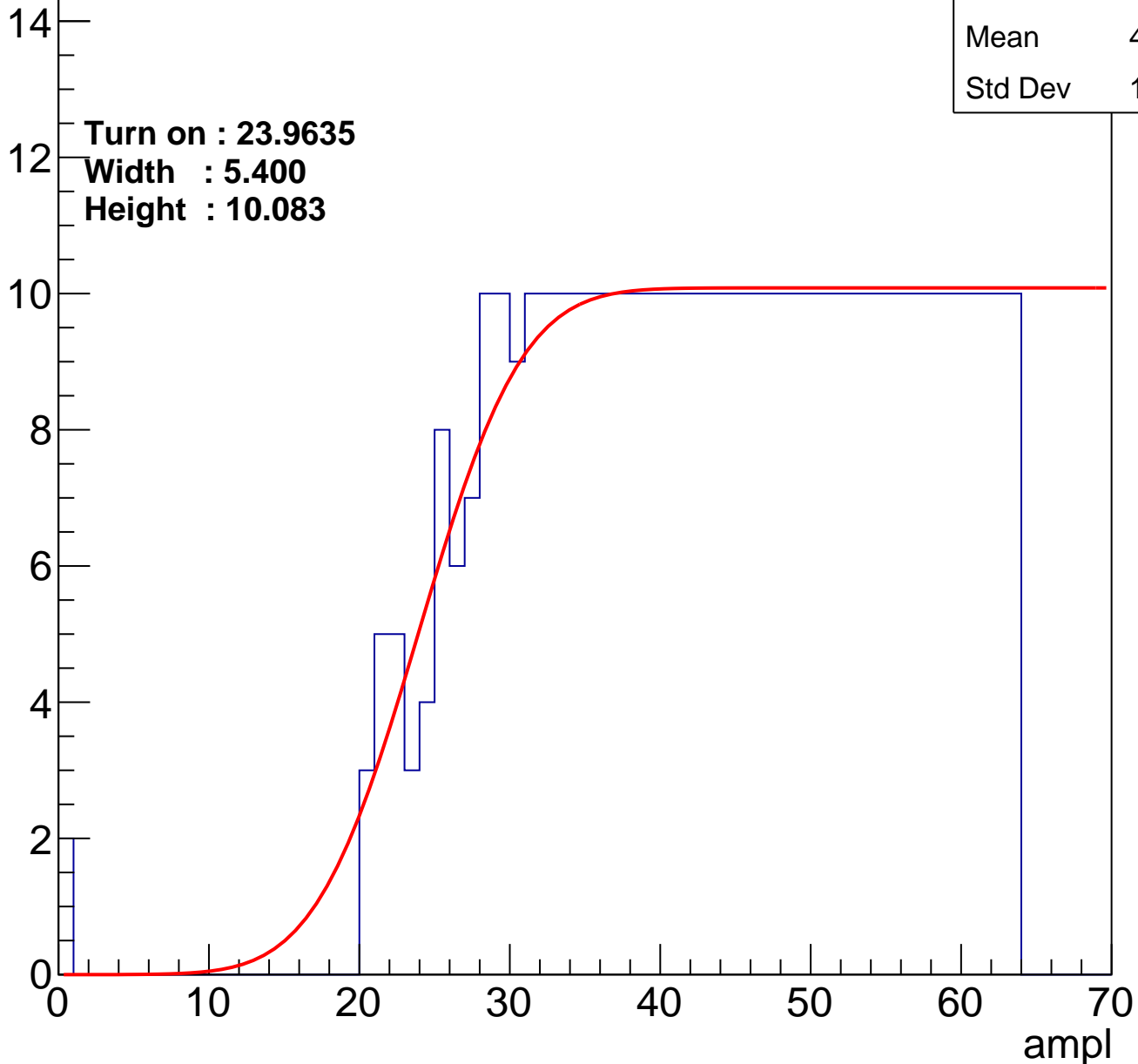
Entries	402
Mean	43.12
Std Dev	12.18

Turn on : 23.9635

Width : 5.400

Height : 10.083

Entry



# B0L102S, U22-ch89

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	376
Mean	44.58
Std Dev	11.27

Turn on : 26.6236

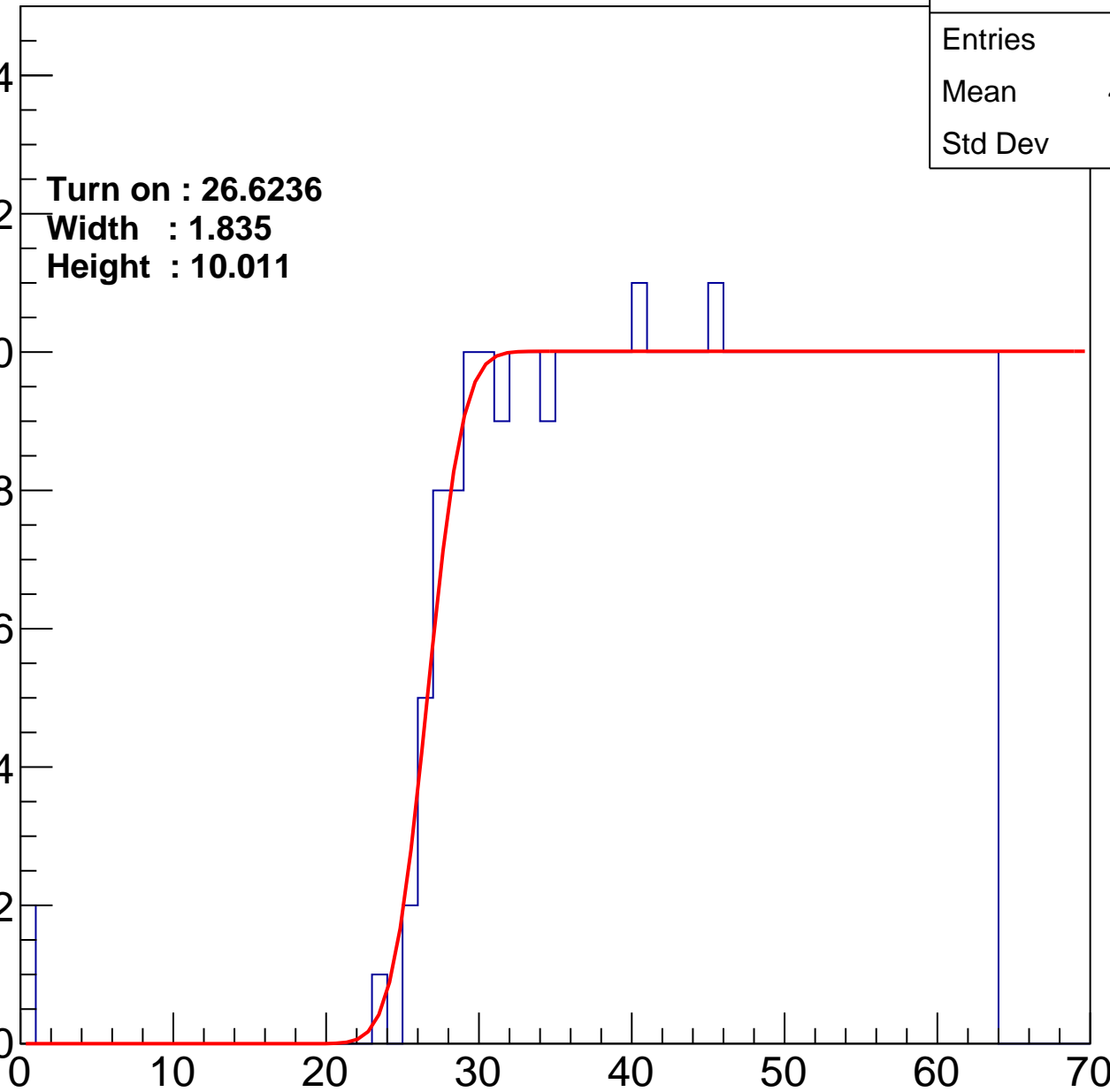
Width : 1.835

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch90

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.27
Std Dev	12.27

Turn on : 24.7730

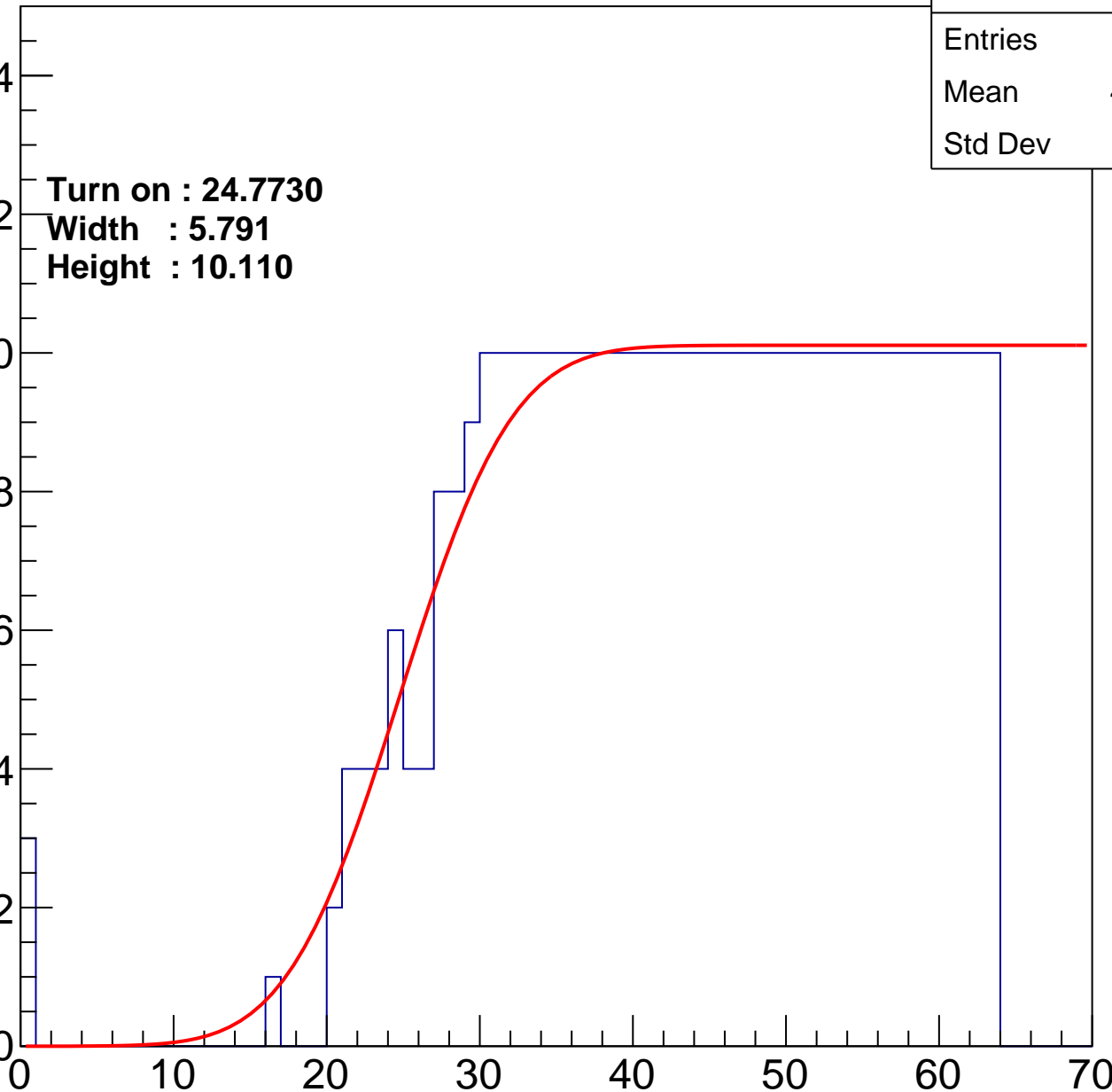
Width : 5.791

Height : 10.110

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch91

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	364
Mean	45.07
Std Dev	11.07

Turn on : 28.3427

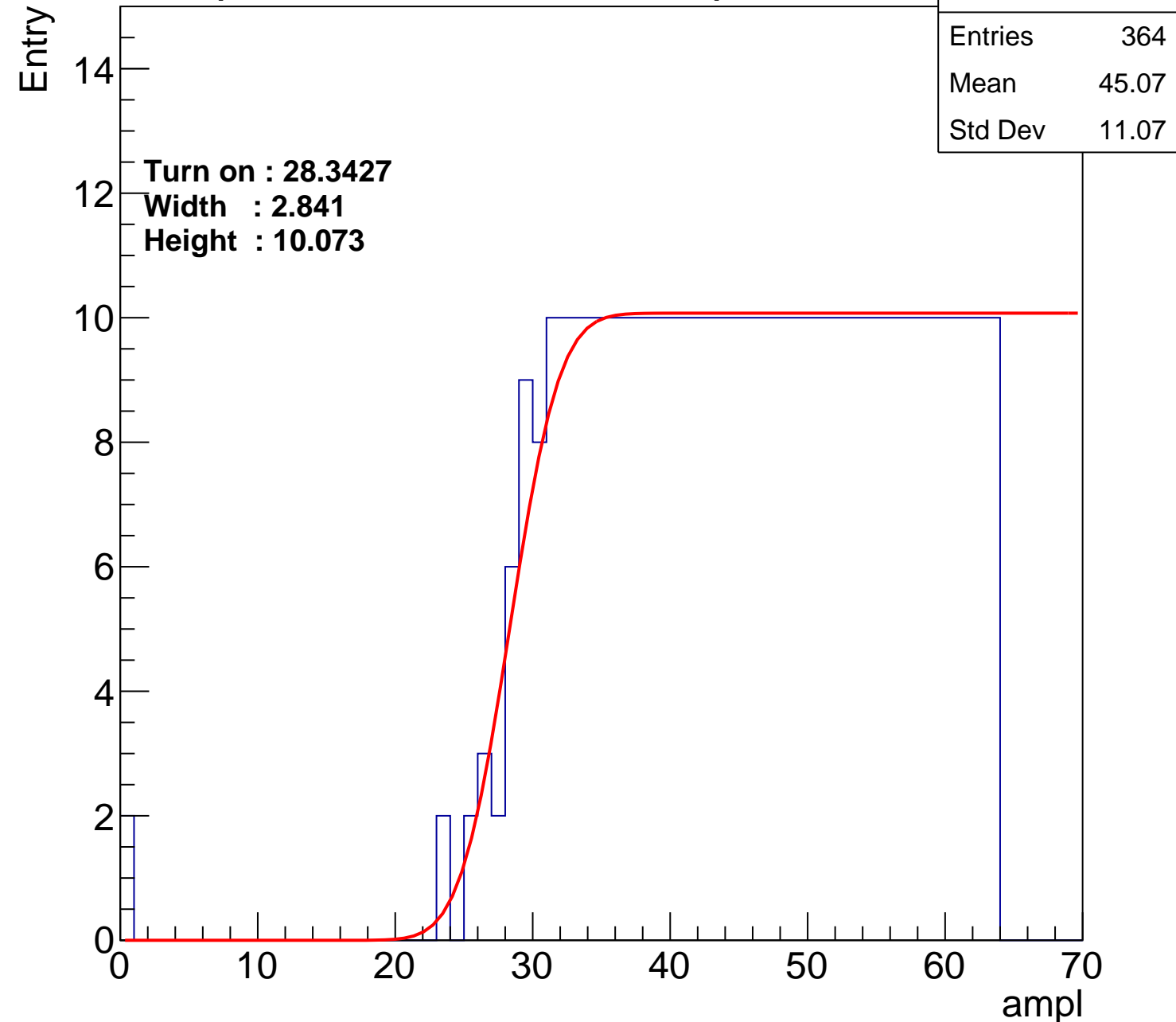
Width : 2.841

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch92

calib\_packv5\_042523\_0143.root, FC#12, port B1

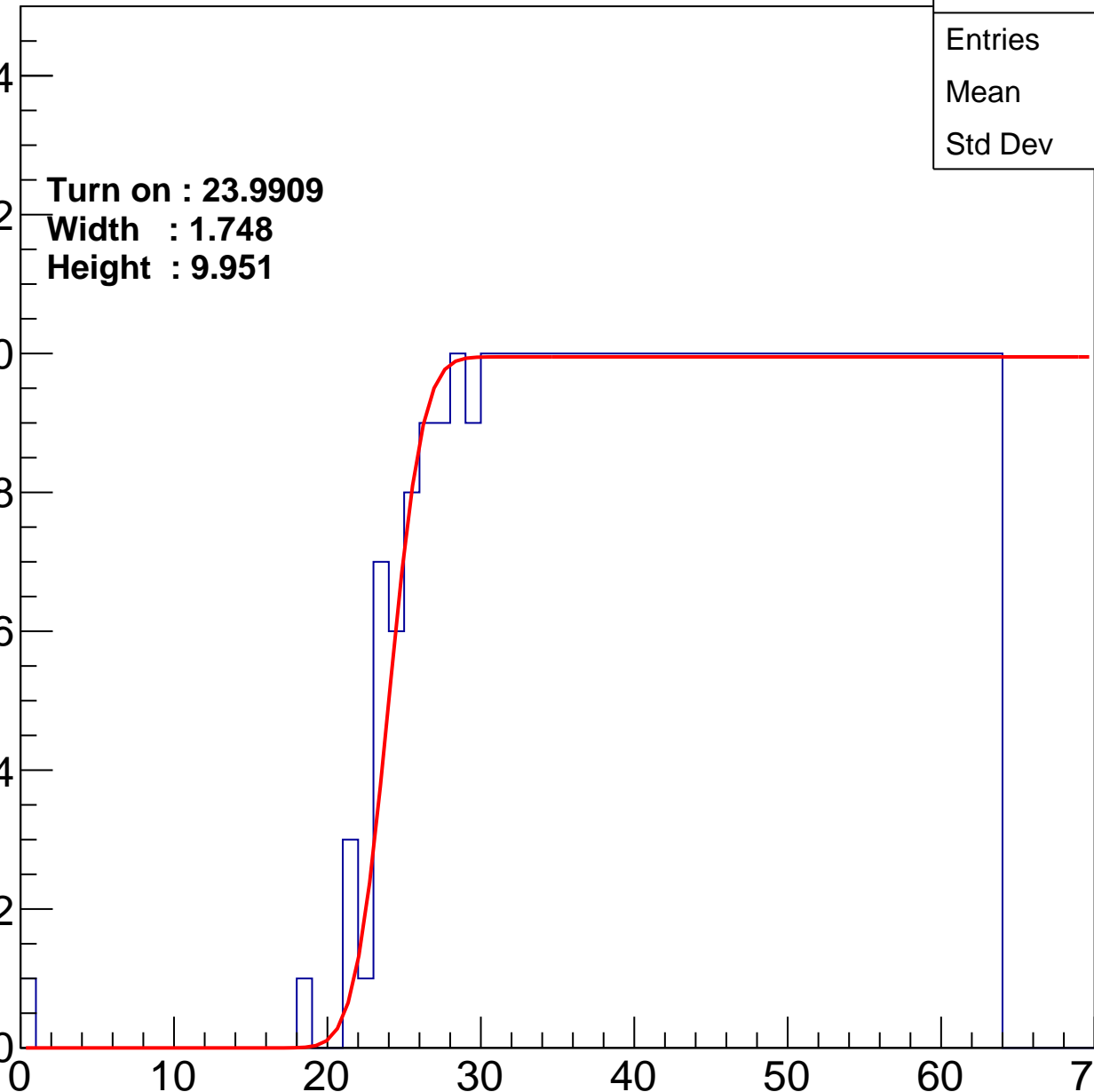
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 23.9909**  
**Width : 1.748**  
**Height : 9.951**

Entries	404
Mean	43.16
Std Dev	11.95

ampl



# B0L102S, U22-ch93

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.27
Std Dev	12.15

Turn on : 24.0784

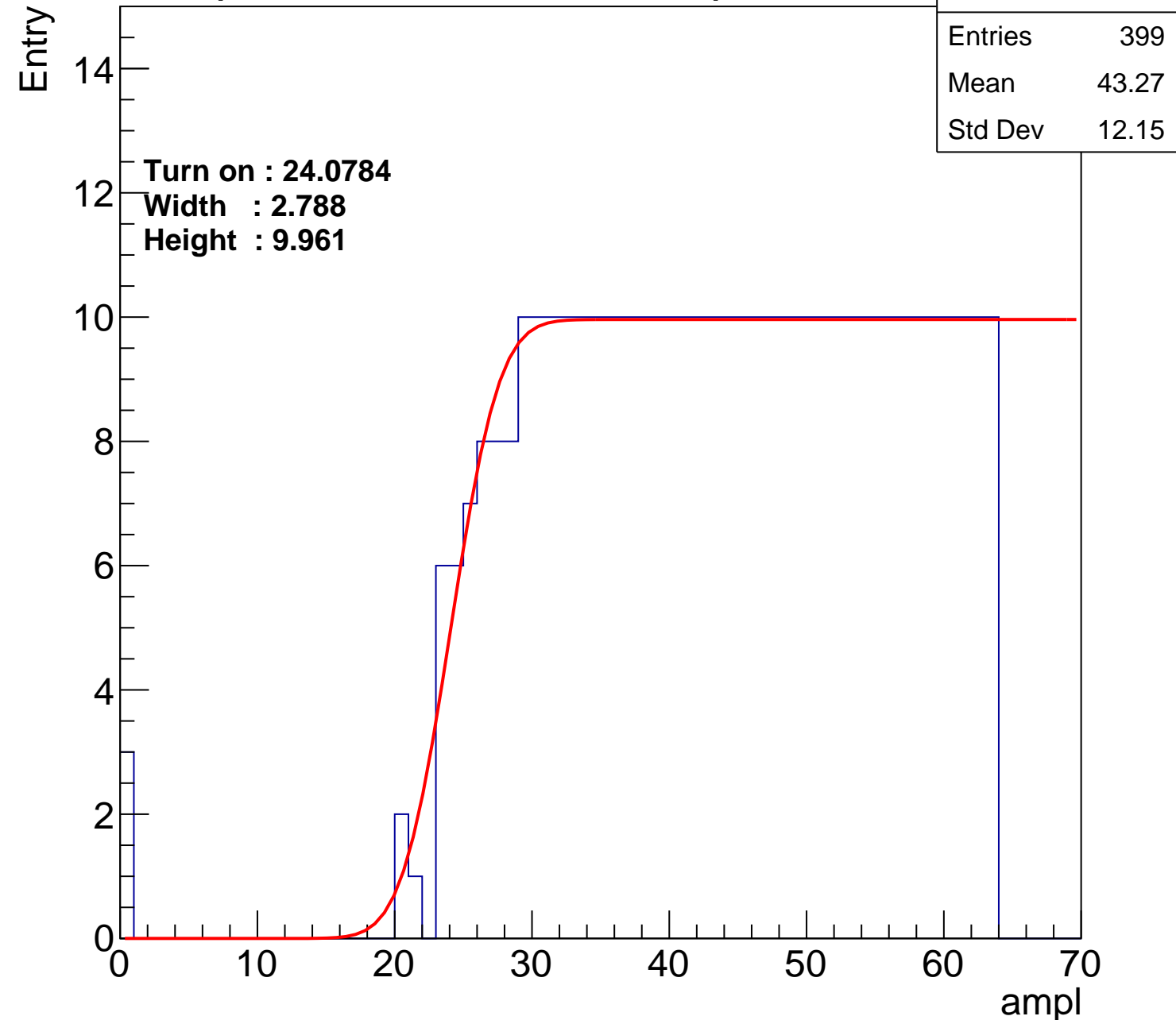
Width : 2.788

Height : 9.961

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch94

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.5518

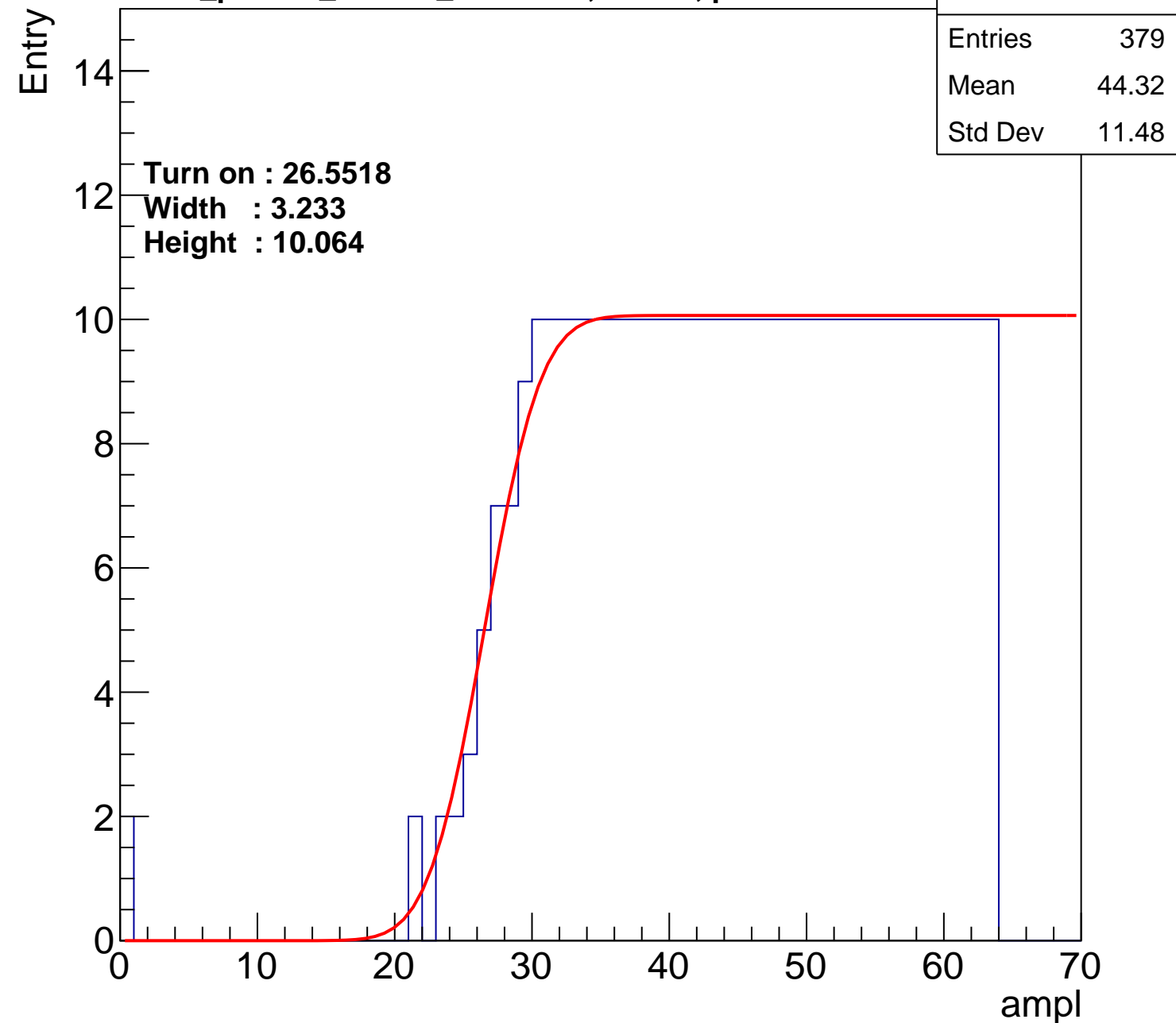
Width : 3.233

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch95

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	409
Mean	42.79
Std Dev	12.39

Turn on : 23.5634

Width : 2.111

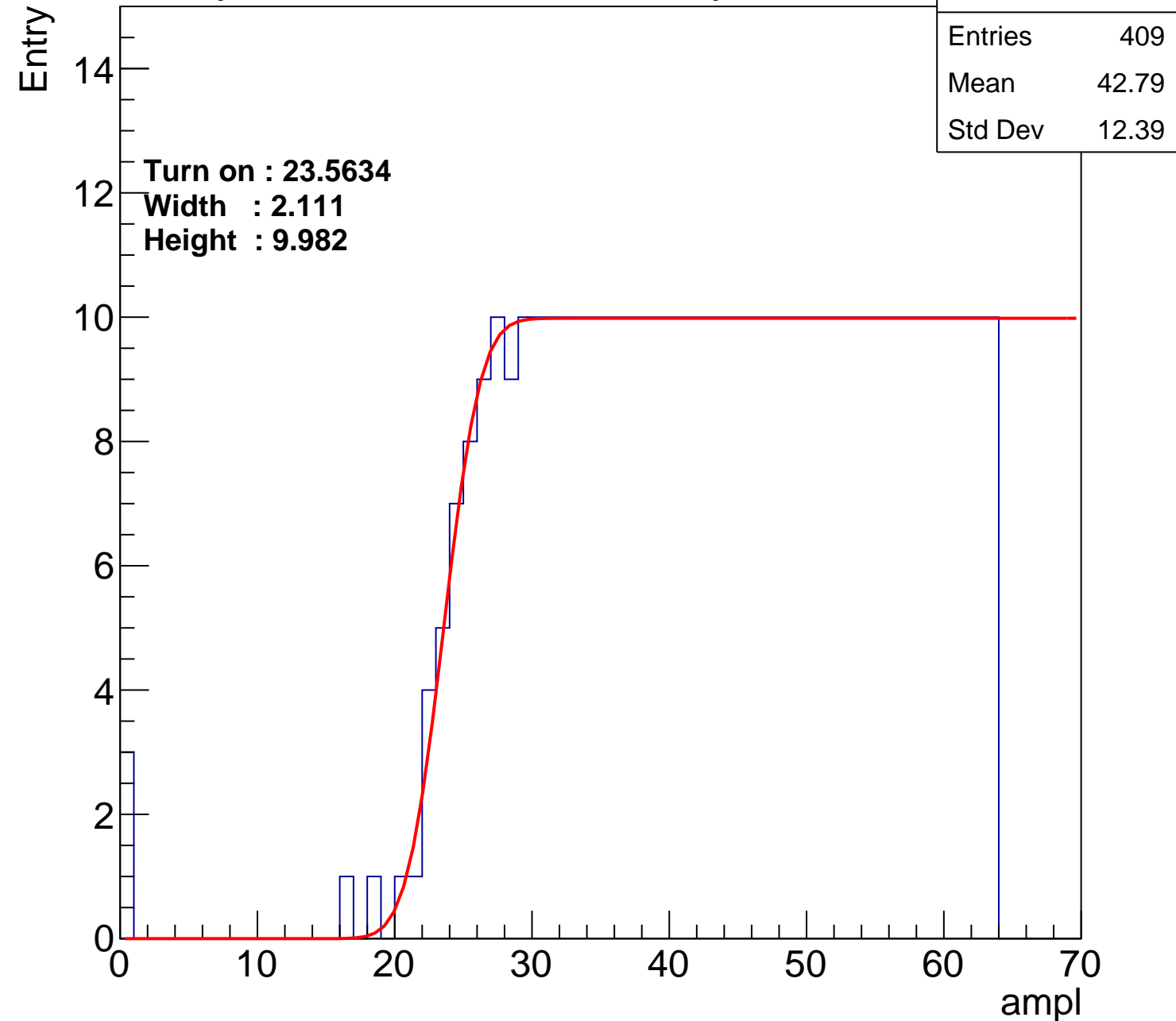
Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U22-ch96

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.42
Std Dev	11.97

Turn on : 25.2902

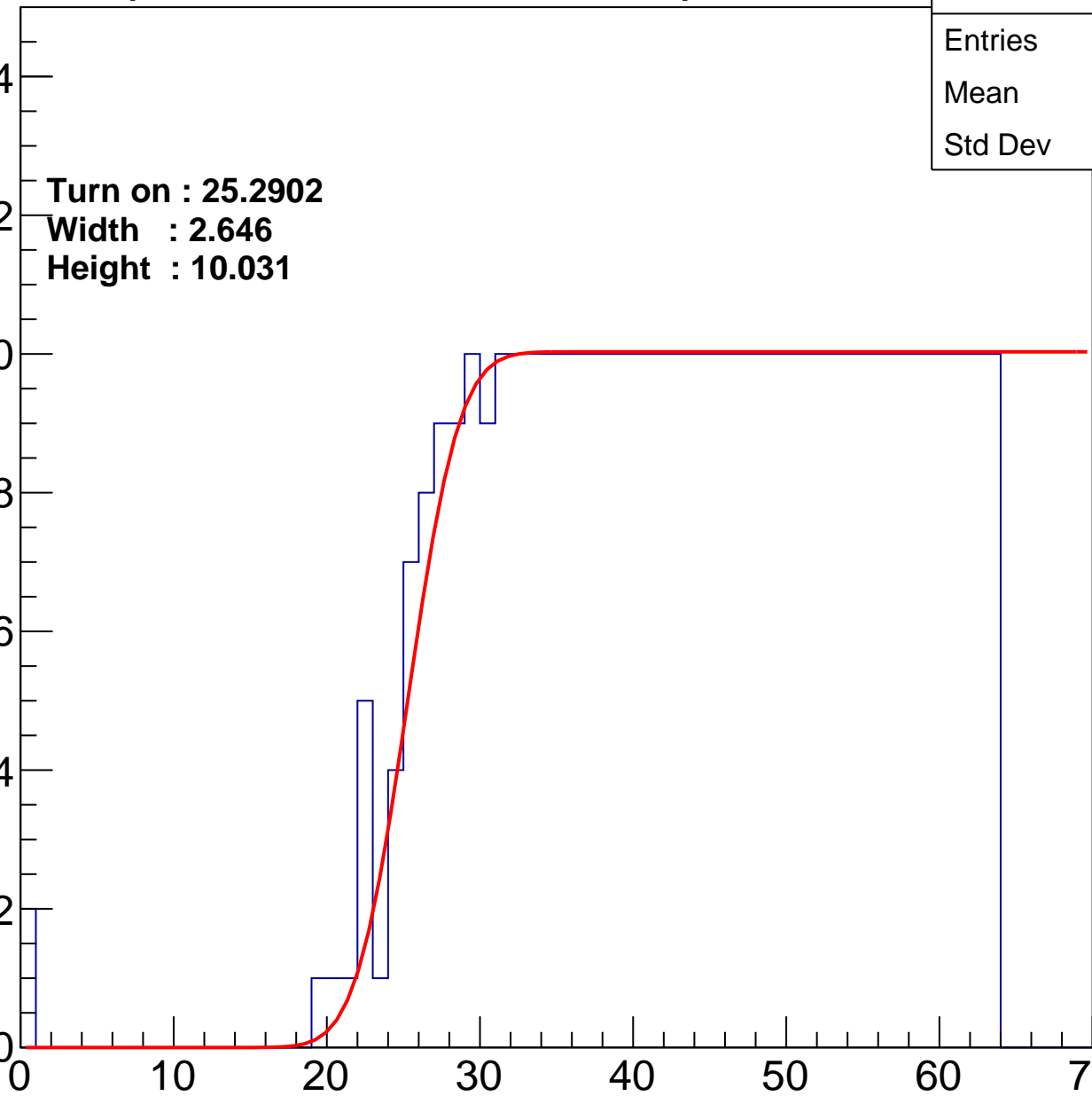
Width : 2.646

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch97

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.19
Std Dev	11.66

**Turn on : 26.3707**

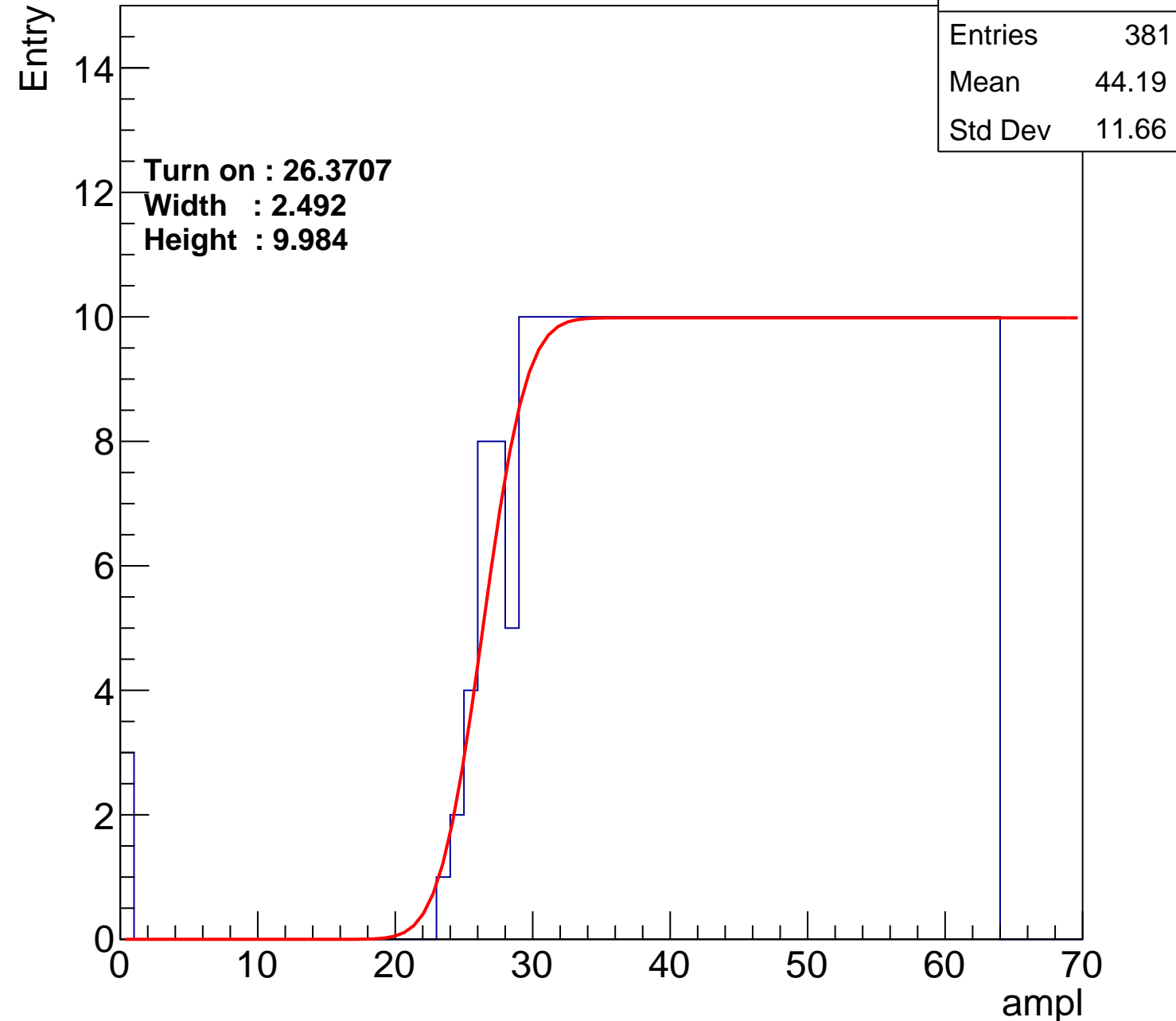
**Width : 2.492**

**Height : 9.984**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch98

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	396
Mean	43.44
Std Dev	11.98

Turn on : 25.0064

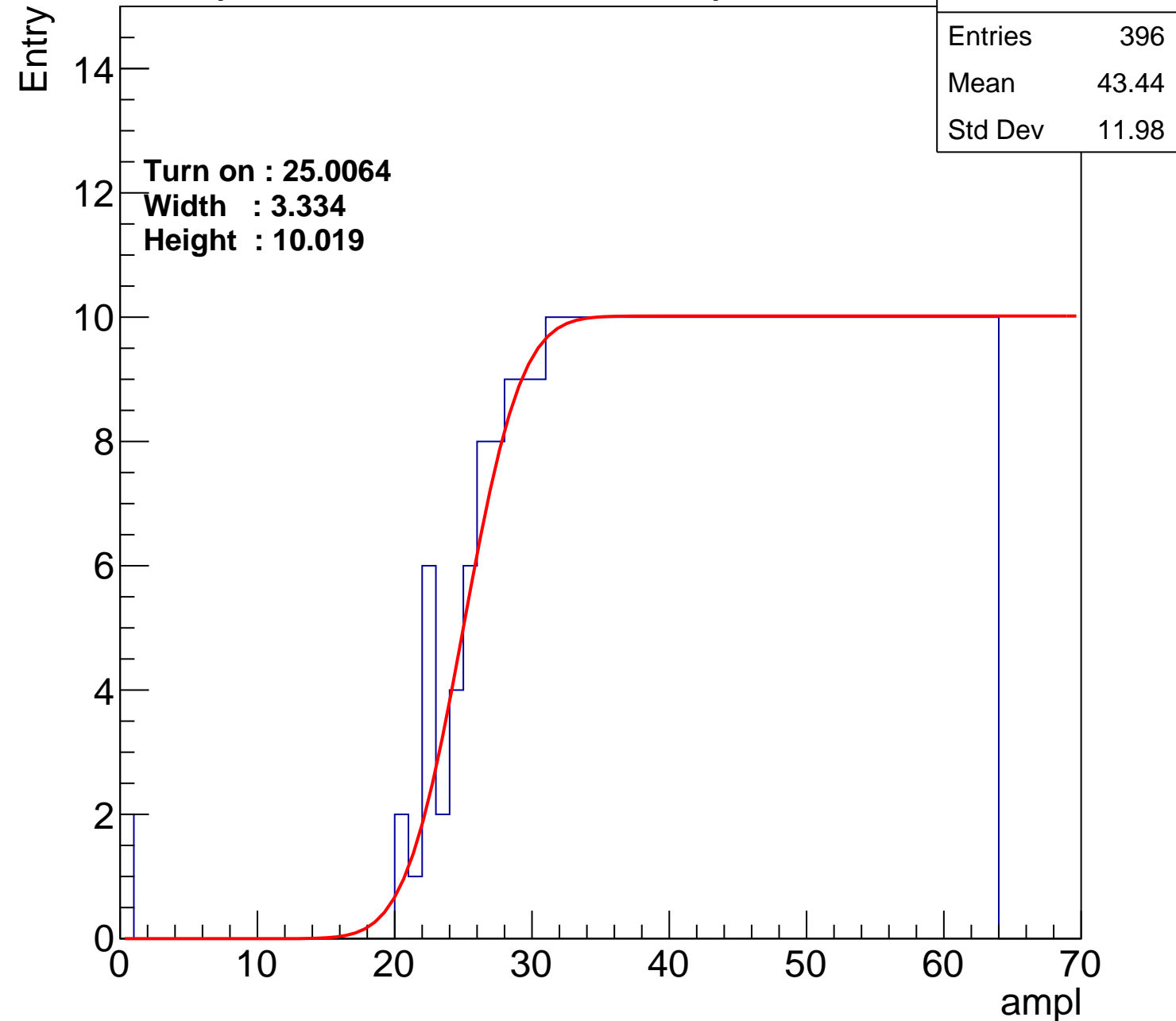
Width : 3.334

Height : 10.019

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch99

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	388
Mean	43.85
Std Dev	11.83

Turn on : 25.7728

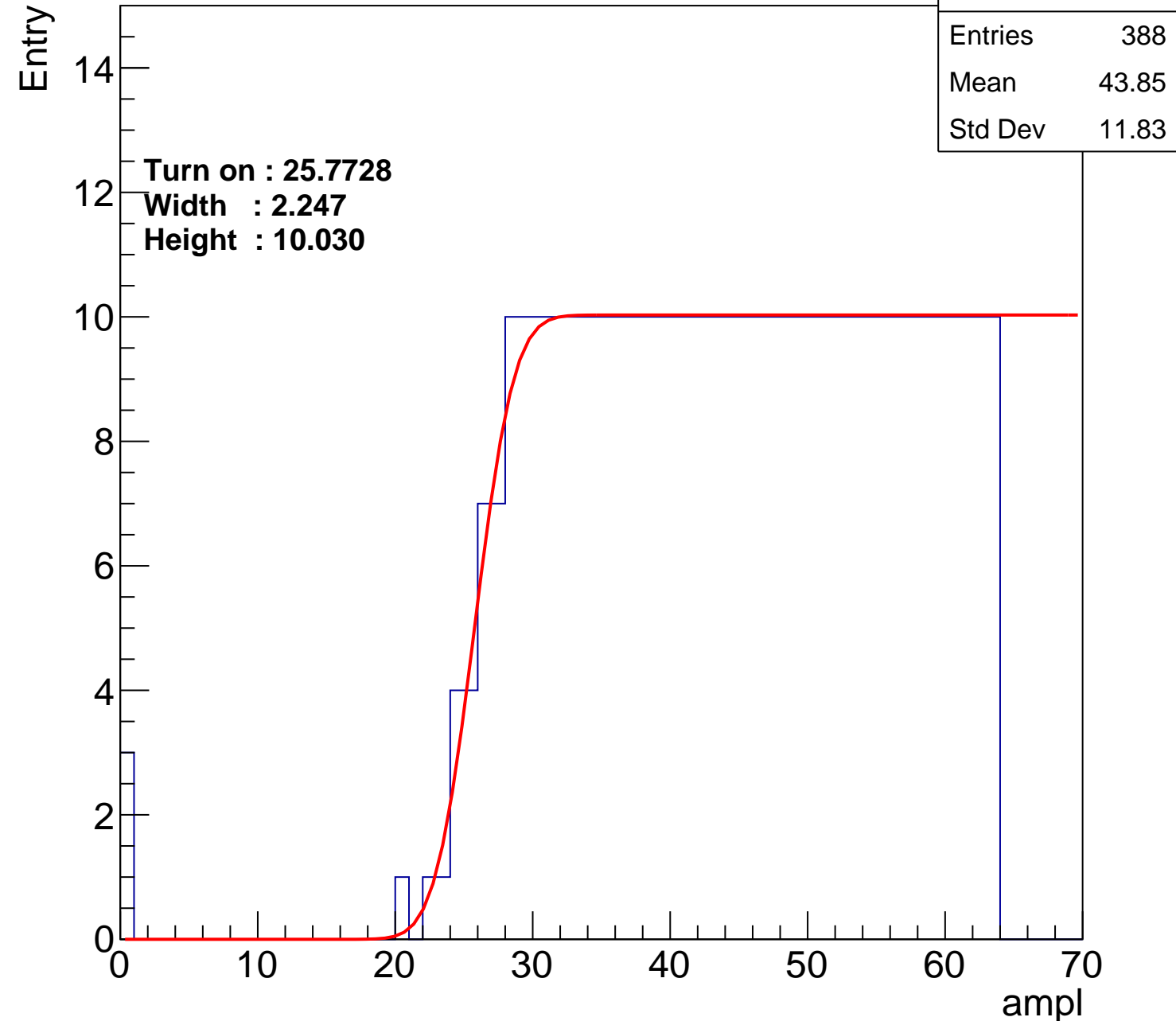
Width : 2.247

Height : 10.030

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch100

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.11
Std Dev	11.78

**Turn on : 26.5330**

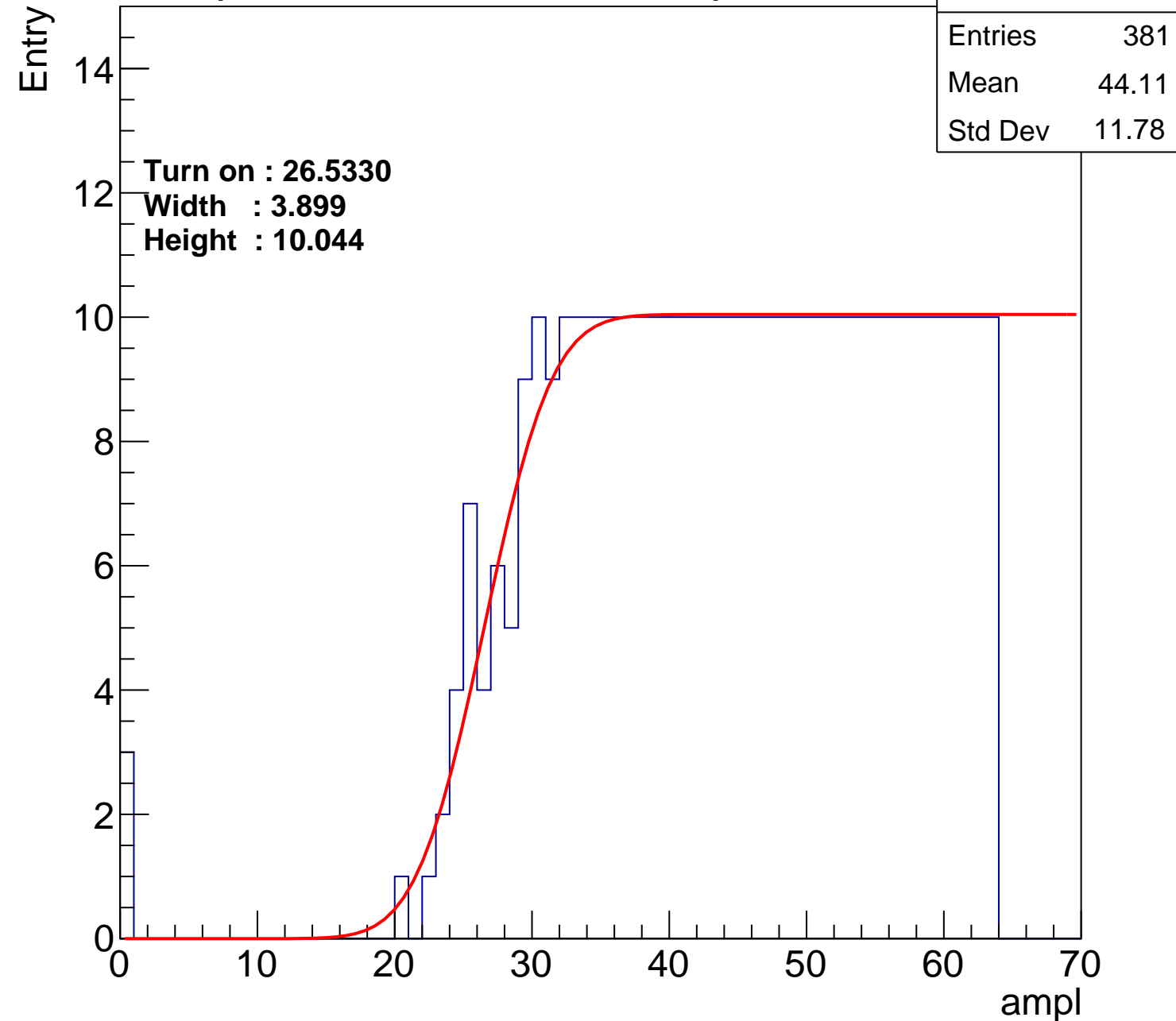
**Width : 3.899**

**Height : 10.044**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch101

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.25
Std Dev	11.35

**Turn on : 26.0572**

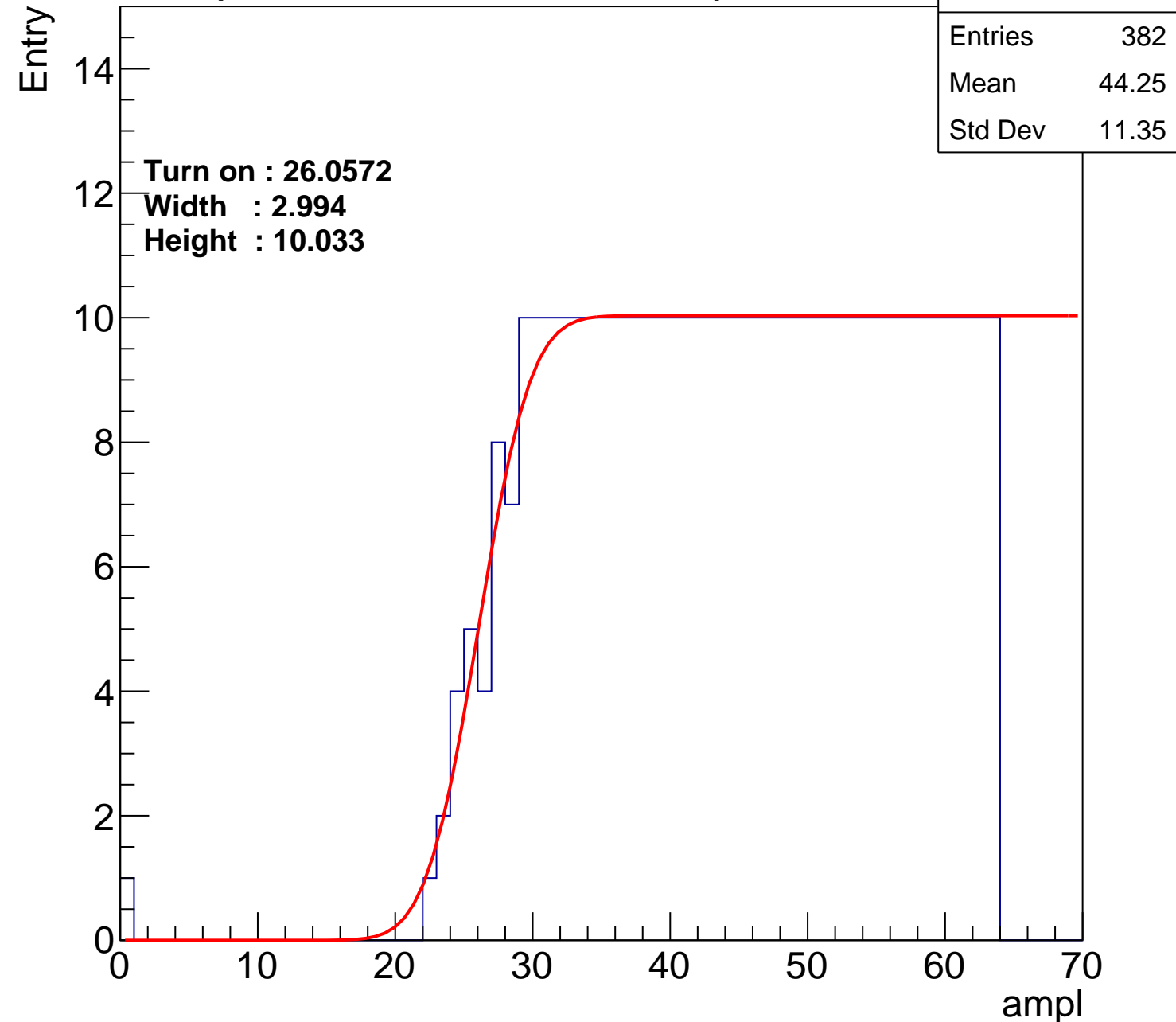
**Width : 2.994**

**Height : 10.033**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch102

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	383
Mean	44.19
Std Dev	11.41

Turn on : 25.9734

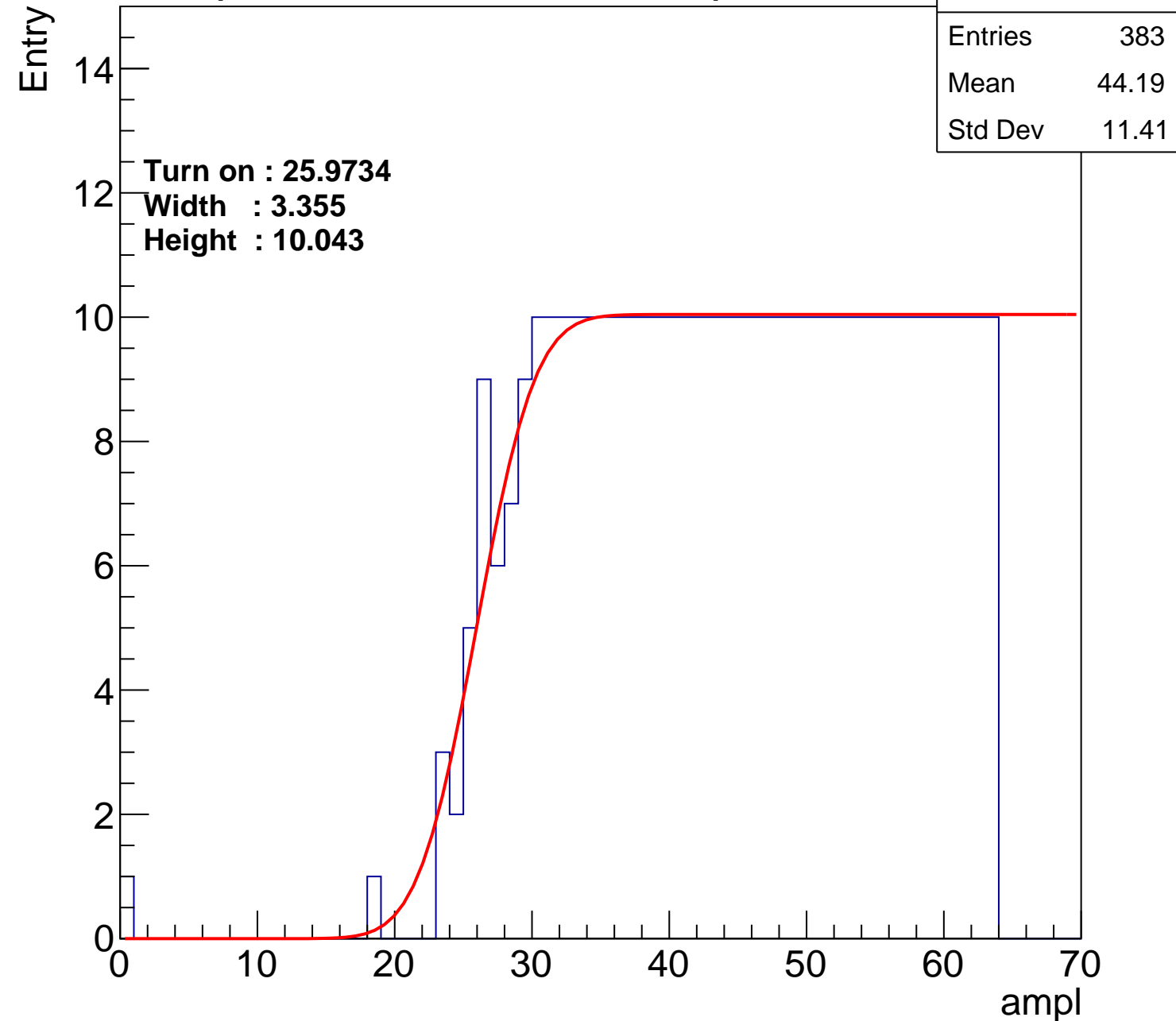
Width : 3.355

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch103

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.6861

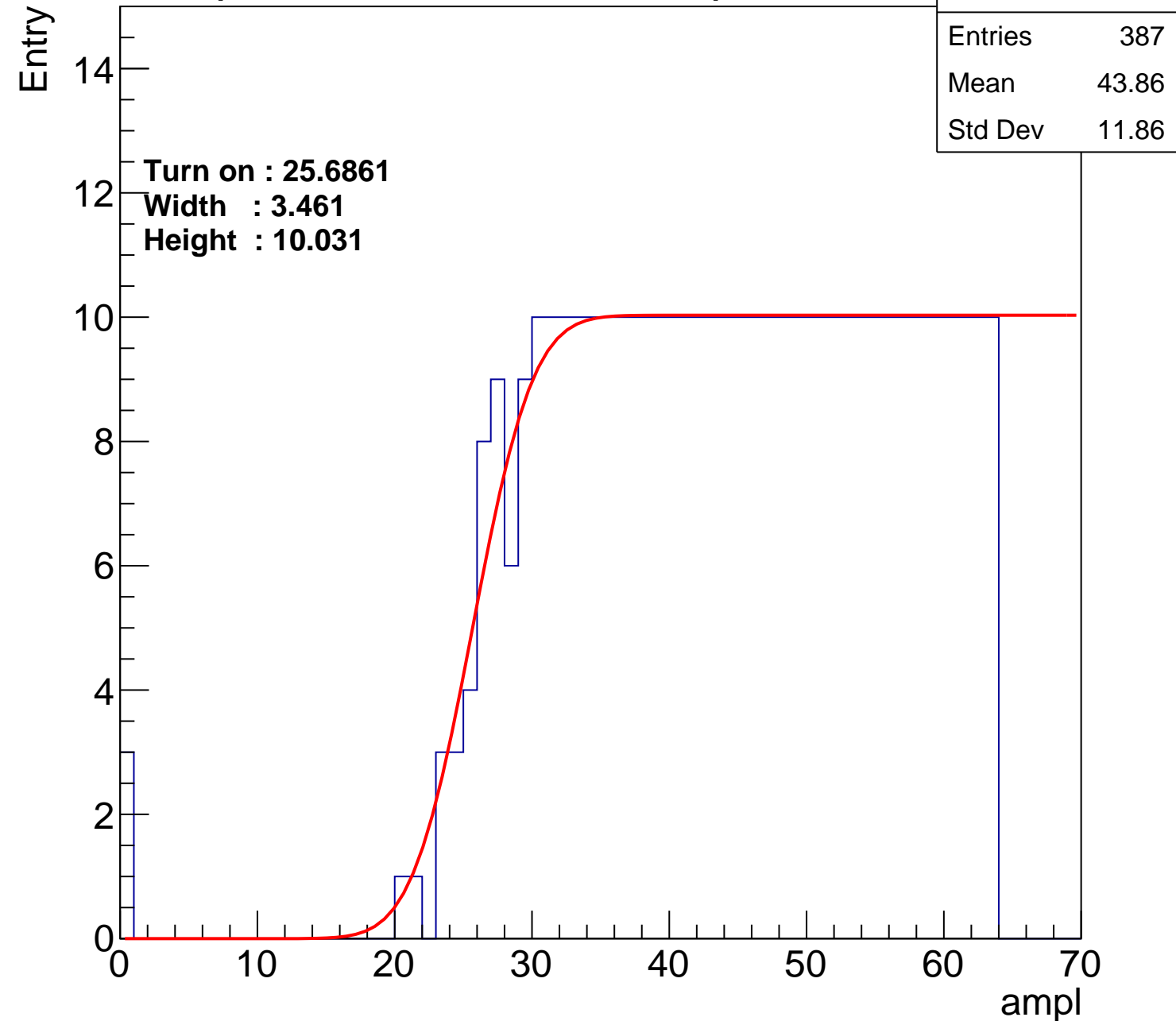
Width : 3.461

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch104

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.6982

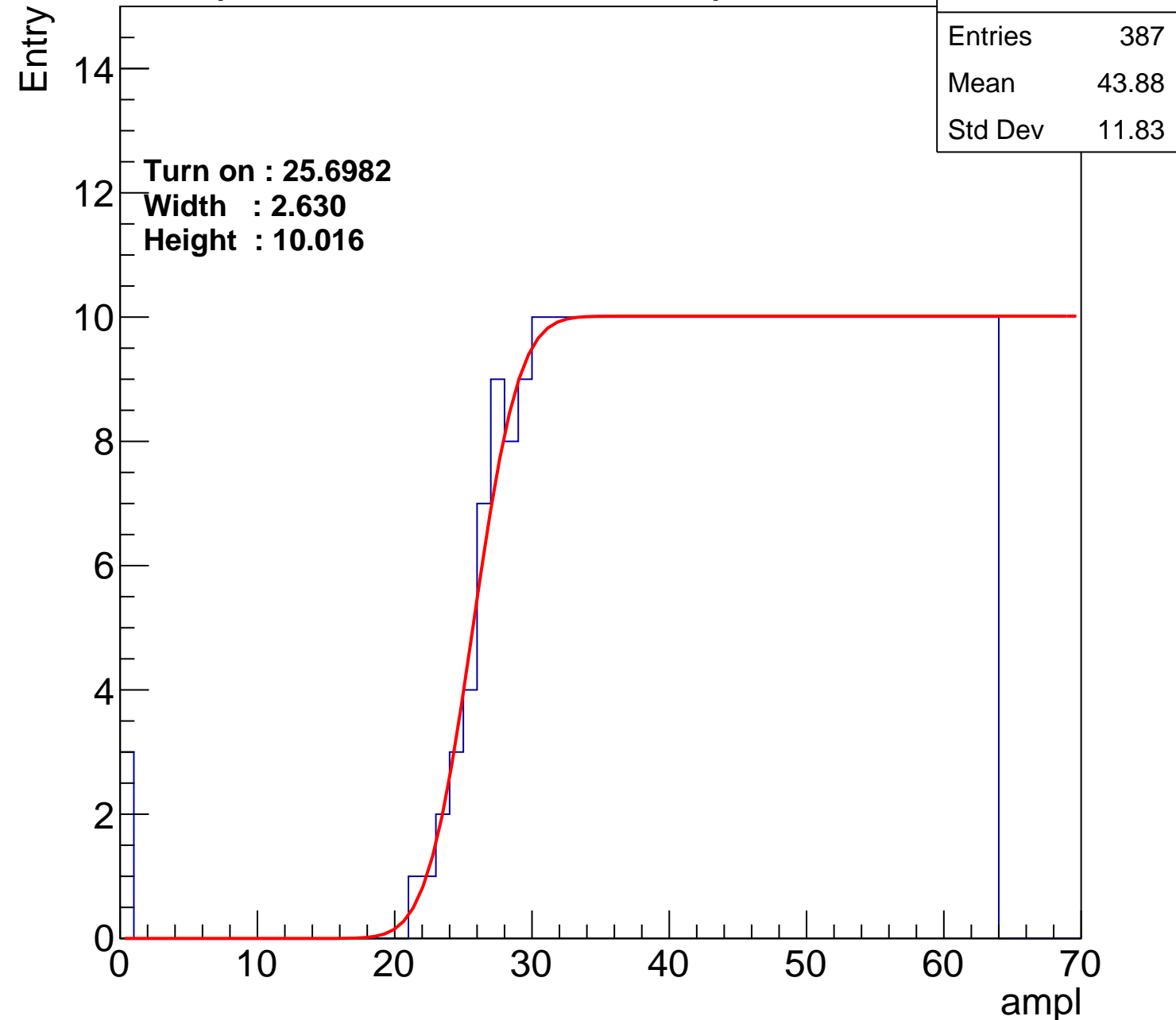
Width : 2.630

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch105

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	382
Mean	44.15
Std Dev	11.59

Turn on : 25.5032

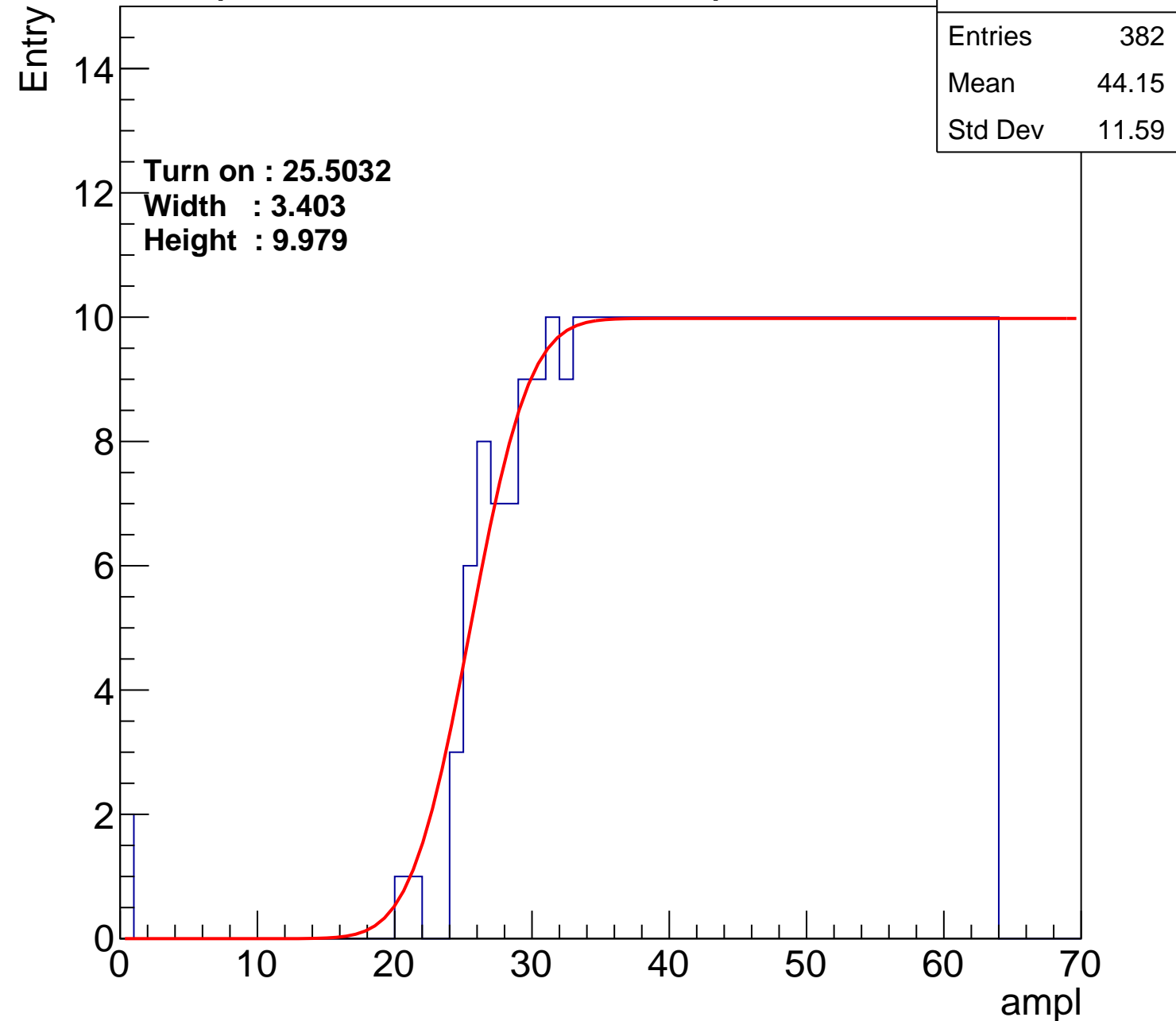
Width : 3.403

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch106

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	406
Mean	42.83
Std Dev	12.59

Turn on : 24.6028

Width : 3.176

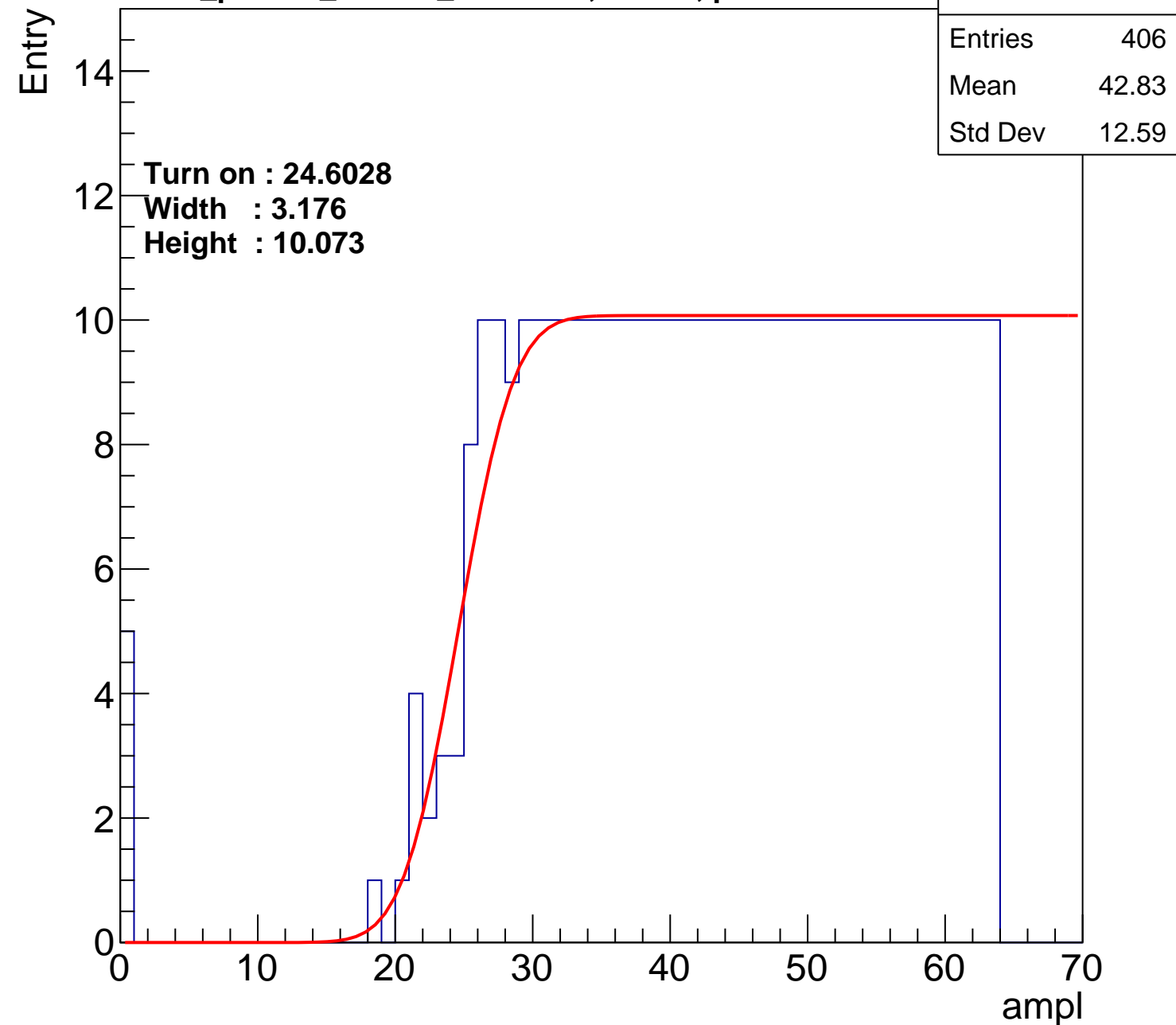
Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L102S, U22-ch107

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	368
Mean	44.7
Std Dev	11.61

Turn on : 28.2292

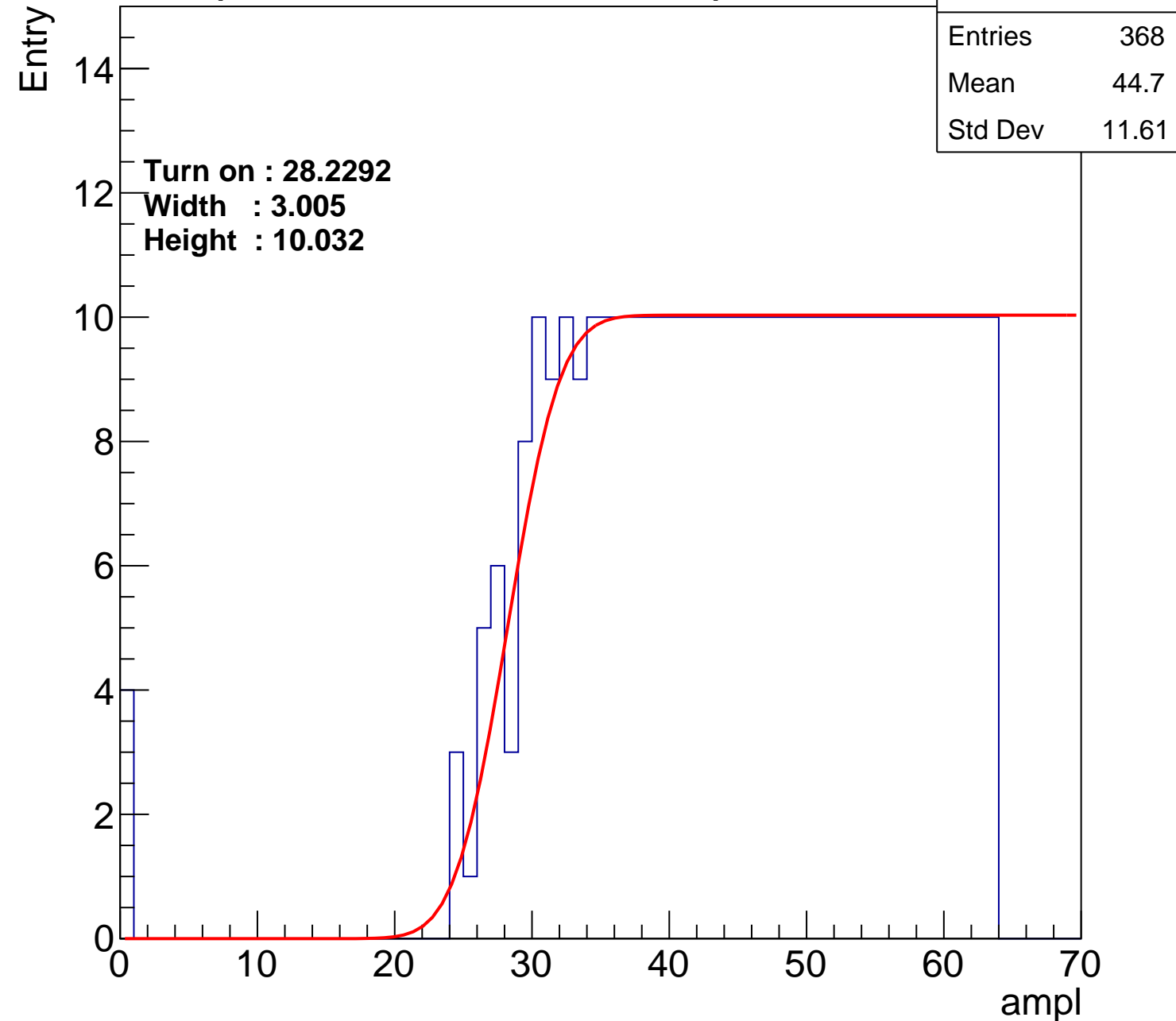
Width : 3.005

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch108

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	385
Mean	44.02
Std Dev	11.57

Turn on : 24.9344

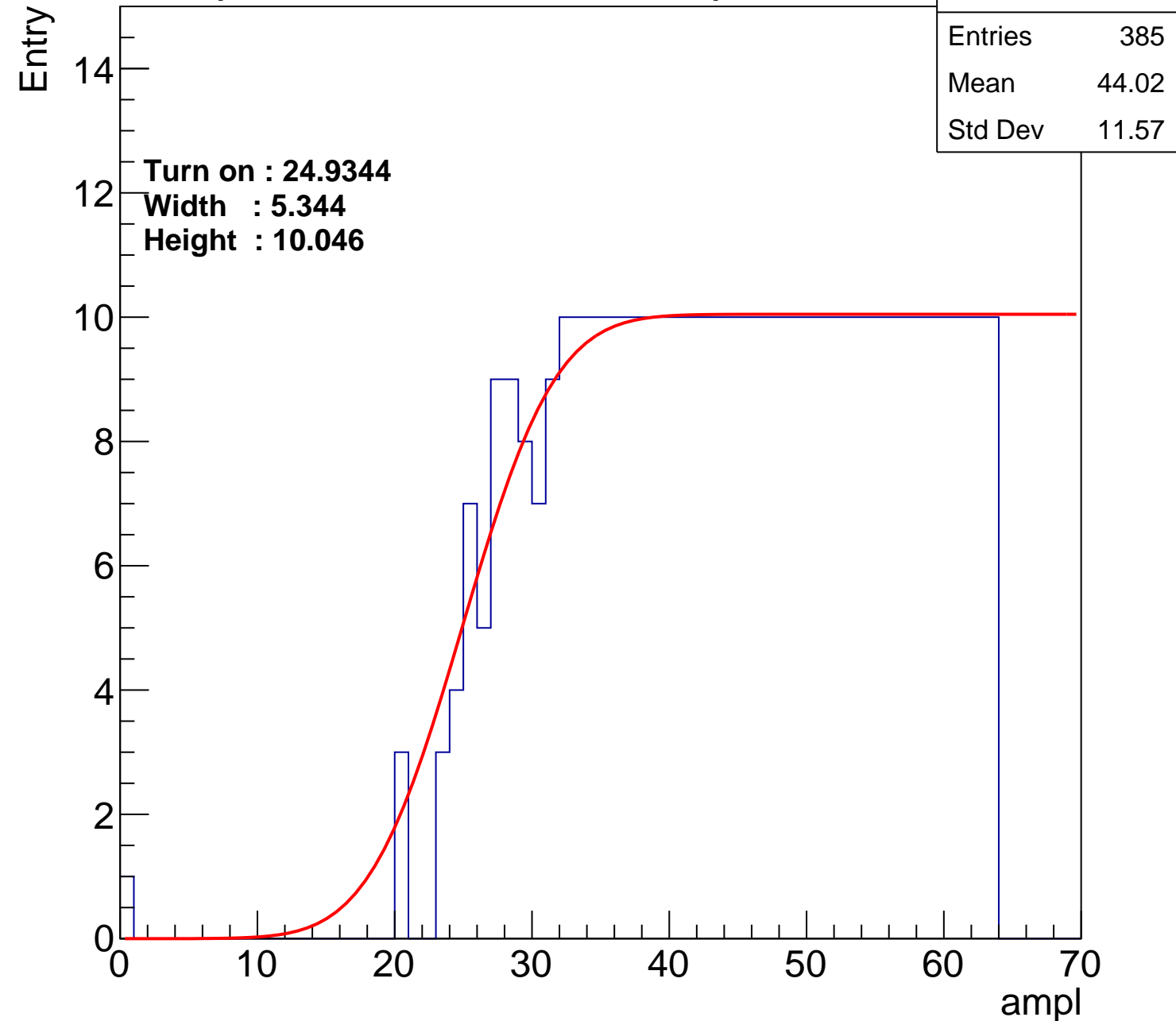
Width : 5.344

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch109

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.31
Std Dev	11.41

Turn on : 27.0502

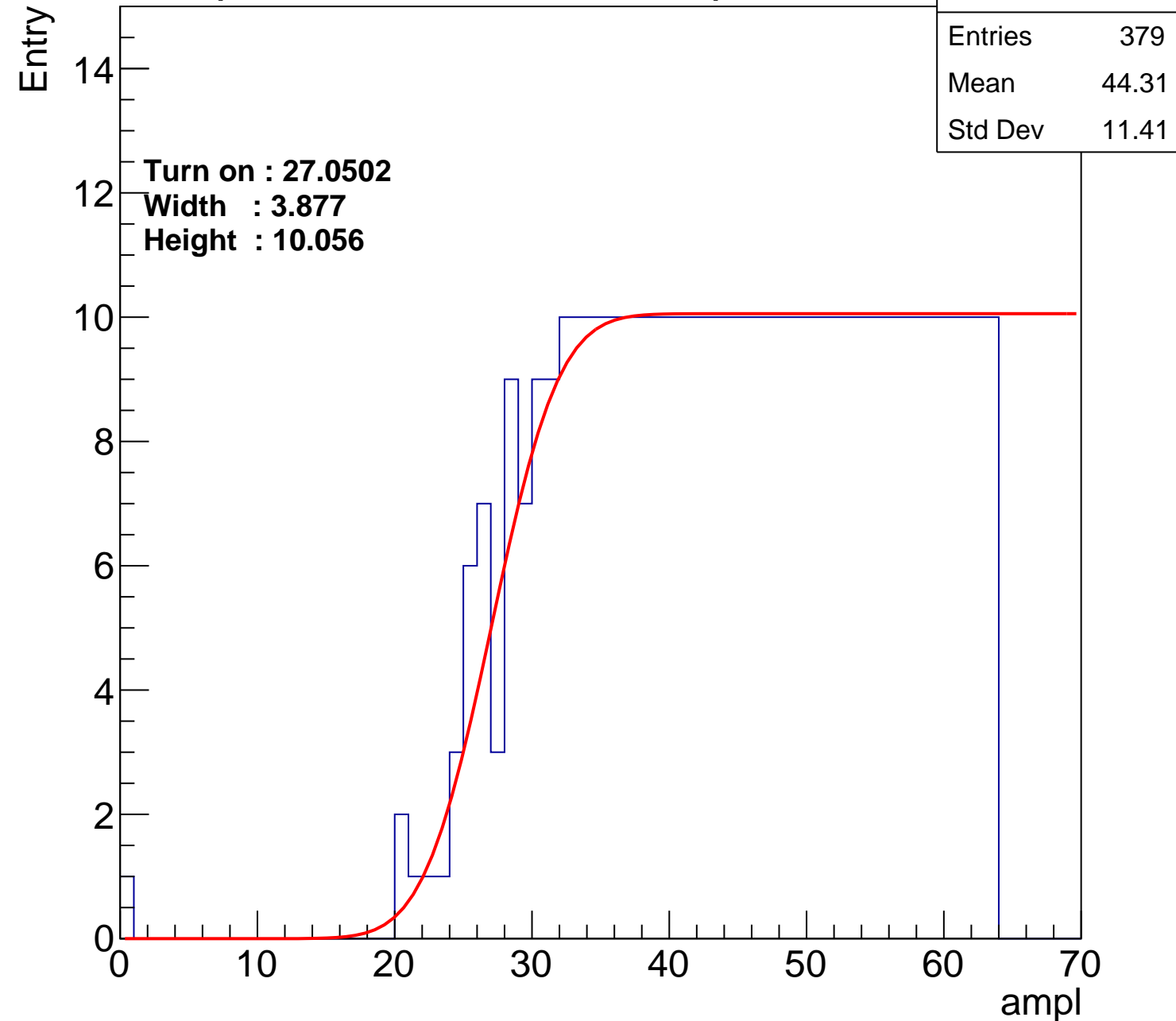
Width : 3.877

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch110

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	378
Mean	44.15
Std Dev	12.02

Turn on : 27.4329

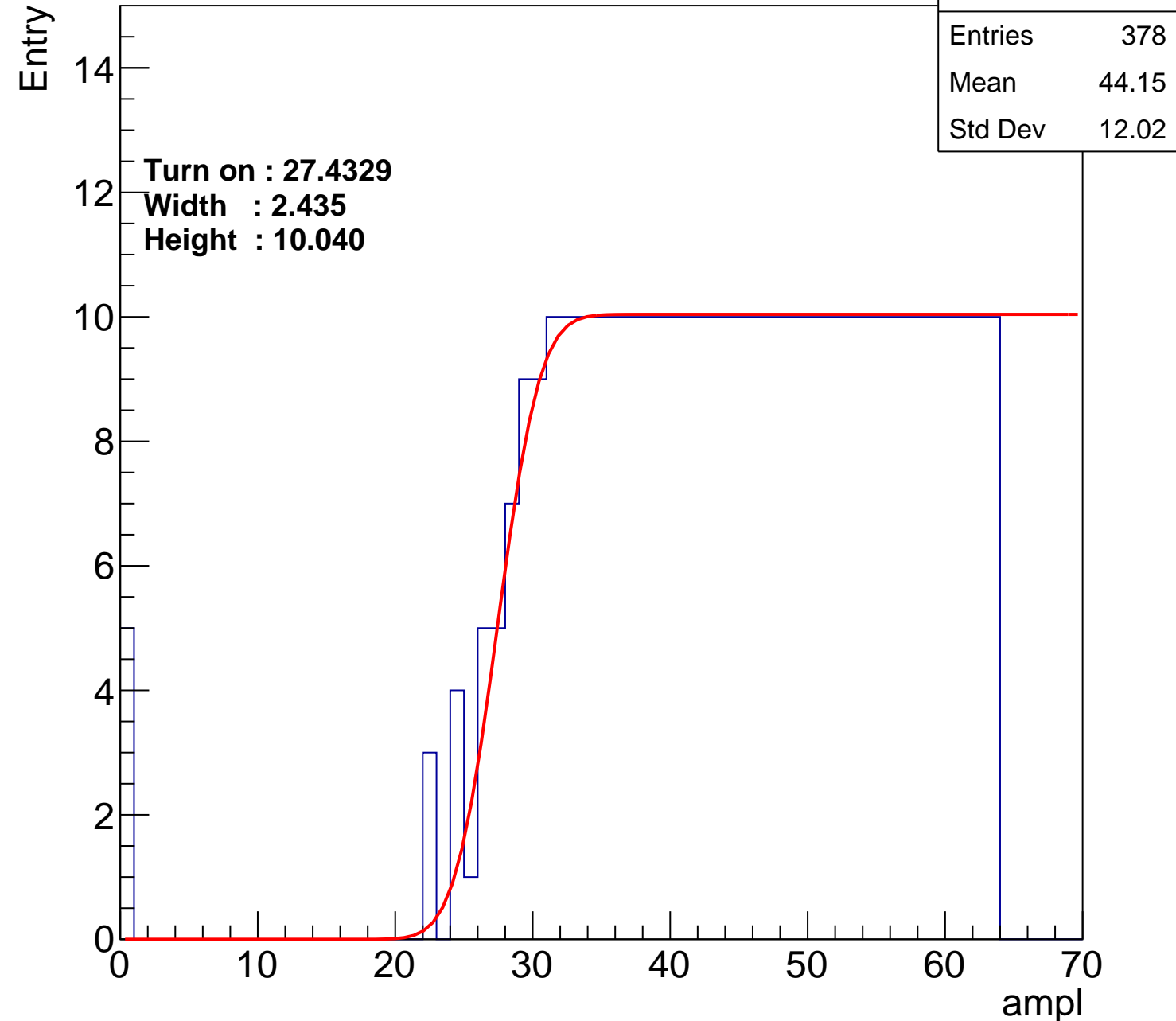
Width : 2.435

Height : 10.040

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch111

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.4
Std Dev	11.99

Turn on : 24.4423

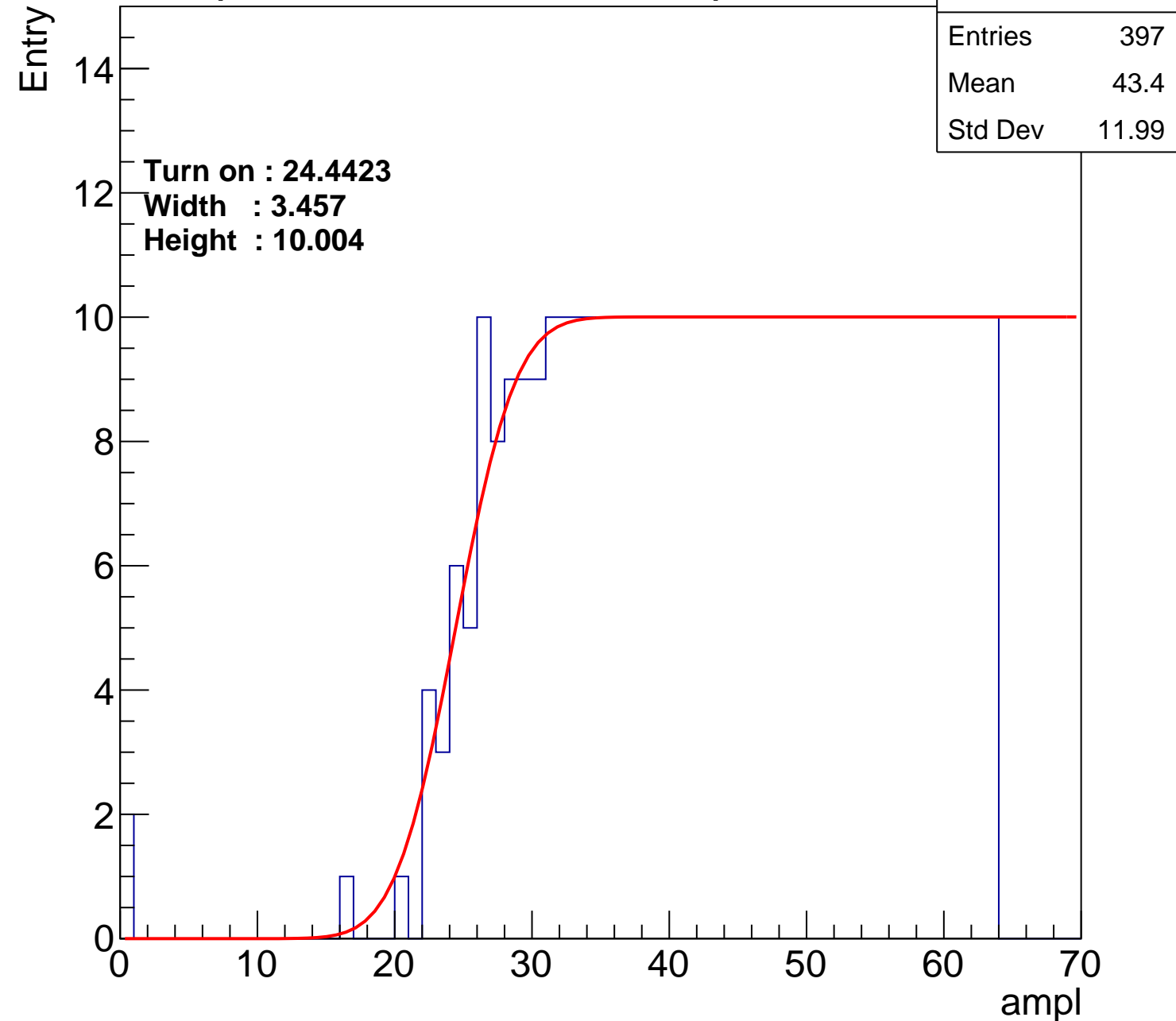
Width : 3.457

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch112

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	394
Mean	43.52
Std Dev	11.96

Turn on : 25.3988

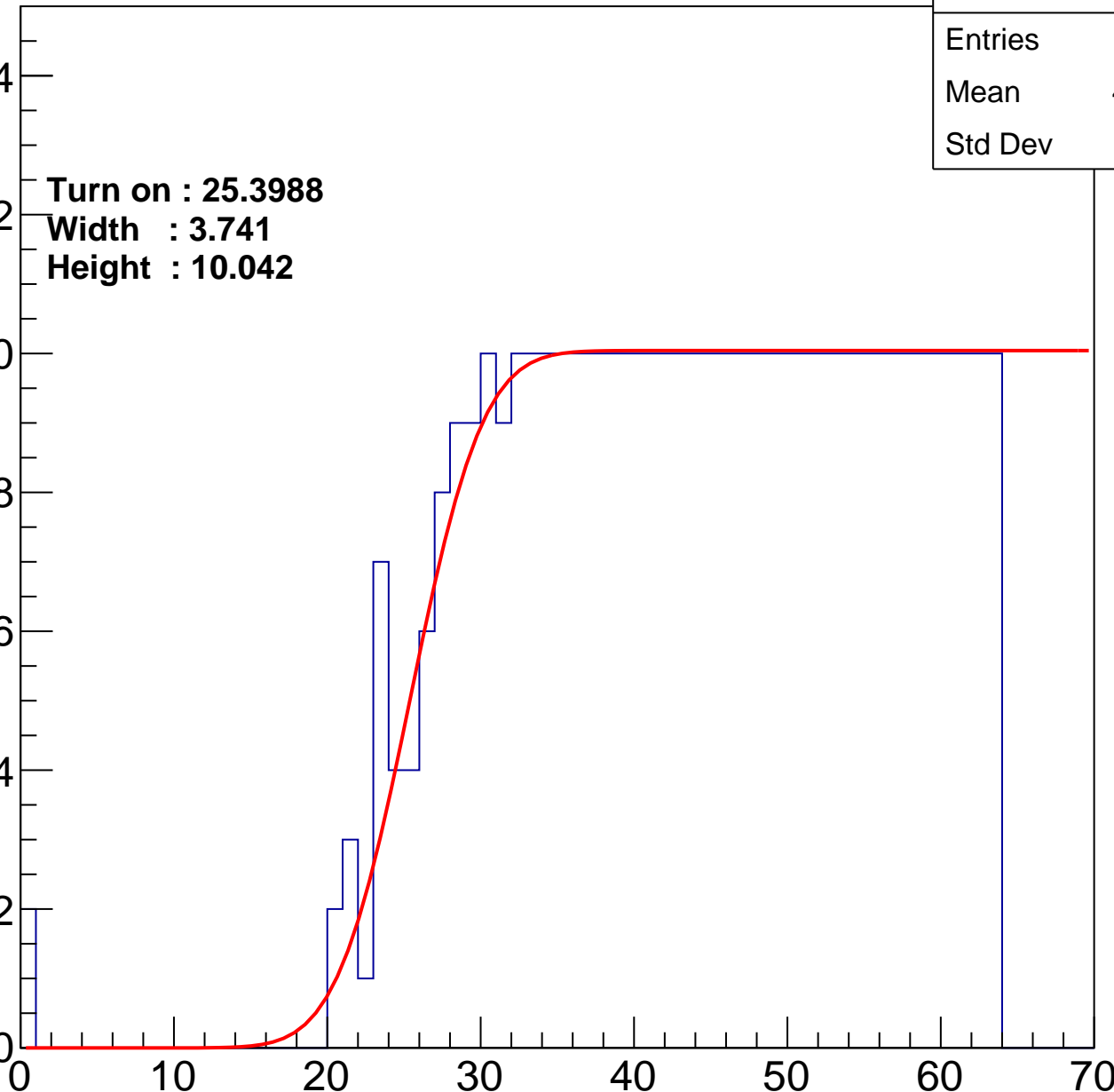
Width : 3.741

Height : 10.042

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch113

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	379
Mean	44.26
Std Dev	11.57

Turn on : 26.5469

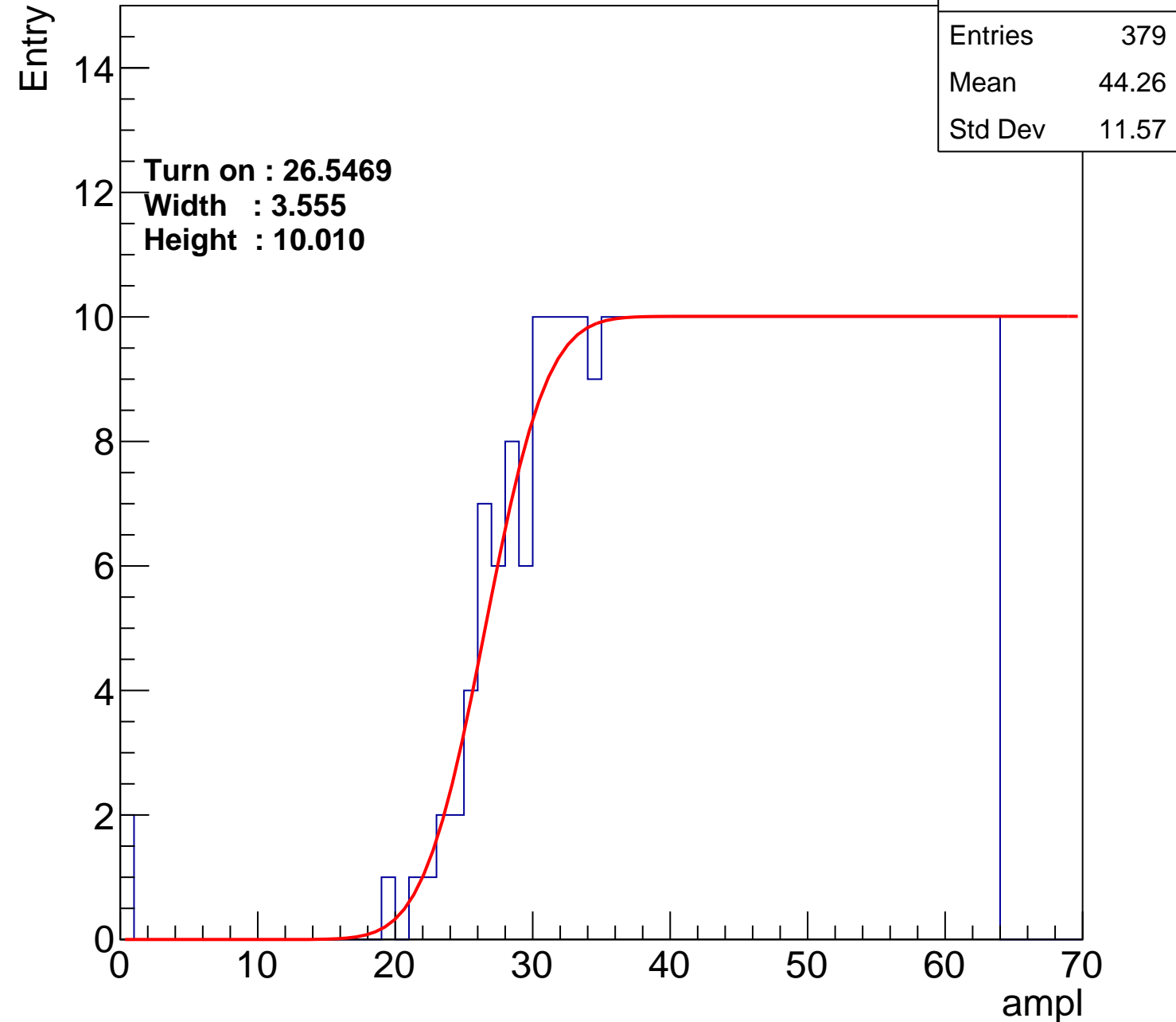
Width : 3.555

Height : 10.010

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch114

calib\_packv5\_042523\_0143.root, FC#12, port B1

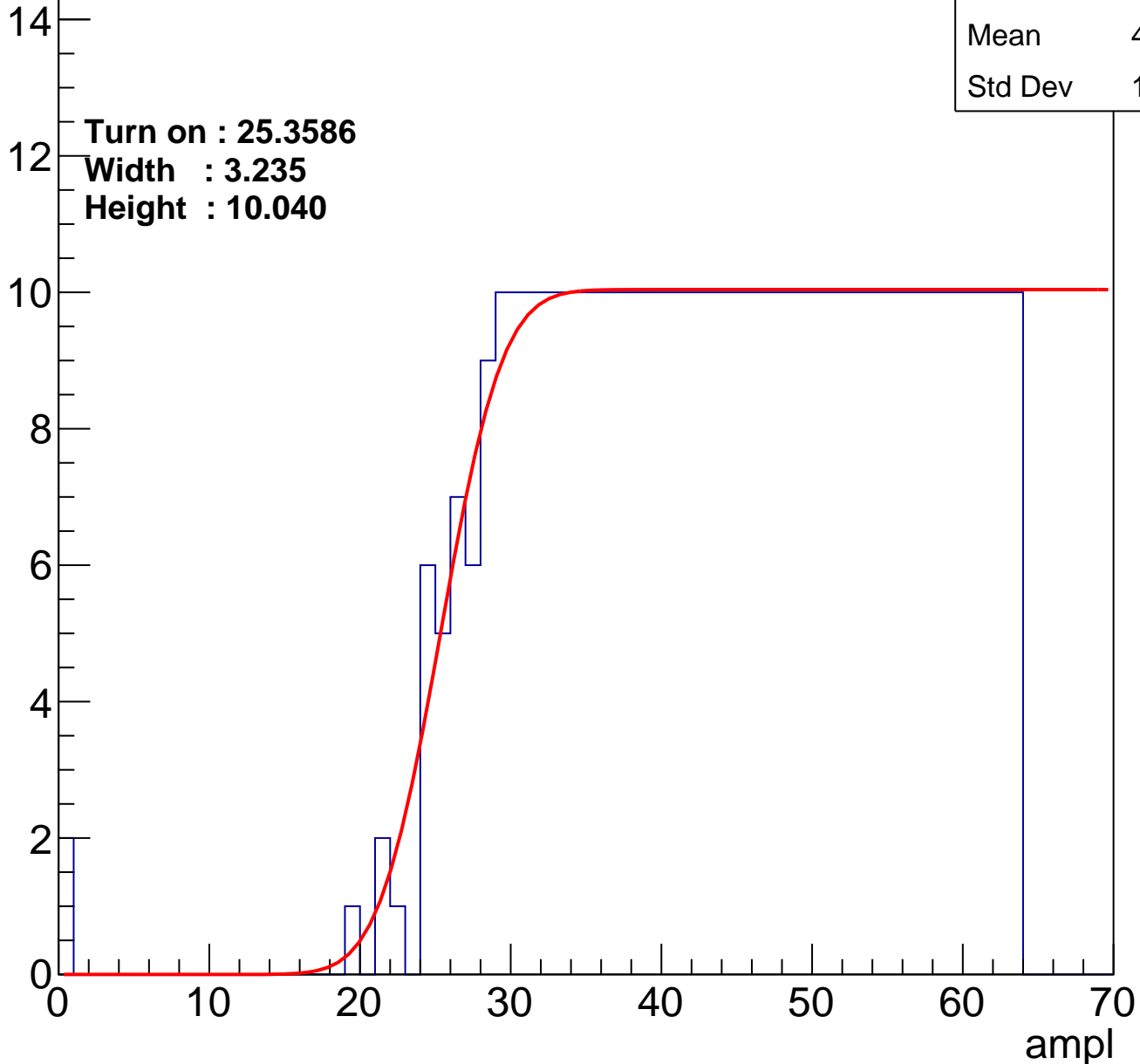
Entries	389
Mean	43.83
Std Dev	11.74

Turn on : 25.3586

Width : 3.235

Height : 10.040

Entry



# B0L102S, U22-ch115

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	381
Mean	44.27
Std Dev	11.41

Turn on : 26.6201

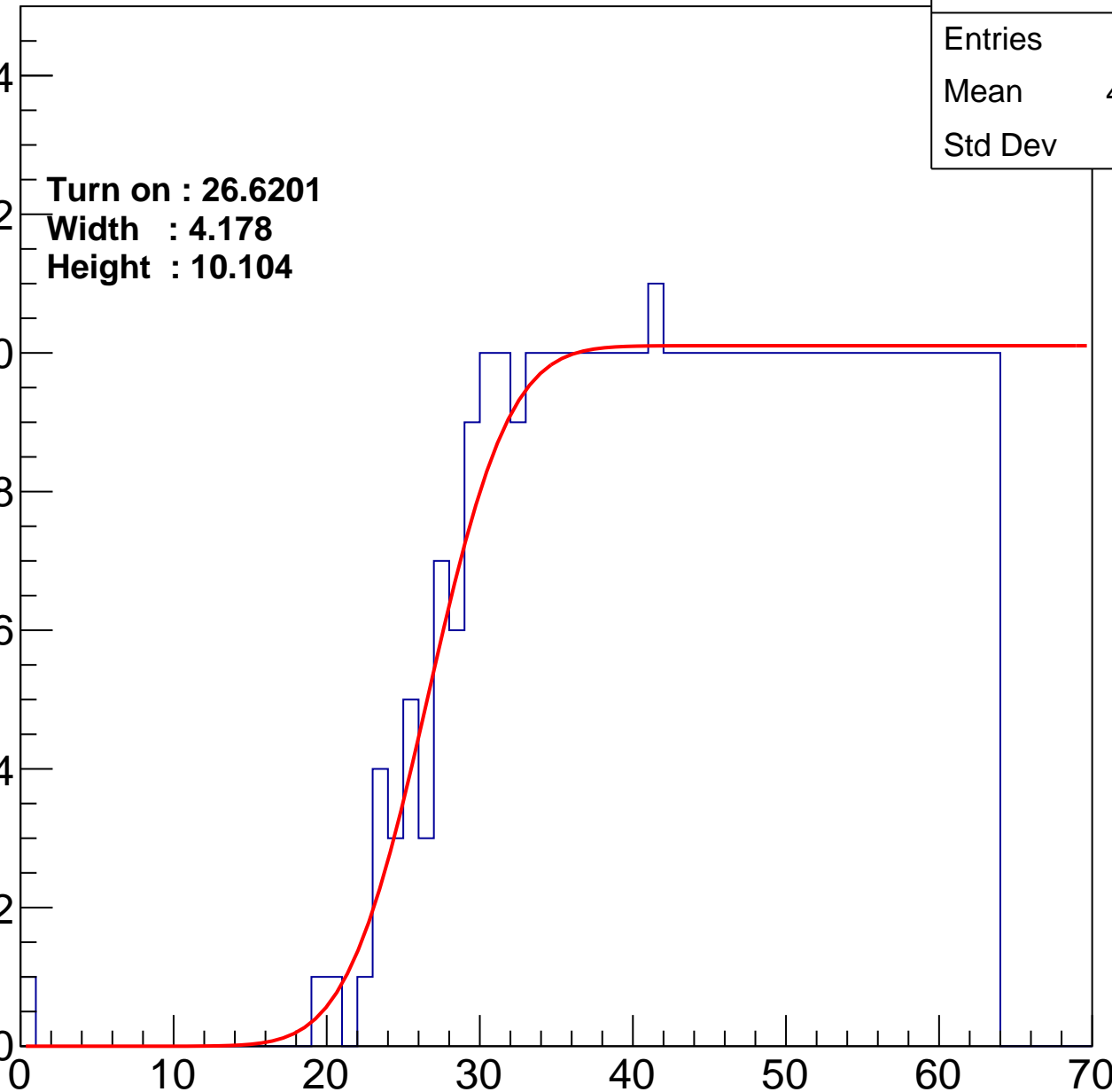
Width : 4.178

Height : 10.104

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch116

calib\_packv5\_042523\_0143.root, FC#12, port B1

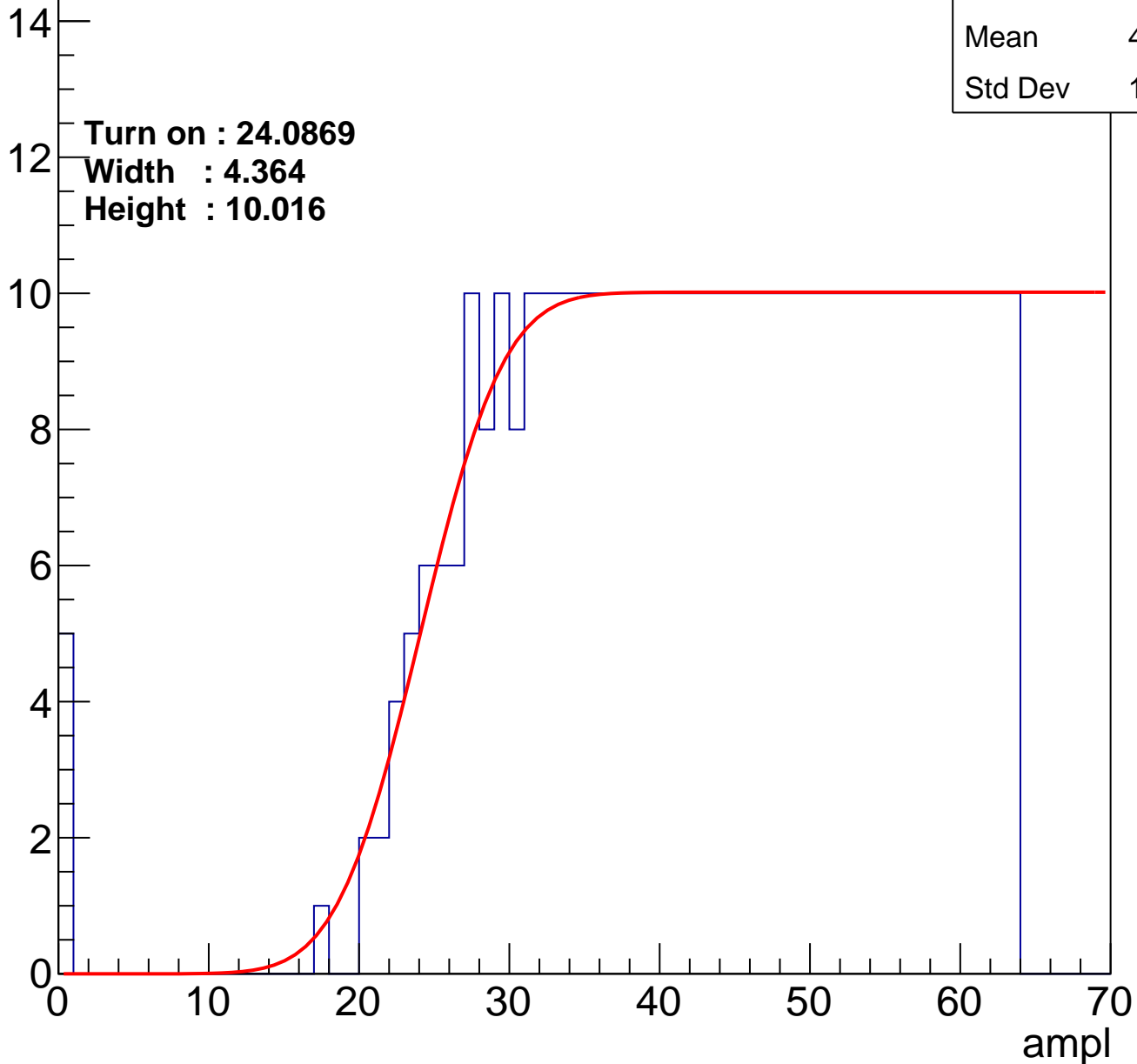
Entries	403
Mean	42.89
Std Dev	12.64

Turn on : 24.0869

Width : 4.364

Height : 10.016

Entry



# B0L102S, U22-ch117

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	384
Mean	43.8
Std Dev	12.24

Turn on : 26.1919

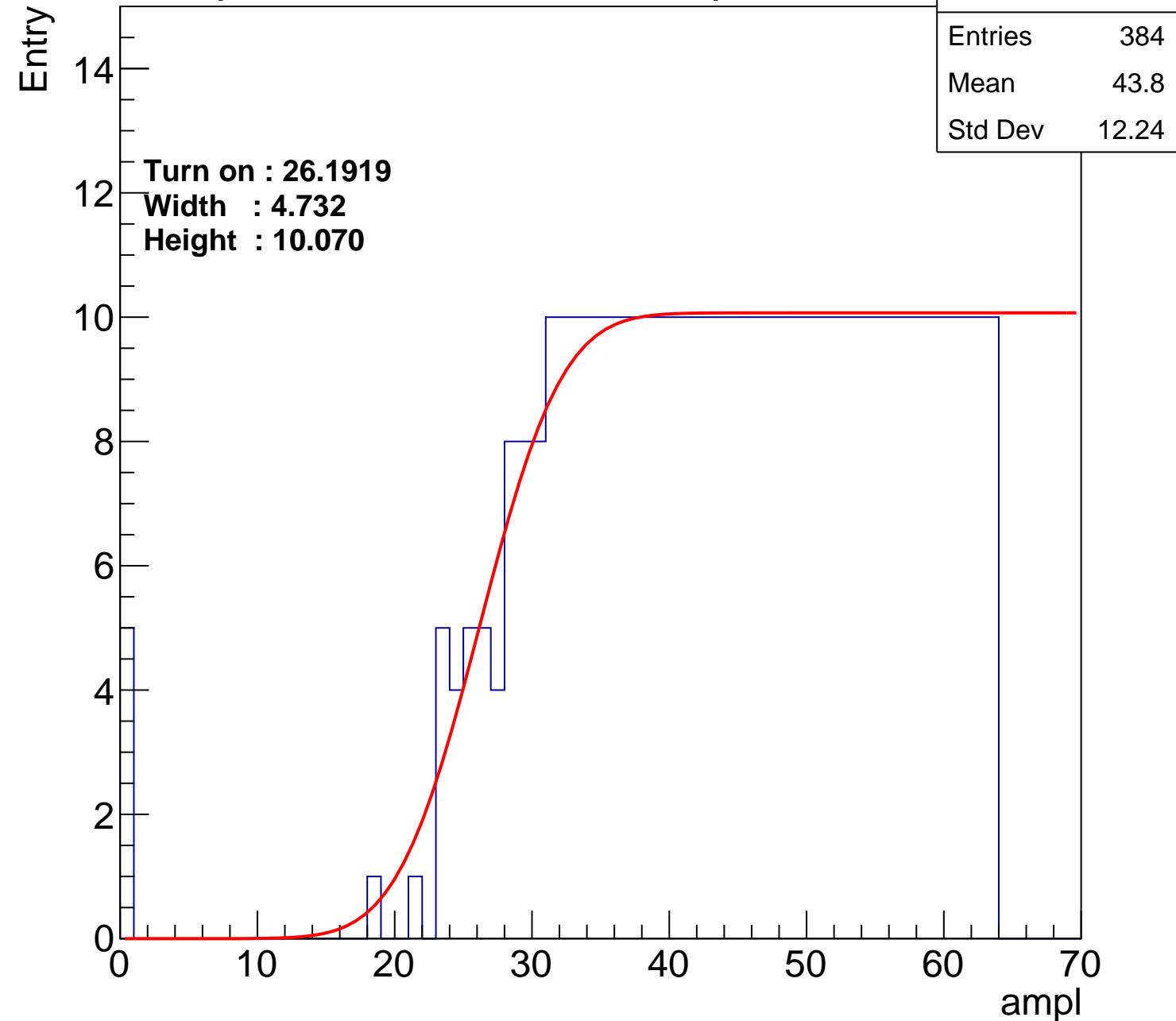
Width : 4.732

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch118

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	403
Mean	43.08
Std Dev	12.21

Turn on : 24.6949

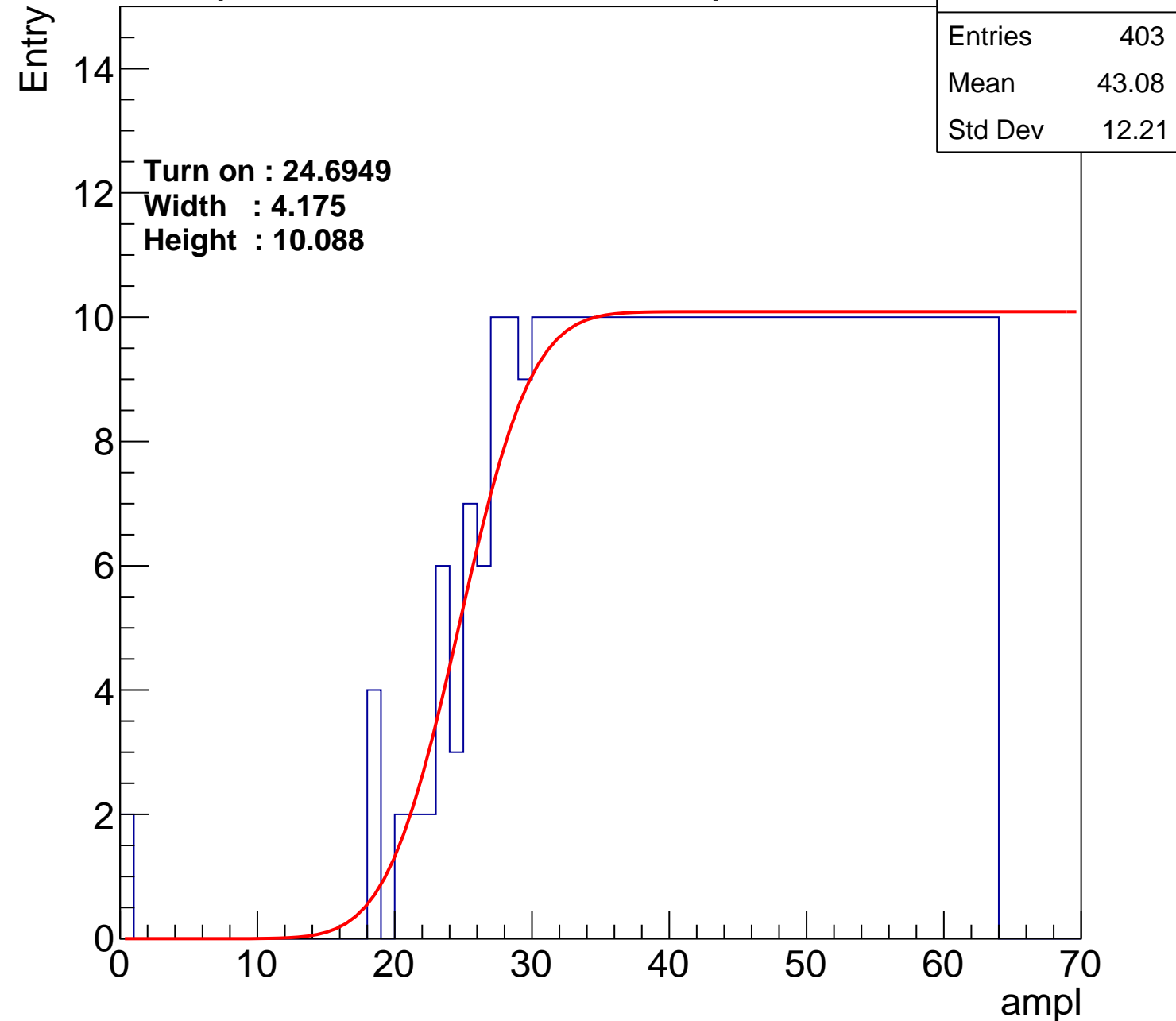
Width : 4.175

Height : 10.088

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch119

calib\_packv5\_042523\_0143.root, FC#12, port B1

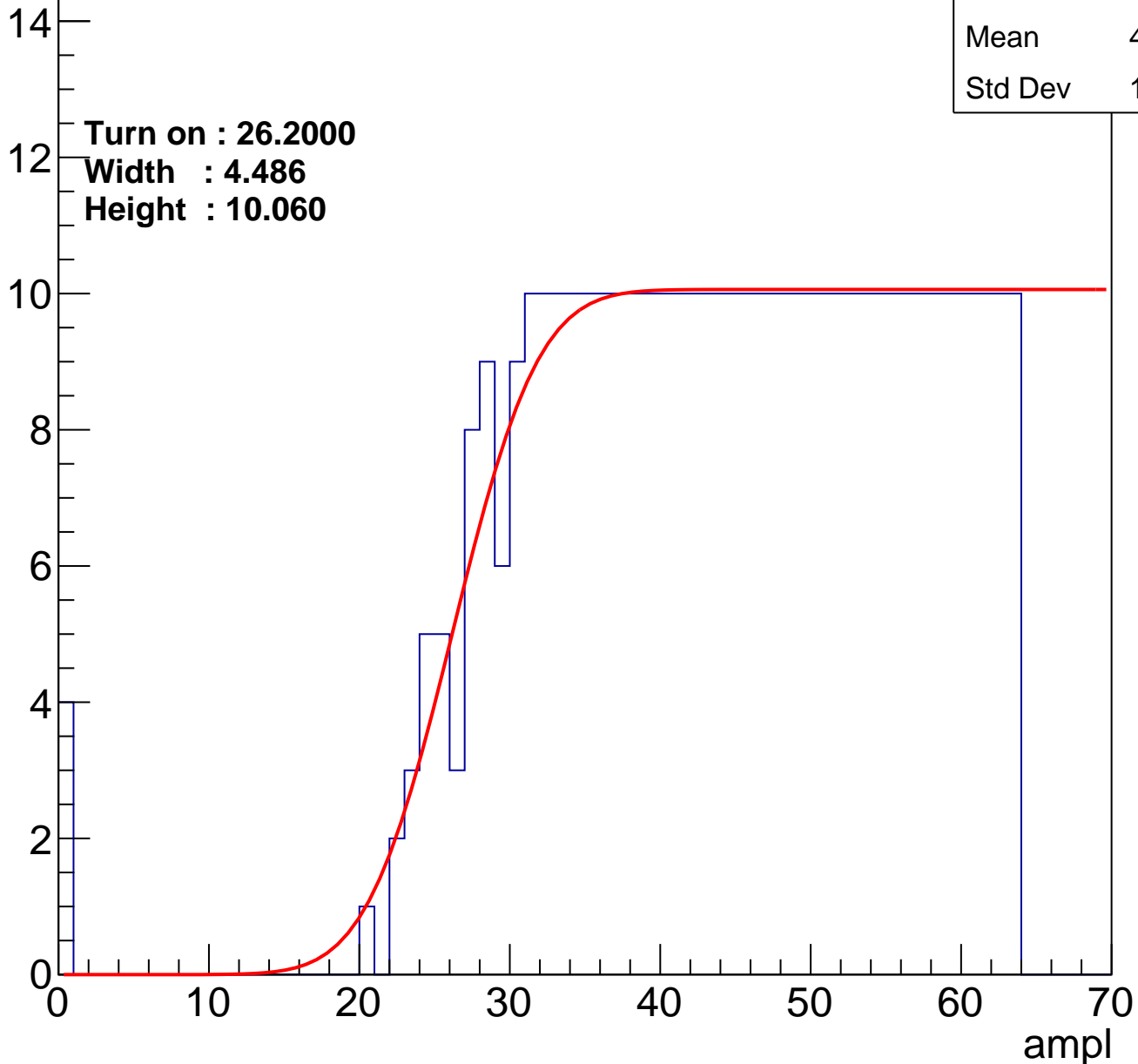
Entries	385
Mean	43.84
Std Dev	12.06

Turn on : 26.2000

Width : 4.486

Height : 10.060

Entry



# B0L102S, U22-ch120

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.57
Std Dev	12.05

Turn on : 25.2084

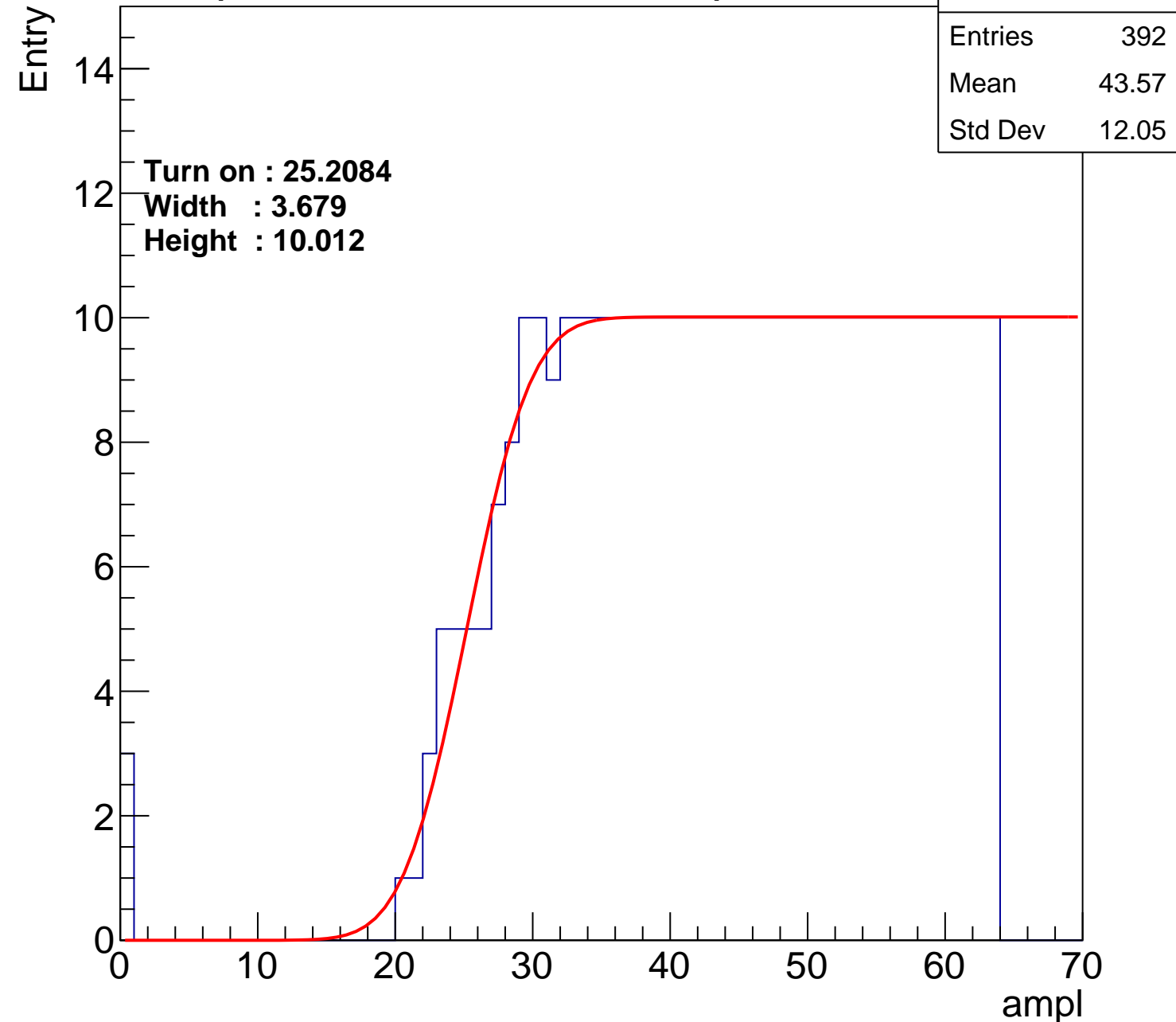
Width : 3.679

Height : 10.012

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch121

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	393
Mean	43.56
Std Dev	12.01

Turn on : 25.0346

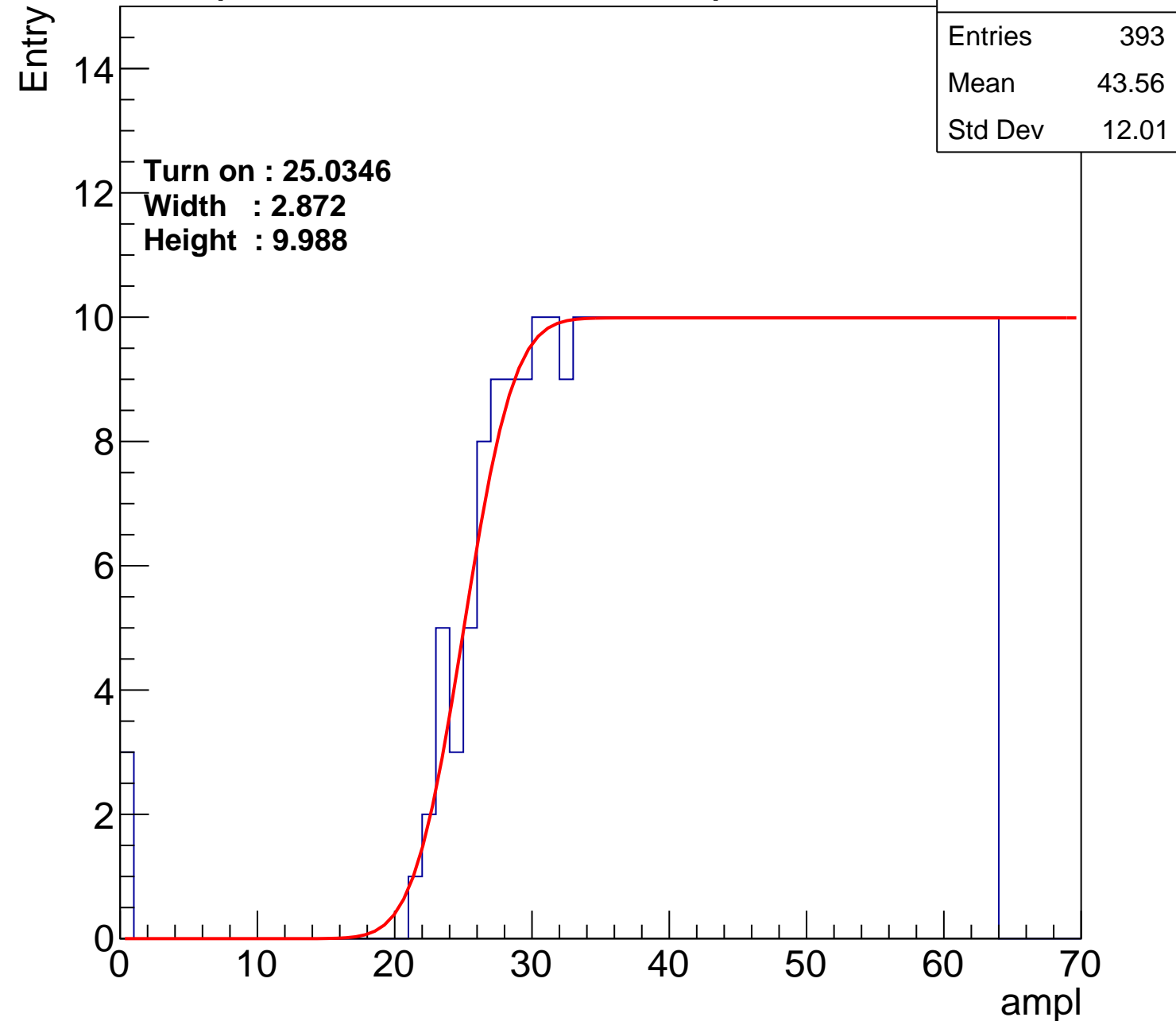
Width : 2.872

Height : 9.988

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch122

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	399
Mean	43.26
Std Dev	12.18

Turn on : 24.3837

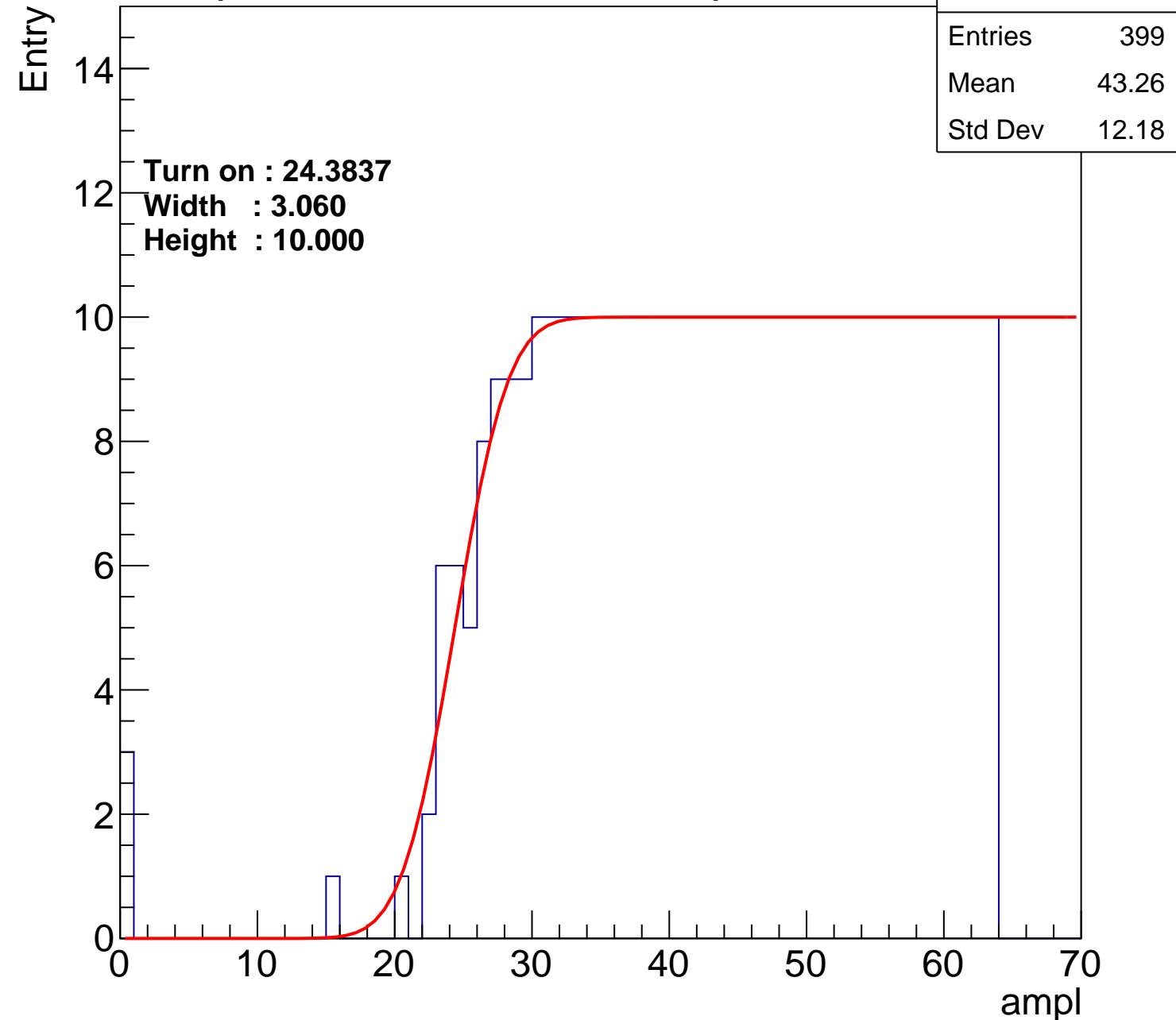
Width : 3.060

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch123

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	390
Mean	43.64
Std Dev	12.1

Turn on : 25.4286

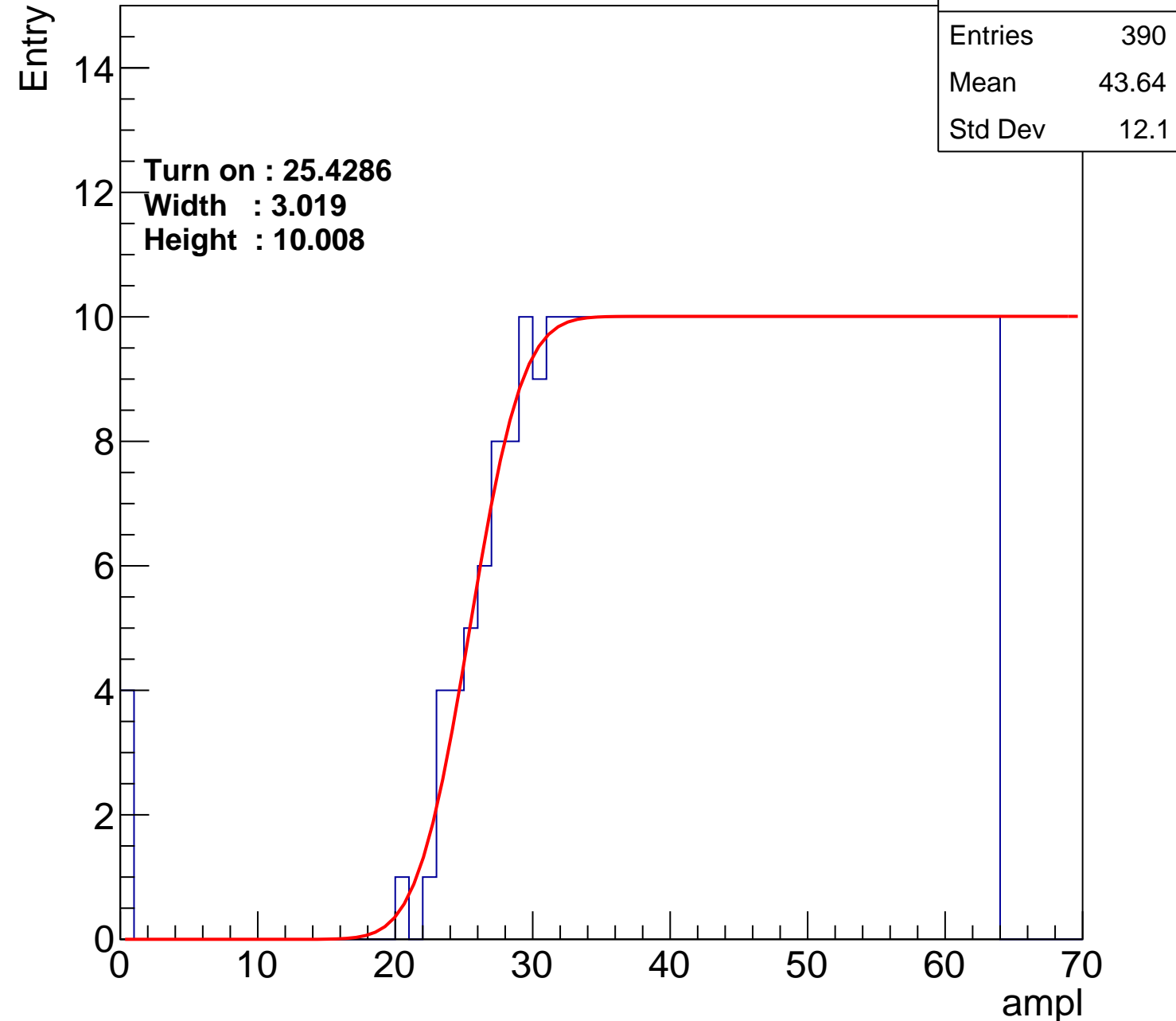
Width : 3.019

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch124

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	397
Mean	43.48
Std Dev	11.81

Turn on : 24.7117

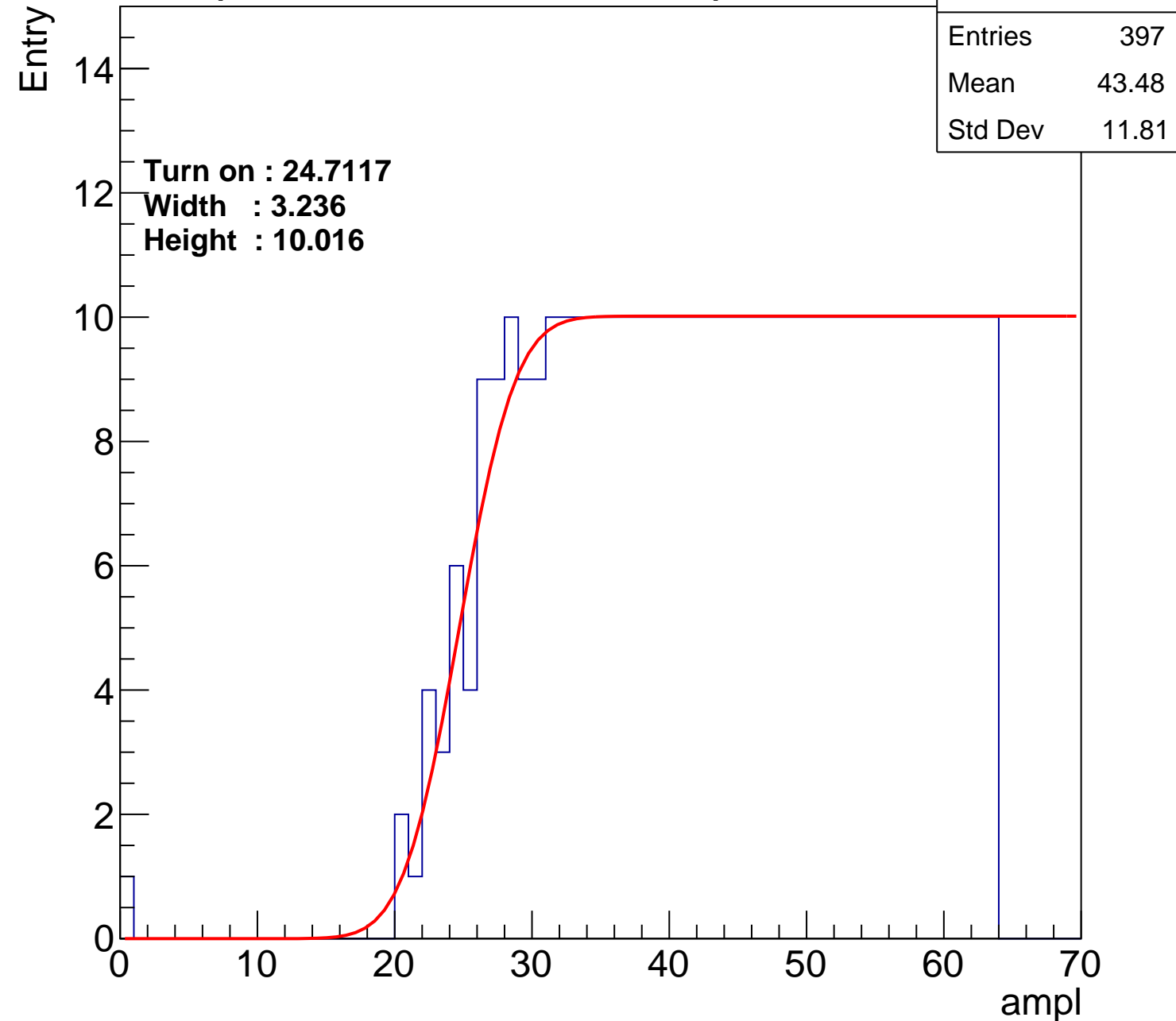
Width : 3.236

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch125

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	392
Mean	43.47
Std Dev	12.33

Turn on : 25.6424

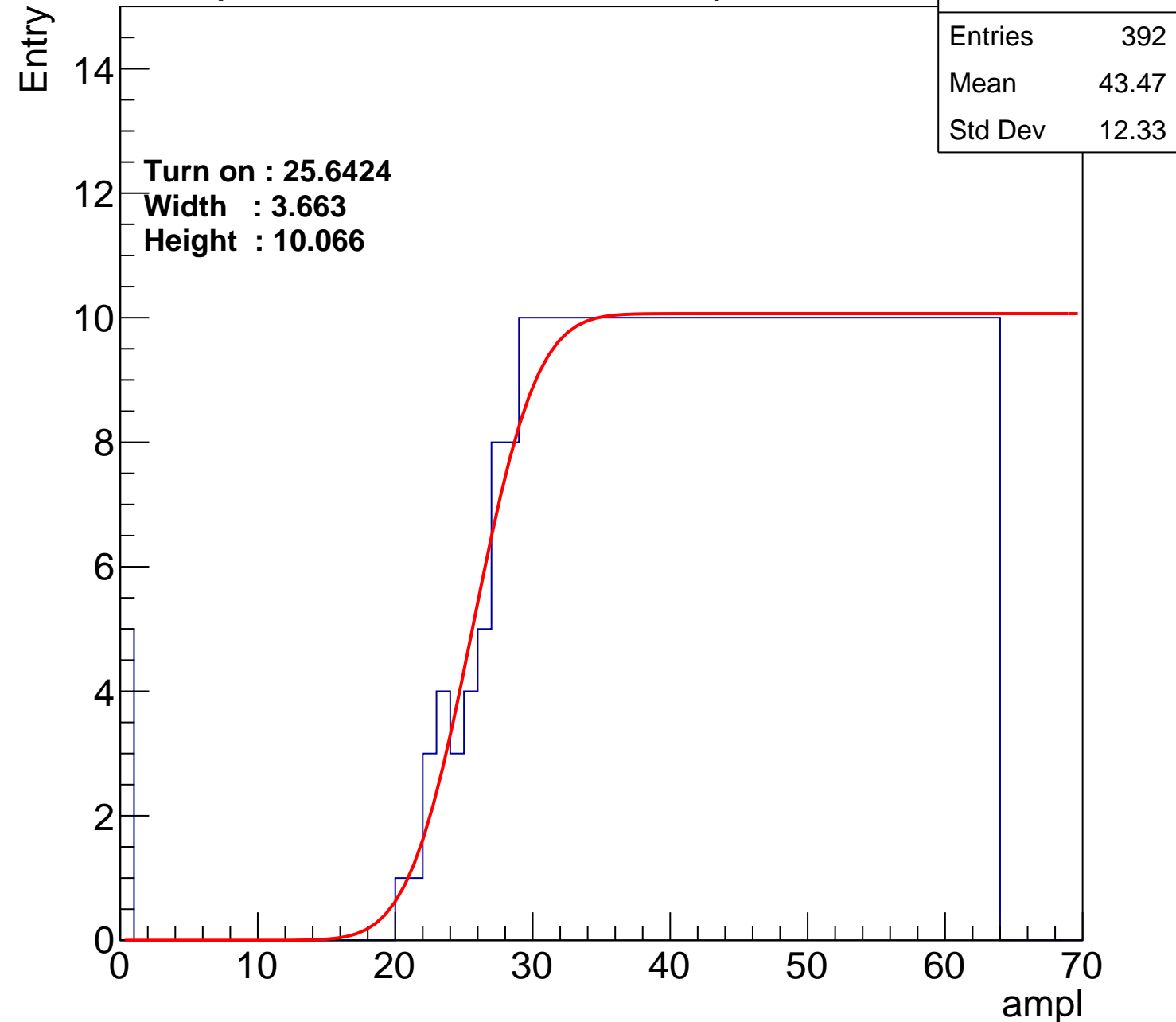
Width : 3.663

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch126

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	398
Mean	43.23
Std Dev	12.33

Turn on : 25.3815

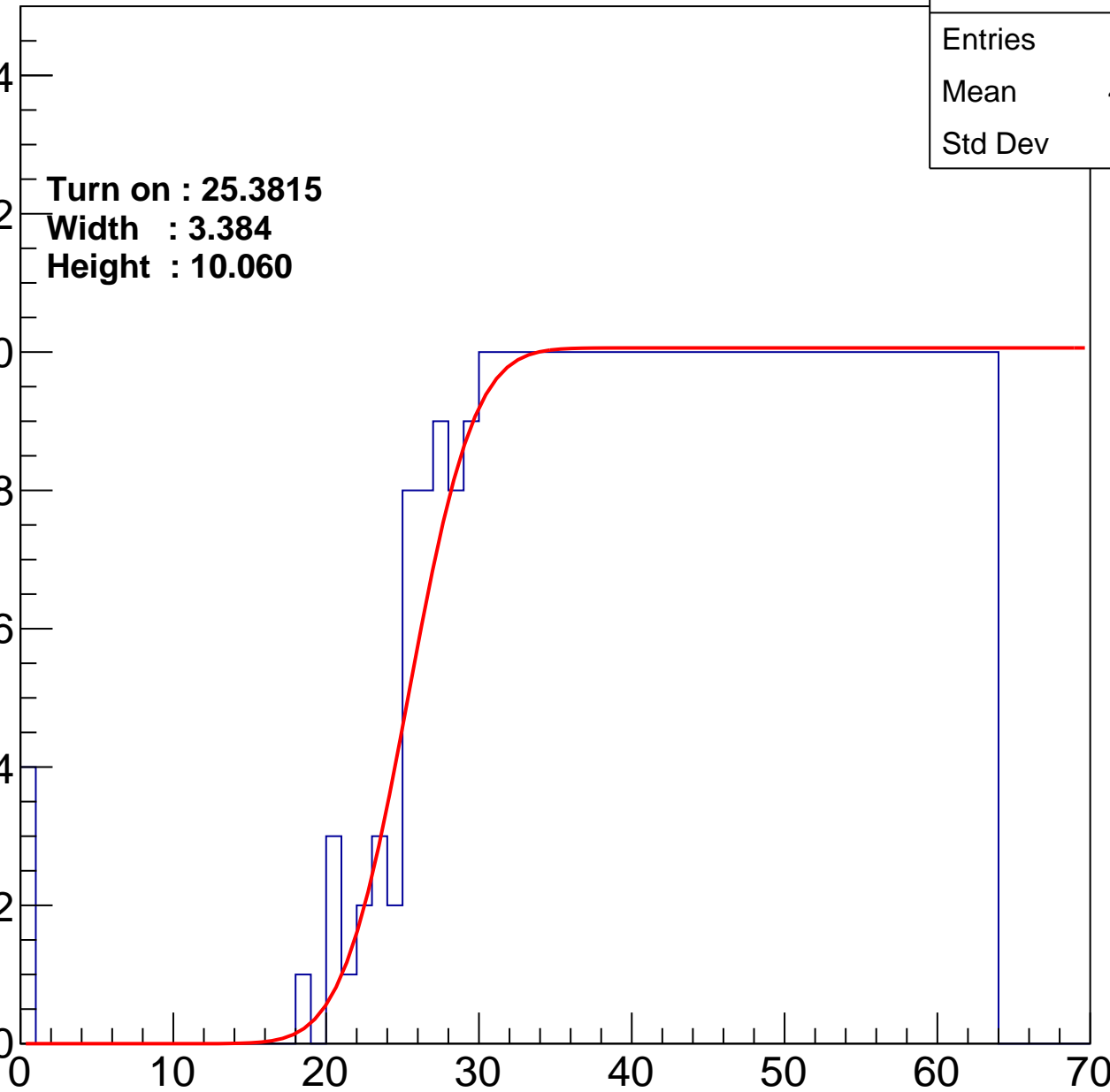
Width : 3.384

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L102S, U22-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	400
Mean	43.16
Std Dev	12.28

Turn on : 25.0074

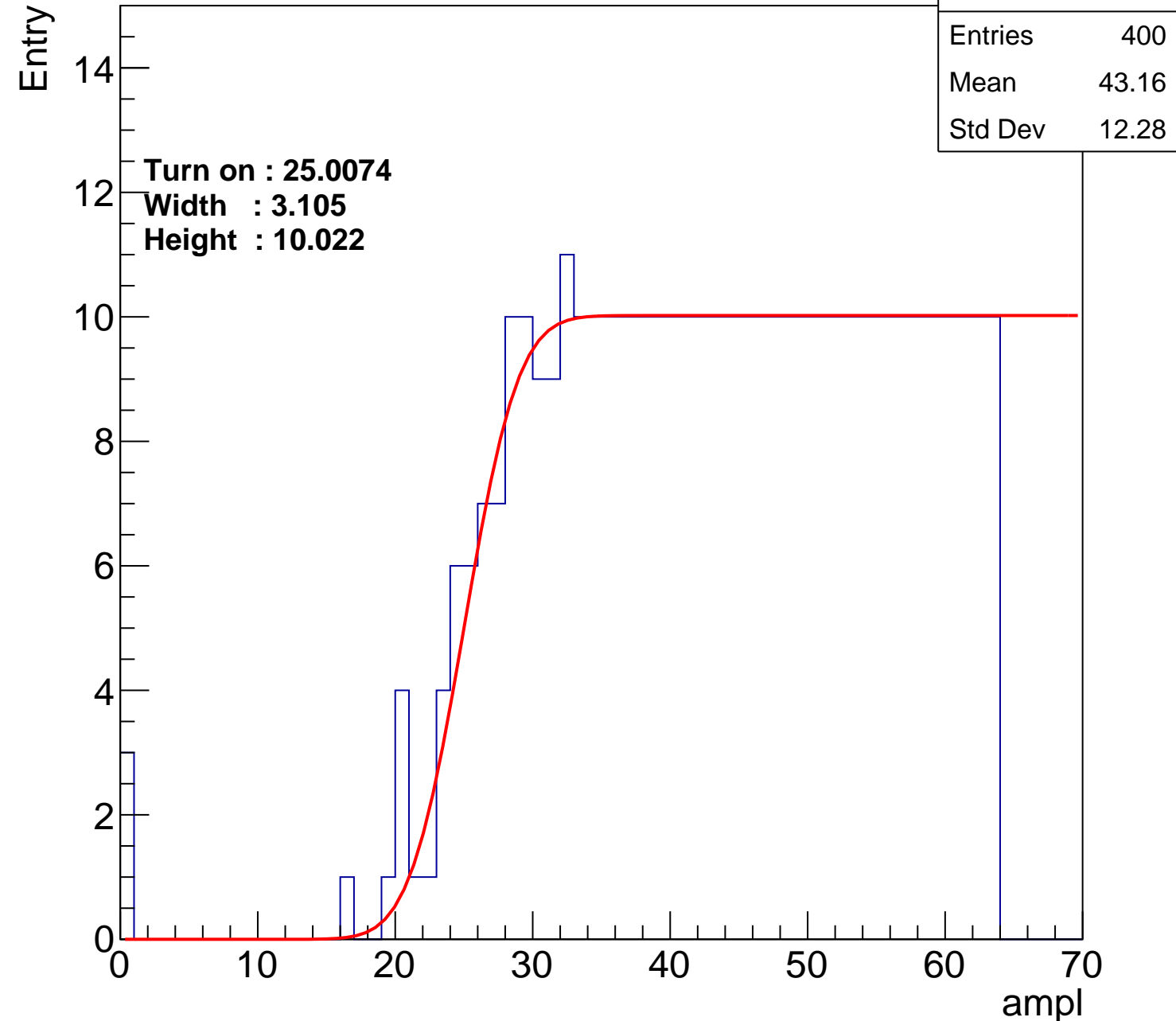
Width : 3.105

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L102S, U22-ch127

calib\_packv5\_042523\_0143.root, FC#12, port B1

Entries	400
Mean	43.16
Std Dev	12.28

Turn on : 25.0074

Width : 3.105

Height : 10.022

Entry

