



# B0L000S, U25-ch0, adc0

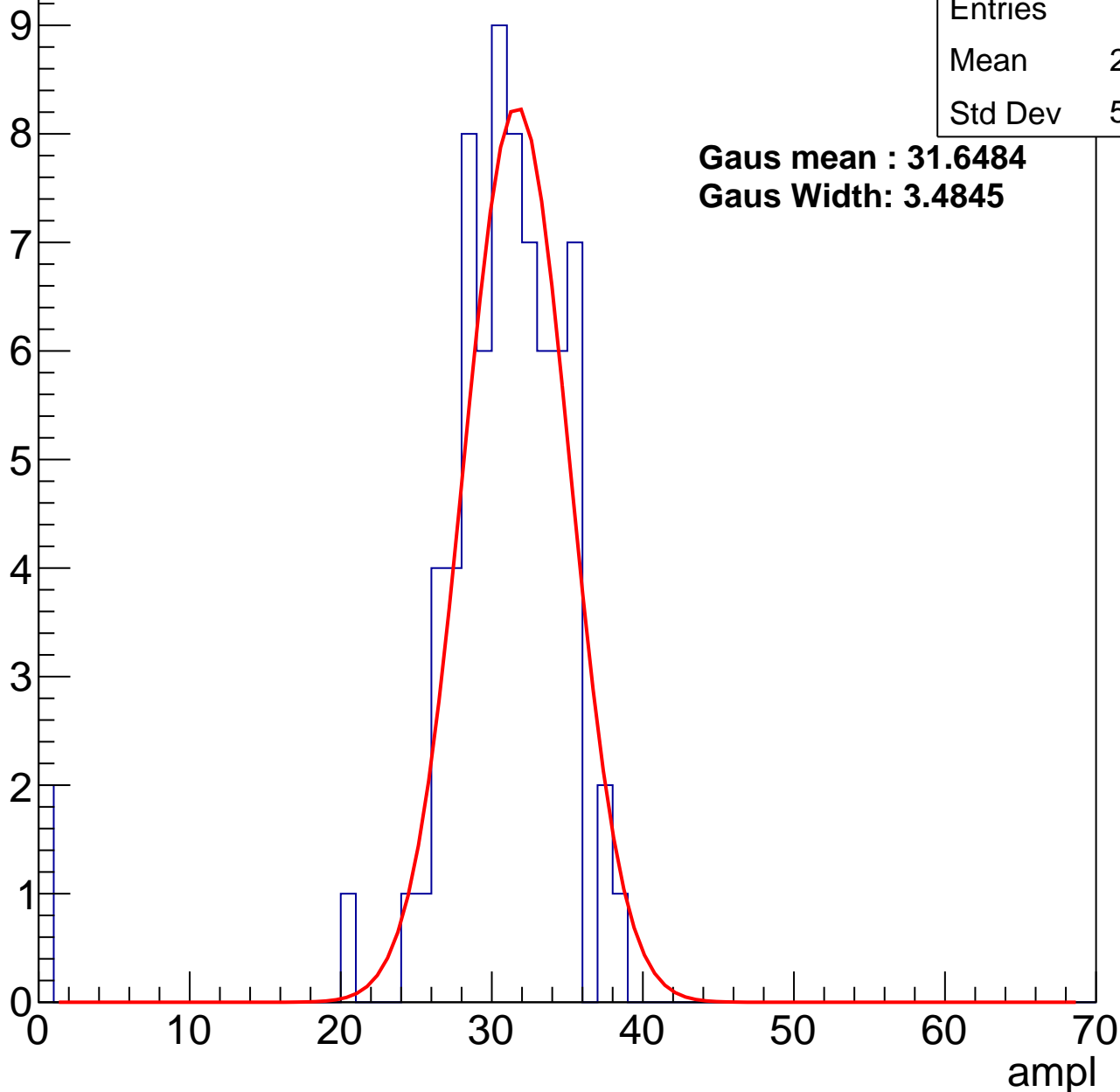
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.86
Std Dev	5.992

**Gaus mean : 31.6484**

**Gaus Width: 3.4845**



# B0L000S, U25-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	38.16
Std Dev	3.509

**Gaus mean : 39.3308**

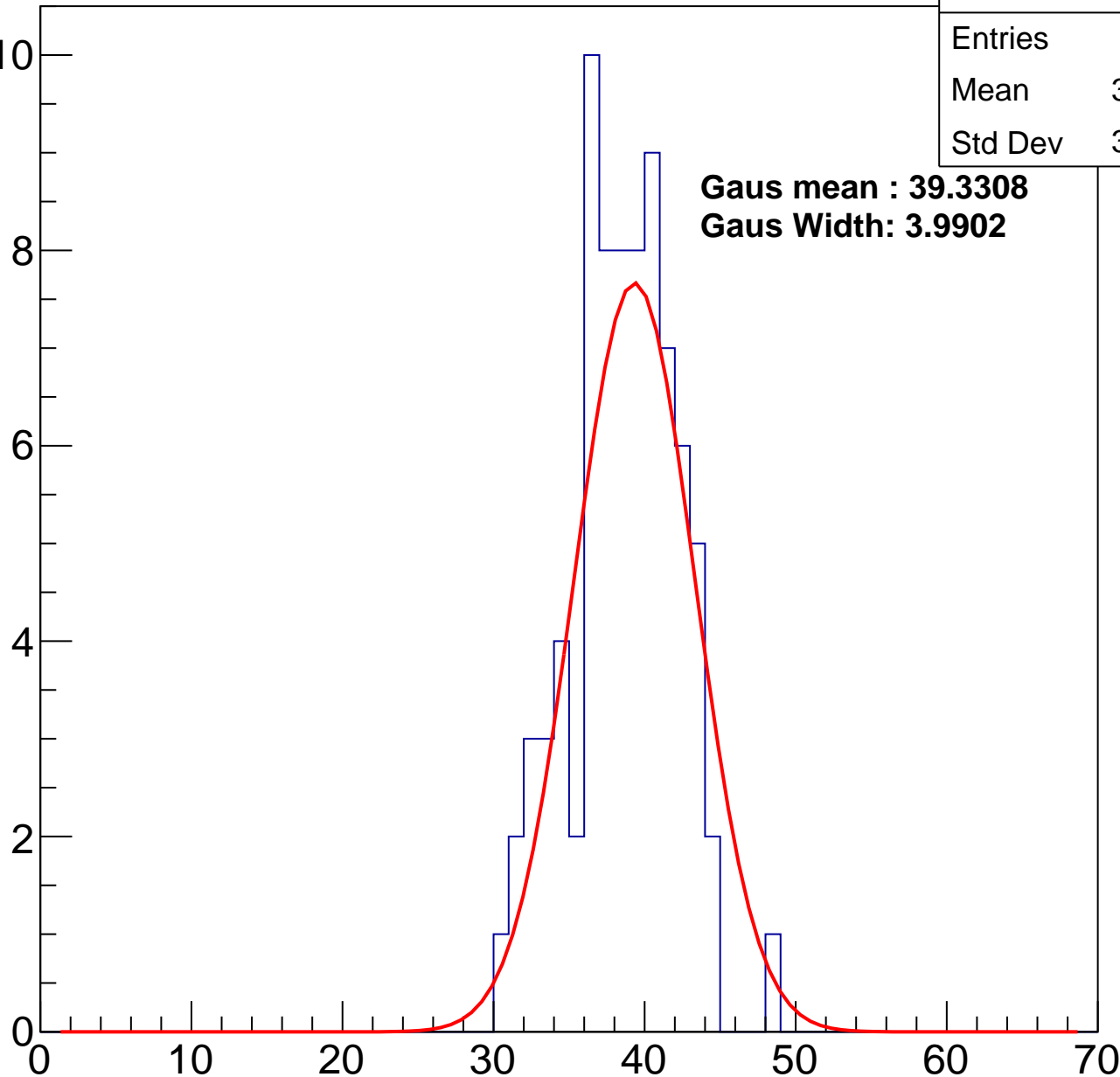
**Gaus Width: 3.9902**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch0, adc2

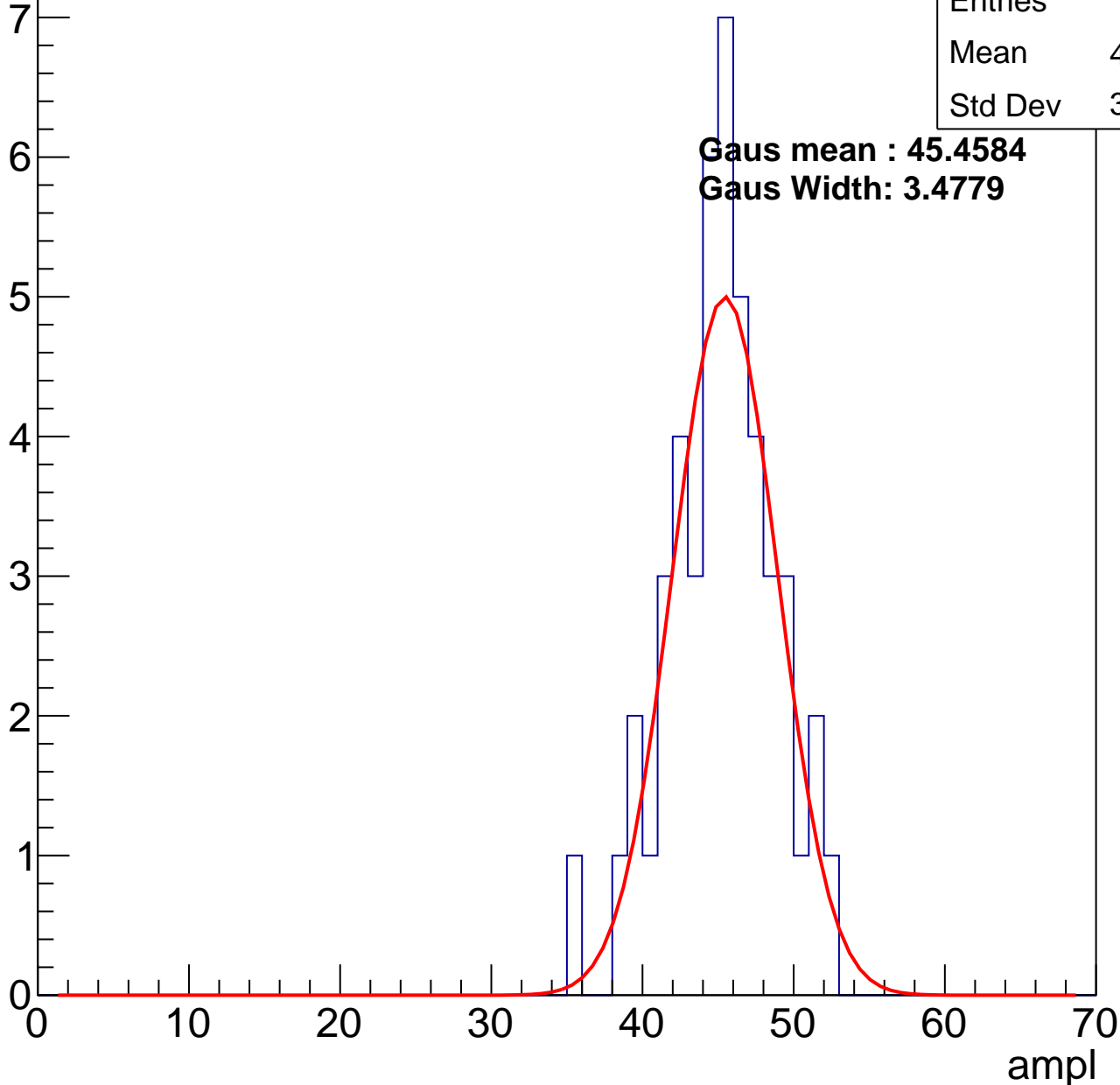
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	44.74
Std Dev	3.534

**Gaus mean : 45.4584**

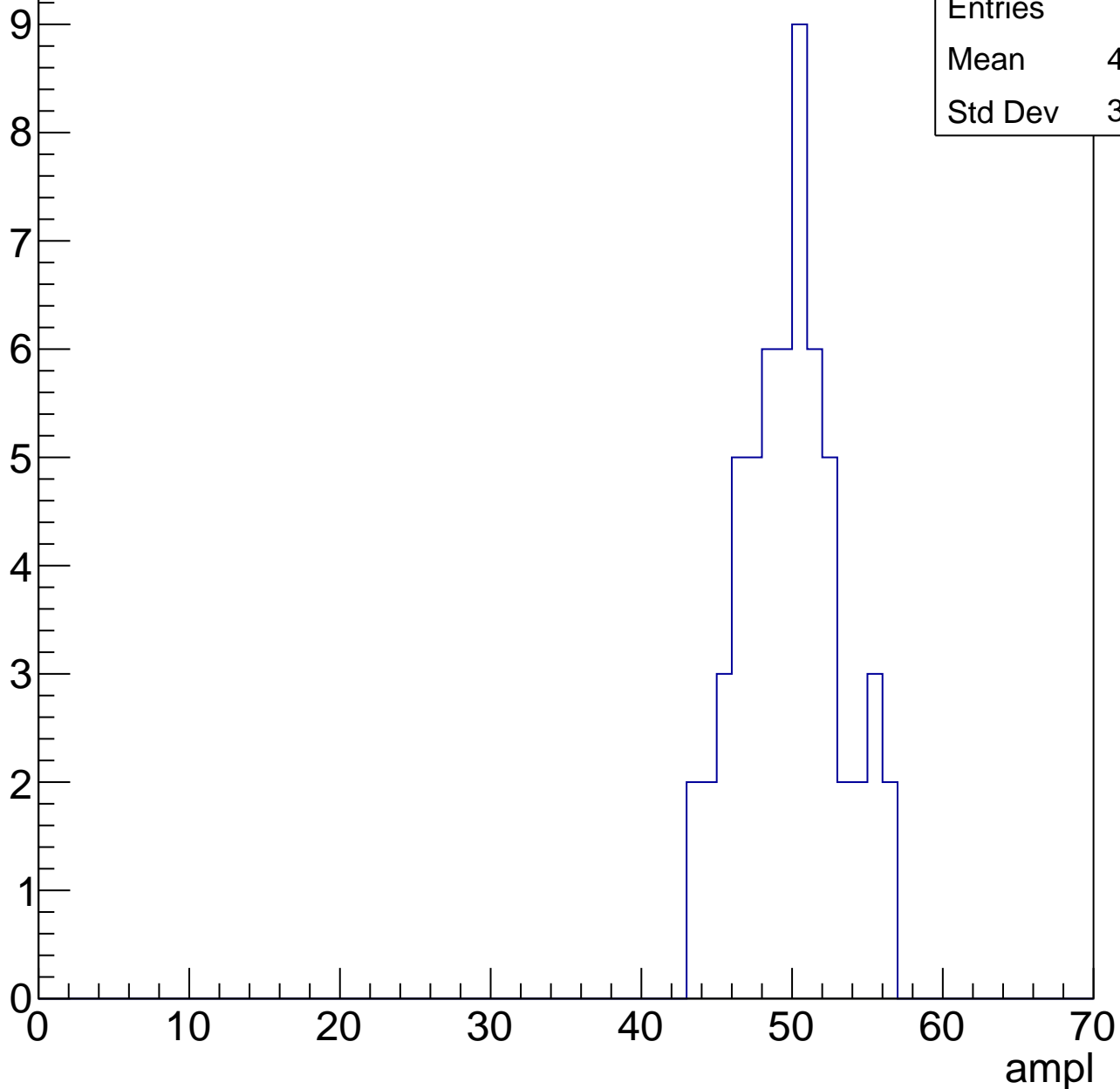
**Gaus Width: 3.4779**



# B0L000S, U25-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

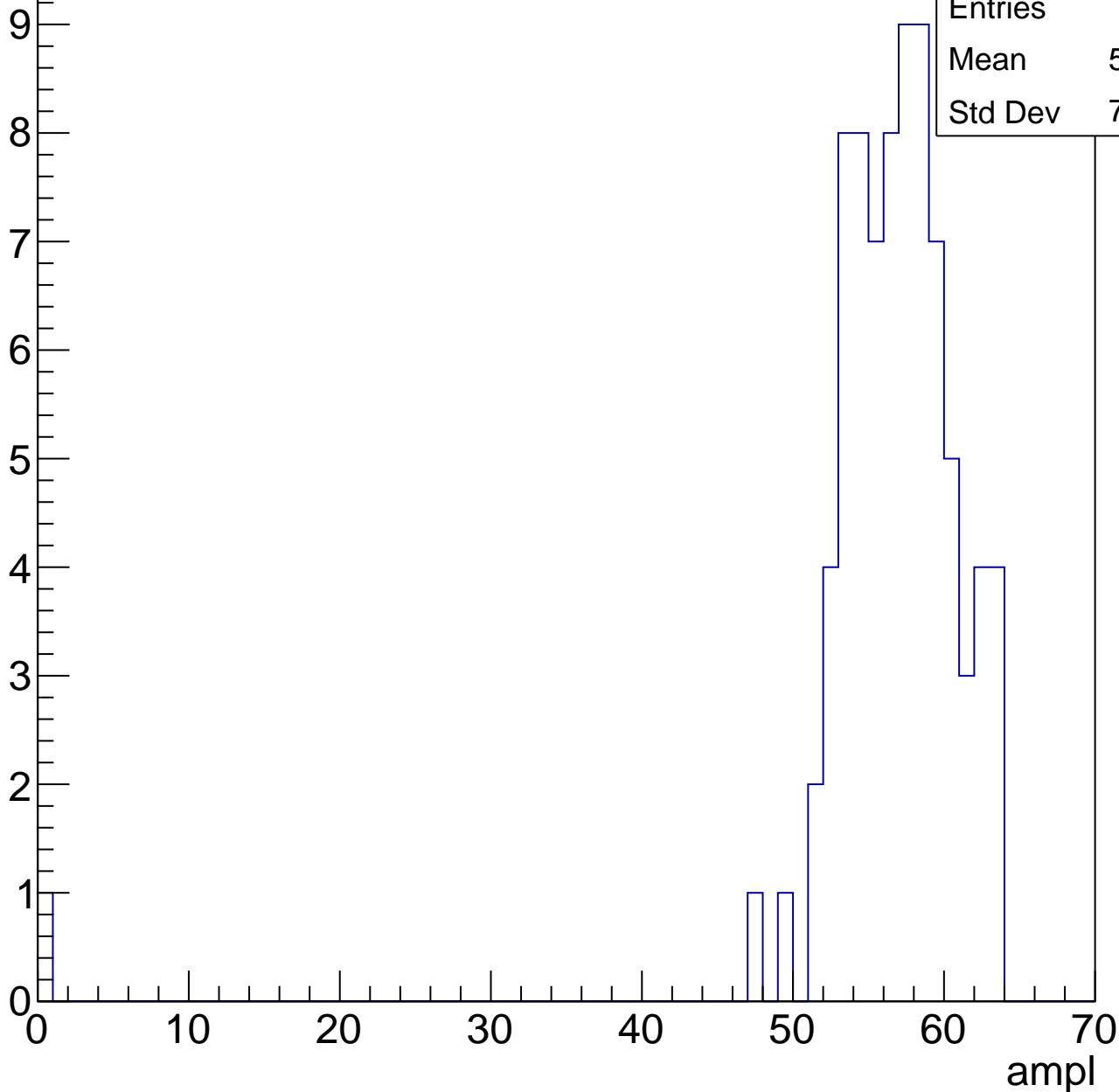


# B0L000S, U25-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	55.88
Std Dev	7.108



# B0L000S, U25-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries	28
Mean	60.93
Std Dev	1.557

ampl

# B0L000S, U25-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch1, adc0

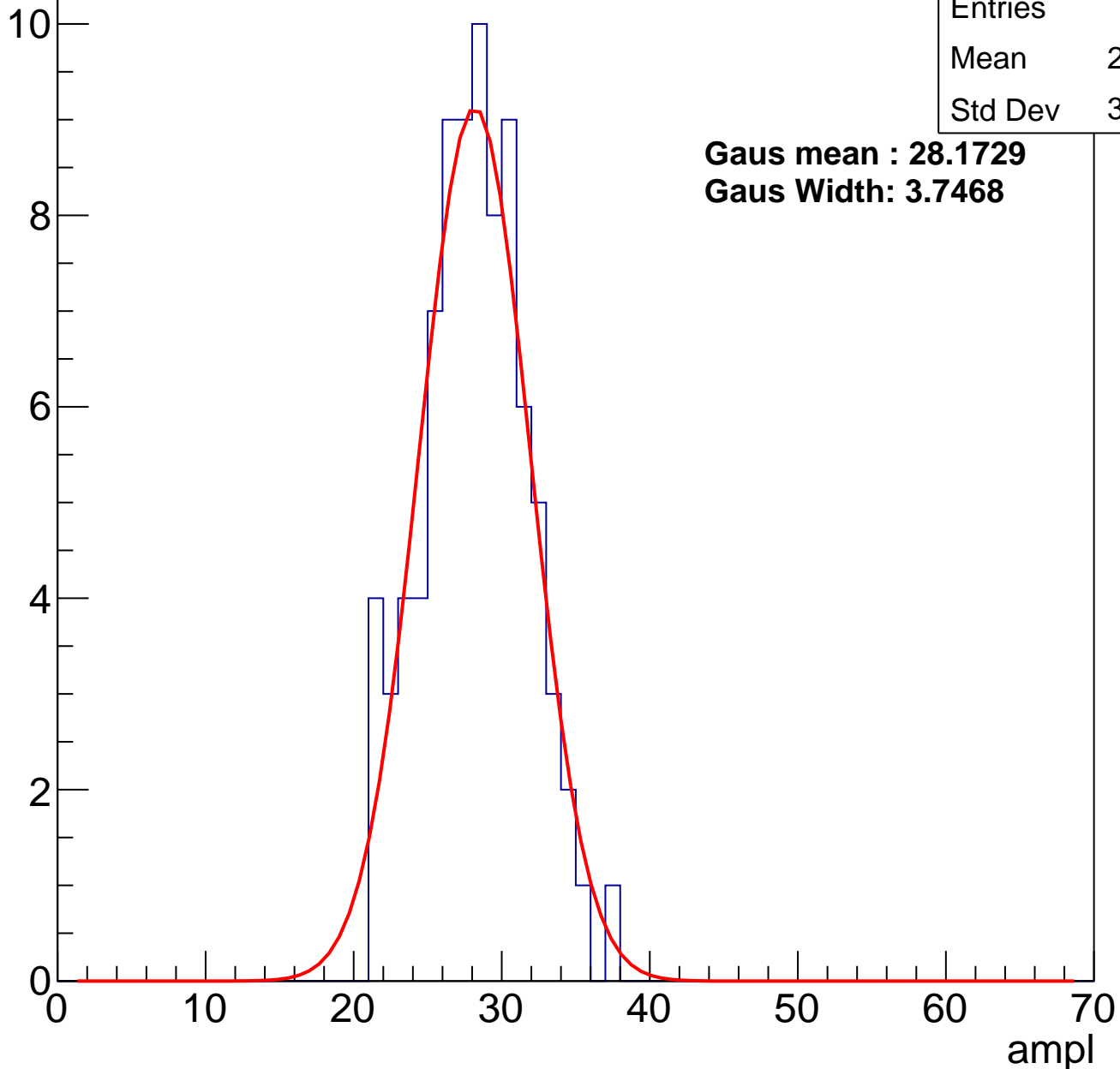
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	27.73
Std Dev	3.472

**Gaus mean : 28.1729**

**Gaus Width: 3.7468**

Entry



# B0L000S, U25-ch1, adc1

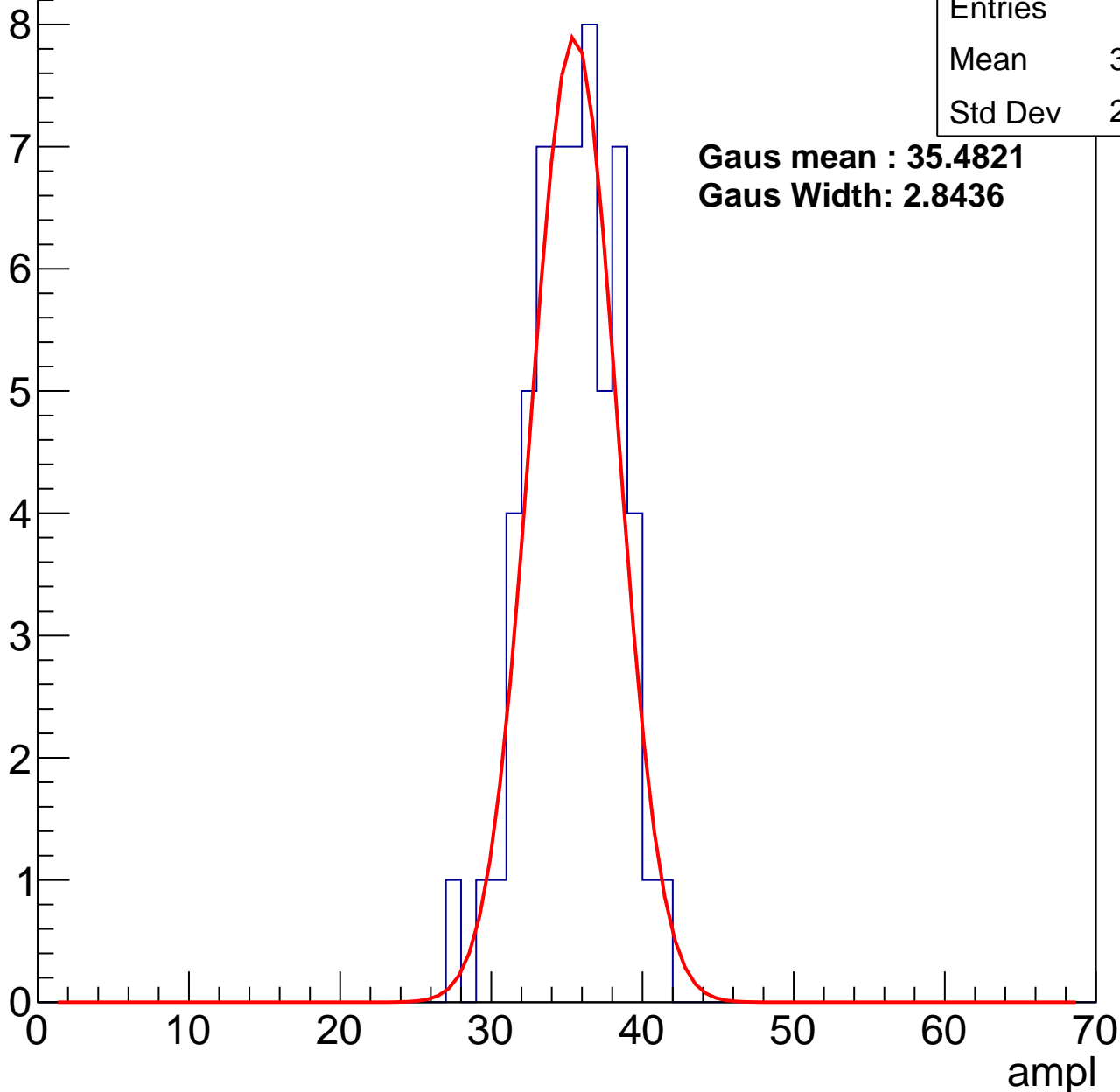
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	34.92
Std Dev	2.866

**Gaus mean : 35.4821**

**Gaus Width: 2.8436**



# B0L000S, U25-ch1, adc2

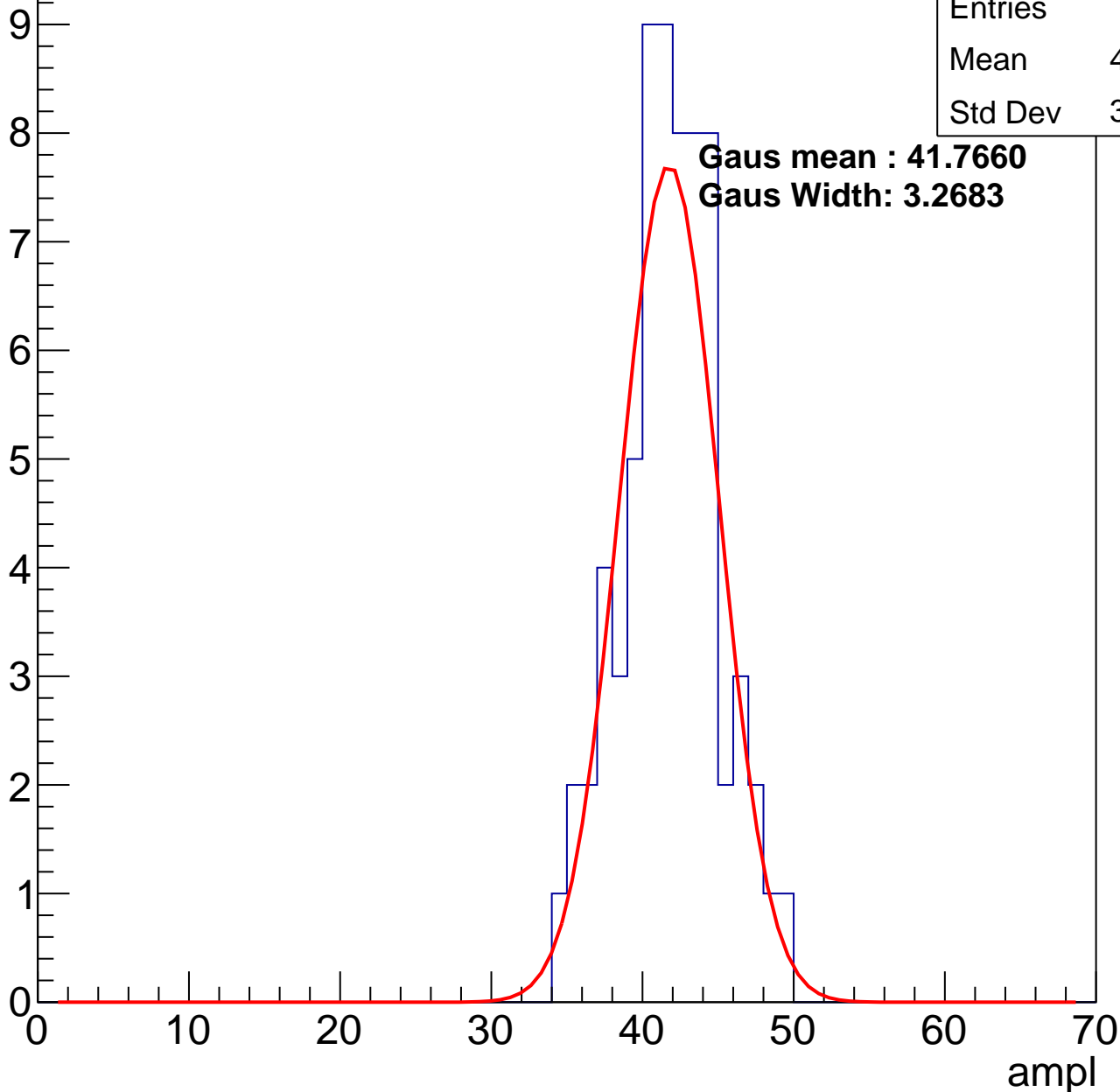
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	41.37
Std Dev	3.176

**Gaus mean : 41.7660**

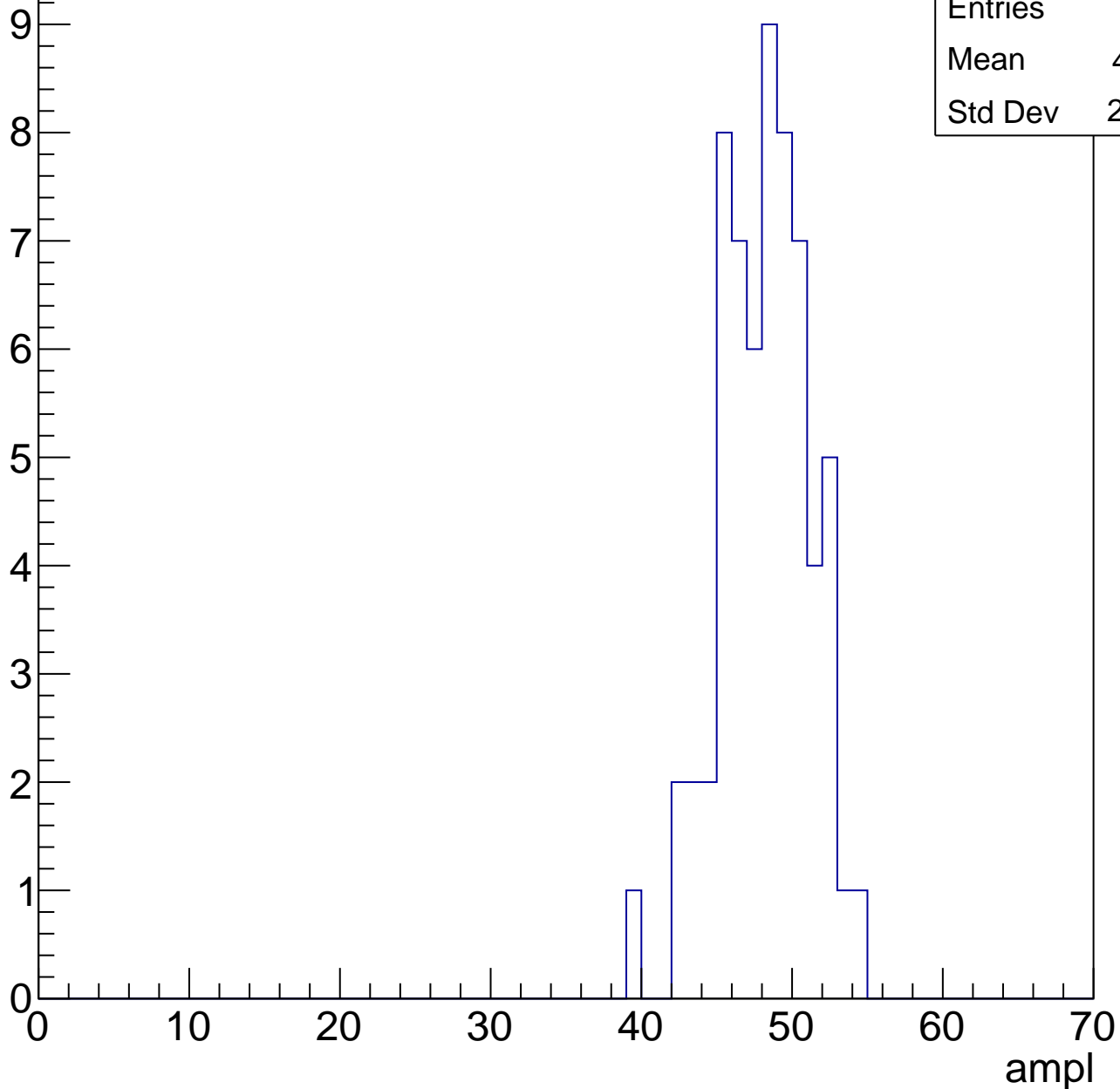
**Gaus Width: 3.2683**



# B0L000S, U25-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

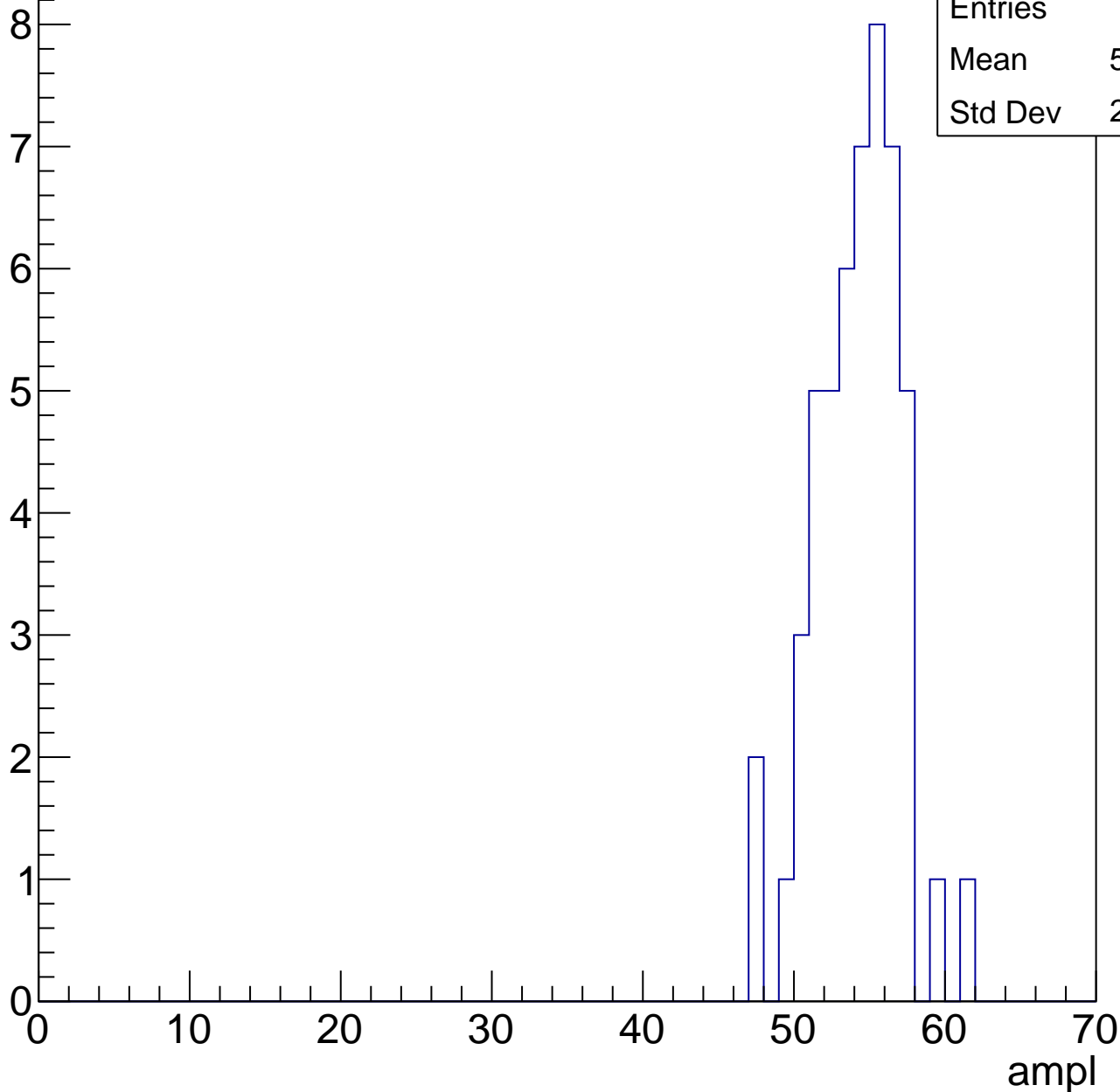


# B0L000S, U25-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	53.75
Std Dev	2.778



# B0L000S, U25-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

Entries 66

Mean 58.12

Std Dev 7.745

8

6

4

2

0

0

10

20

30

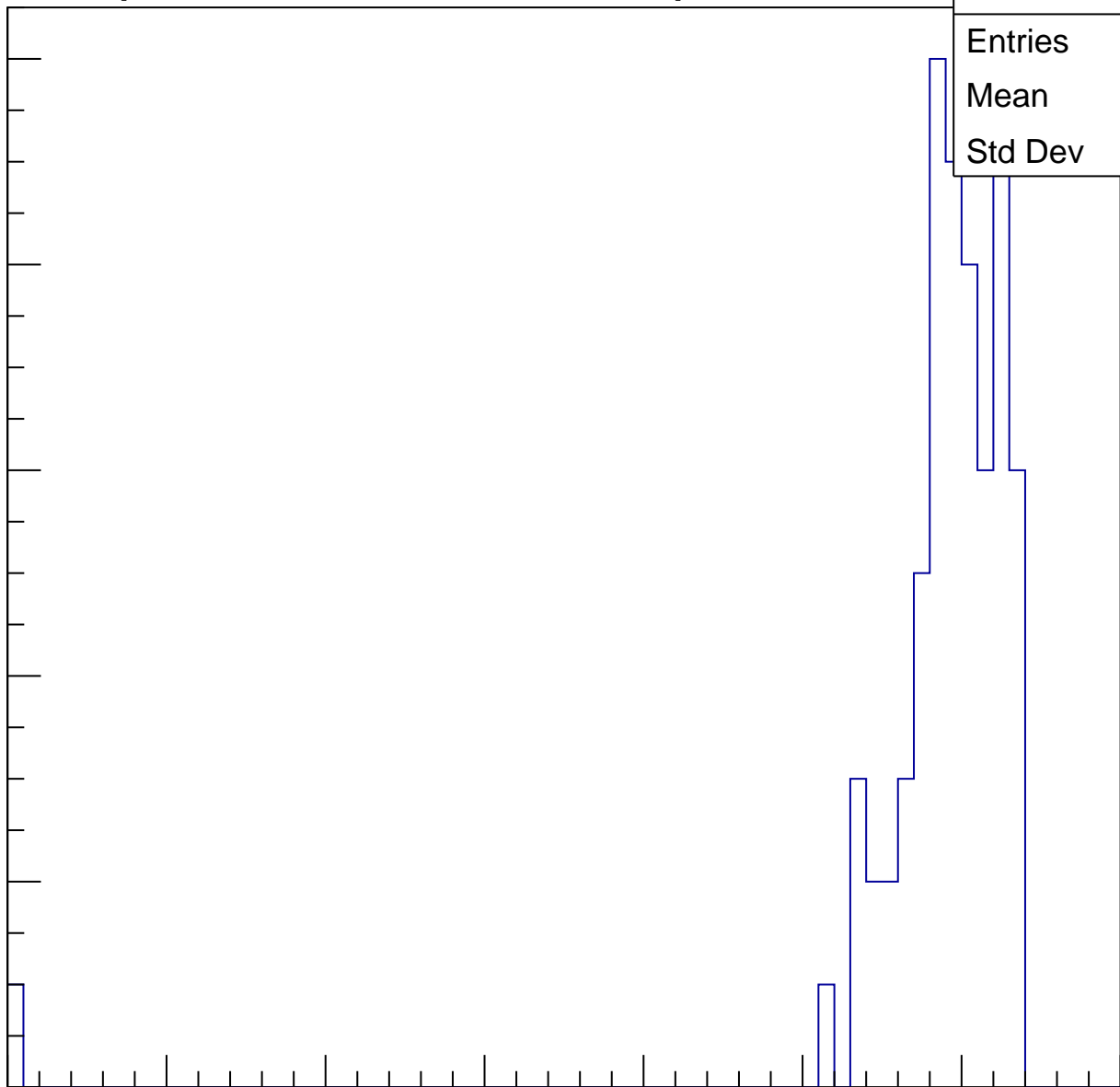
40

50

60

70

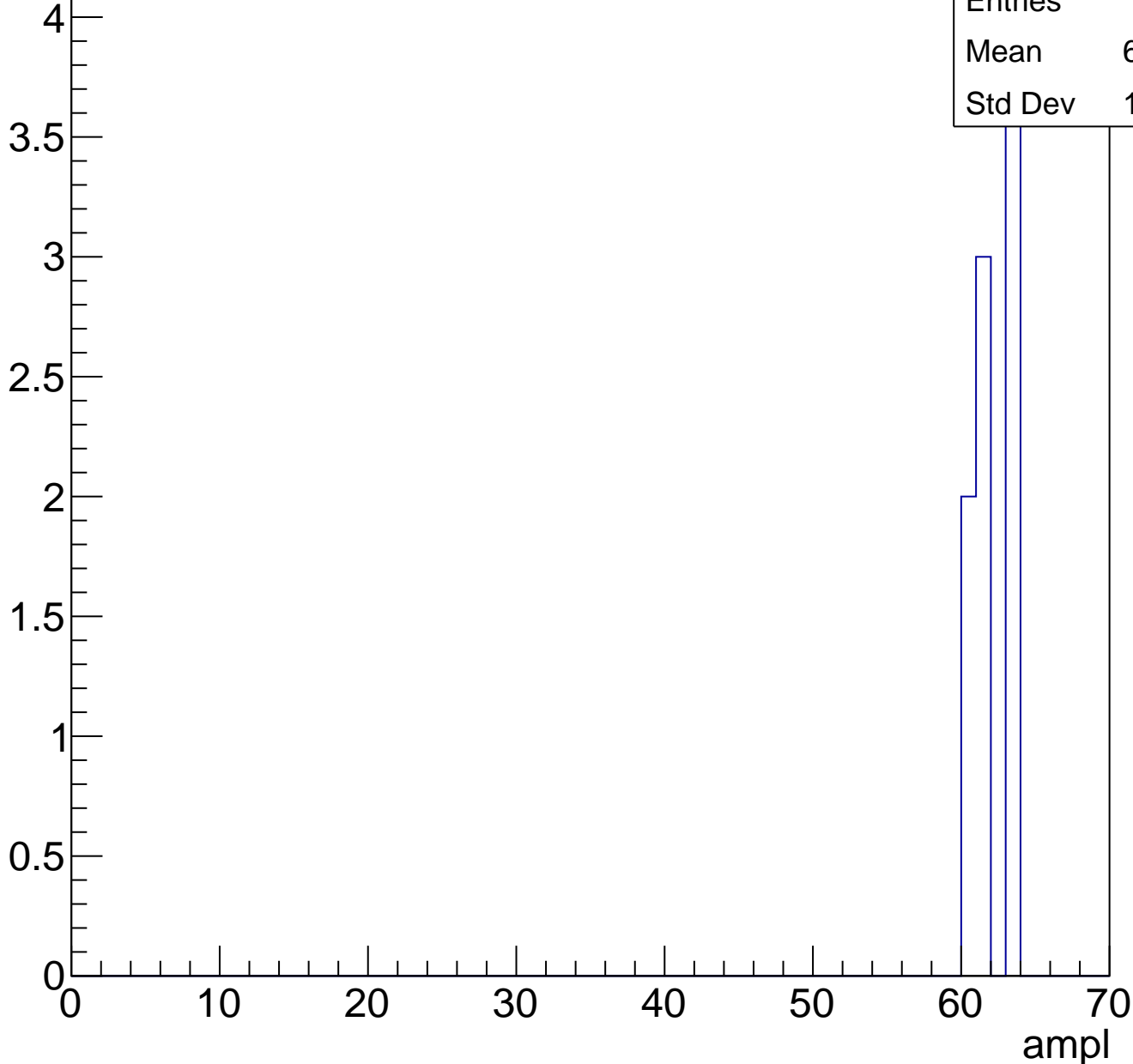
ampl



# B0L000S, U25-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L000S, U25-ch2, adc0

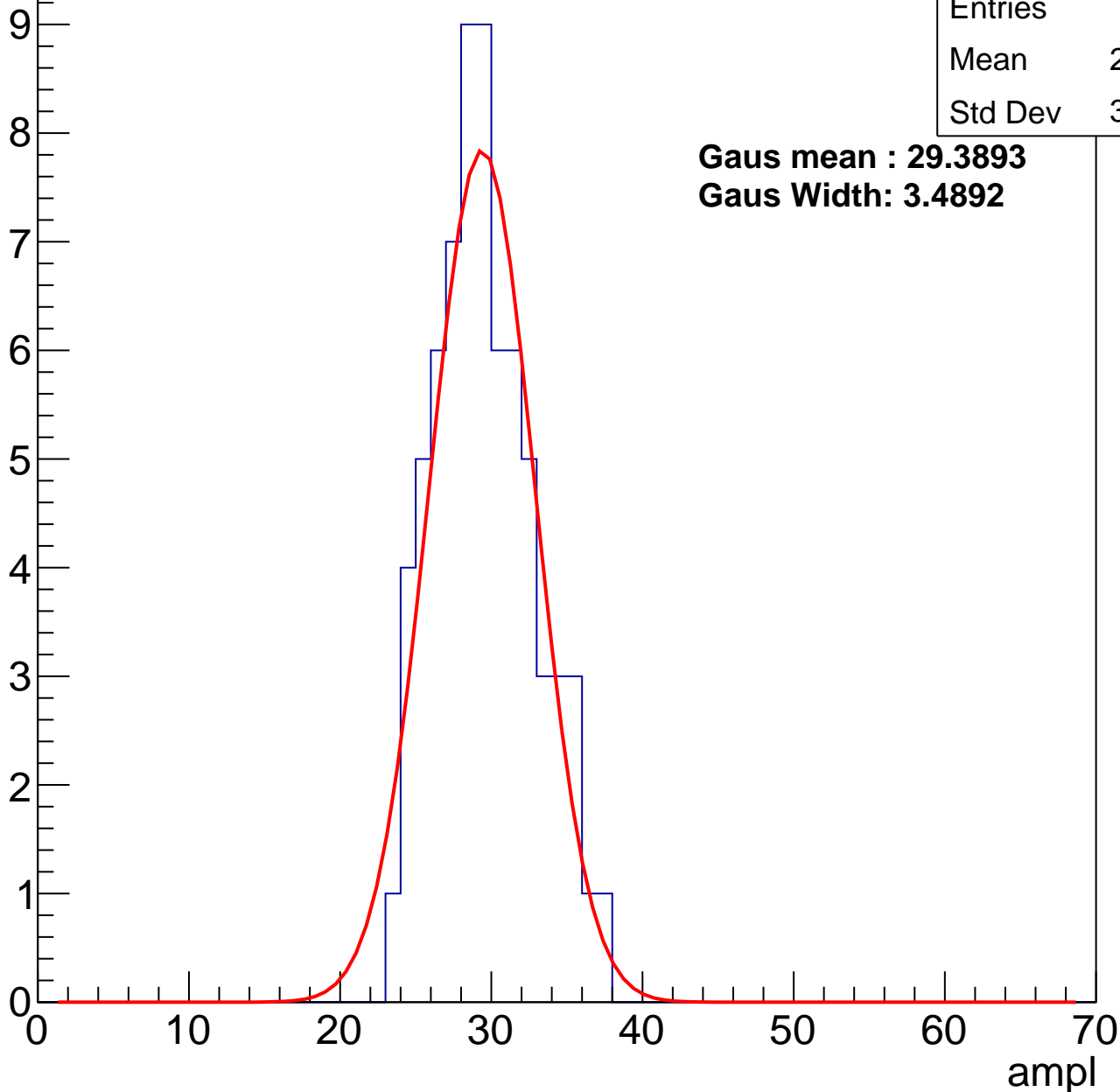
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.09
Std Dev	3.243

**Gaus mean : 29.3893**

**Gaus Width: 3.4892**



# B0L000S, U25-ch2, adc1

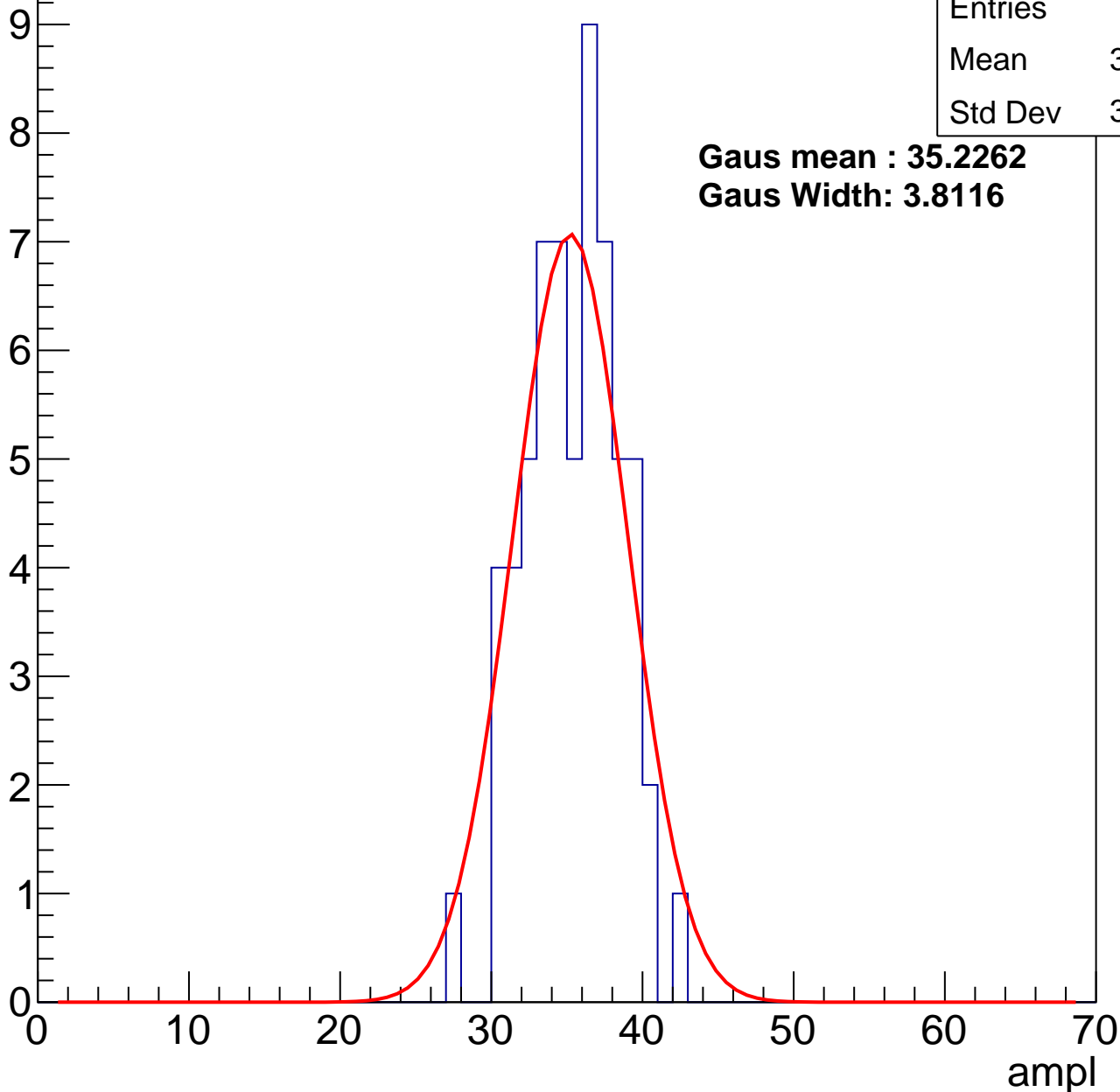
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	34.92
Std Dev	3.028

**Gaus mean : 35.2262**

**Gaus Width: 3.8116**



# B0L000S, U25-ch2, adc2

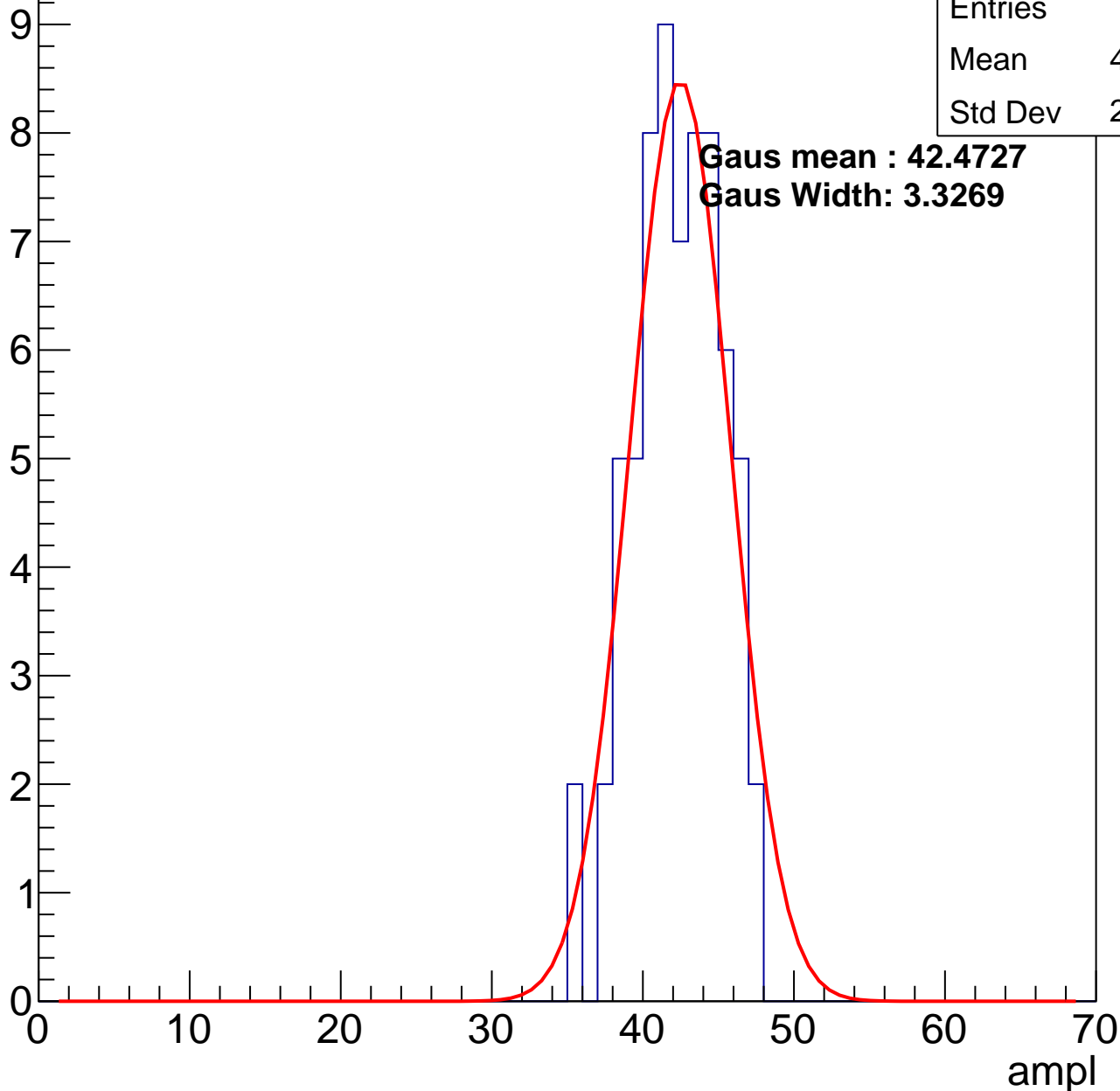
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	41.82
Std Dev	2.828

**Gaus mean : 42.4727**

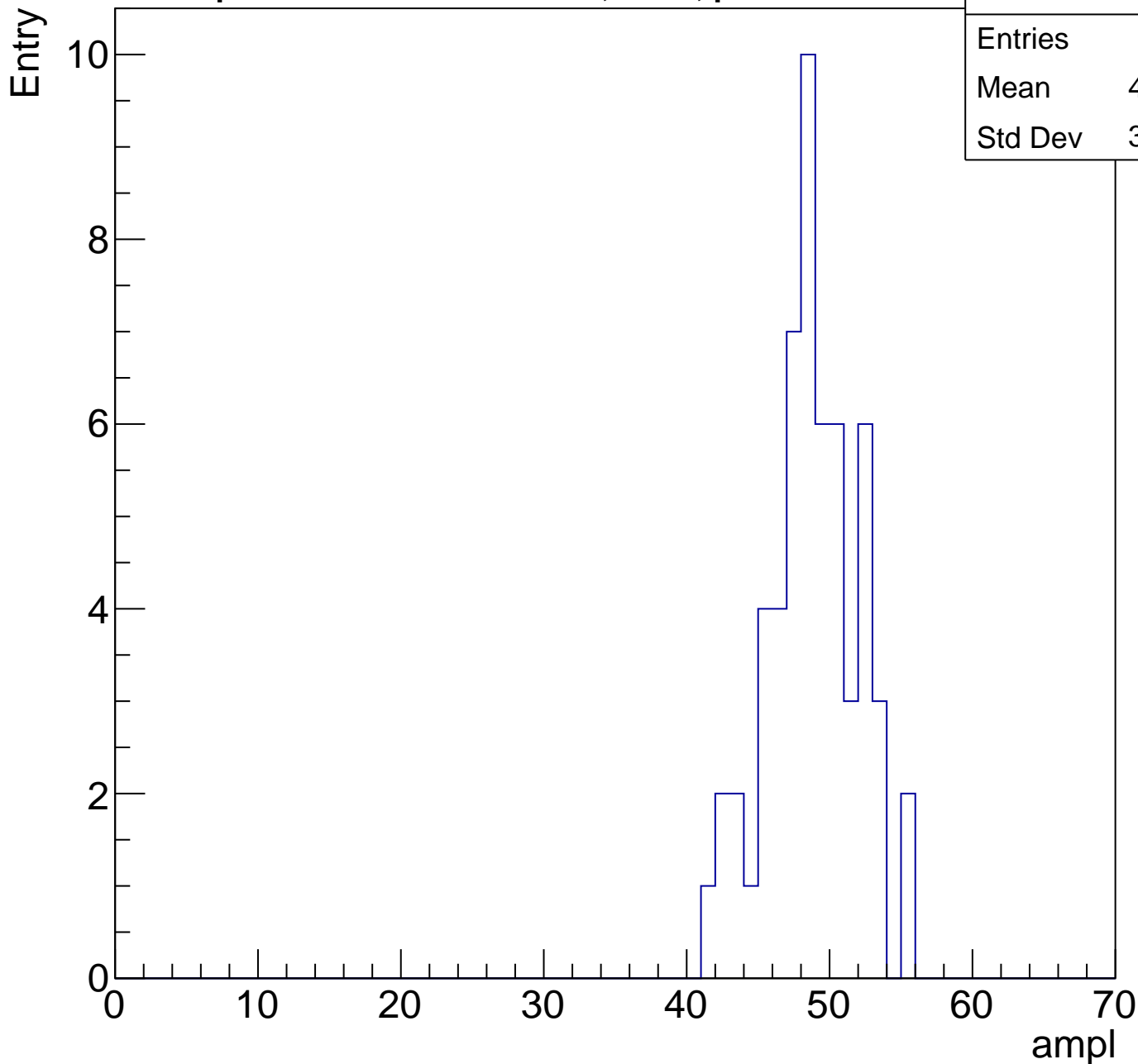
**Gaus Width: 3.3269**



# B0L000S, U25-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	48.35
Std Dev	3.148

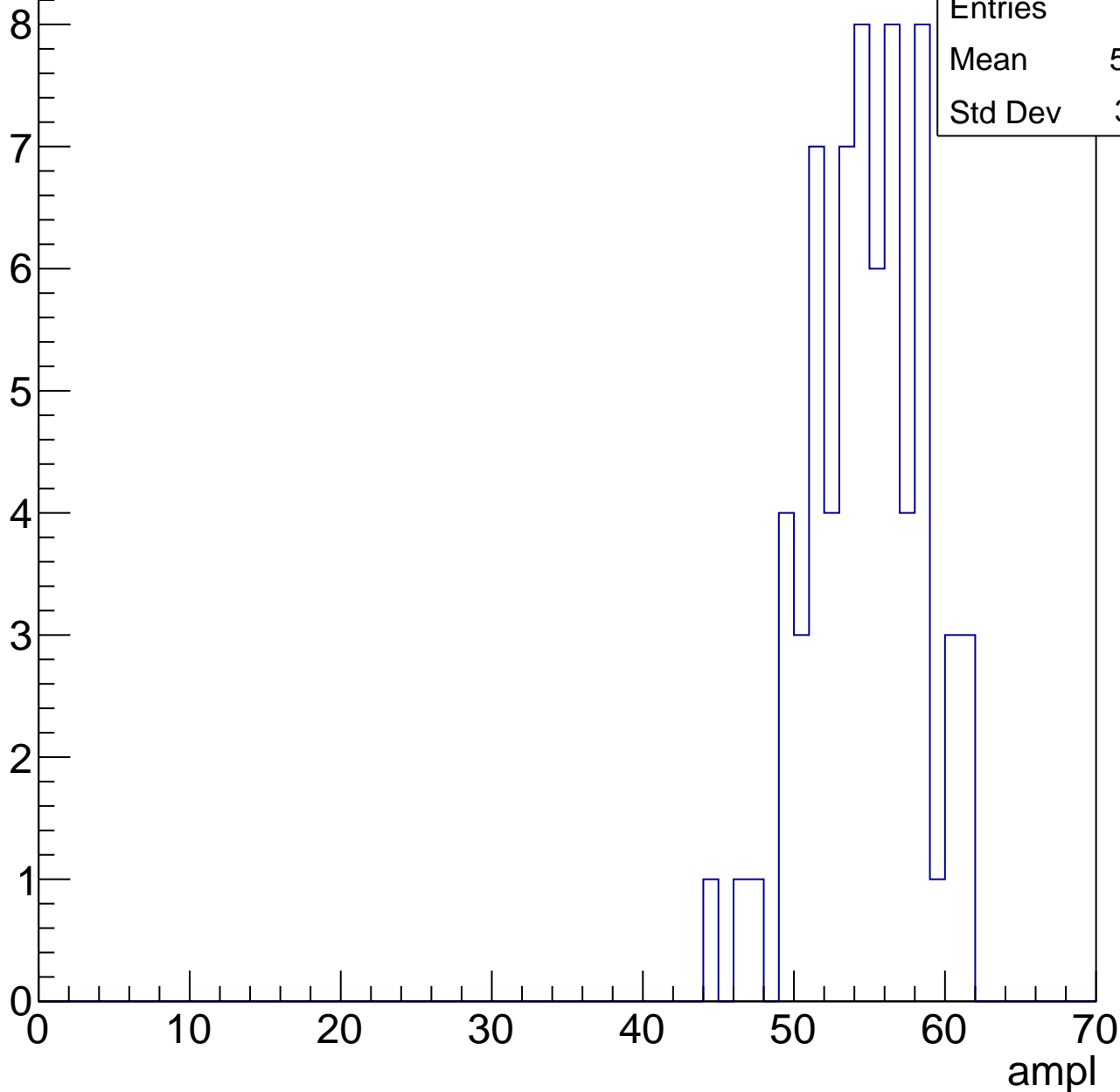


# B0L000S, U25-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

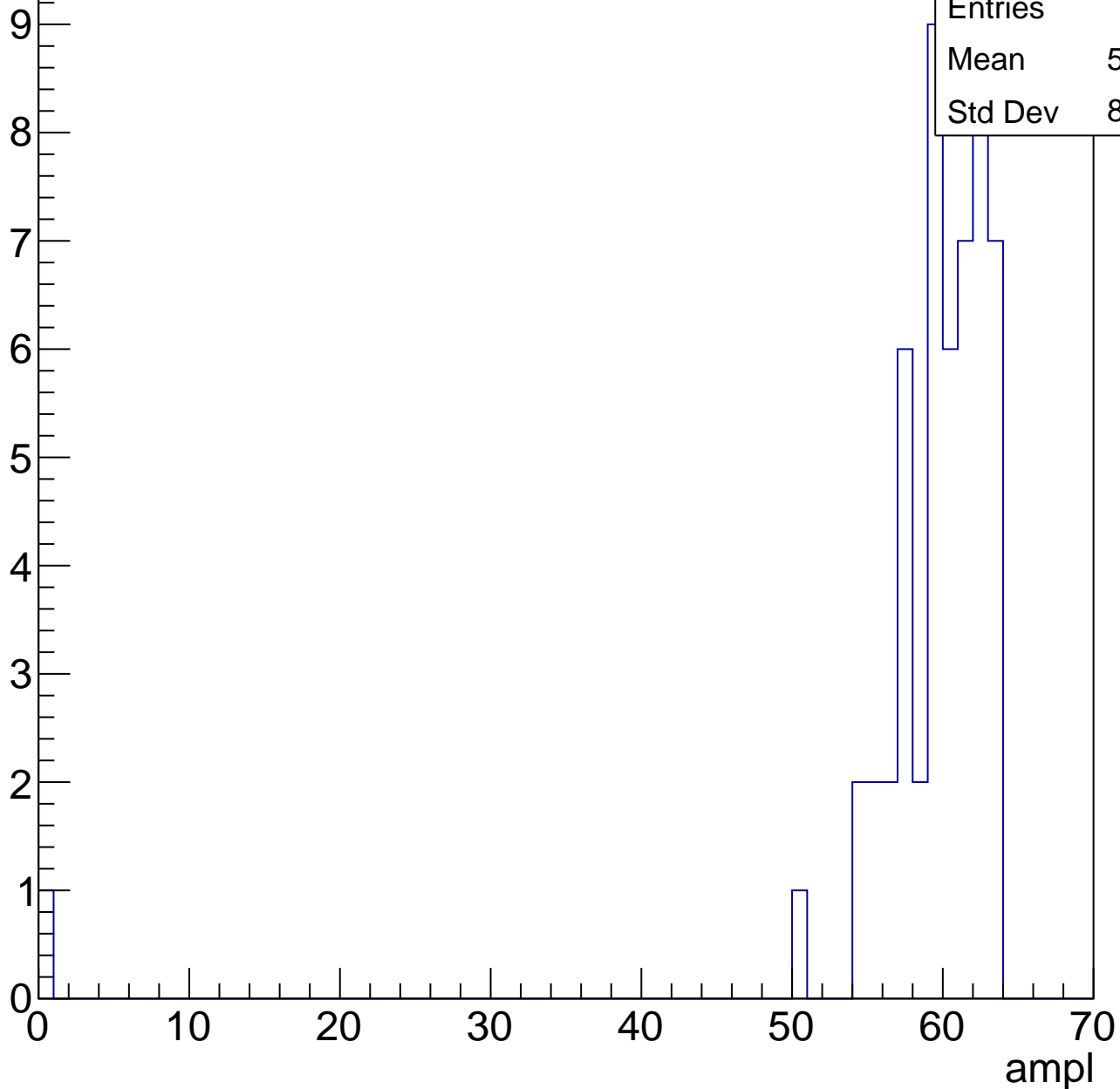
Entries	69
Mean	54.25
Std Dev	3.661



# B0L000S, U25-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

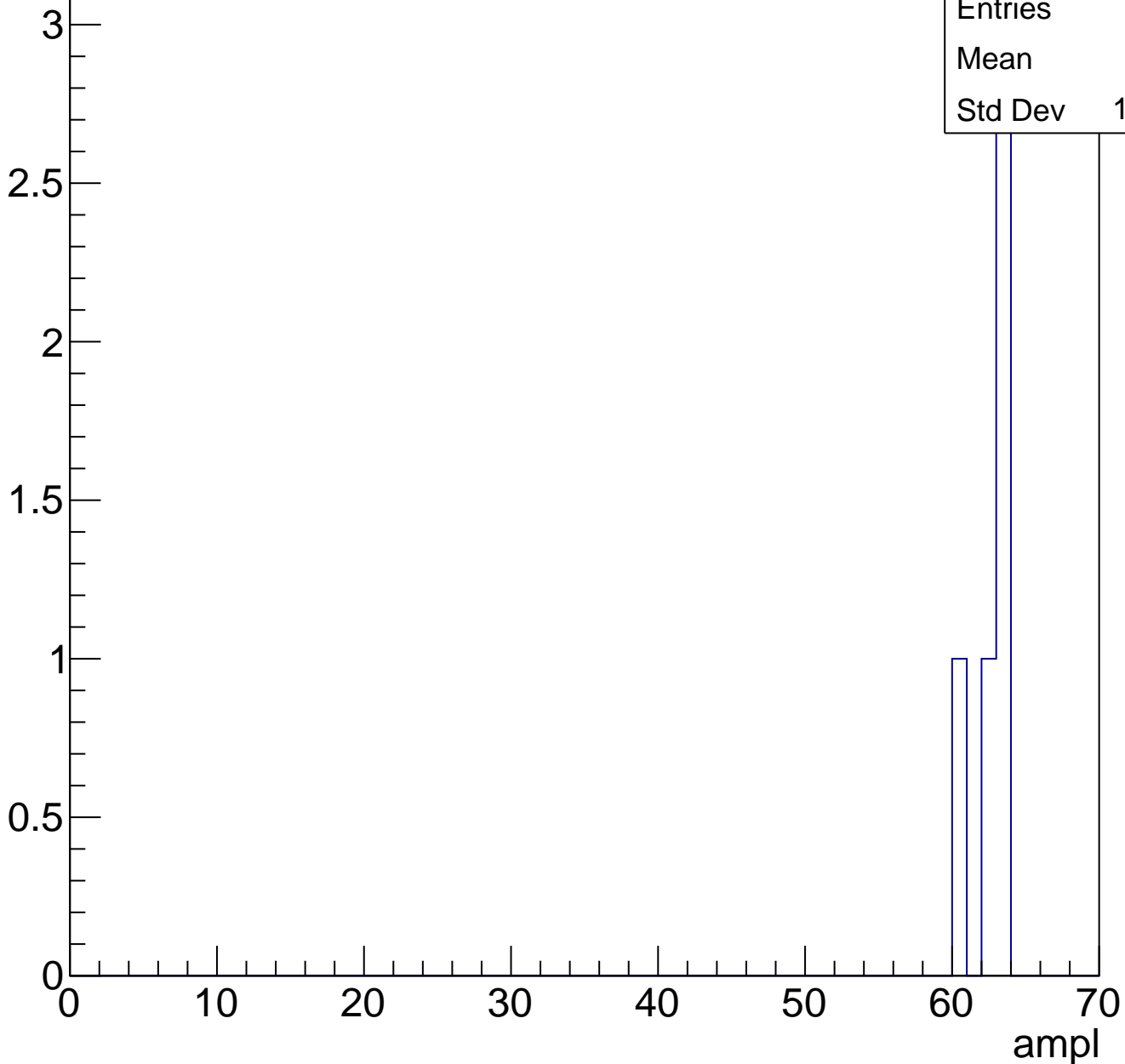
Entry



# B0L000S, U25-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch2, adc7

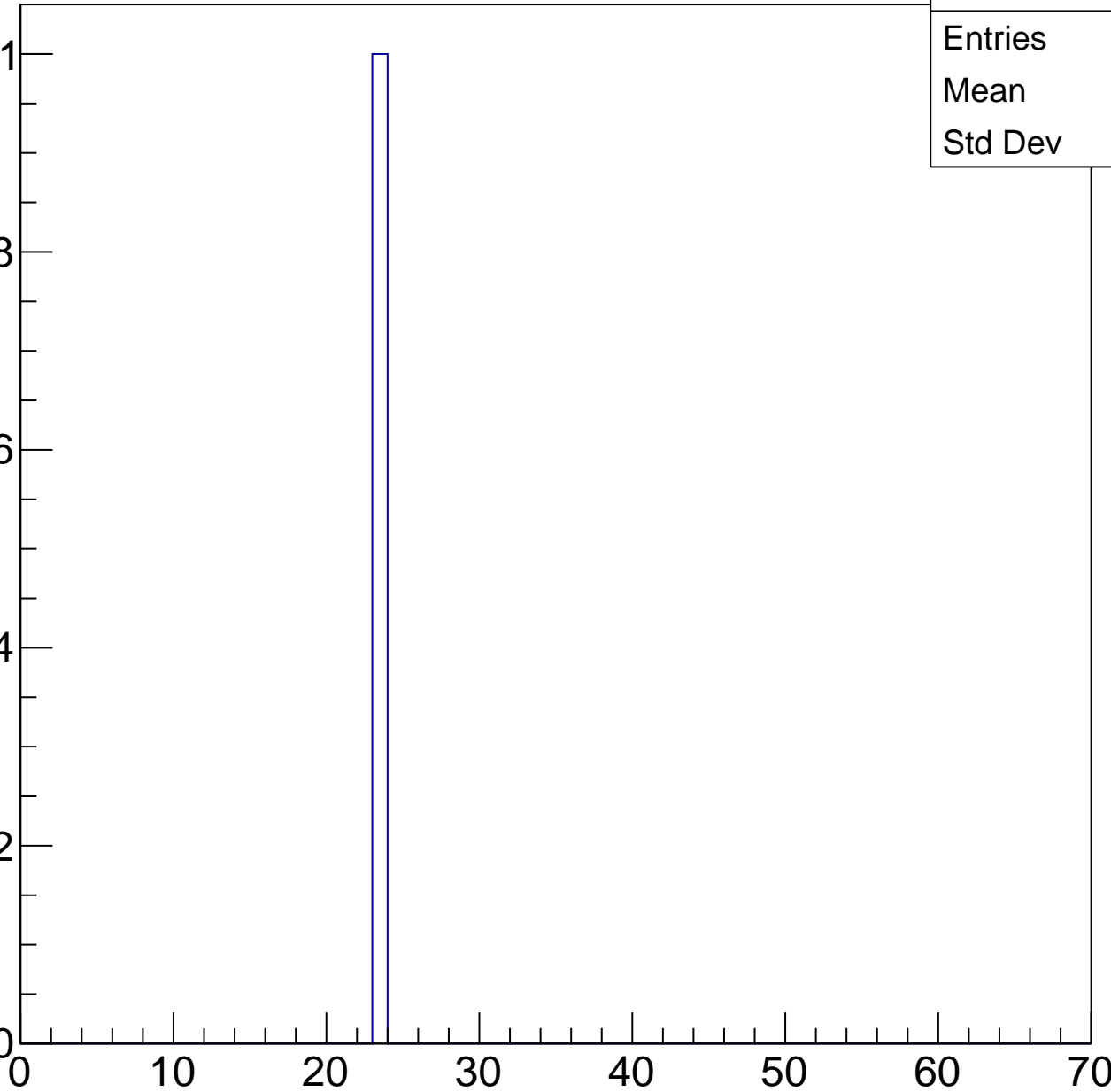
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	23
Std Dev	0

ampl



# B0L000S, U25-ch3, adc0

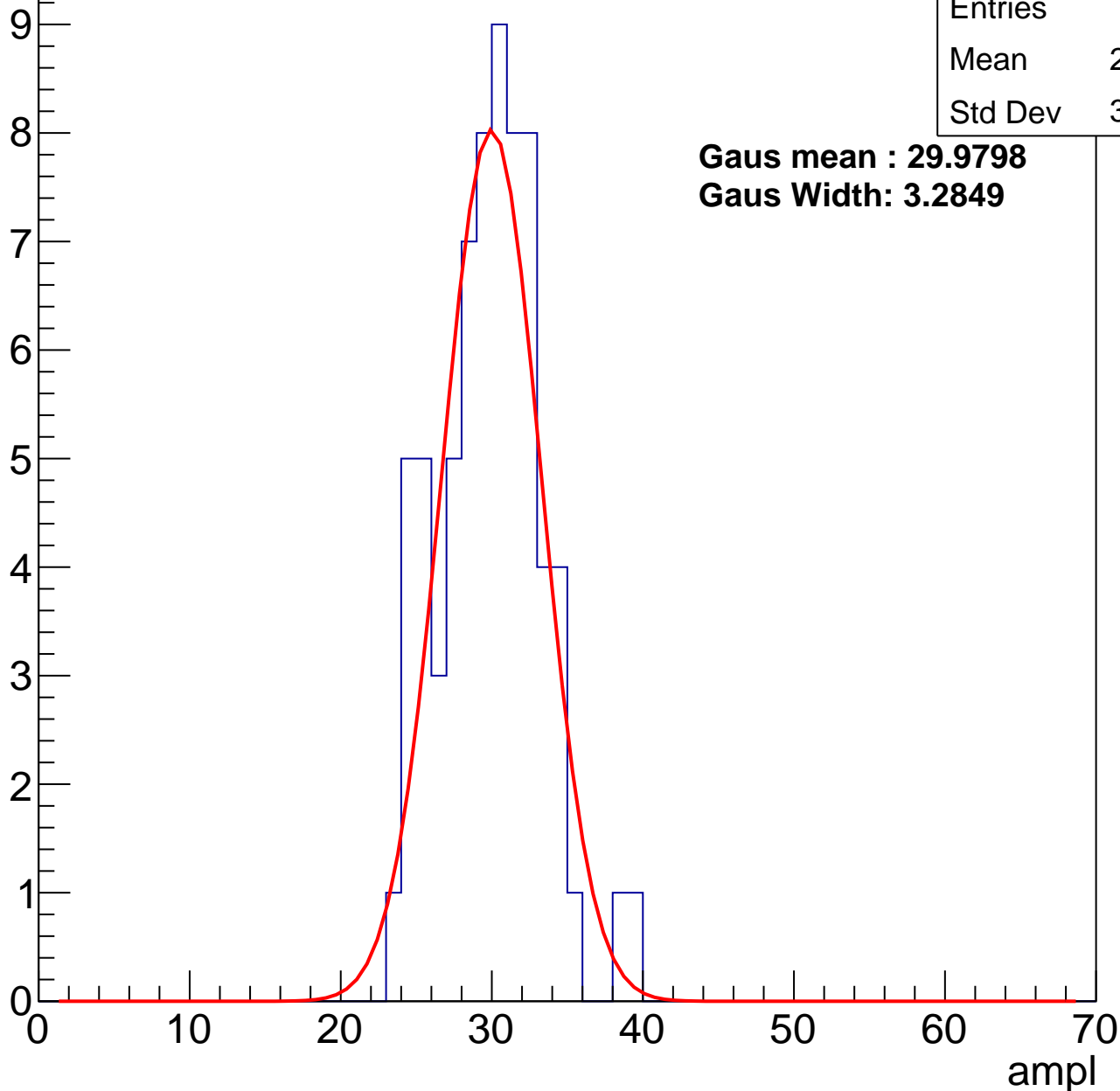
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.47
Std Dev	3.324

**Gaus mean : 29.9798**

**Gaus Width: 3.2849**



# B0L000S, U25-ch3, adc1

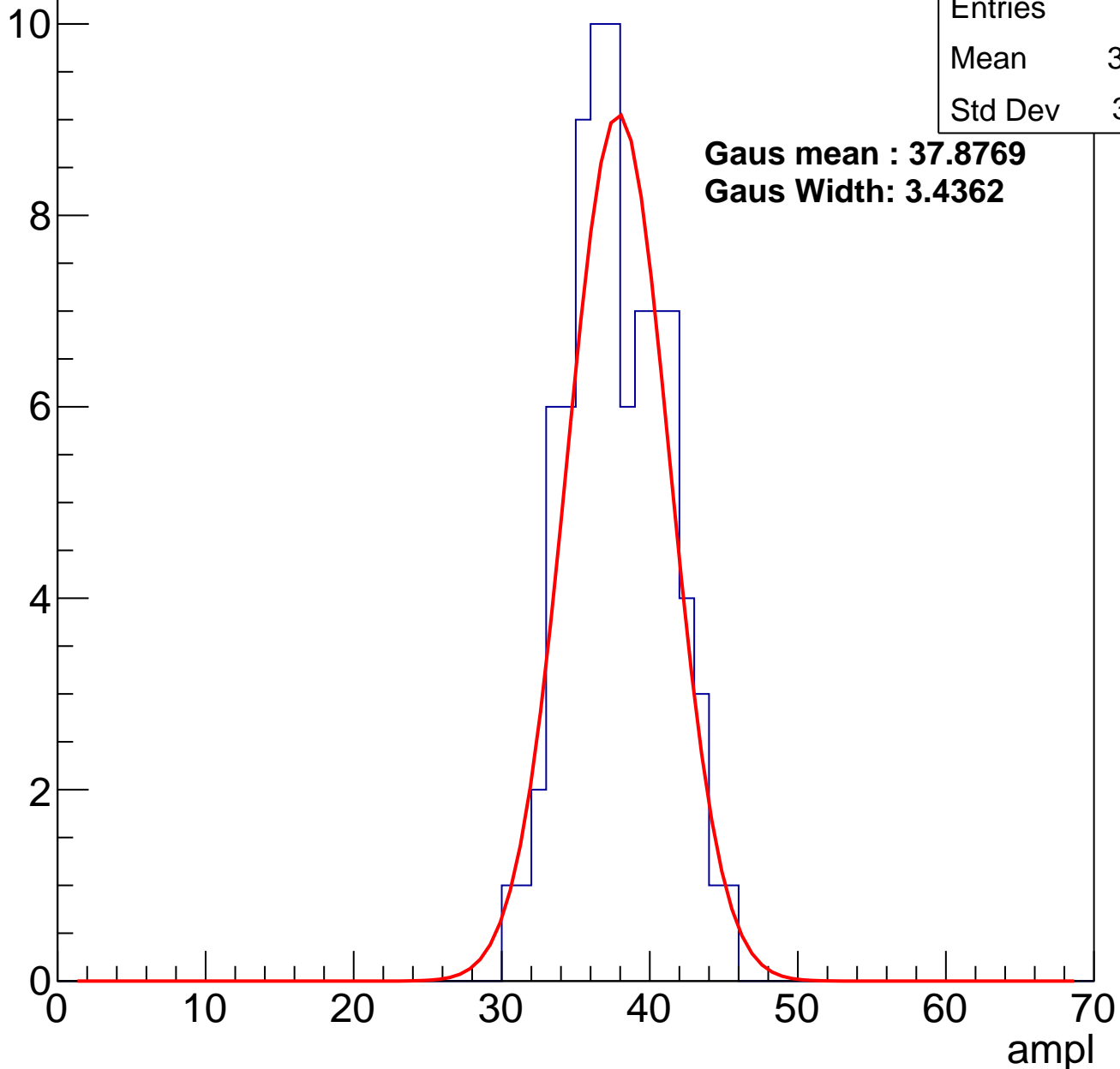
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	37.36
Std Dev	3.241

**Gaus mean : 37.8769**

**Gaus Width: 3.4362**

Entry



# B0L000S, U25-ch3, adc2

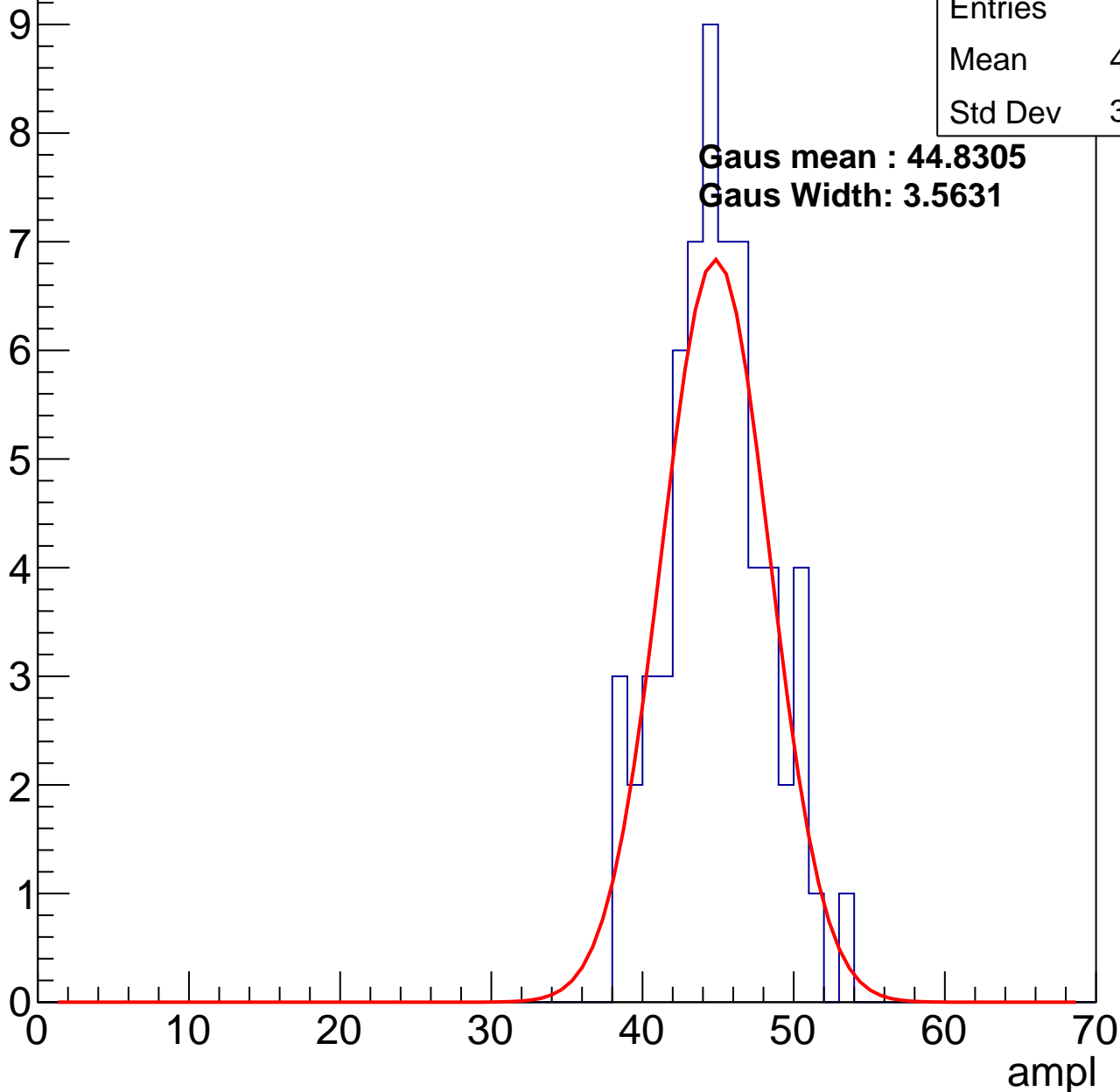
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.49
Std Dev	3.352

**Gaus mean : 44.8305**

**Gaus Width: 3.5631**

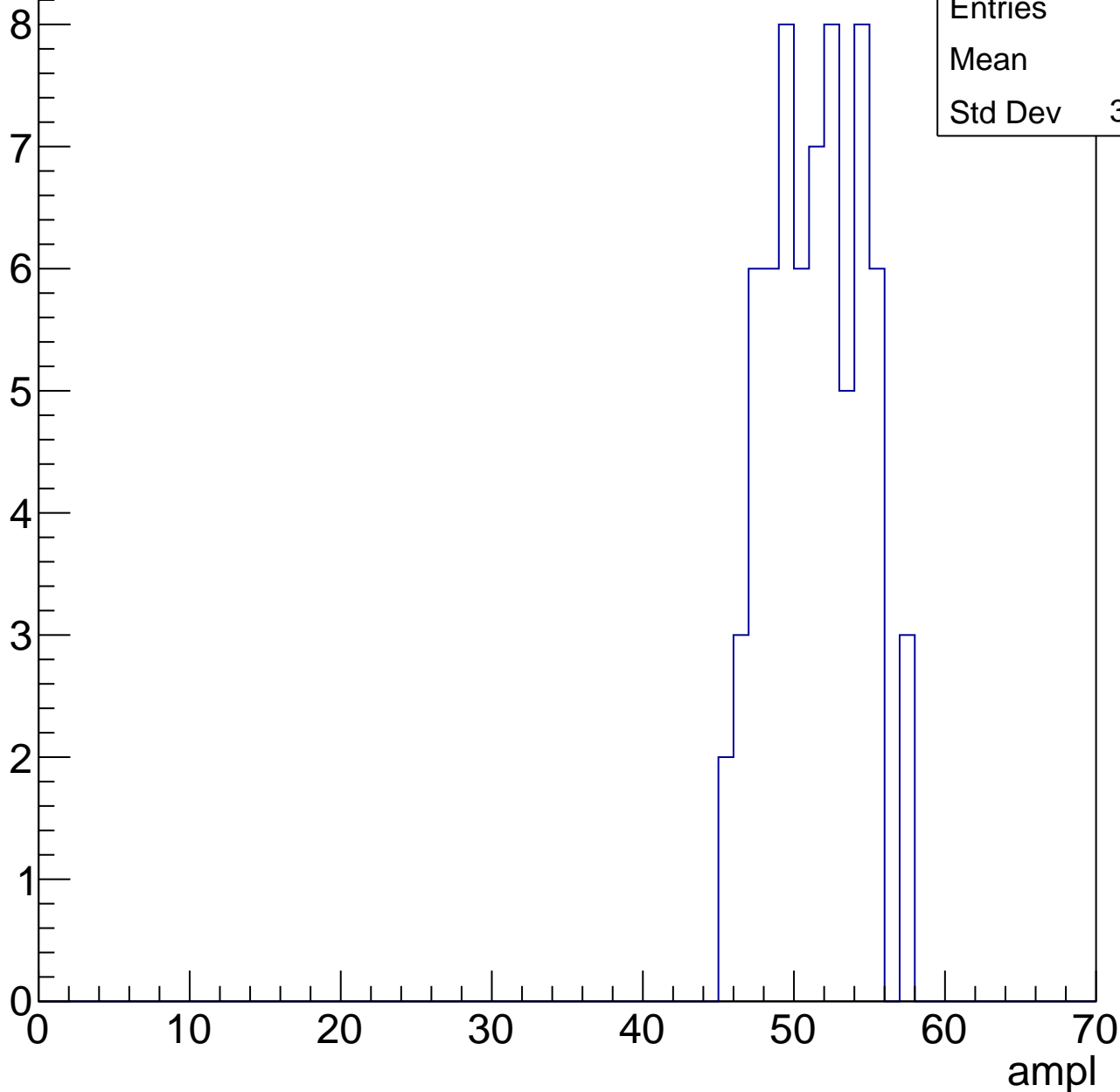


# B0L000S, U25-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.9
Std Dev	3.064

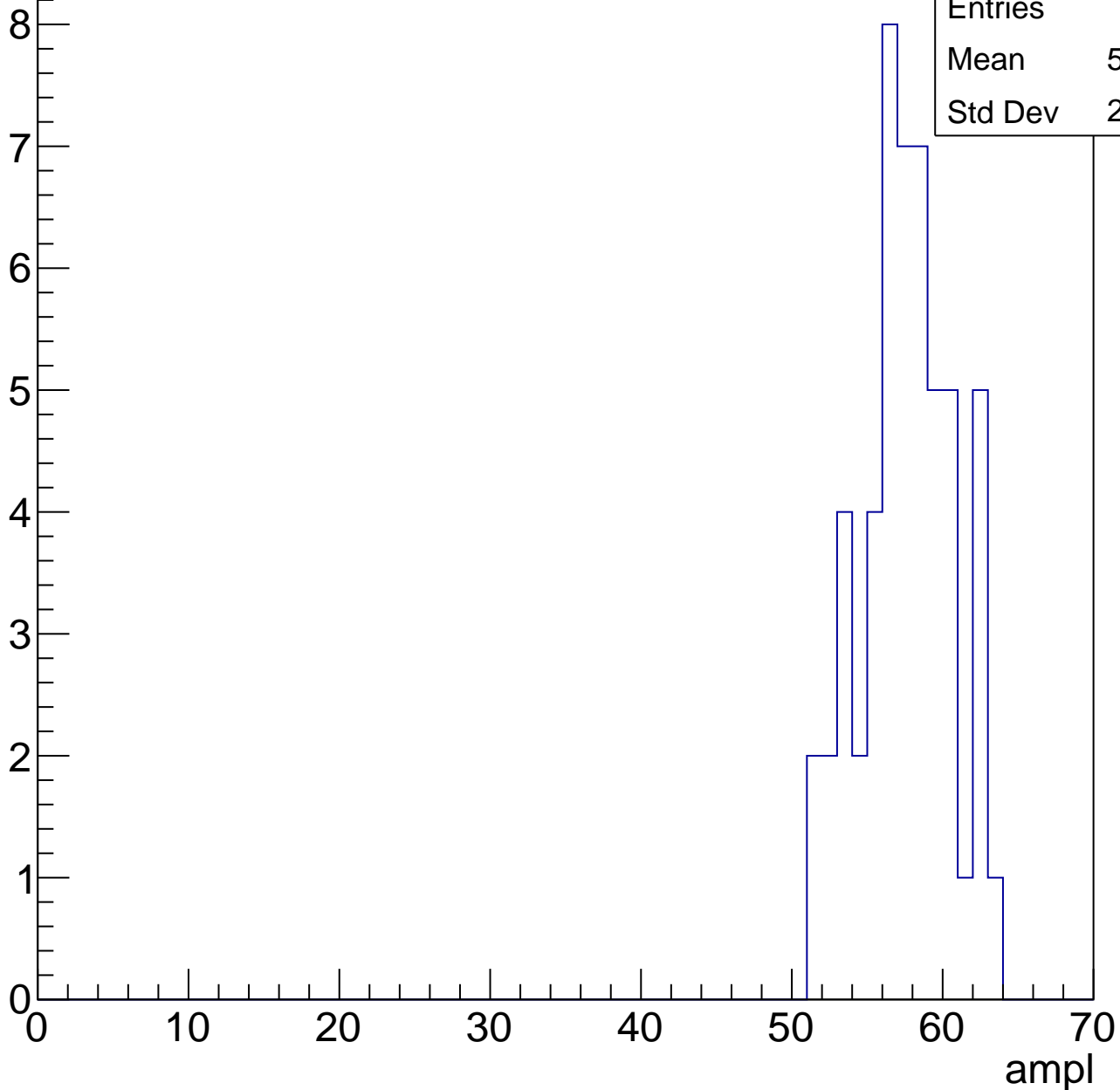


# B0L000S, U25-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.13
Std Dev	2.997

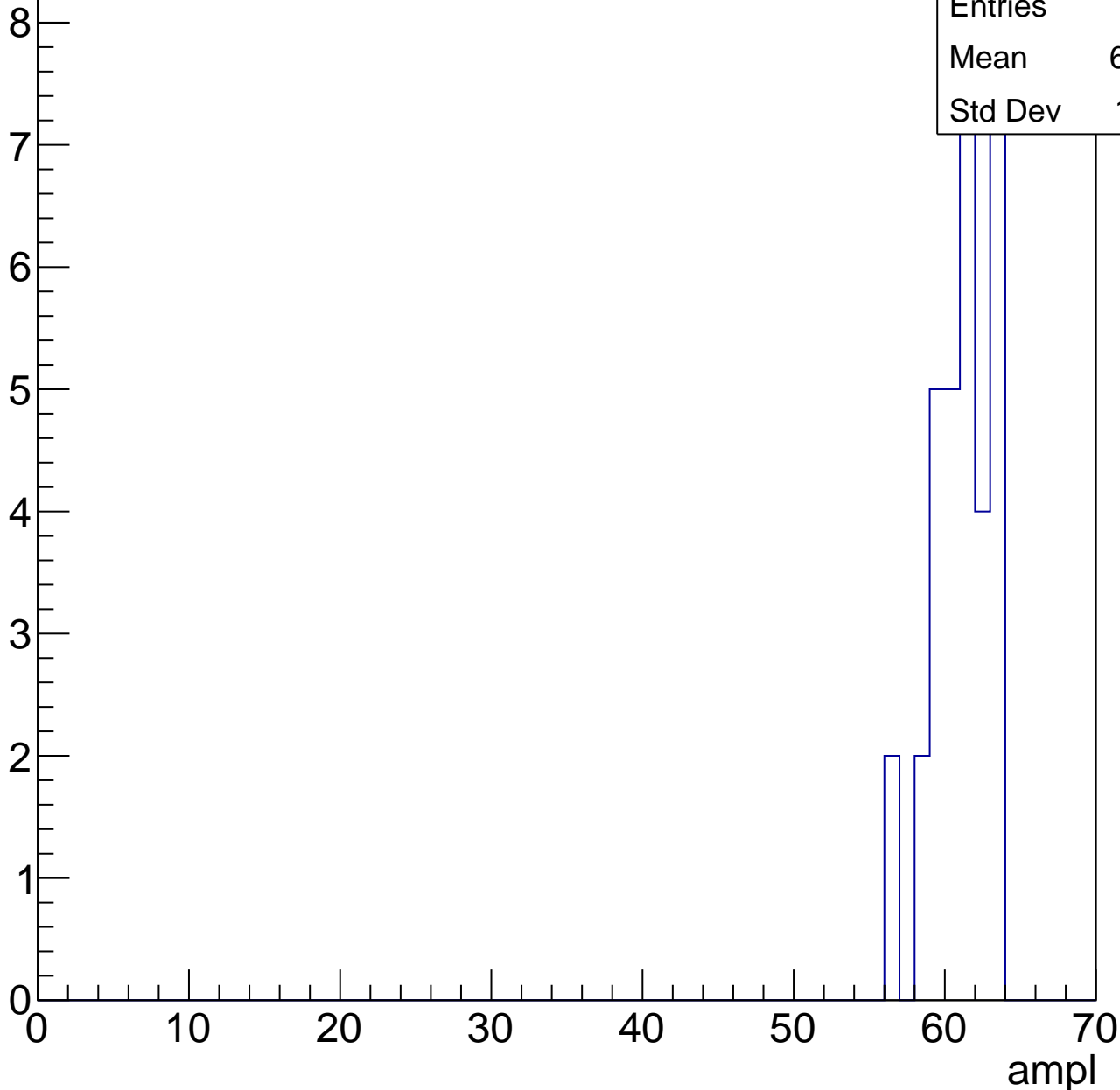


# B0L000S, U25-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

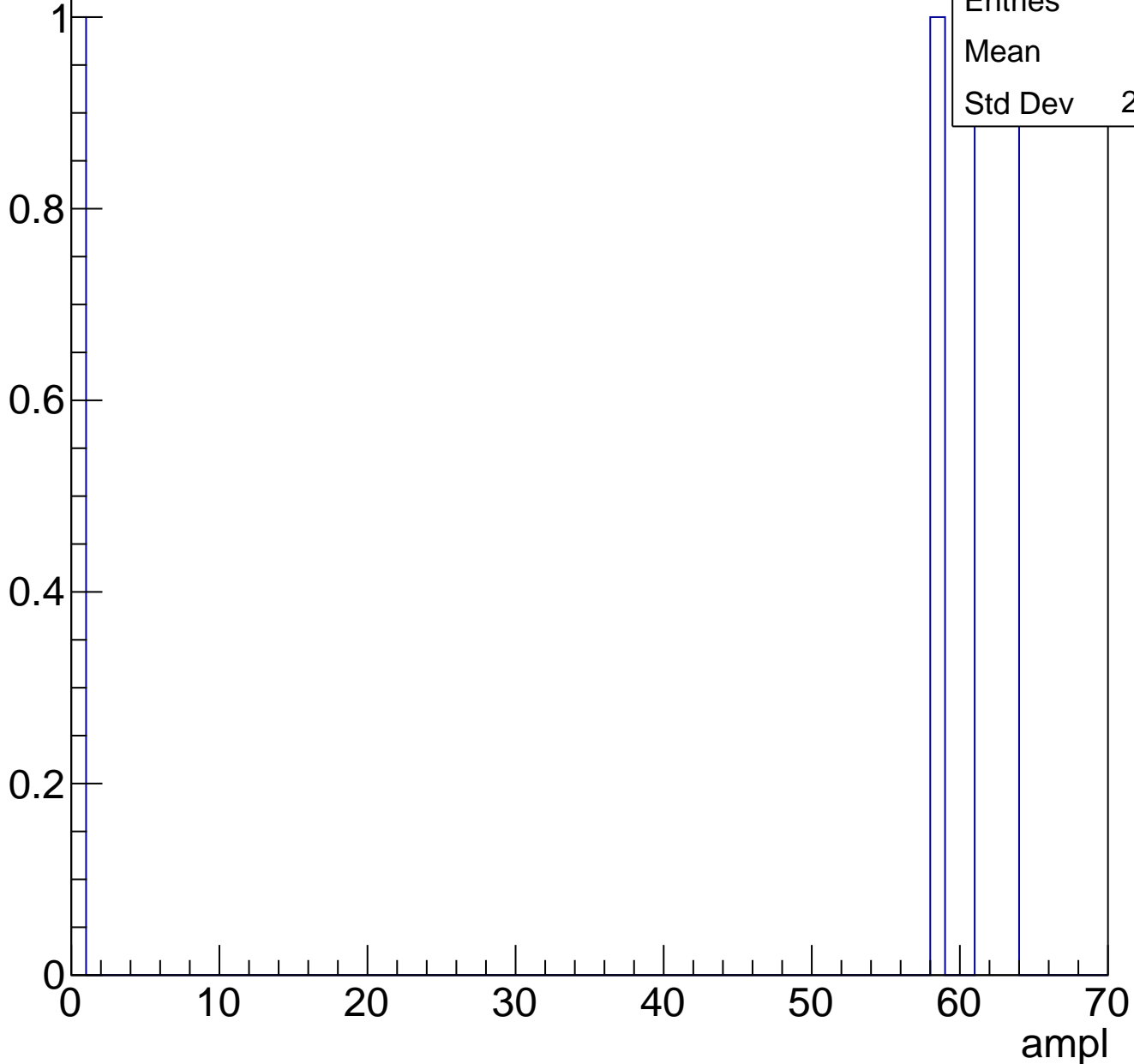
Entries	34
Mean	60.68
Std Dev	1.921



# B0L000S, U25-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch4, adc0

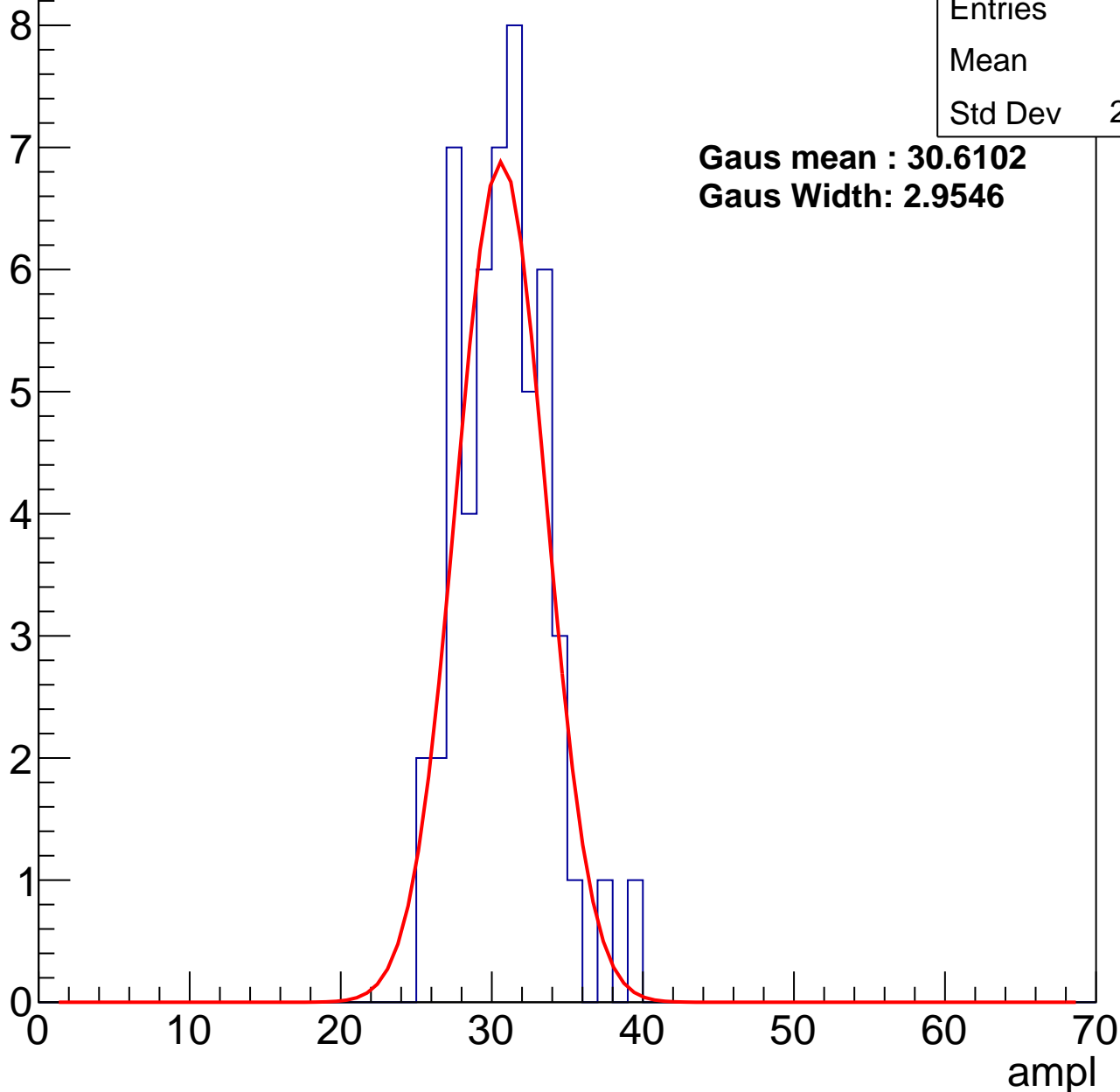
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	30.3
Std Dev	2.905

**Gaus mean : 30.6102**

**Gaus Width: 2.9546**



# B0L000S, U25-ch4, adc1

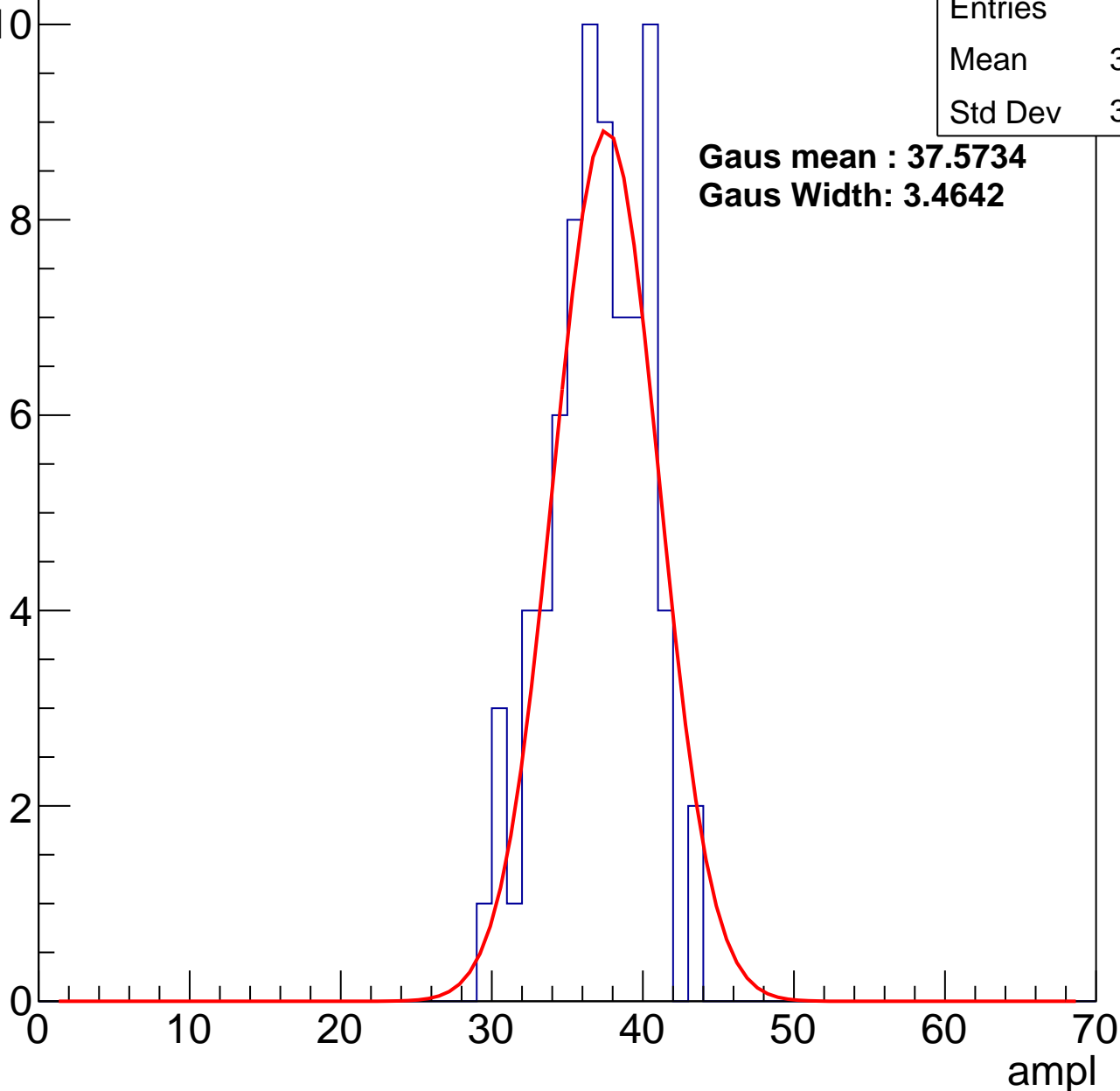
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	36.53
Std Dev	3.143

**Gaus mean : 37.5734**

**Gaus Width: 3.4642**



# B0L000S, U25-ch4, adc2

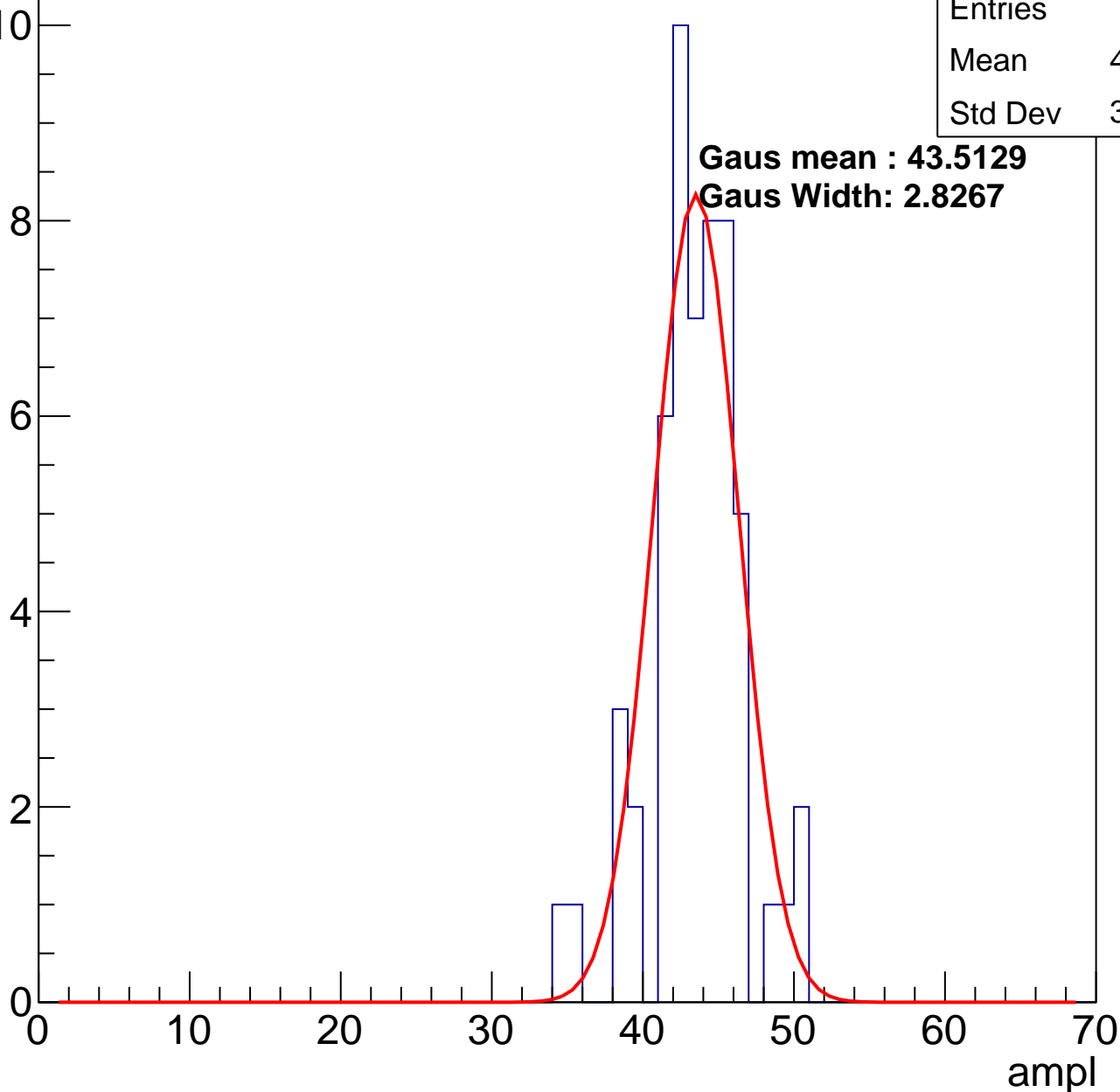
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	43.04
Std Dev	3.104

**Gaus mean : 43.5129**

**Gaus Width: 2.8267**



# B0L000S, U25-ch4, adc3

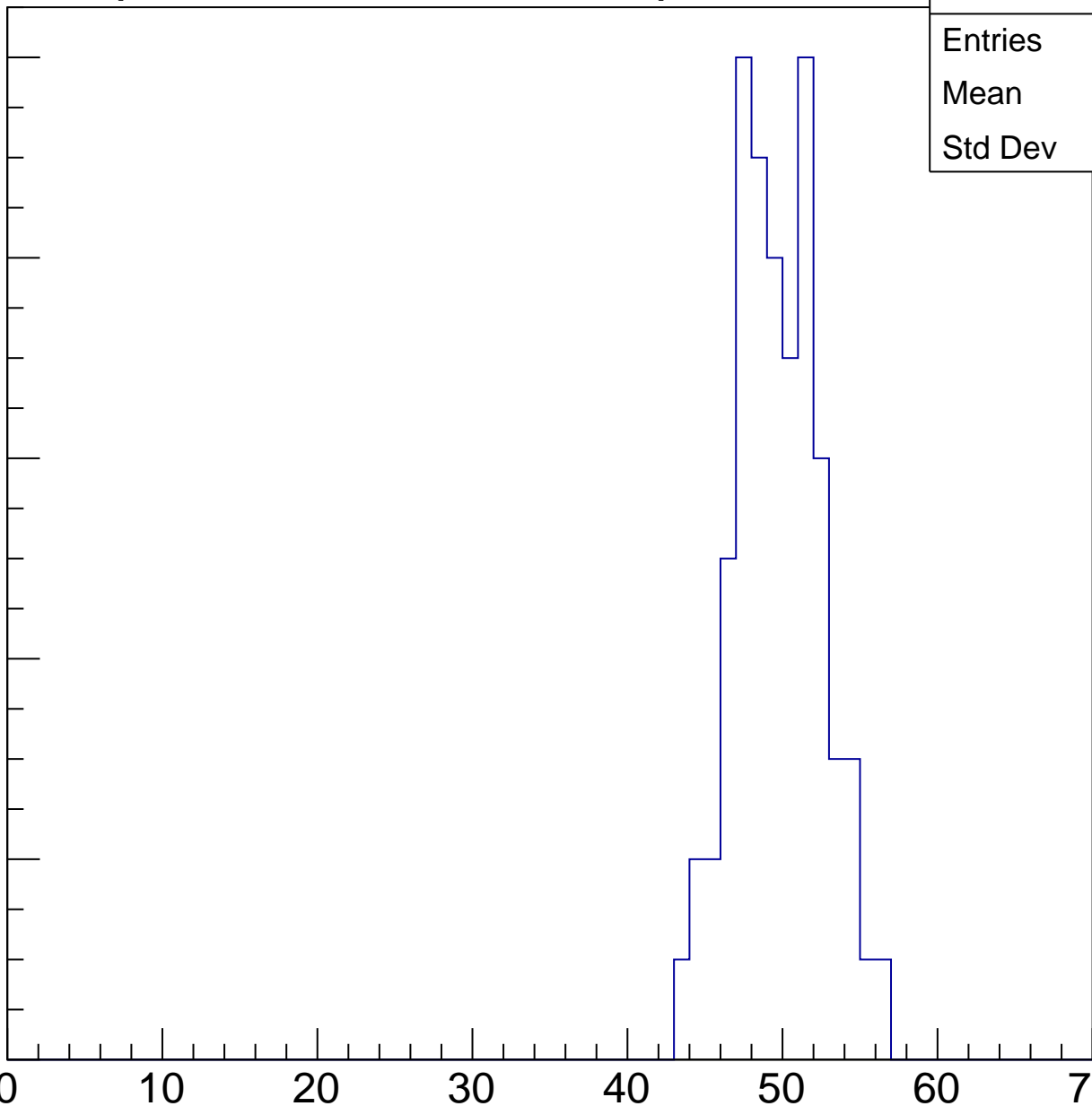
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10  
8  
6  
4  
2  
0

Entries	68
Mean	49.25
Std Dev	2.757

ampl

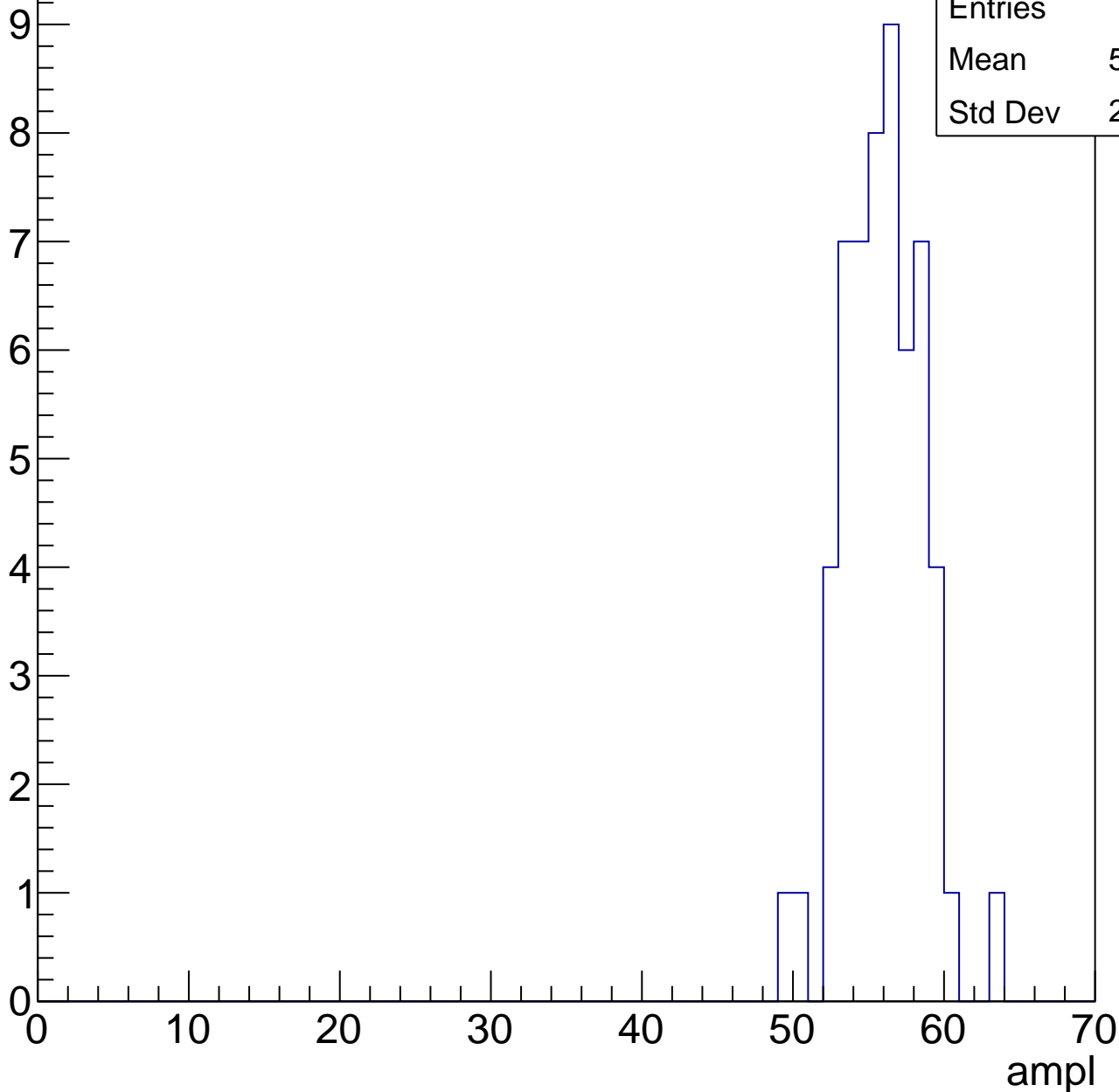


# B0L000S, U25-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.48
Std Dev	2.563

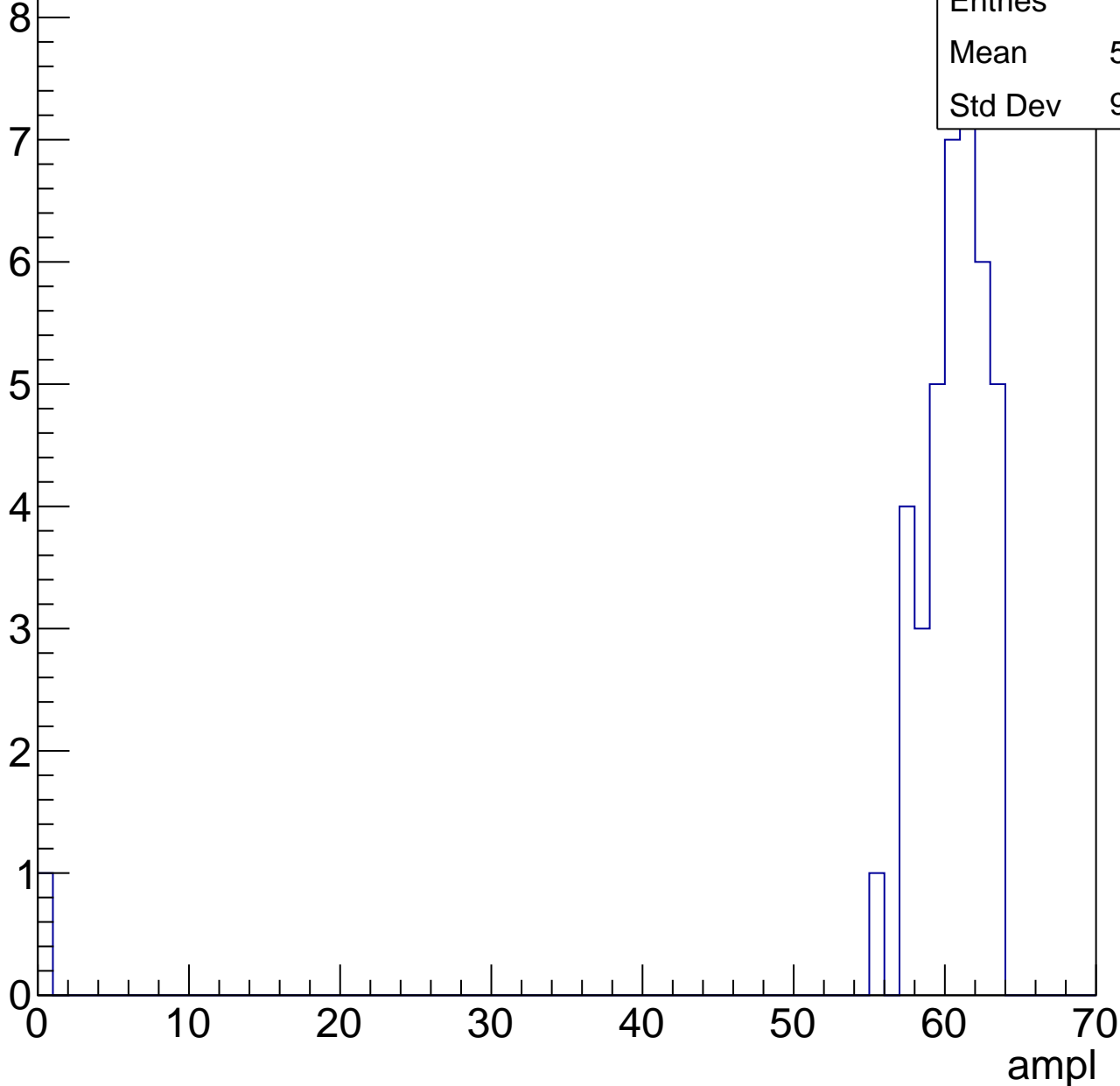


# B0L000S, U25-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

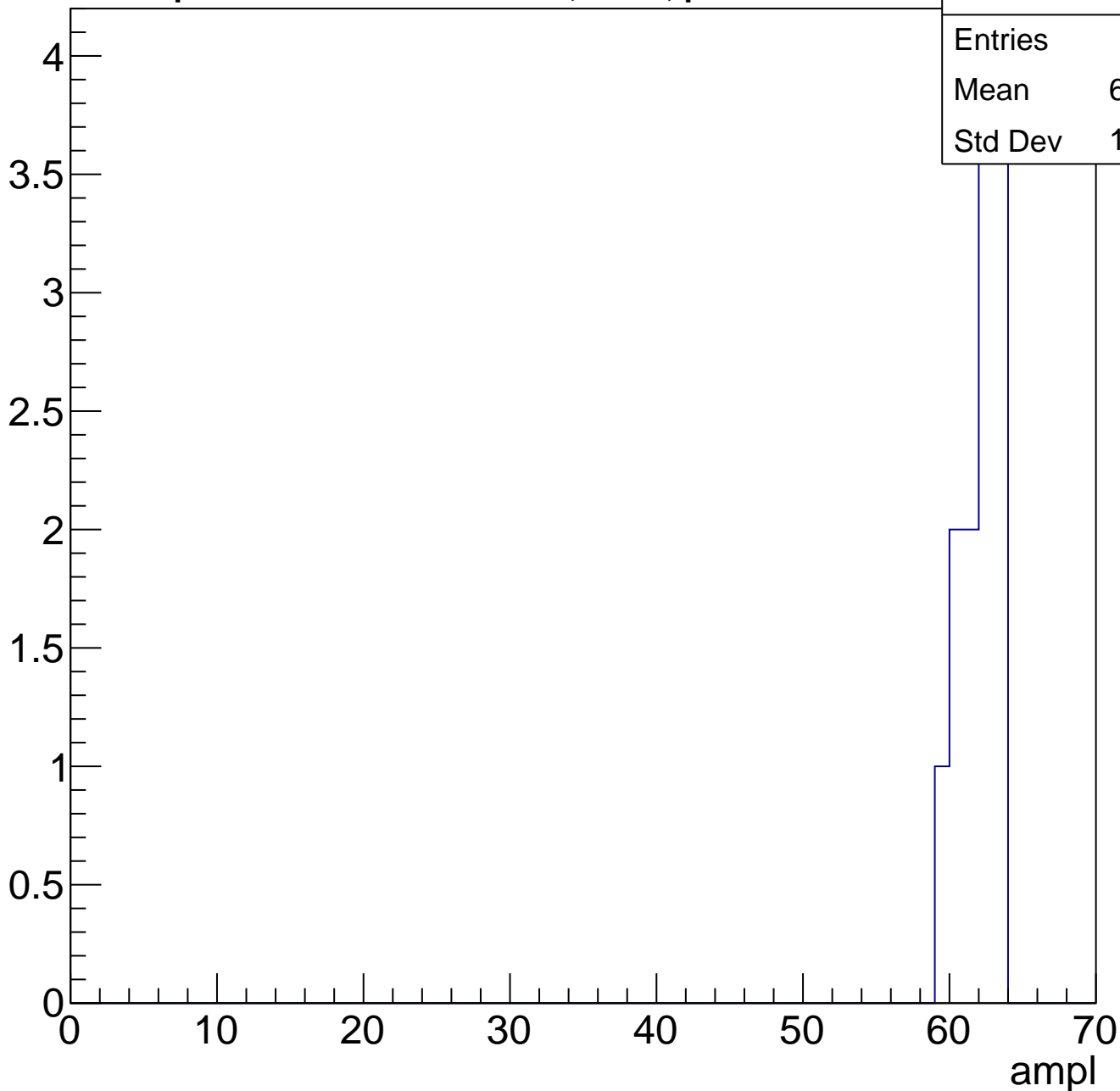
Entries	40
Mean	58.67
Std Dev	9.598



# B0L000S, U25-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U25-ch5, adc0

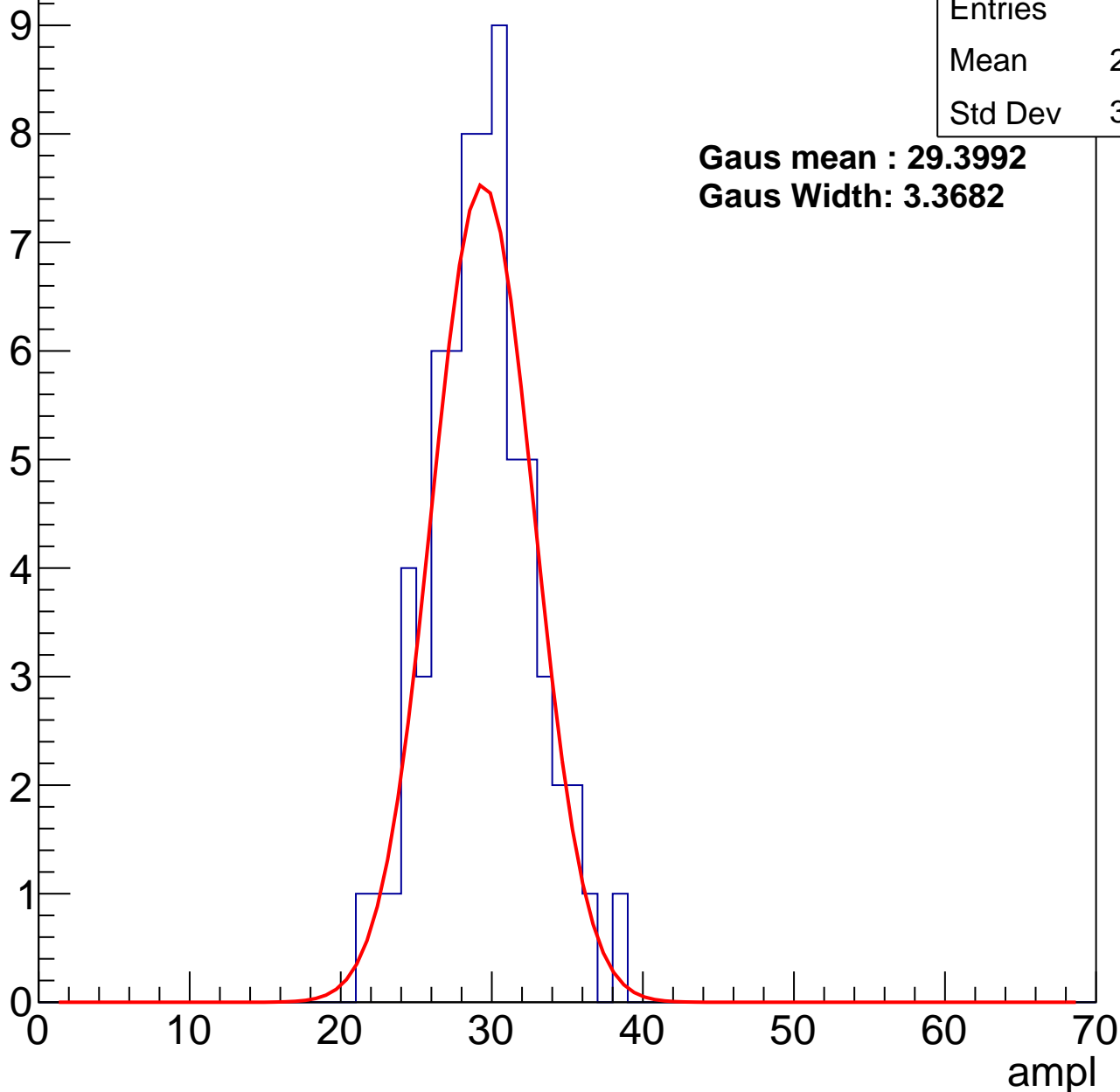
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	28.89
Std Dev	3.385

**Gaus mean : 29.3992**

**Gaus Width: 3.3682**



# B0L000S, U25-ch5, adc1

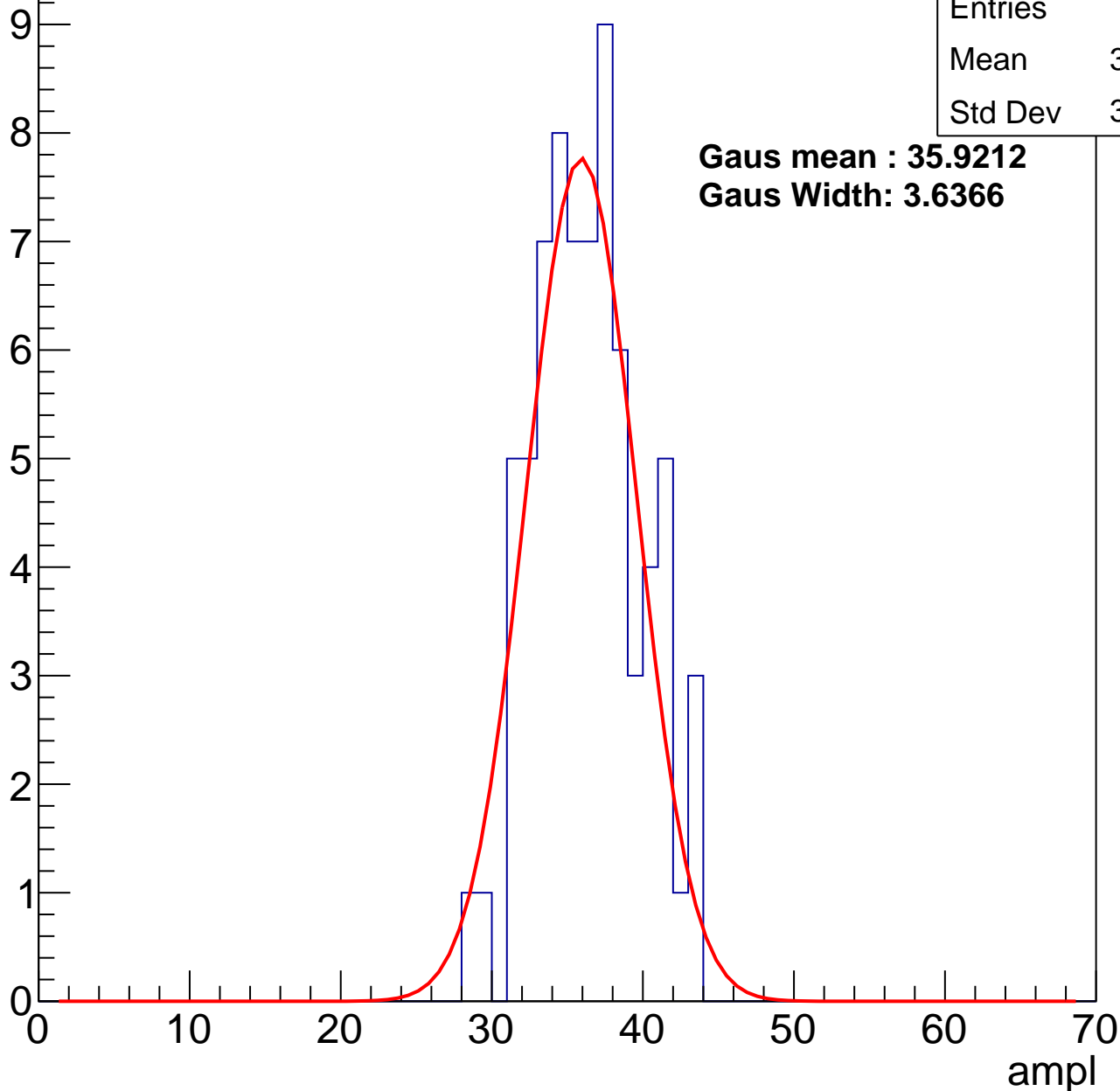
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	35.92
Std Dev	3.439

**Gaus mean : 35.9212**

**Gaus Width: 3.6366**



# B0L000S, U25-ch5, adc2

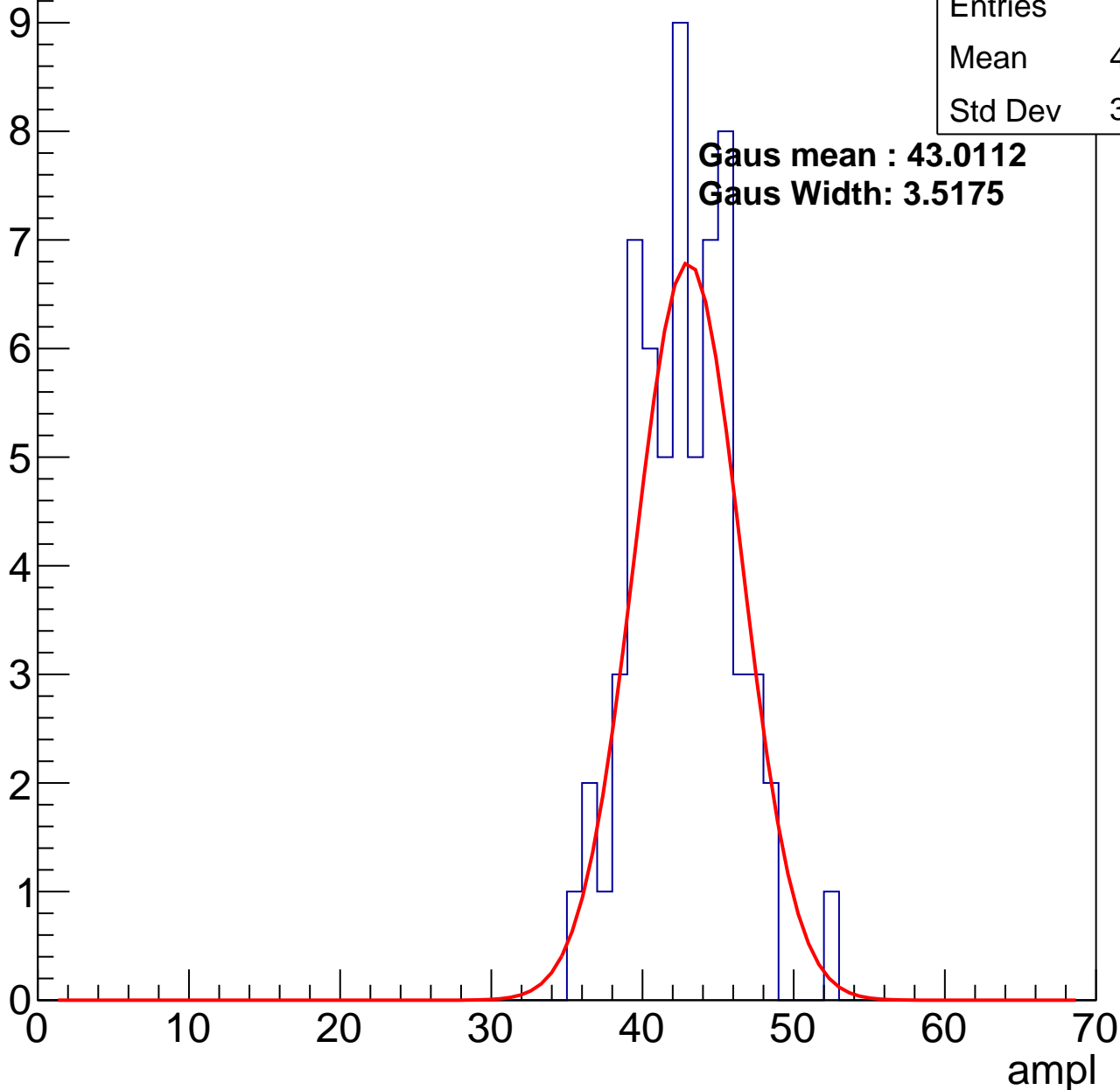
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.29
Std Dev	3.287

**Gaus mean : 43.0112**

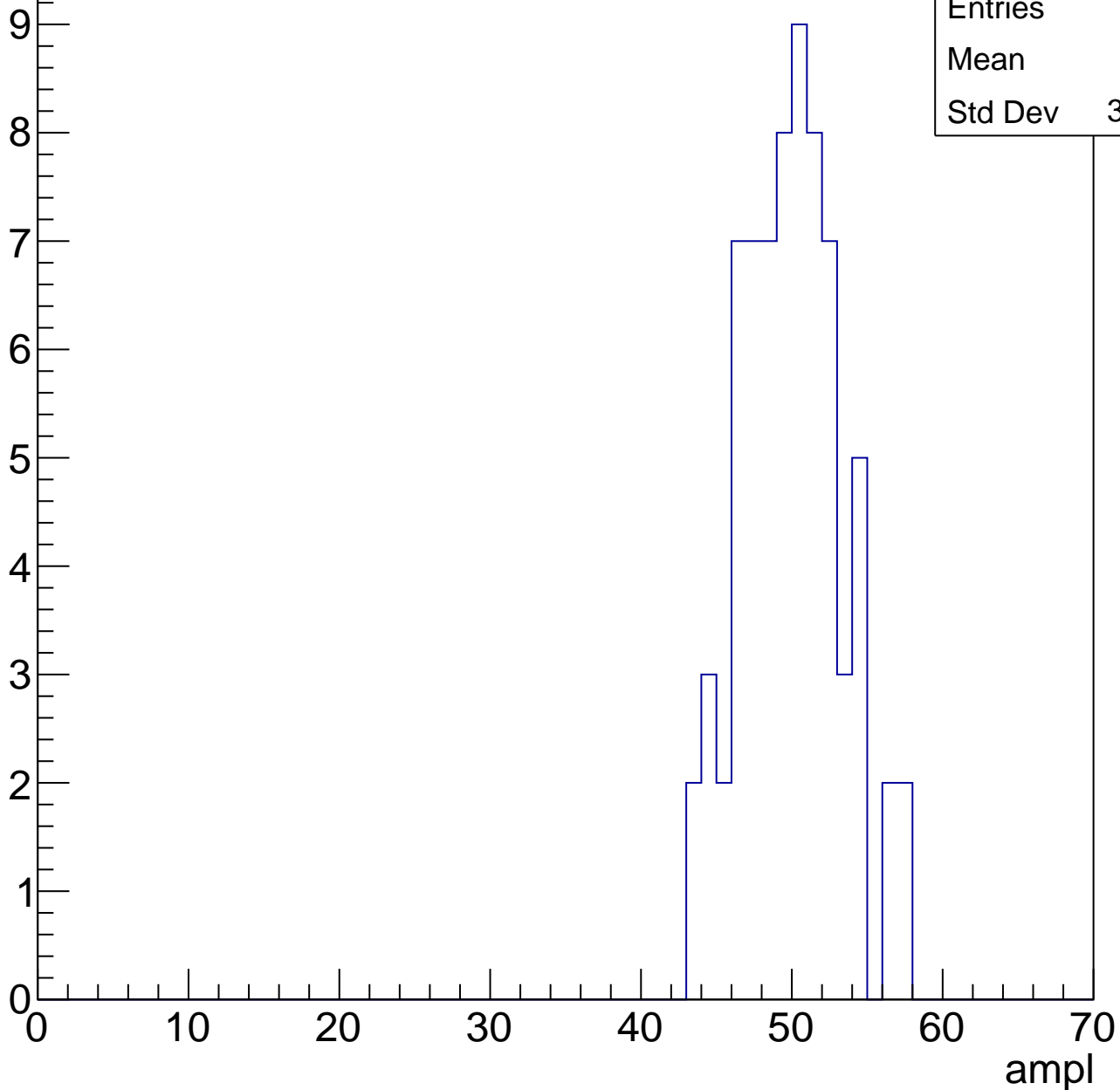
**Gaus Width: 3.5175**



# B0L000S, U25-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



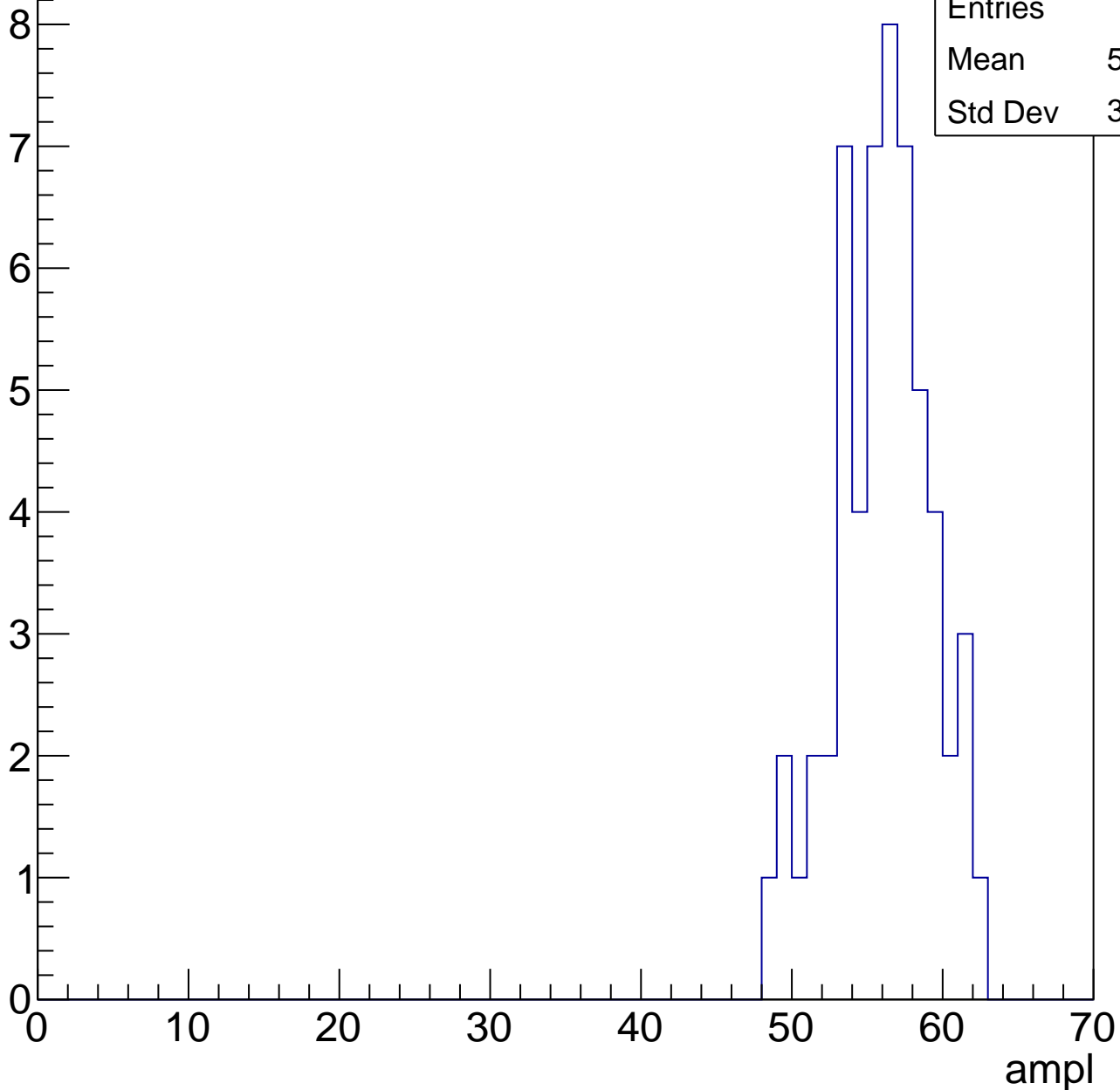
Entries	72
Mean	49.5
Std Dev	3.253

# B0L000S, U25-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.57
Std Dev	3.167



# B0L000S, U25-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

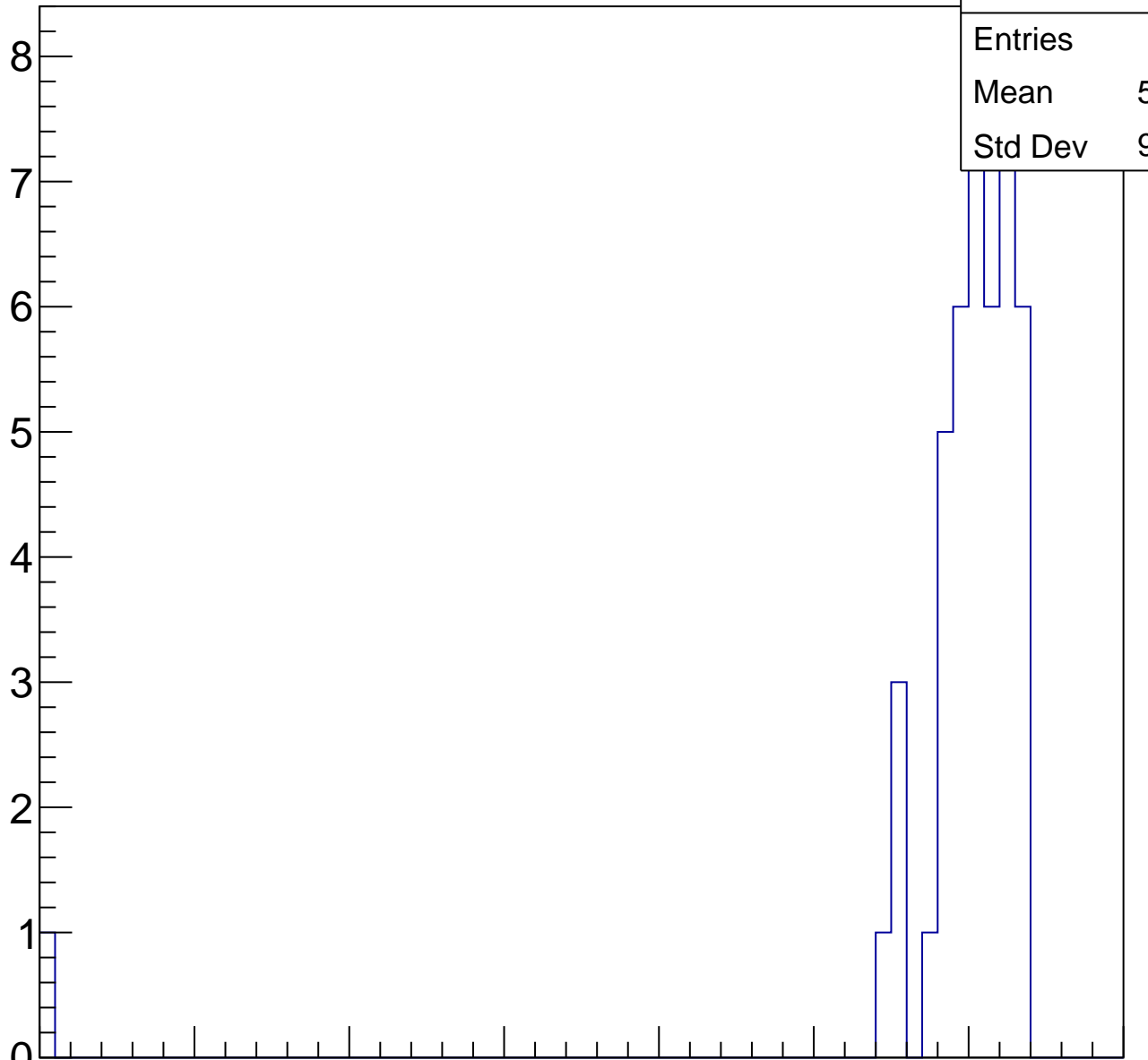
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	58.67
Std Dev	9.138

ampl

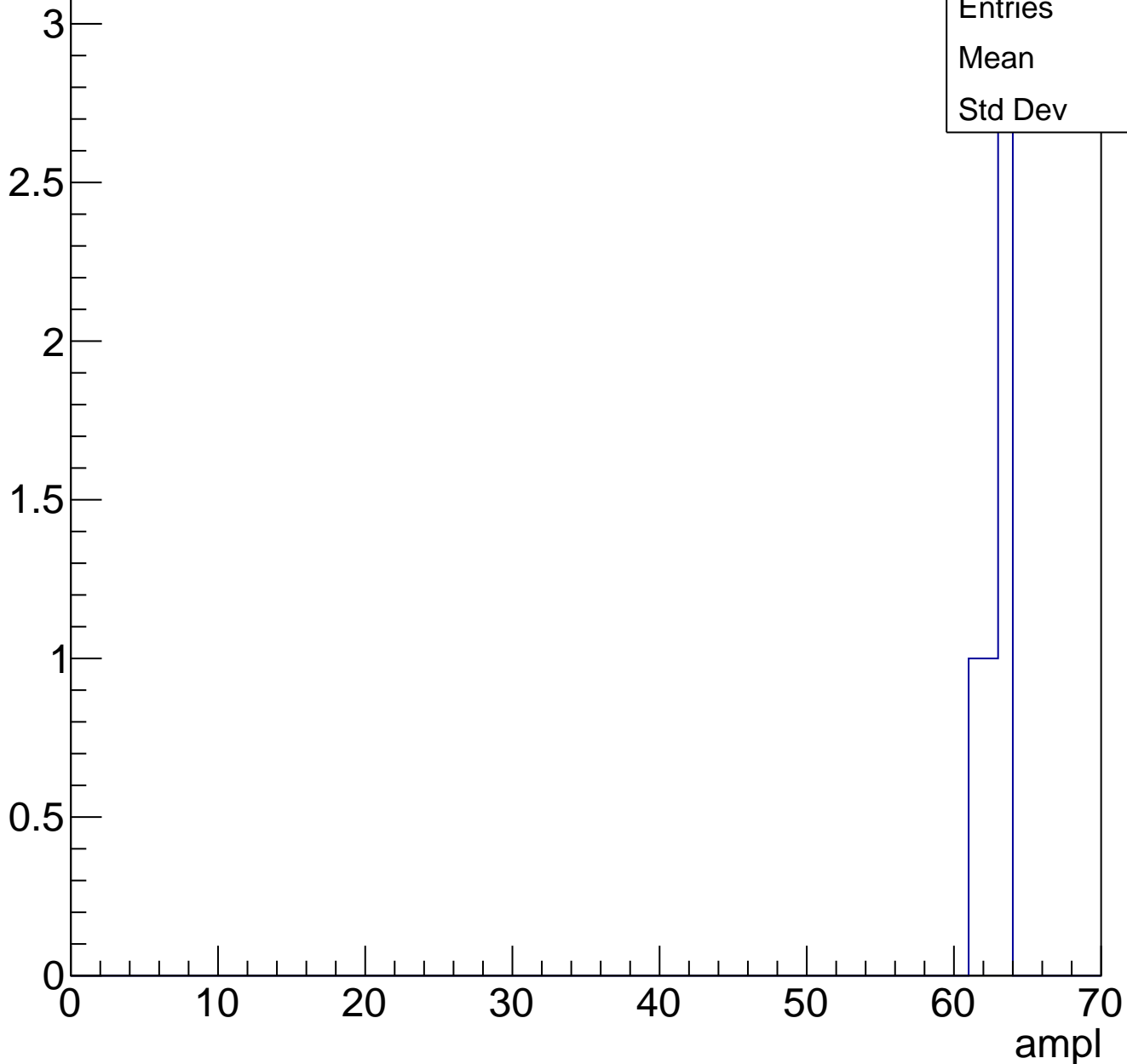
0 10 20 30 40 50 60 70



# B0L000S, U25-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch6, adc0

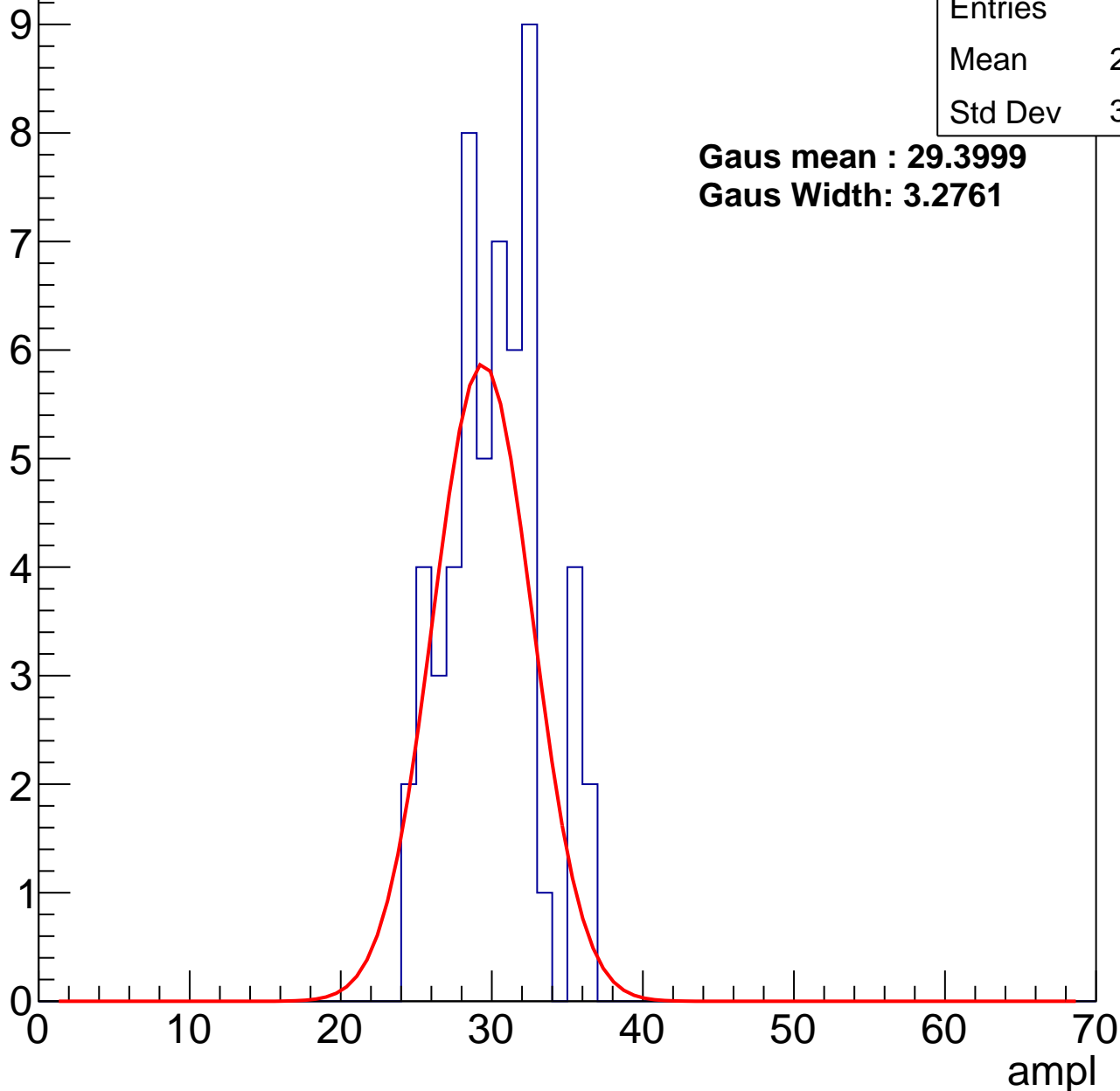
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	29.67
Std Dev	3.045

**Gaus mean : 29.3999**

**Gaus Width: 3.2761**



# B0L000S, U25-ch6, adc1

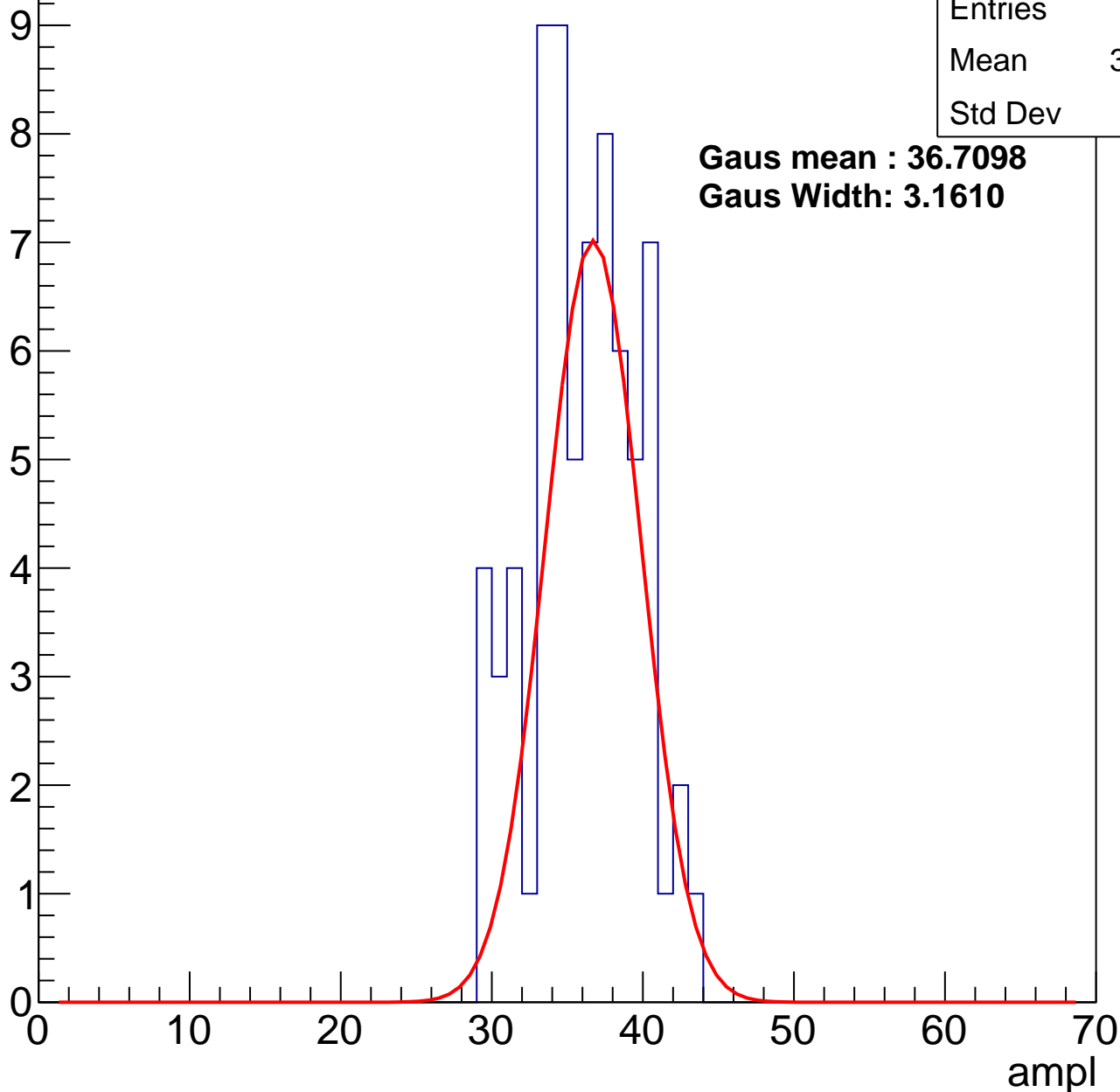
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	35.54
Std Dev	3.46

**Gaus mean : 36.7098**

**Gaus Width: 3.1610**



# B0L000S, U25-ch6, adc2

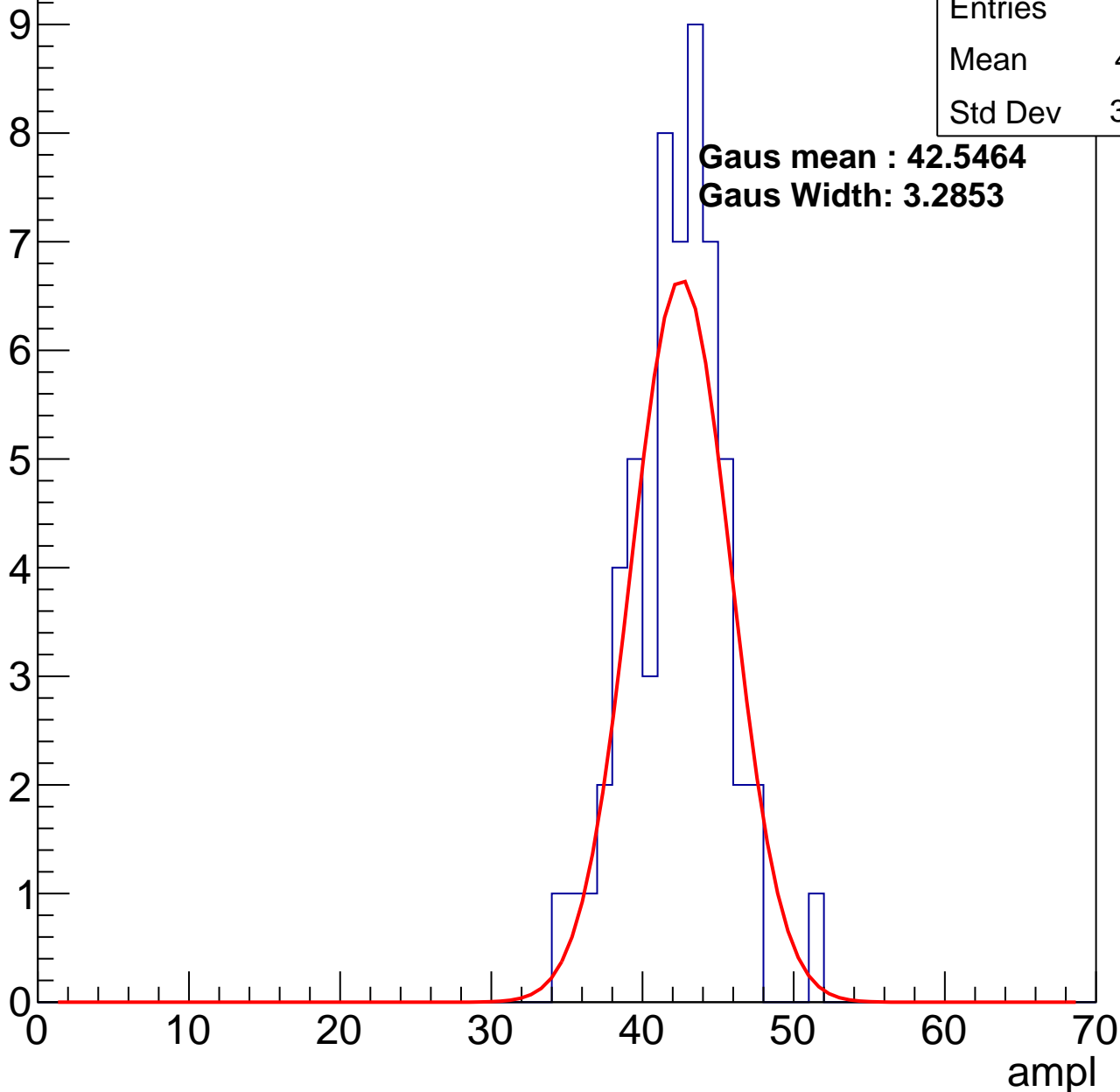
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	41.81
Std Dev	3.137

**Gaus mean : 42.5464**

**Gaus Width: 3.2853**

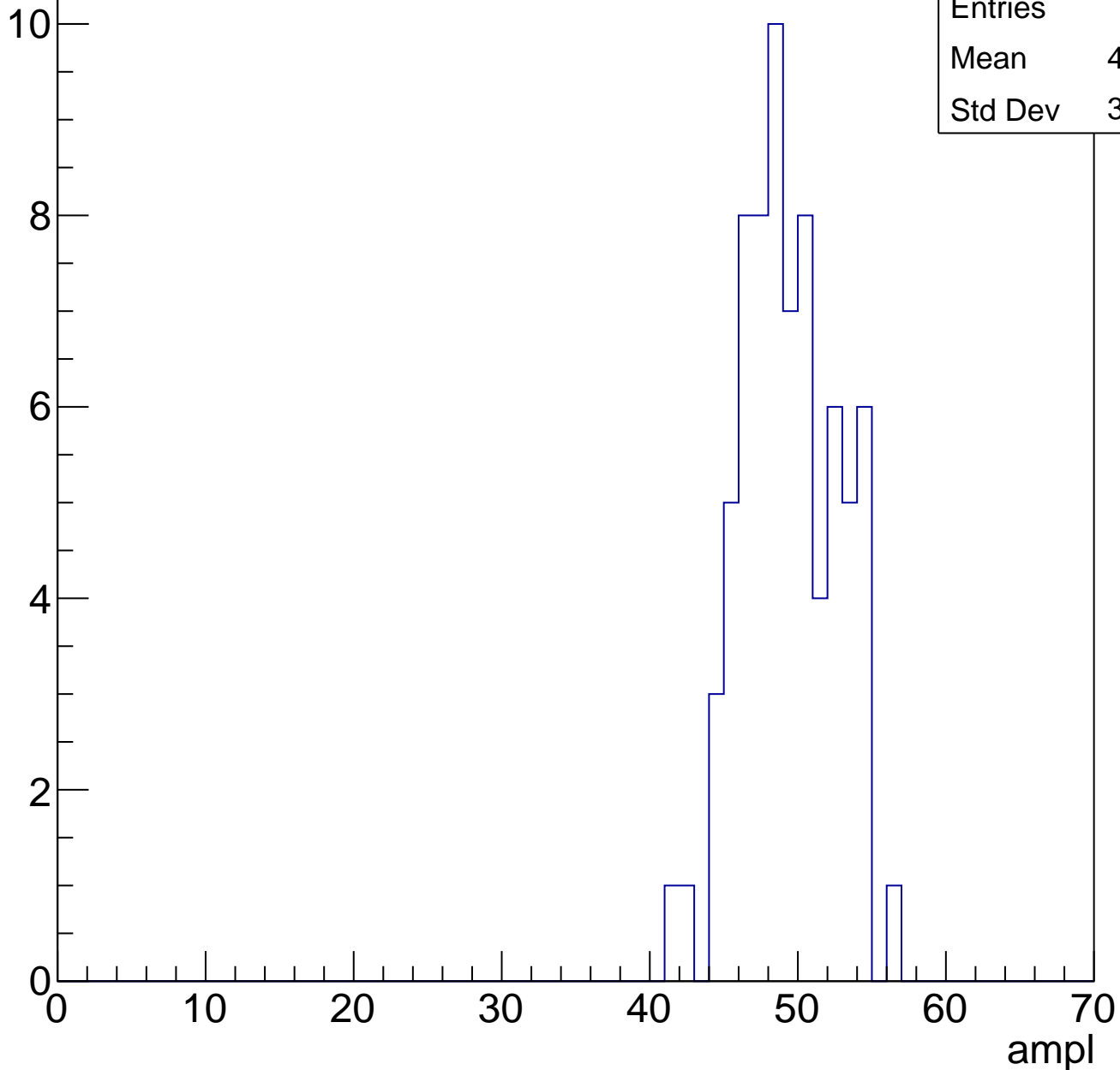


# B0L000S, U25-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	48.88
Std Dev	3.179

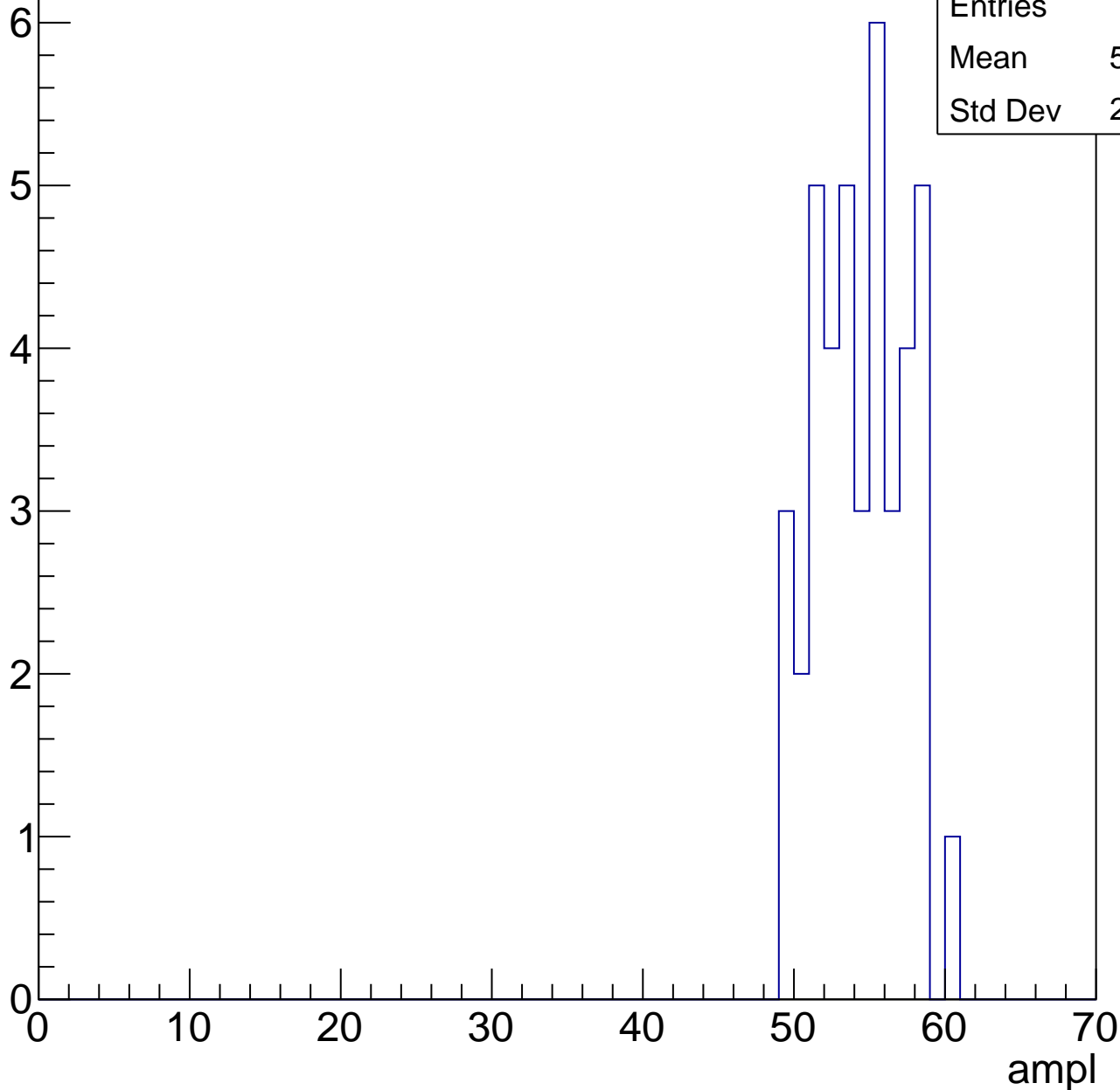
Entry



# B0L000S, U25-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

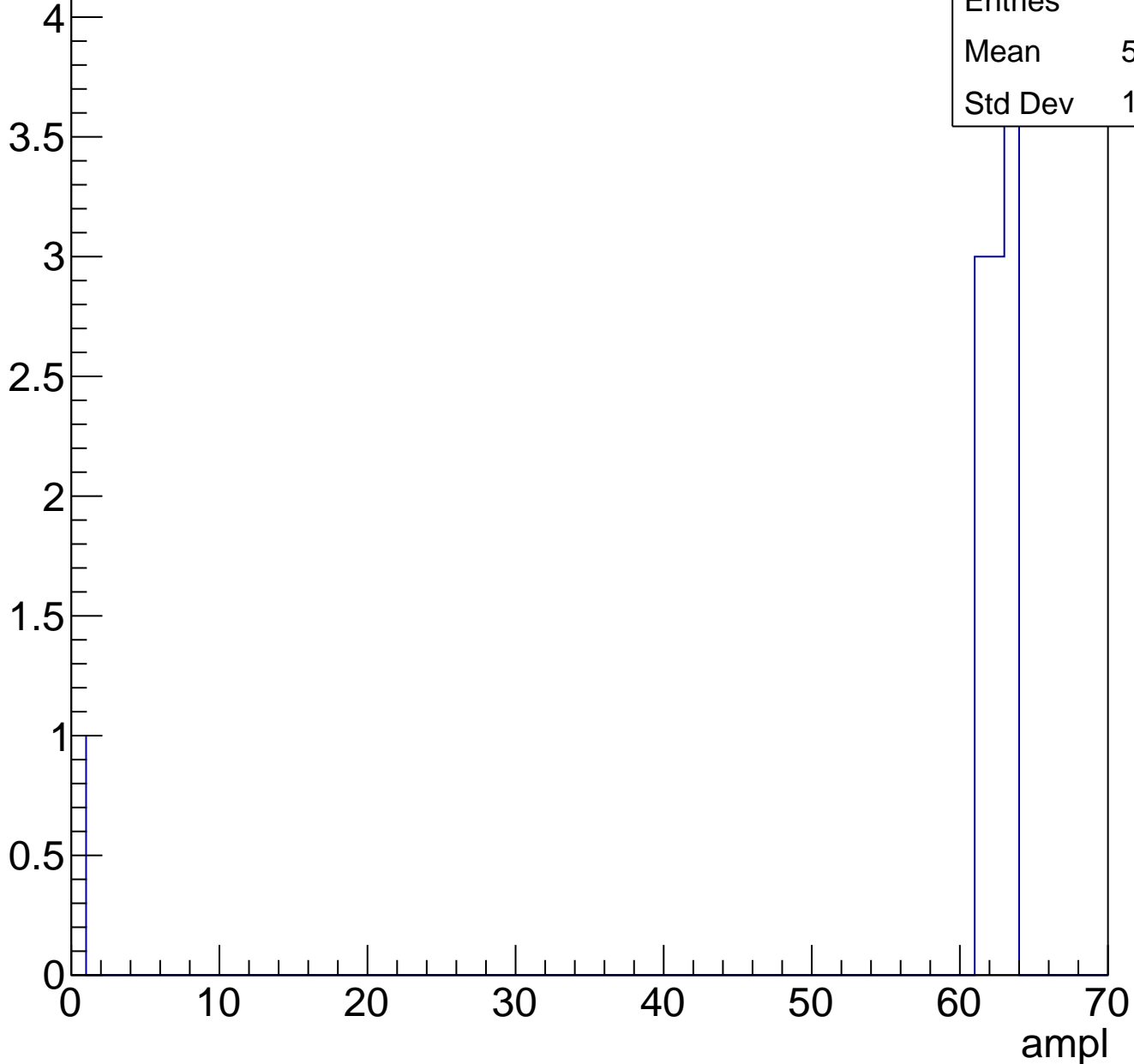
ampl

Entries	61
Mean	59.2
Std Dev	2.448

# B0L000S, U25-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch7, adc0

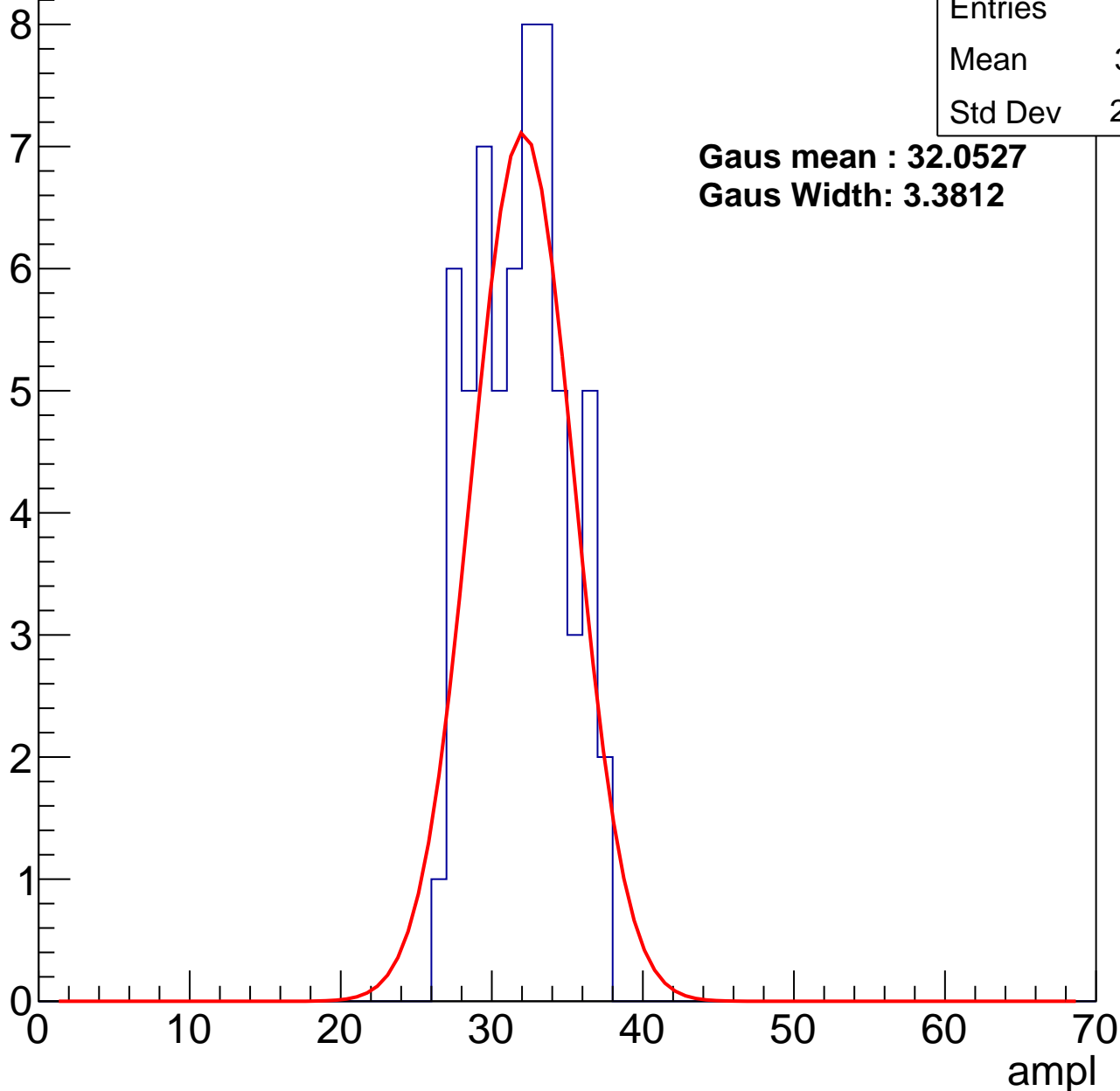
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	31.41
Std Dev	2.916

**Gaus mean : 32.0527**

**Gaus Width: 3.3812**



# B0L000S, U25-ch7, adc1

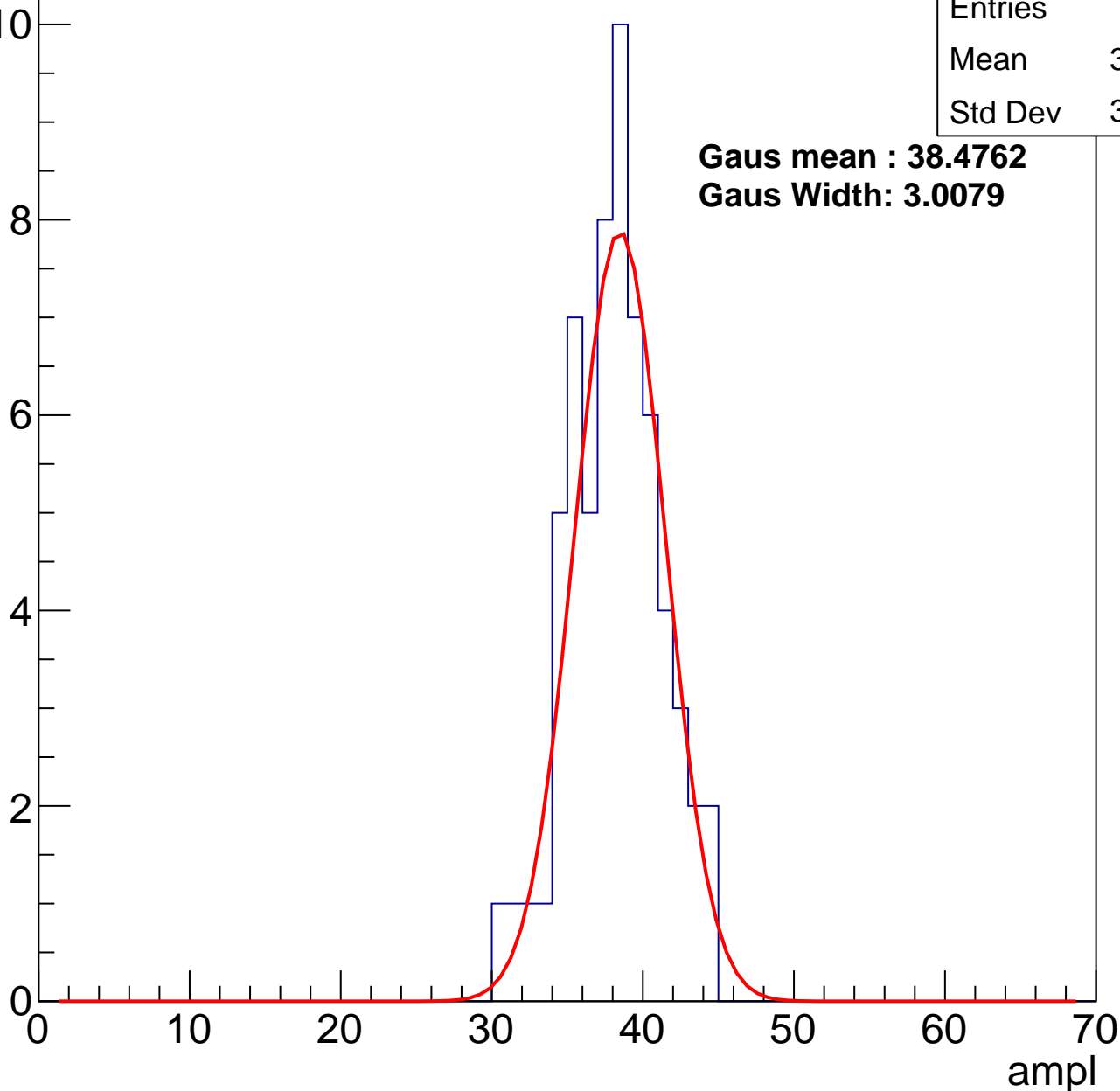
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.68
Std Dev	3.023

**Gaus mean : 38.4762**

**Gaus Width: 3.0079**



# B0L000S, U25-ch7, adc2

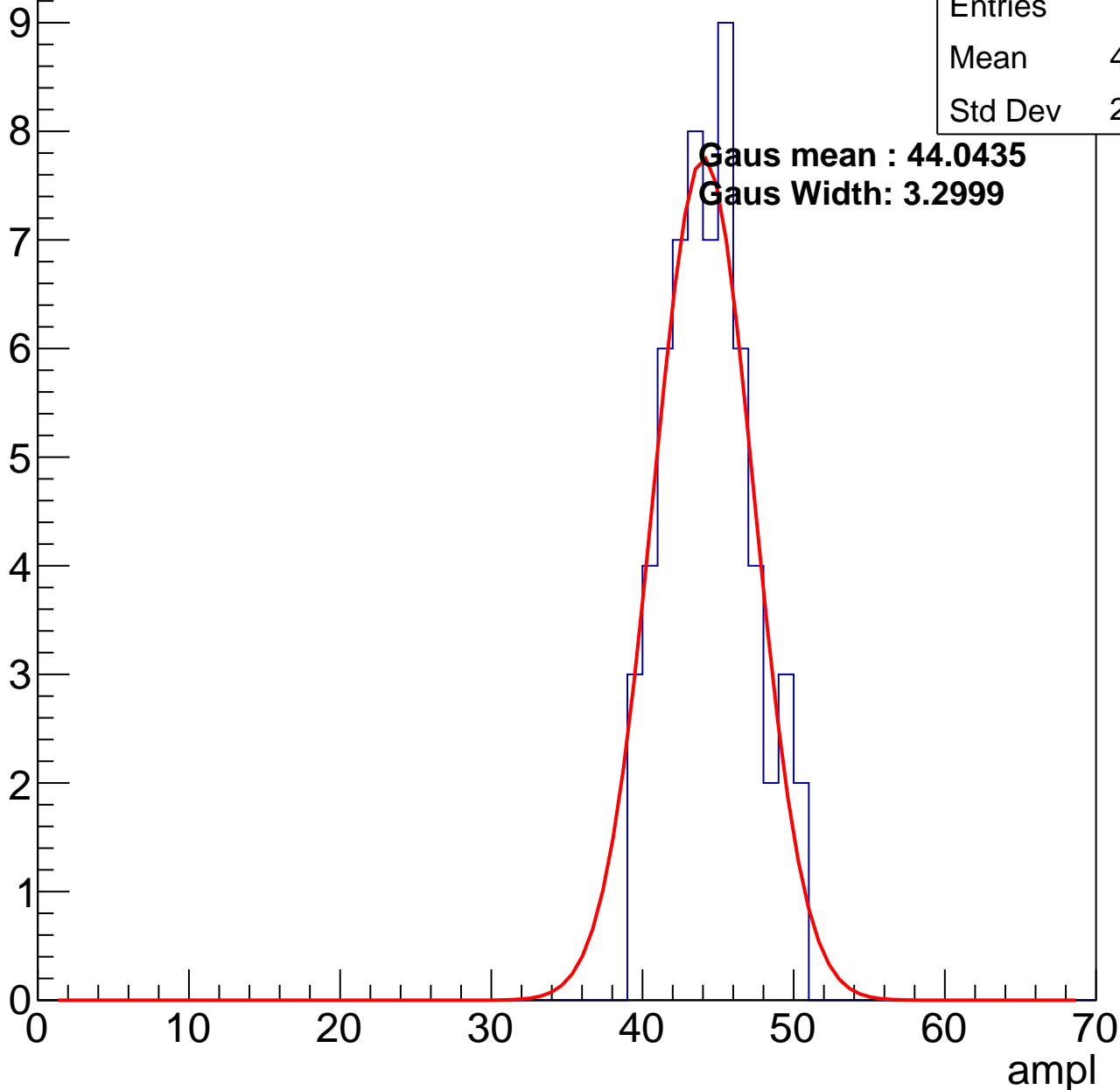
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.95
Std Dev	2.796

**Gaus mean : 44.0435**

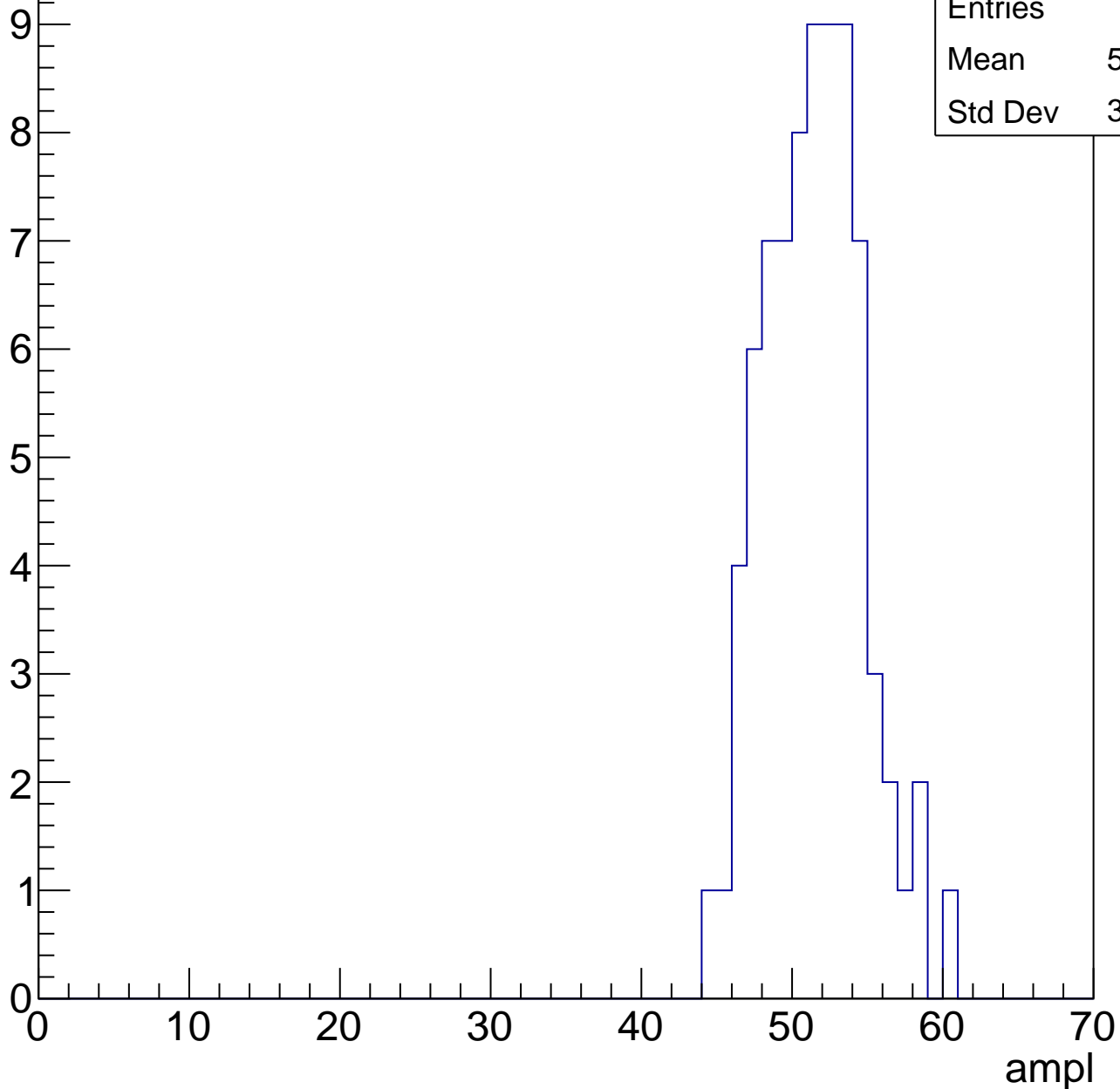
**Gaus Width: 3.2999**



# B0L000S, U25-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

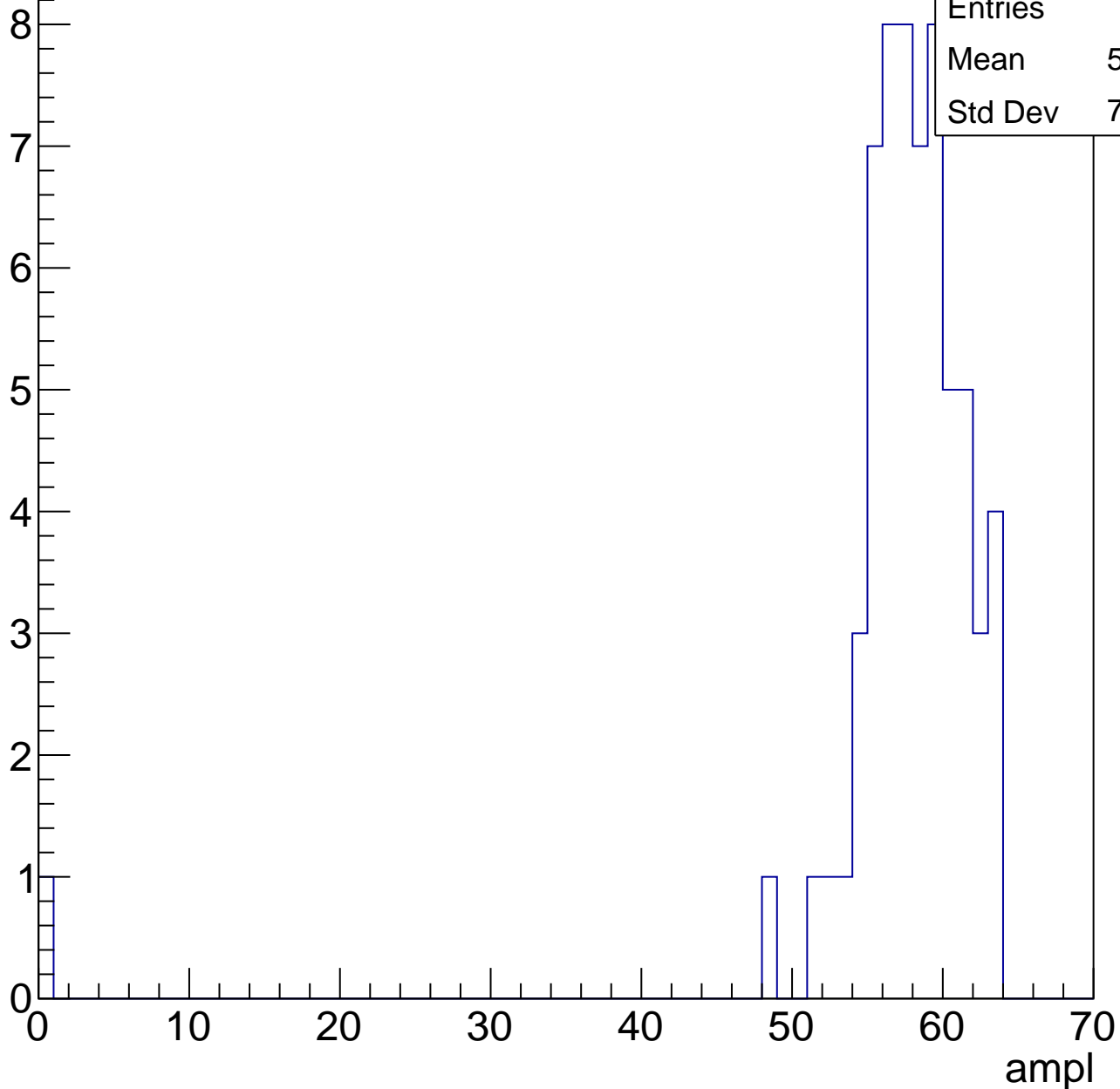


Entries	77
Mean	50.99
Std Dev	3.225

# B0L000S, U25-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

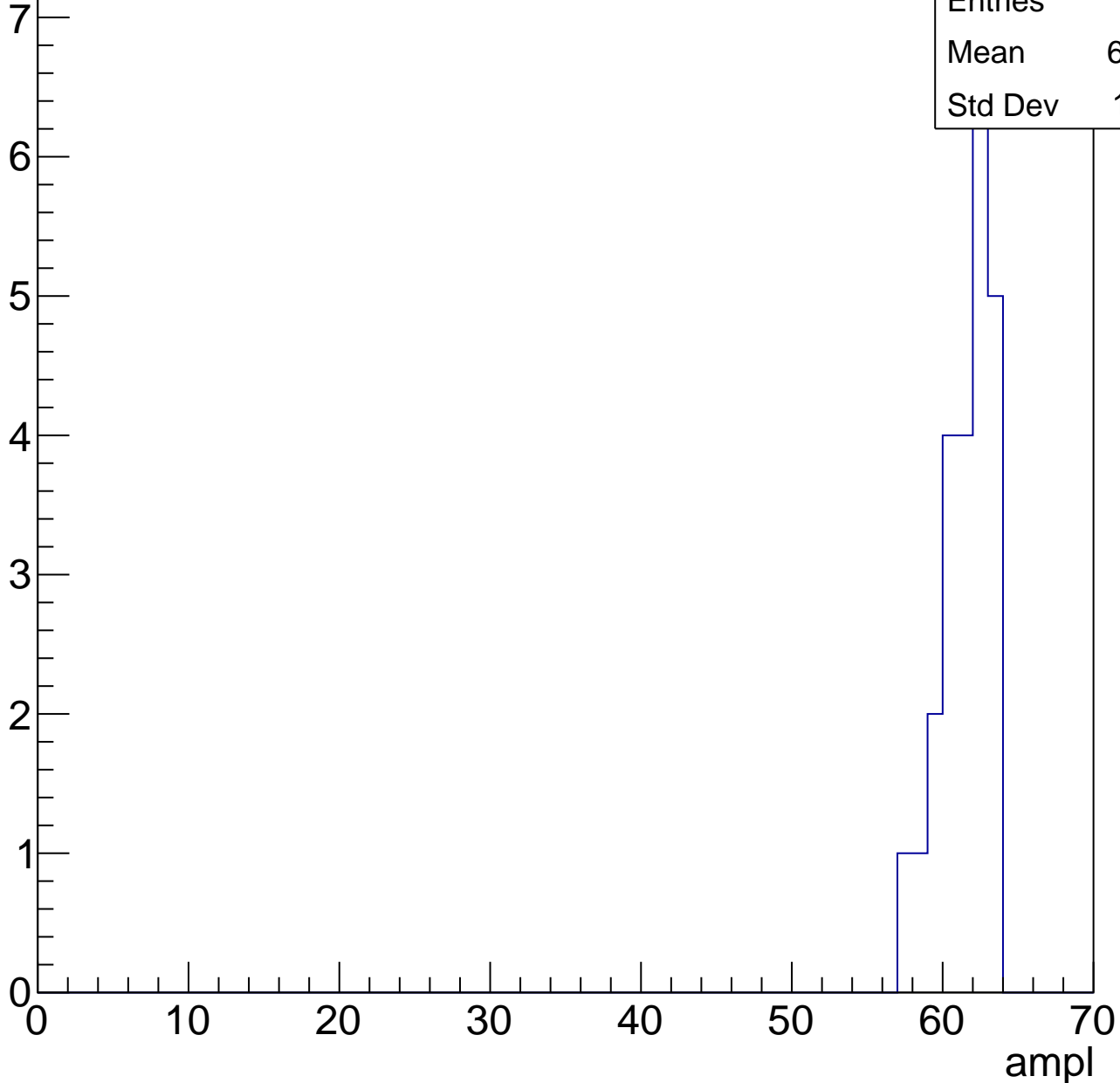


# B0L000S, U25-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	24
Mean	61.08
Std Dev	1.631



# B0L000S, U25-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch8, adc0

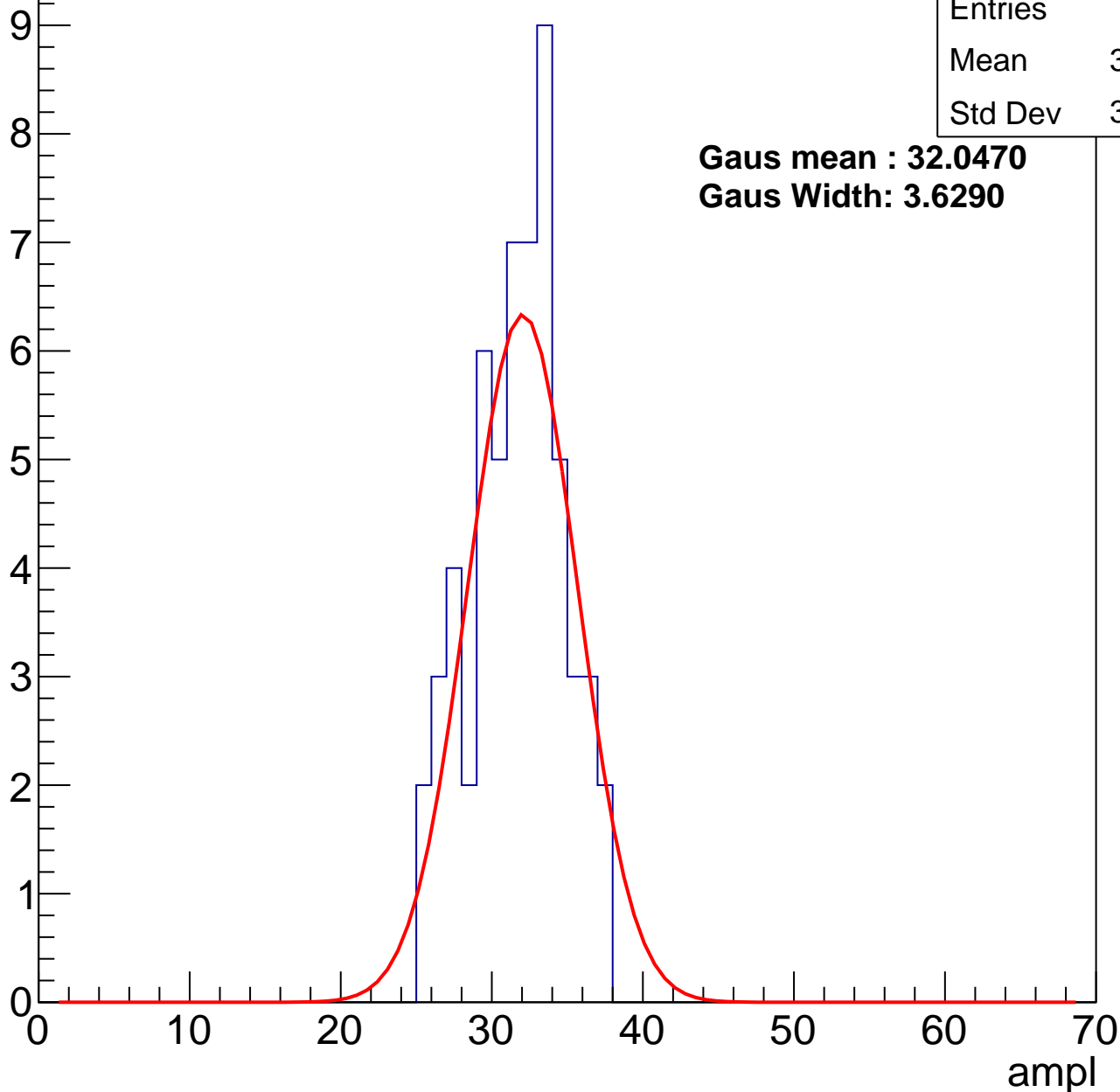
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	31.22
Std Dev	3.046

**Gaus mean : 32.0470**

**Gaus Width: 3.6290**



# B0L000S, U25-ch8, adc1

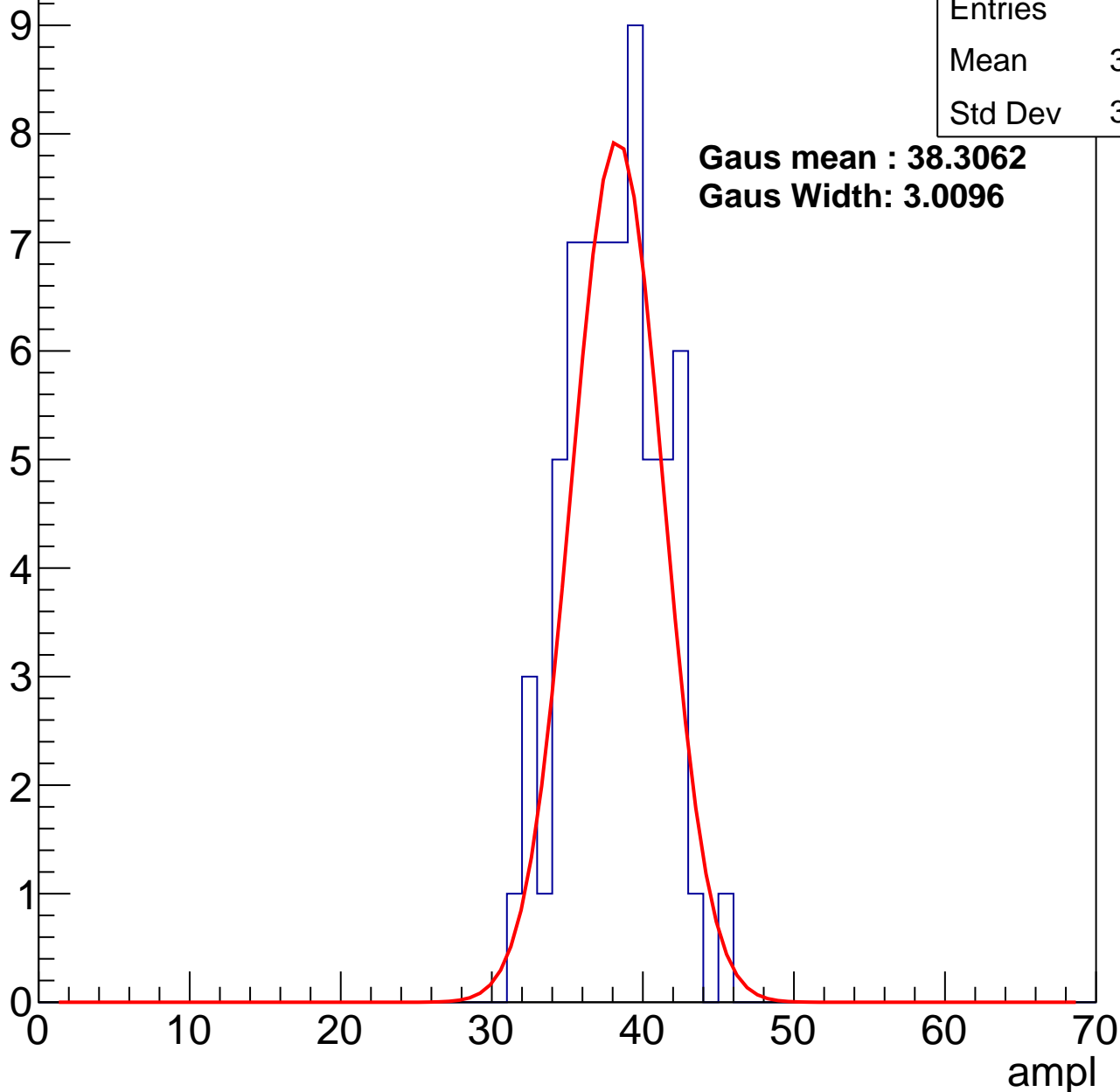
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	37.66
Std Dev	3.029

**Gaus mean : 38.3062**

**Gaus Width: 3.0096**



# B0L000S, U25-ch8, adc2

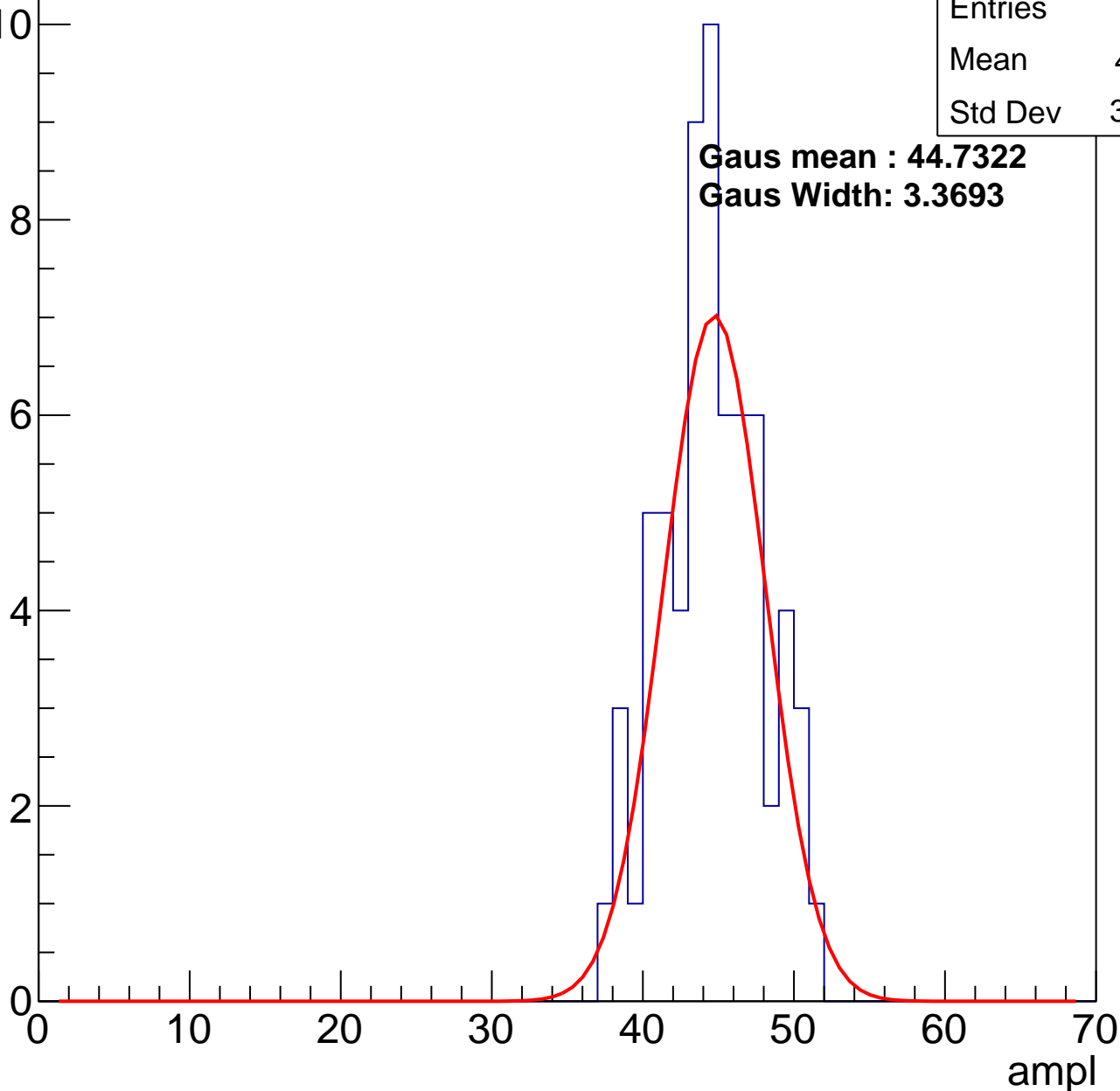
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	44.11
Std Dev	3.267

**Gaus mean : 44.7322**

**Gaus Width: 3.3693**

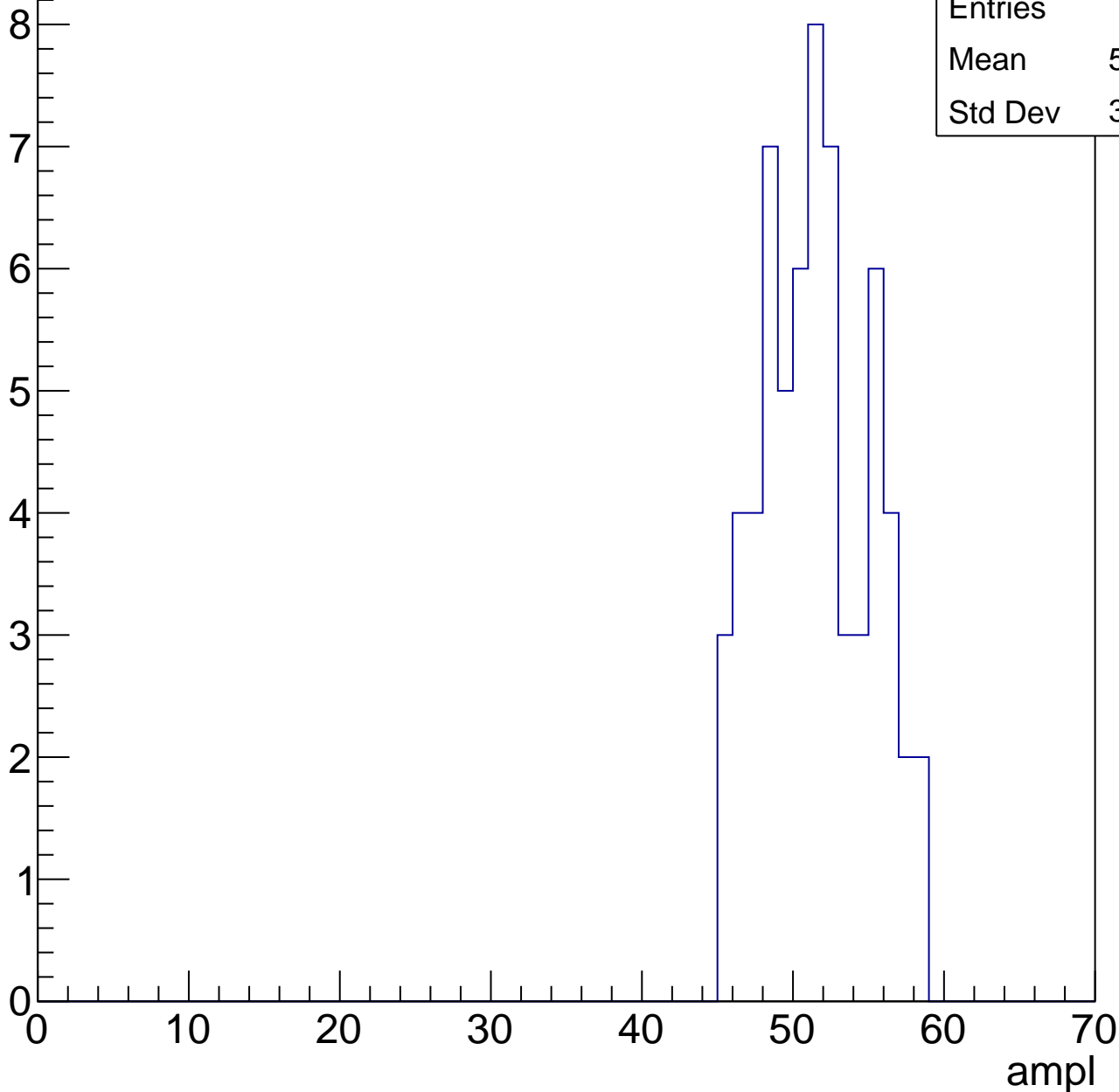


# B0L000S, U25-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	51.02
Std Dev	3.475

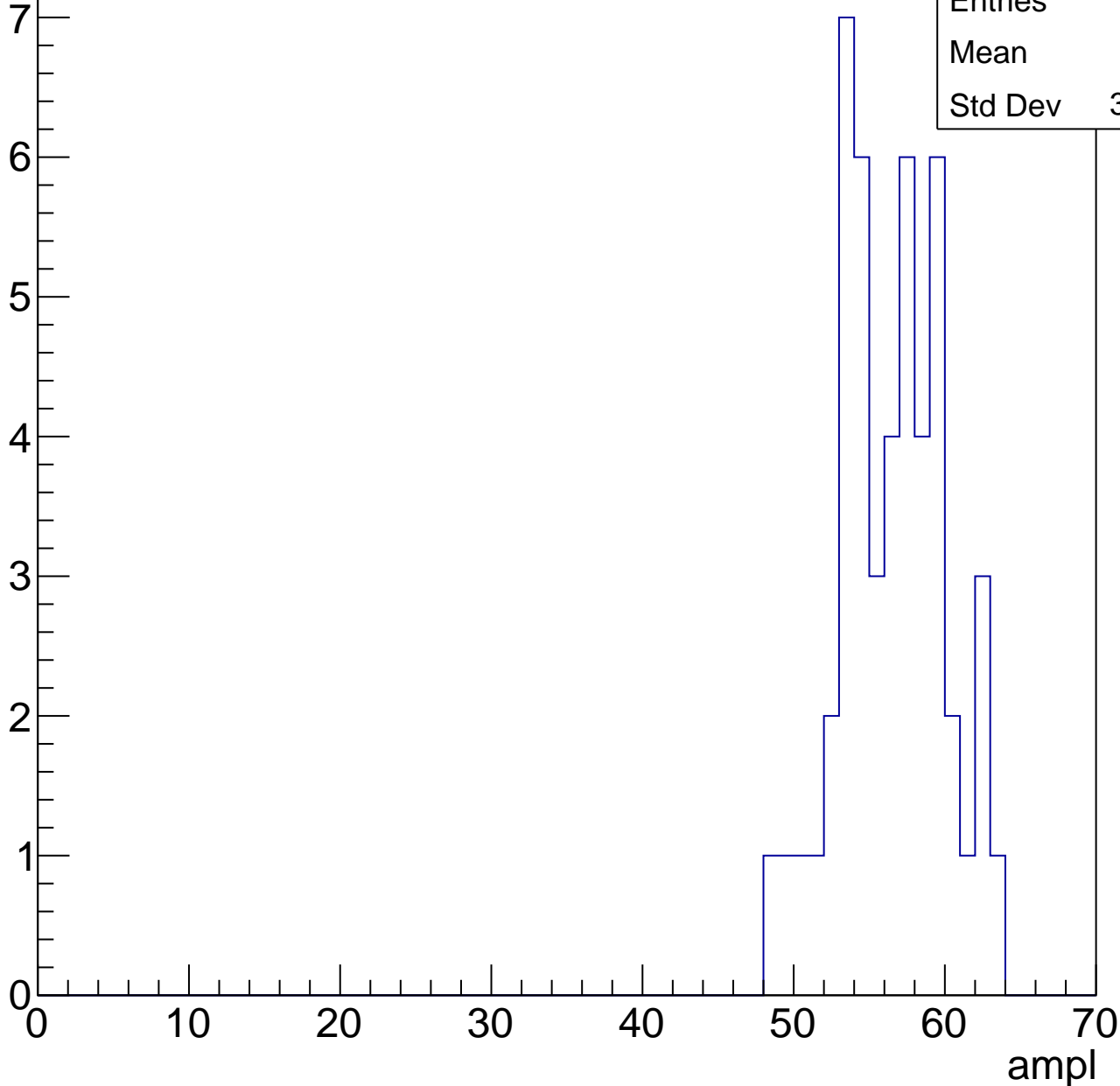


# B0L000S, U25-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	56
Std Dev	3.458



# B0L000S, U25-ch8, adc5

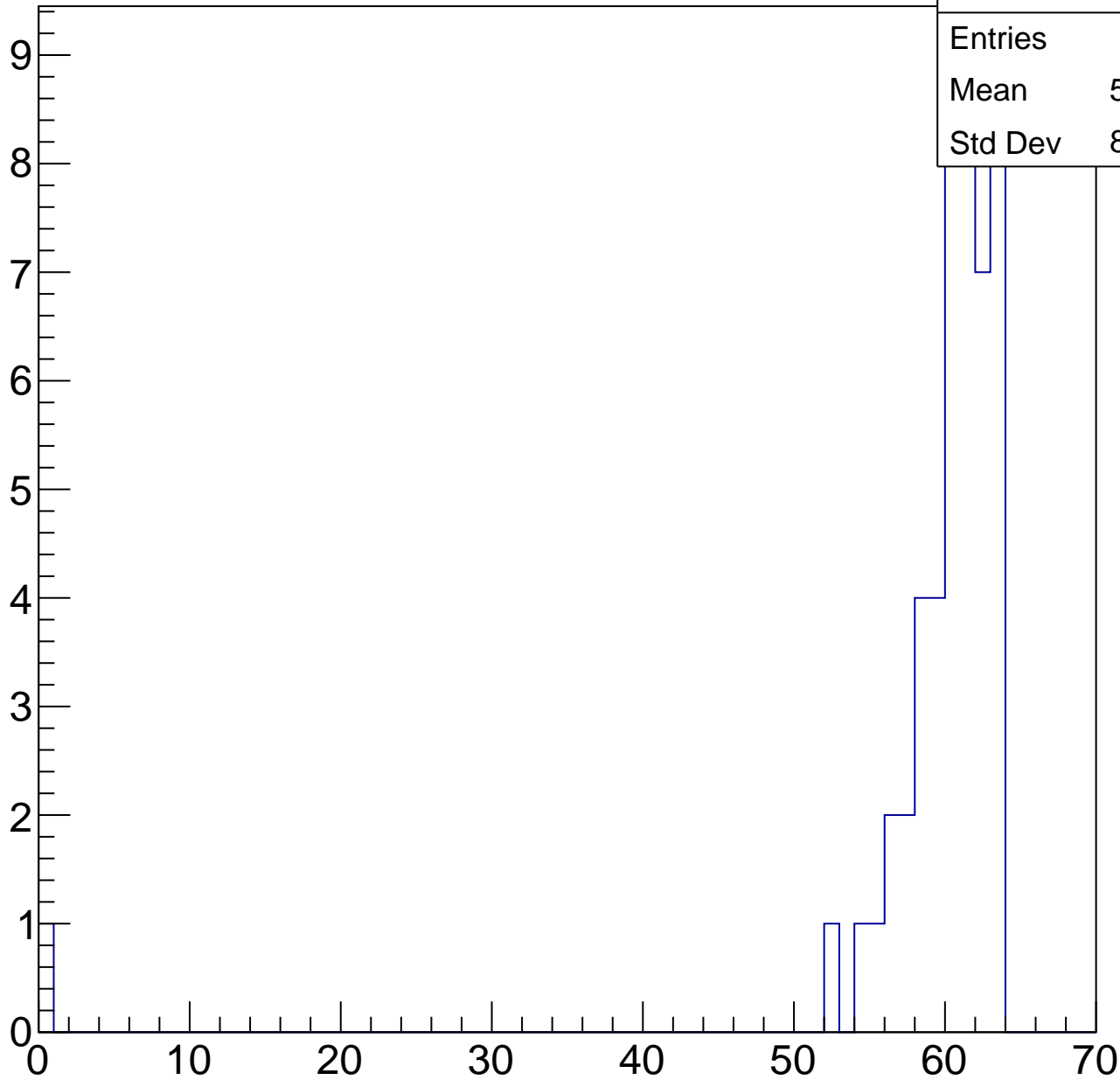
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.82
Std Dev	8.847

ampl



# B0L000S, U25-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U25-ch9, adc0

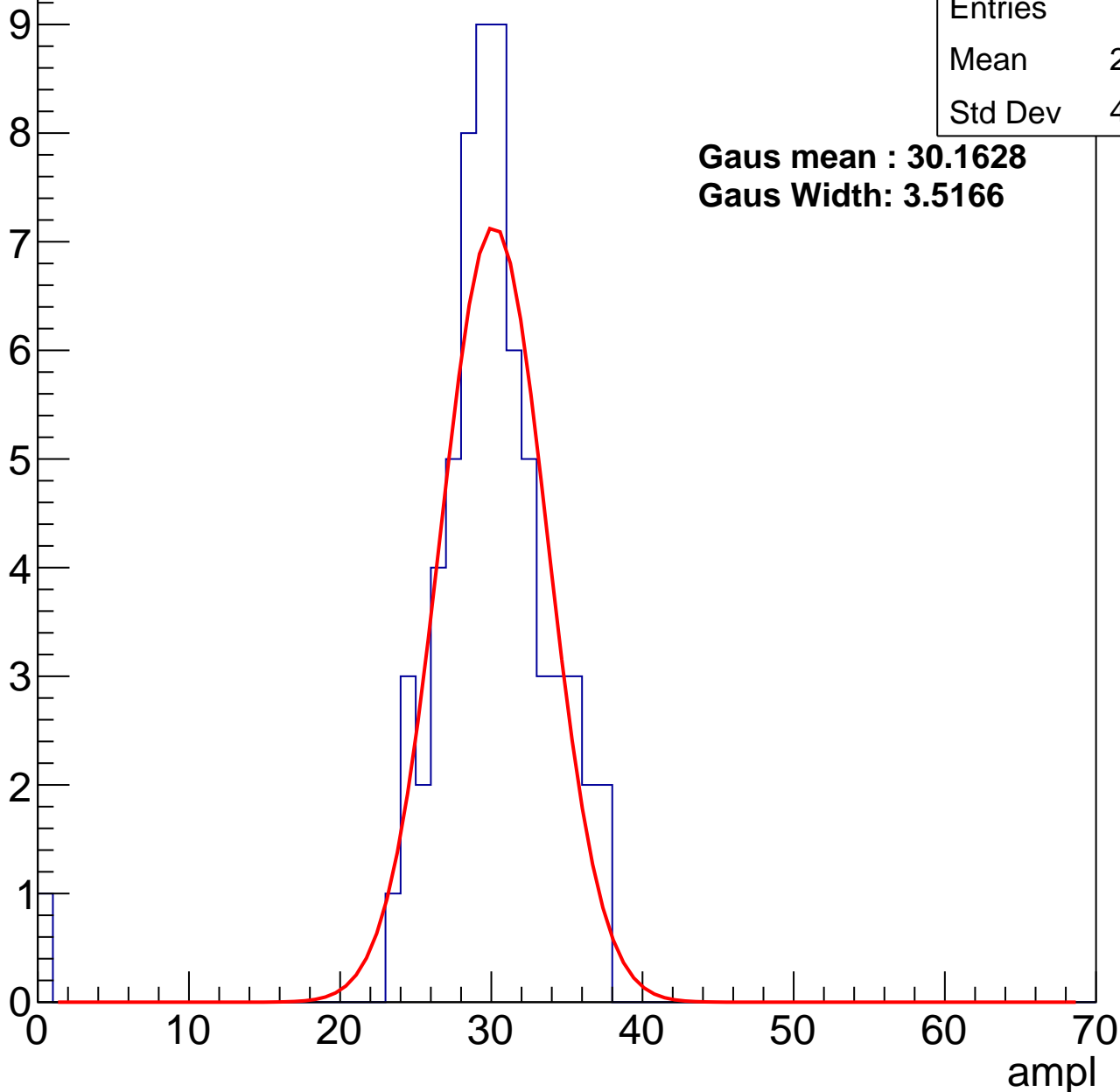
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.35
Std Dev	4.885

**Gaus mean : 30.1628**

**Gaus Width: 3.5166**



# B0L000S, U25-ch9, adc1

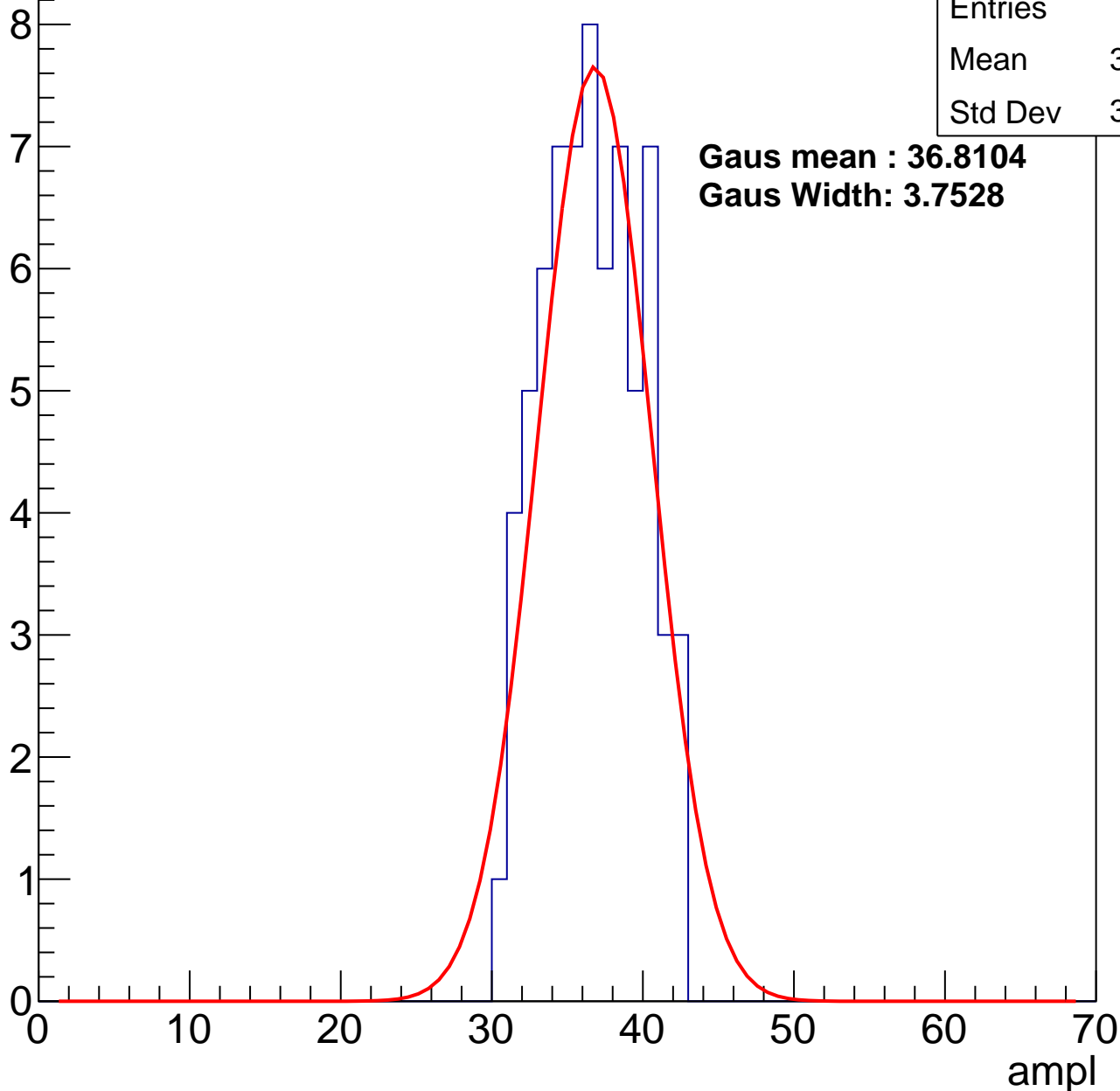
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.16
Std Dev	3.133

**Gaus mean : 36.8104**

**Gaus Width: 3.7528**



# B0L000S, U25-ch9, adc2

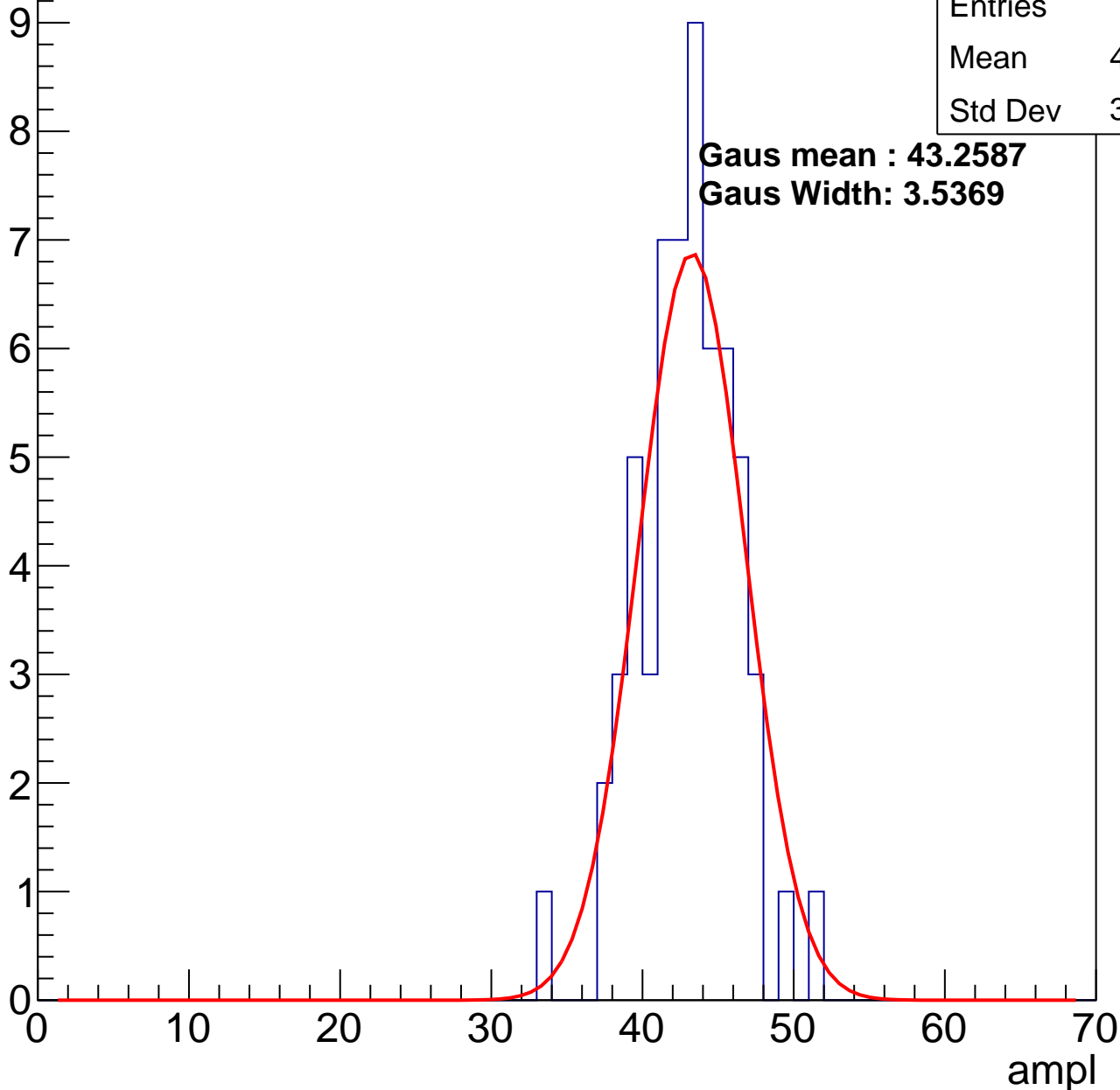
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.53
Std Dev	3.186

**Gaus mean : 43.2587**

**Gaus Width: 3.5369**



# B0L000S, U25-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

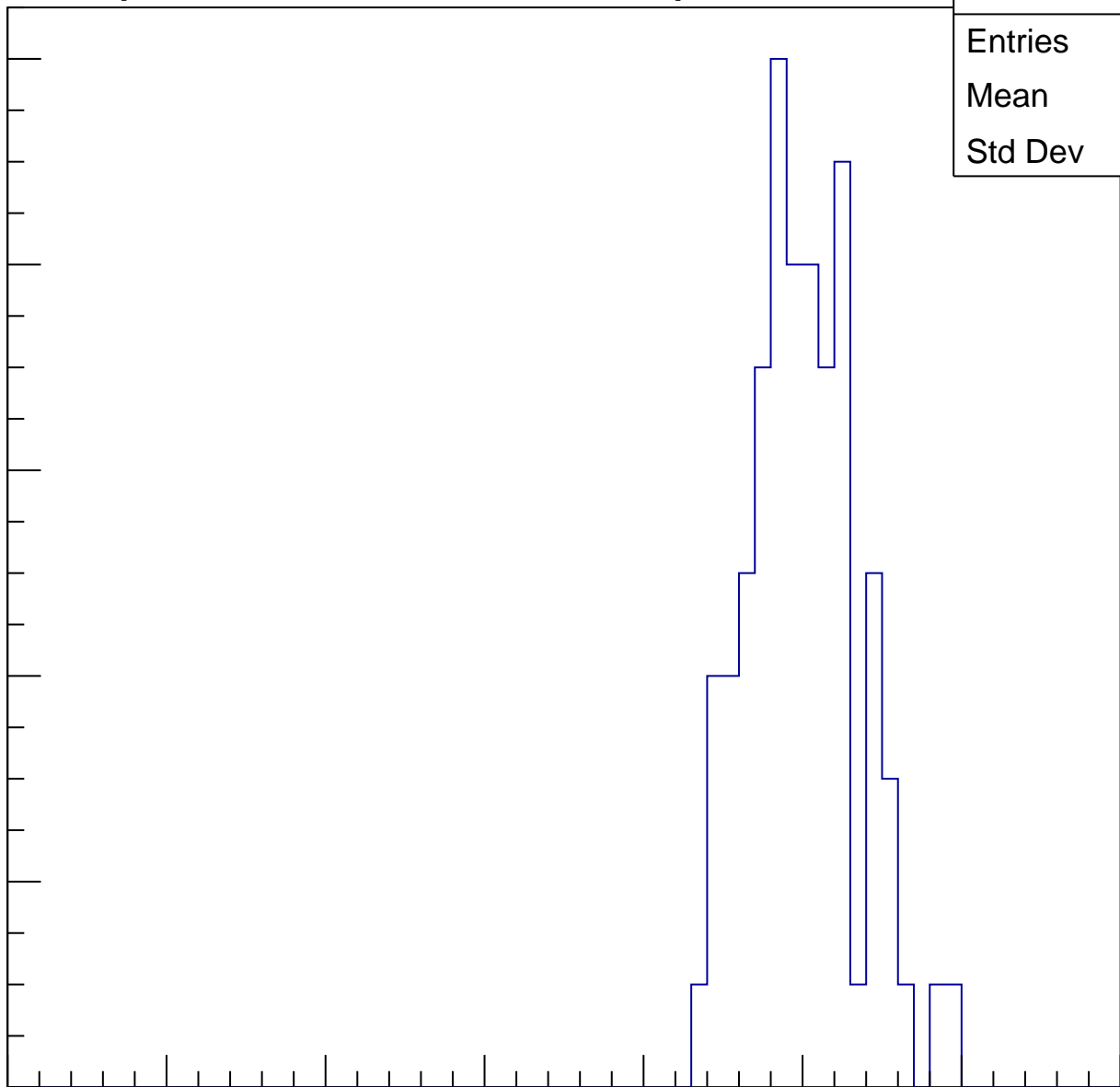
Entries	75
Mean	49.55
Std Dev	3.38

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

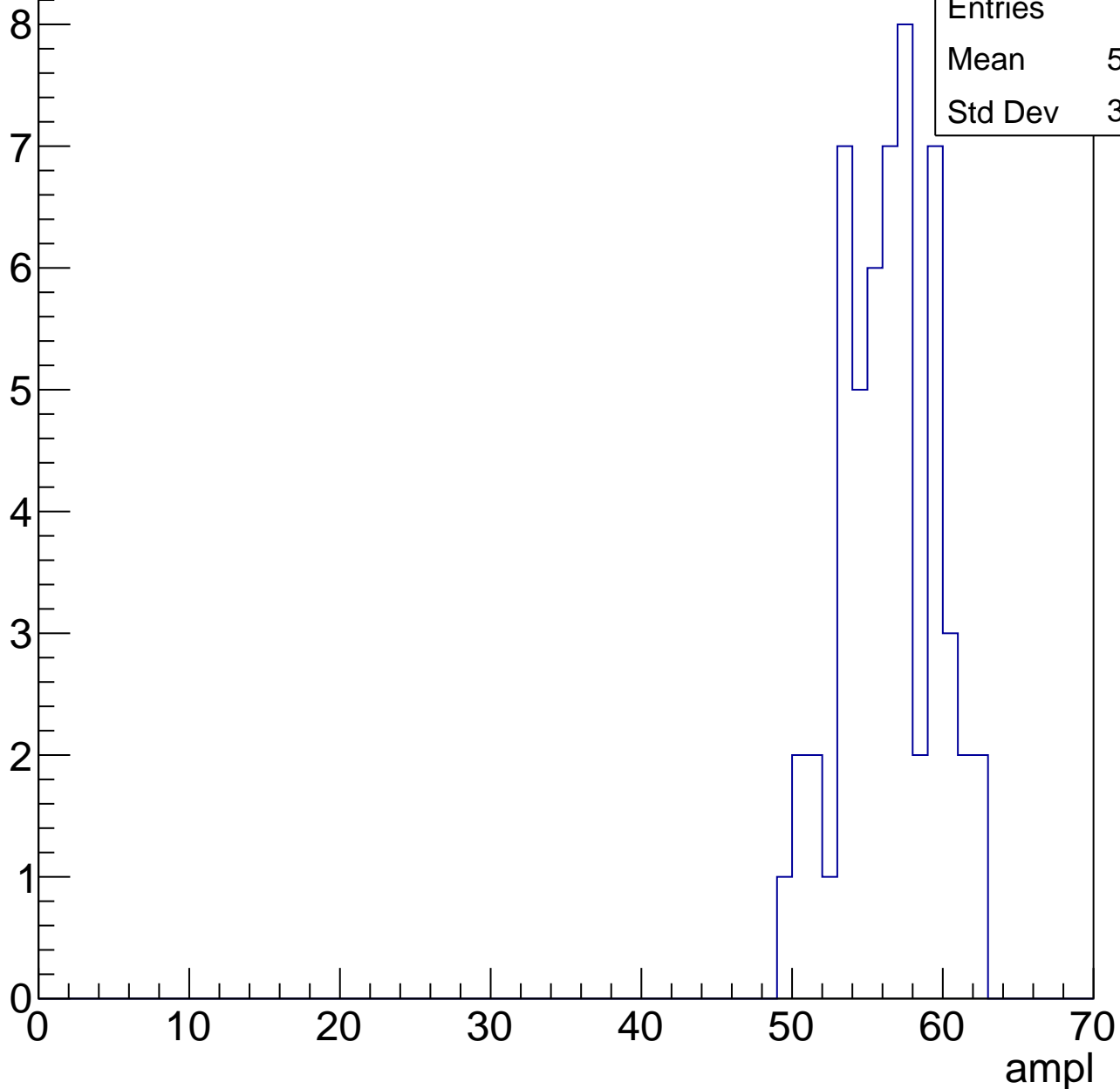
ampl



# B0L000S, U25-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



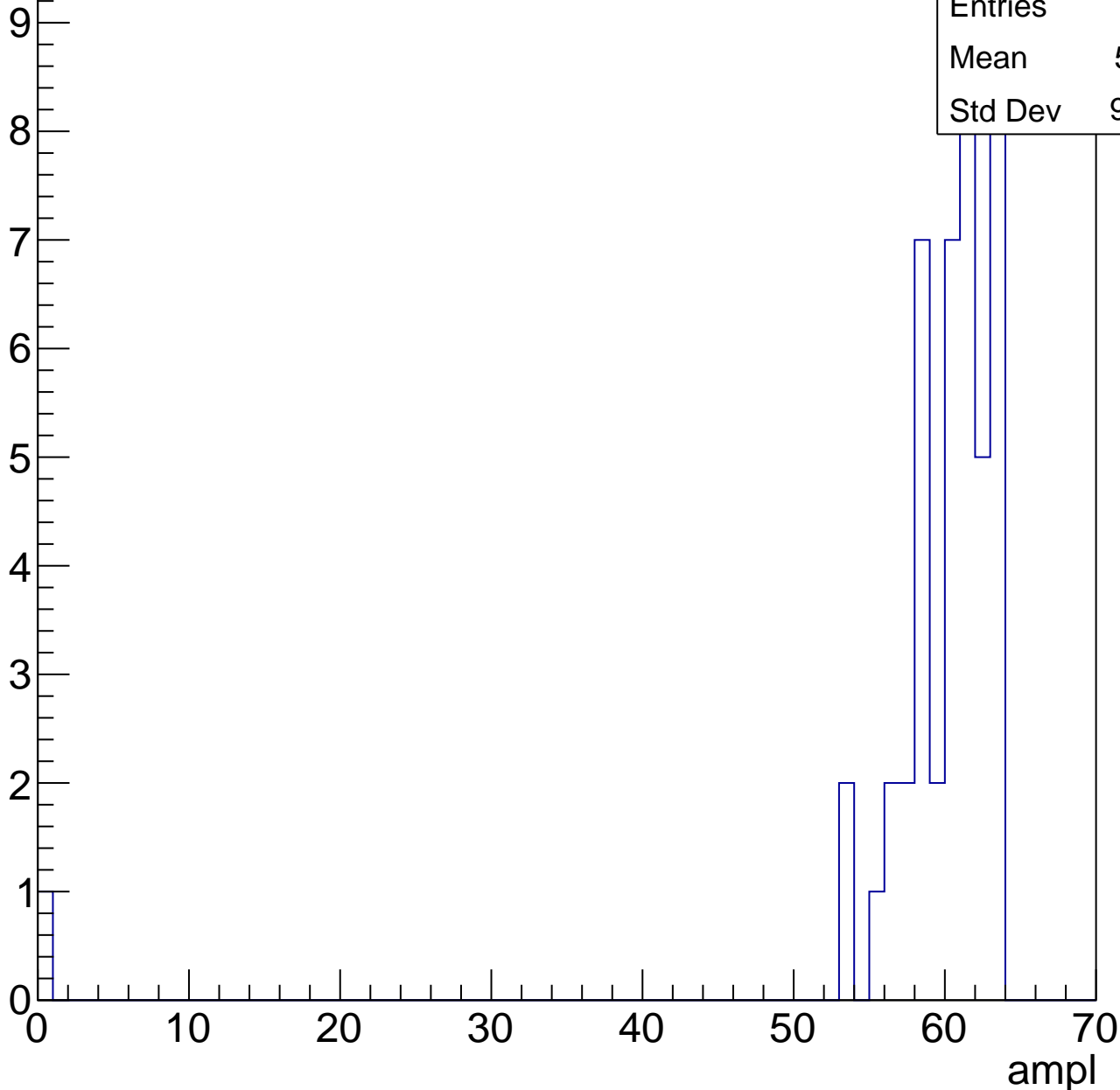
Entries	55
Mean	55.95
Std Dev	3.089

# B0L000S, U25-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

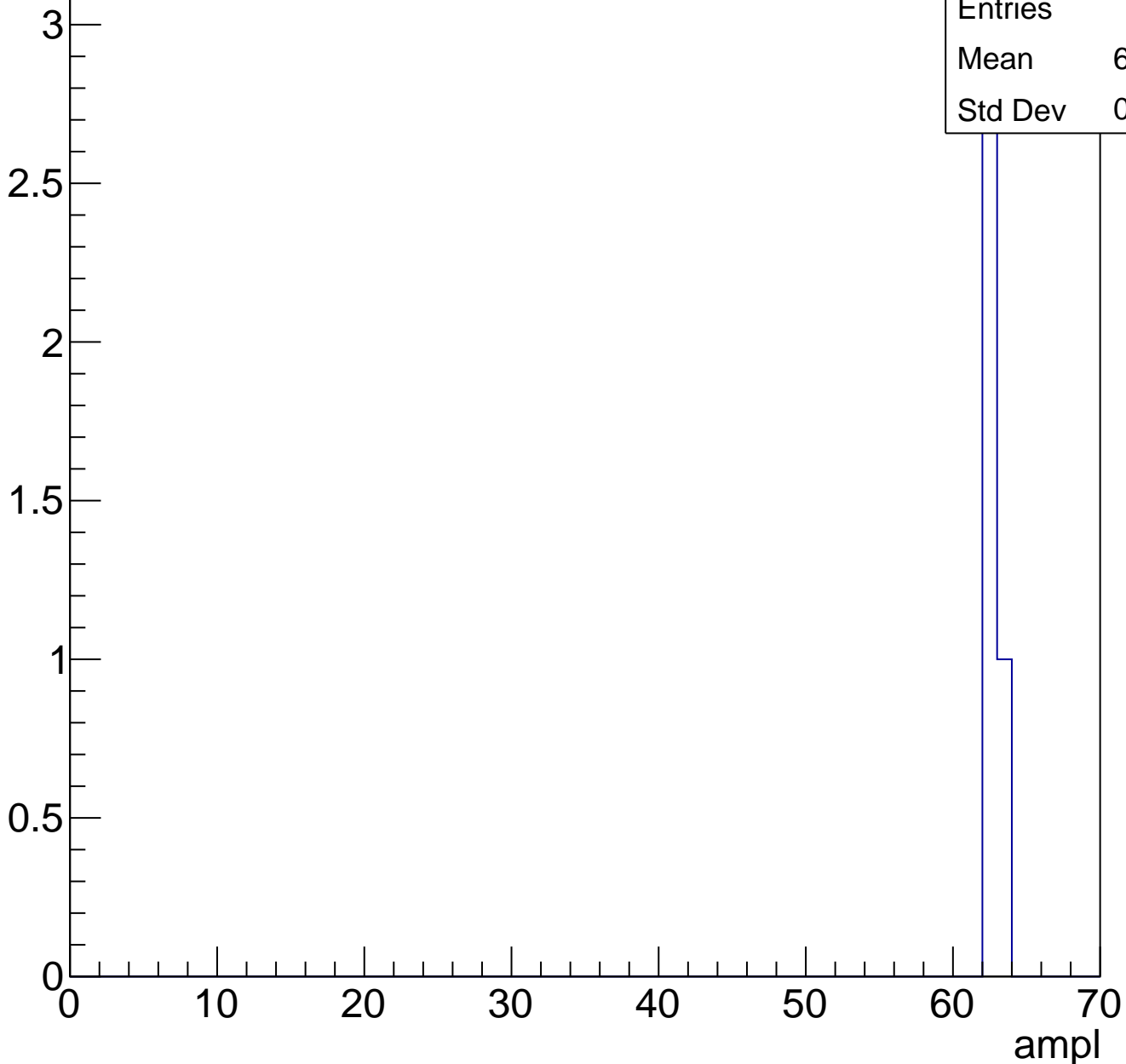
Entries	46
Mean	58.61
Std Dev	9.116



# B0L000S, U25-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch10, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	28.52
Std Dev	3.137

**Gaus mean : 28.8533**

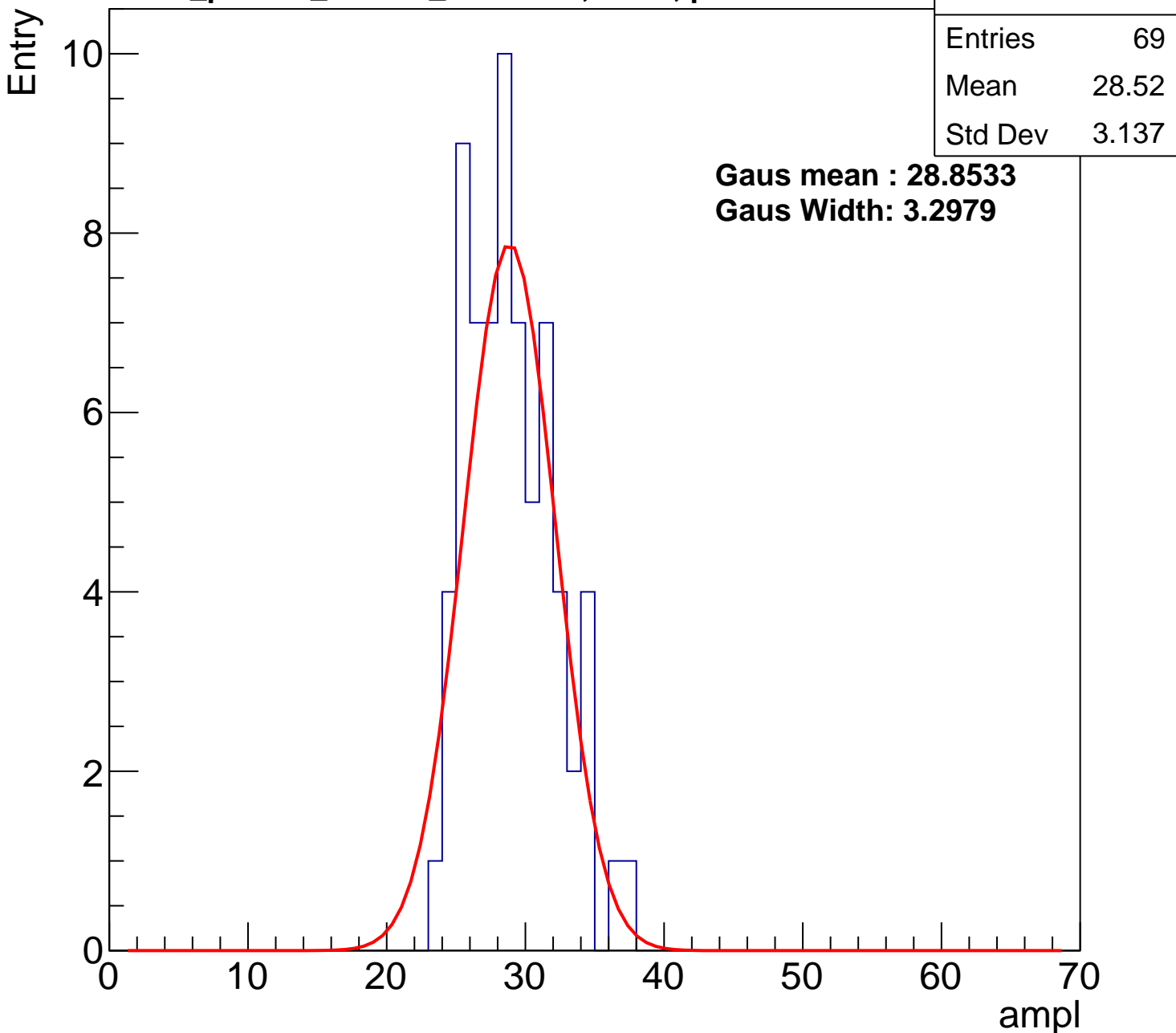
**Gaus Width: 3.2979**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch10, adc1

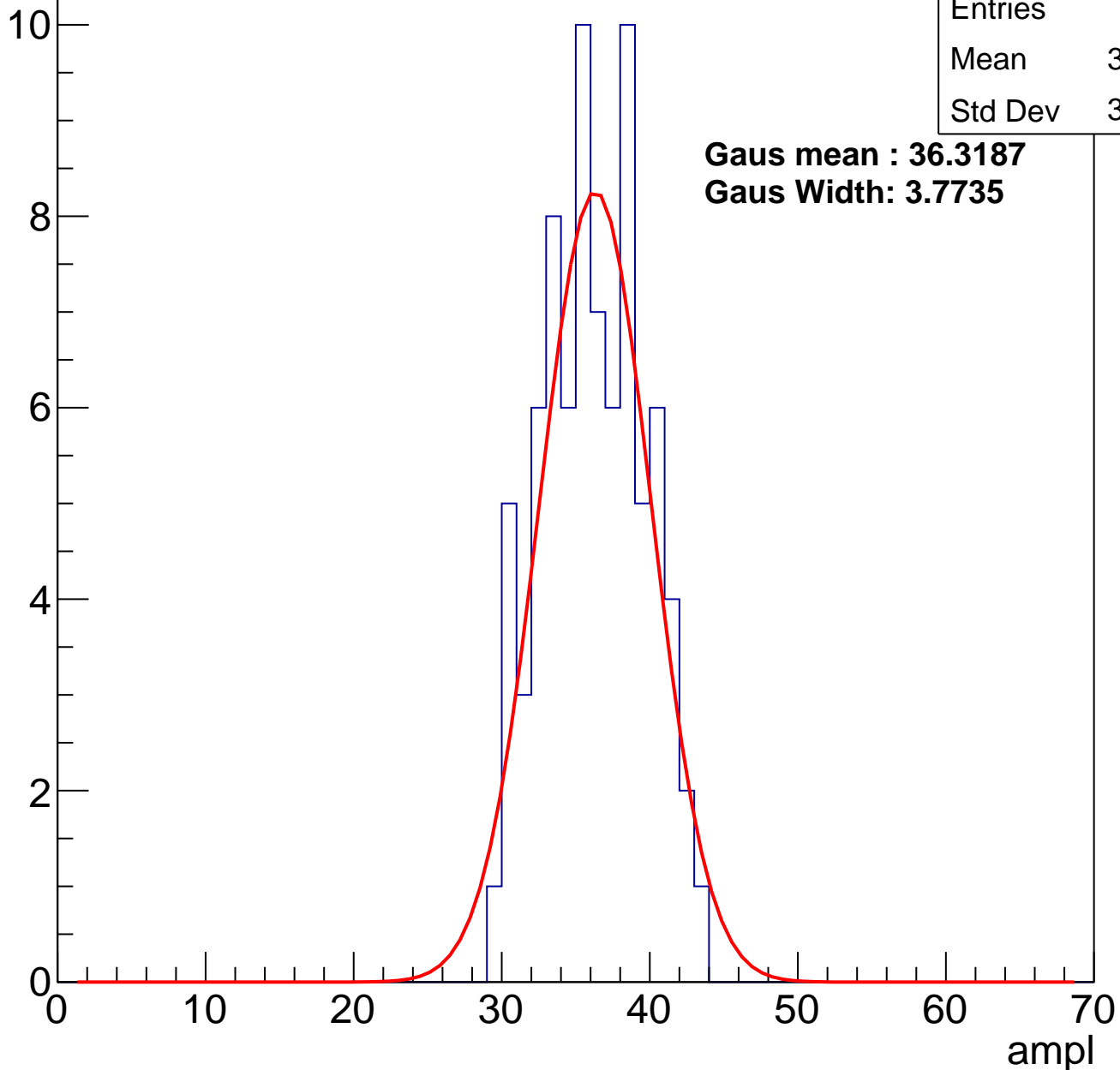
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	35.77
Std Dev	3.373

**Gaus mean : 36.3187**

**Gaus Width: 3.7735**

Entry



# B0L000S, U25-ch10, adc2

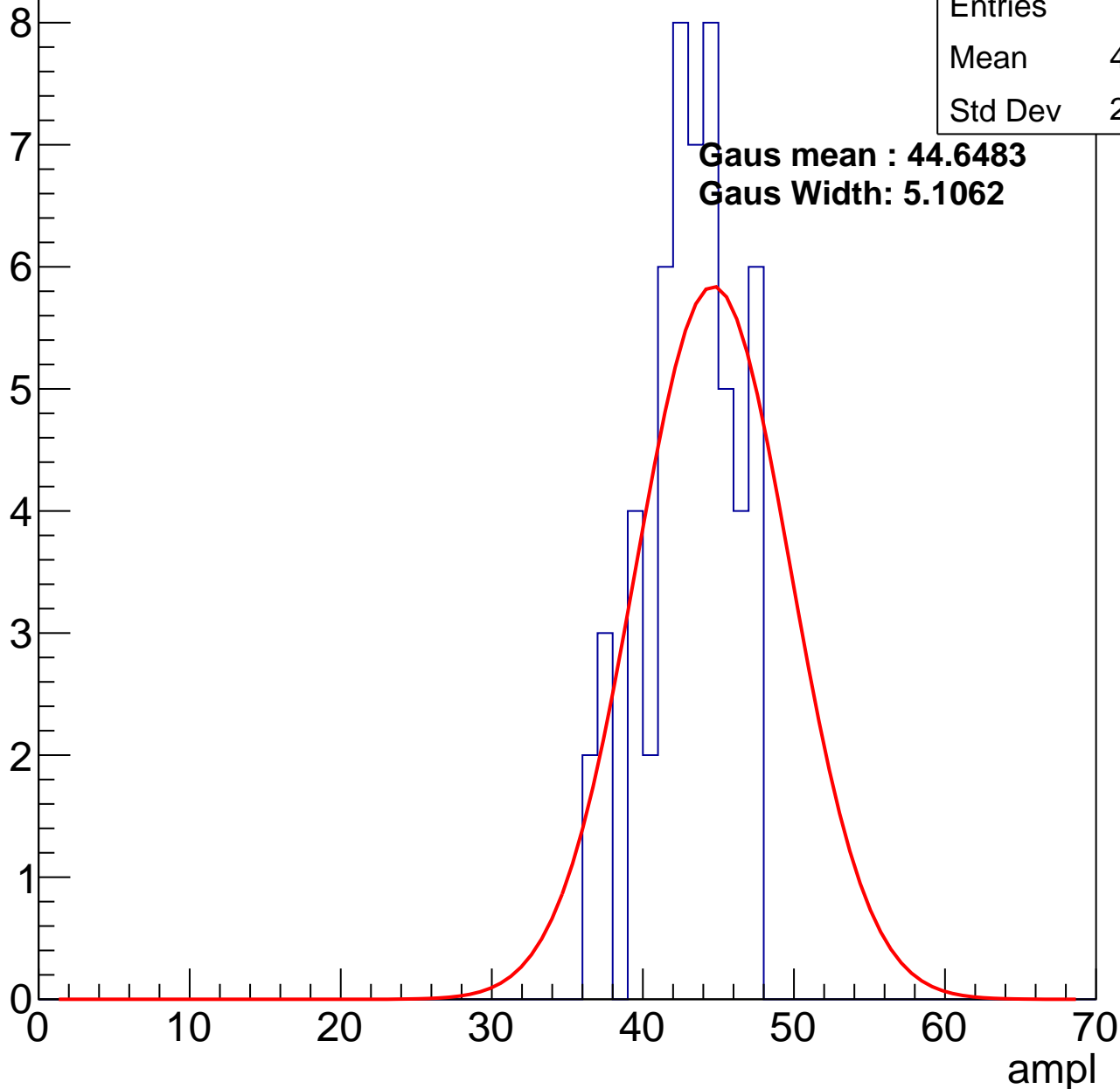
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	42.64
Std Dev	2.932

**Gaus mean : 44.6483**

**Gaus Width: 5.1062**



# B0L000S, U25-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	59
Mean	48.25
Std Dev	3.559

Entry

10

8

6

4

2

0

0

10

20

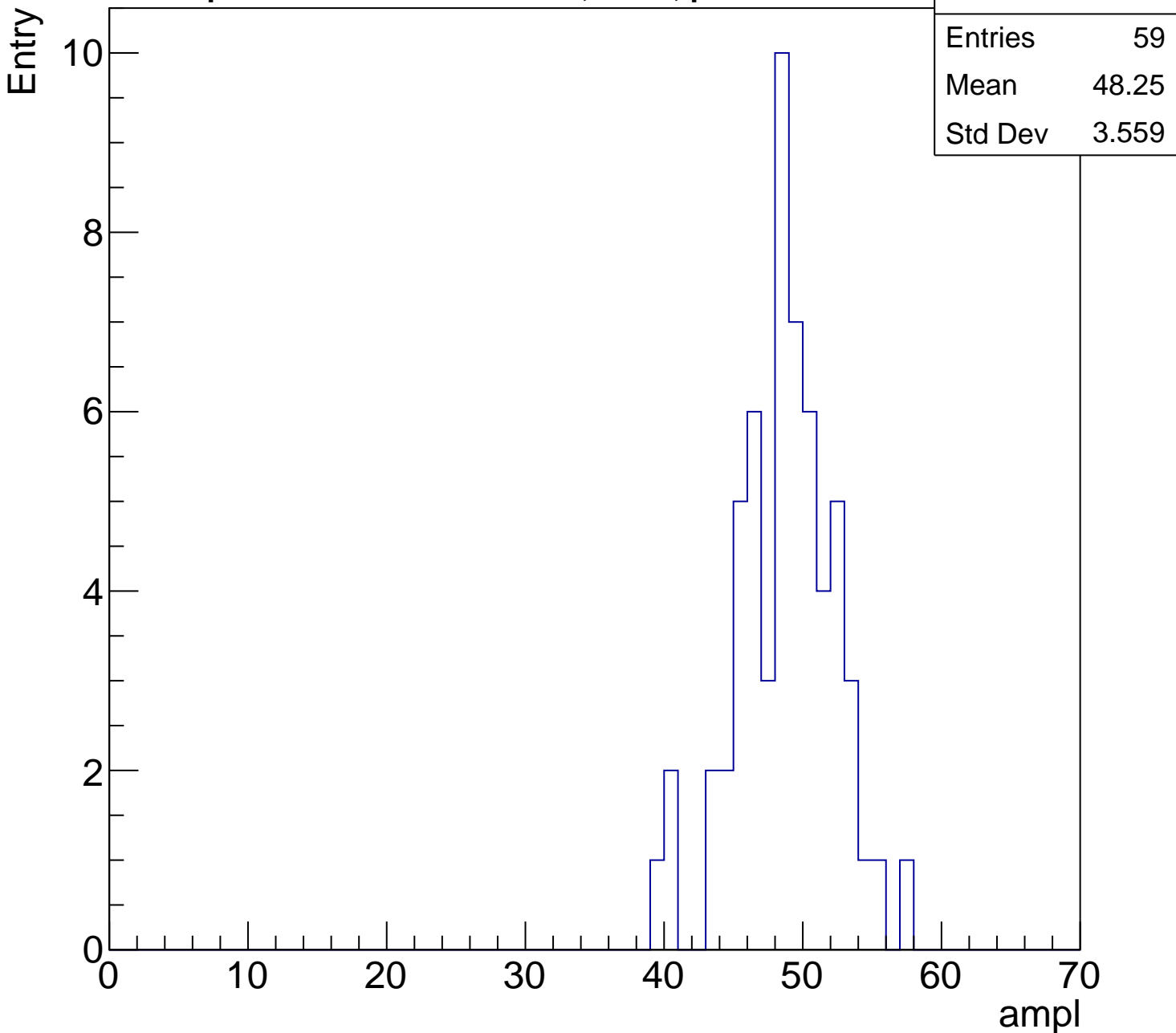
30

40

50

60

ampl

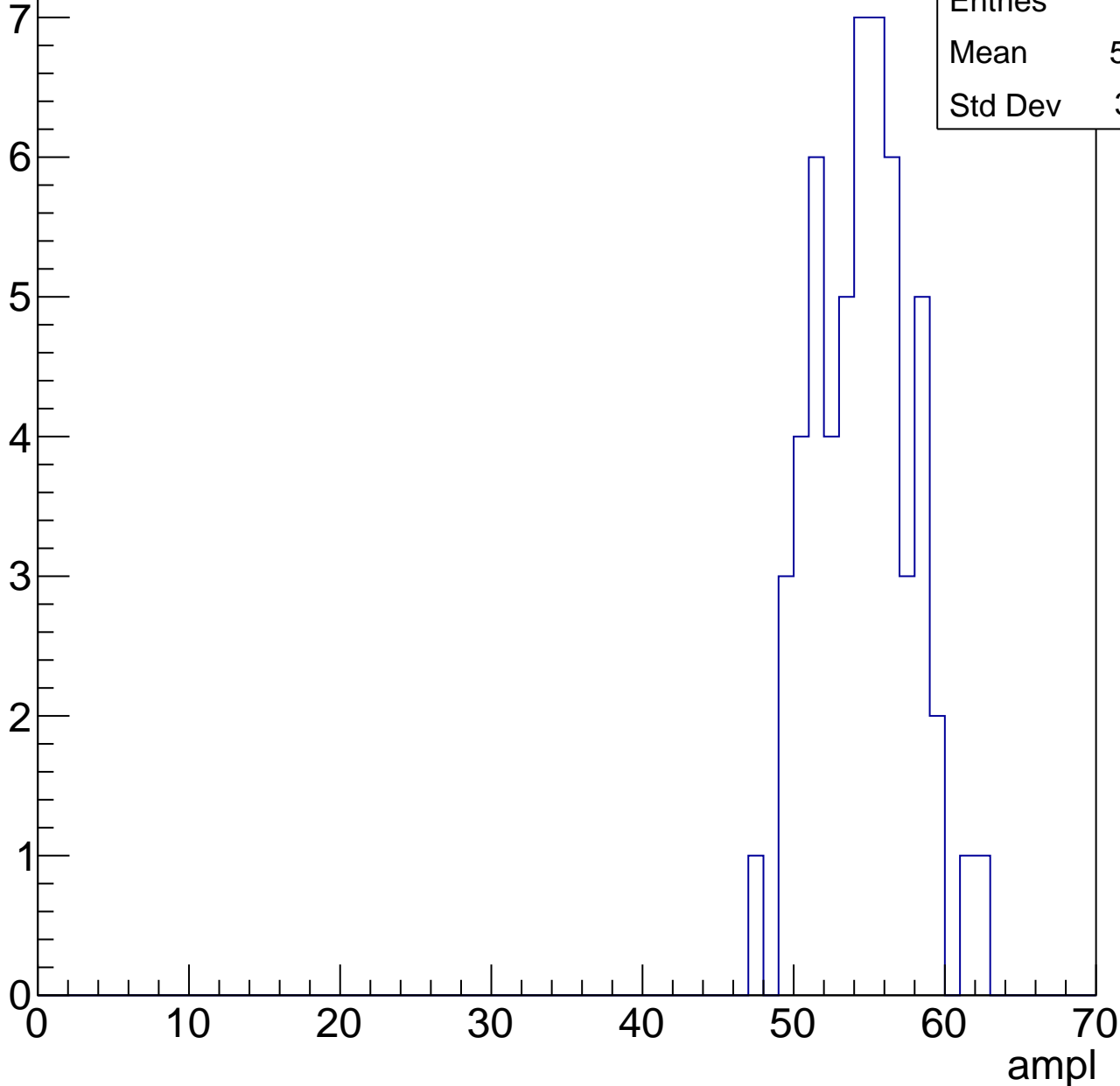


# B0L000S, U25-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

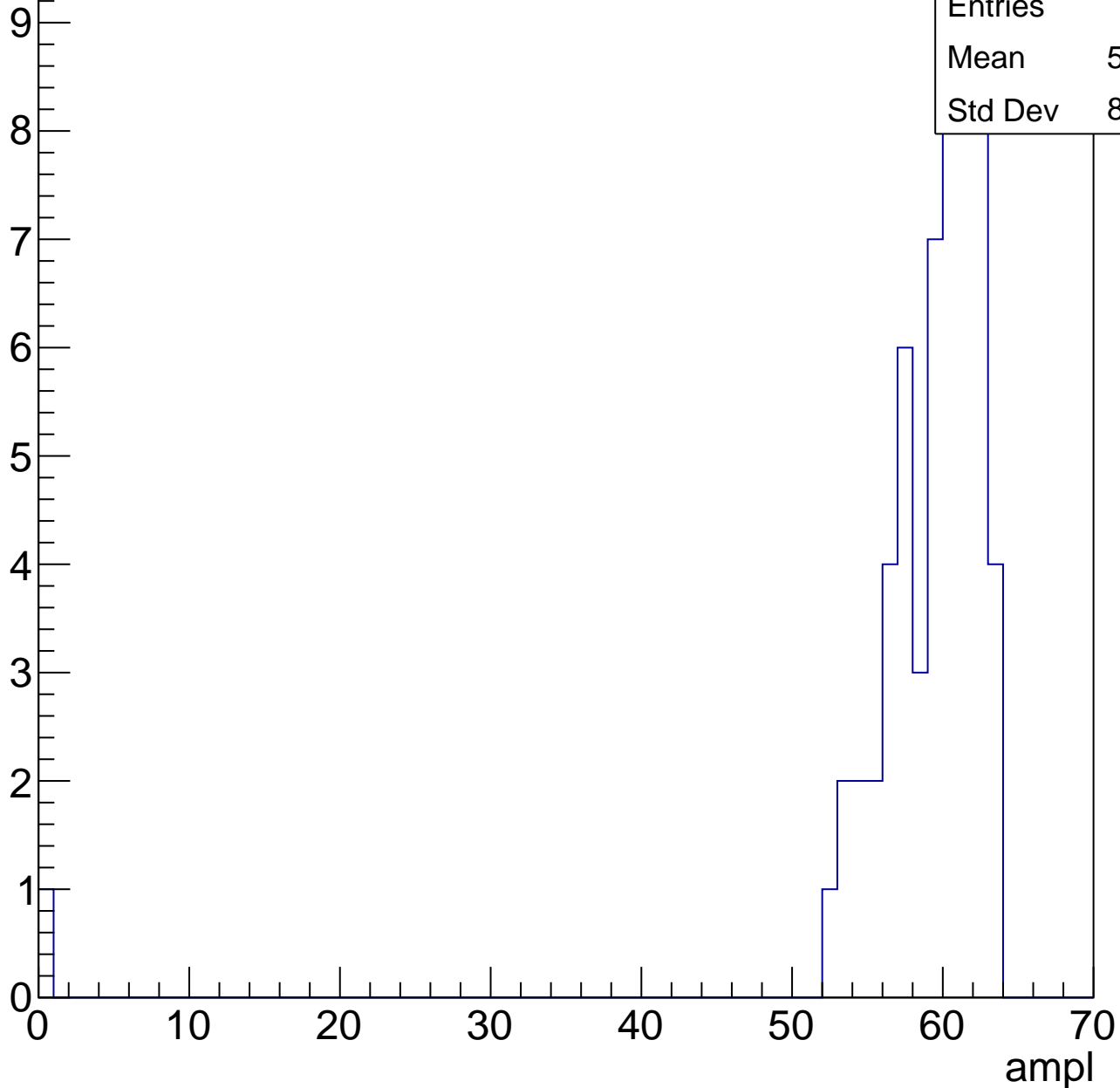
Entries	55
Mean	54.07
Std Dev	3.201



# B0L000S, U25-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

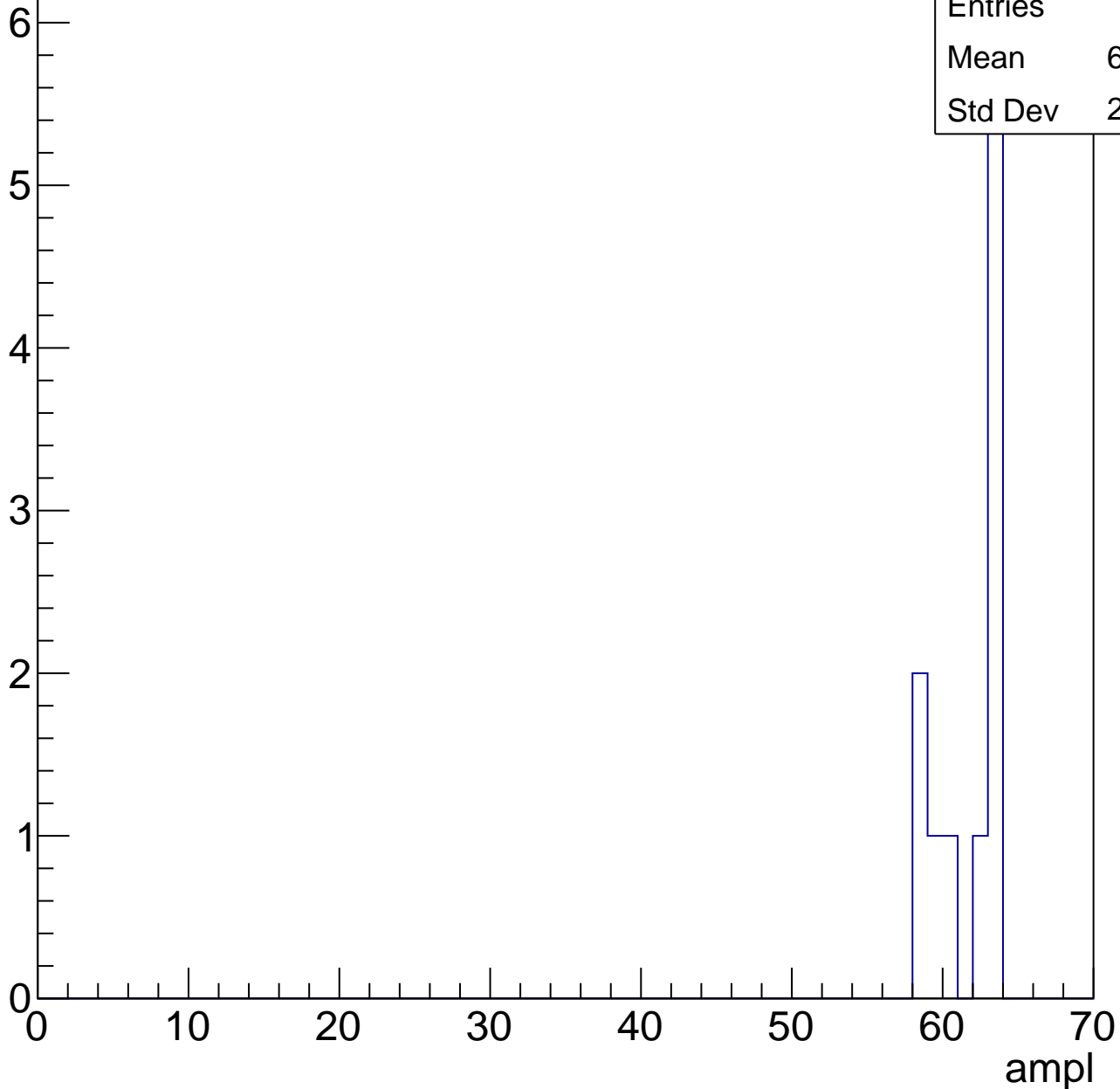


# B0L000S, U25-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	11
Mean	61.36
Std Dev	2.057

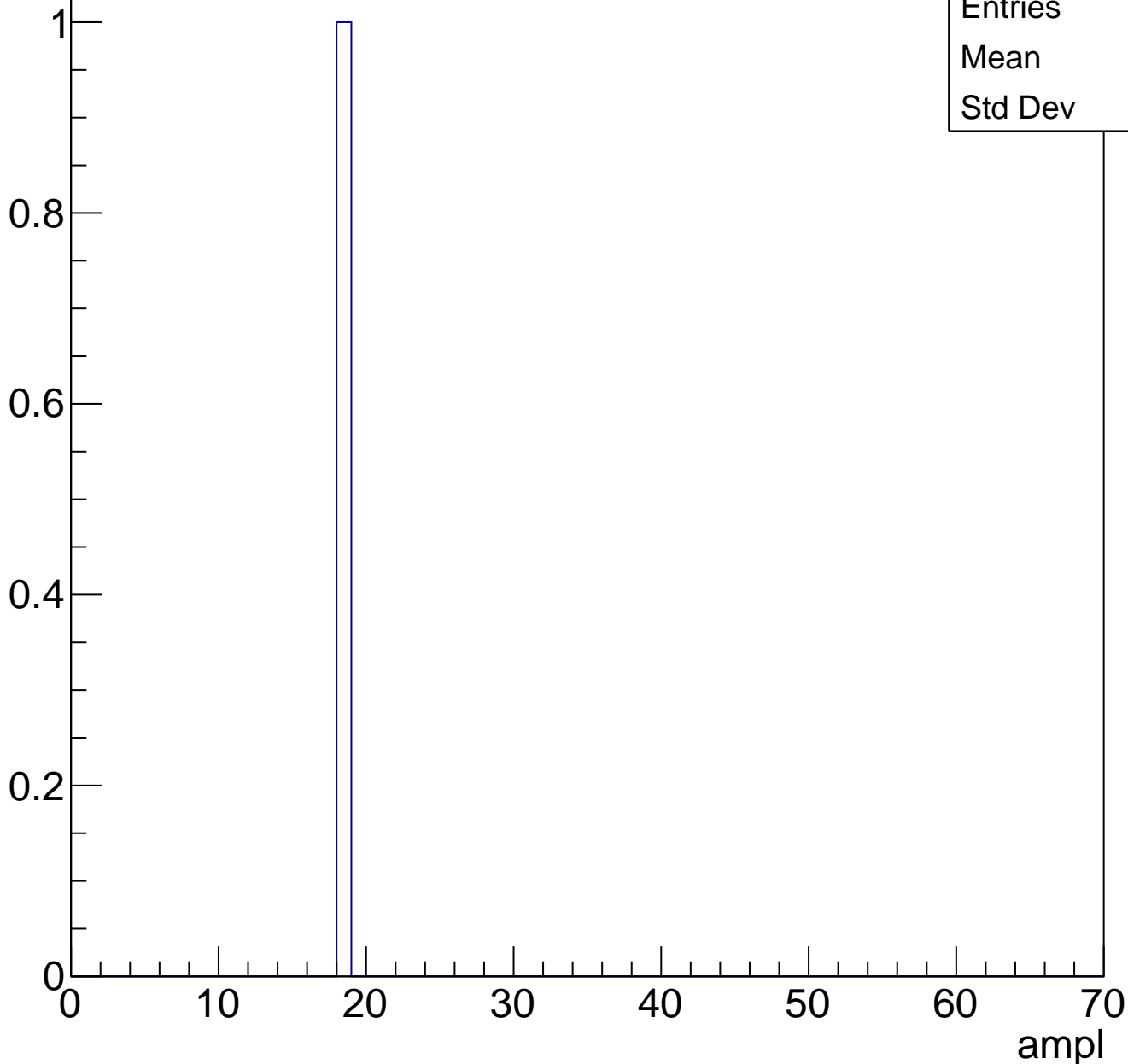




# B0L000S, U25-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch11, adc0

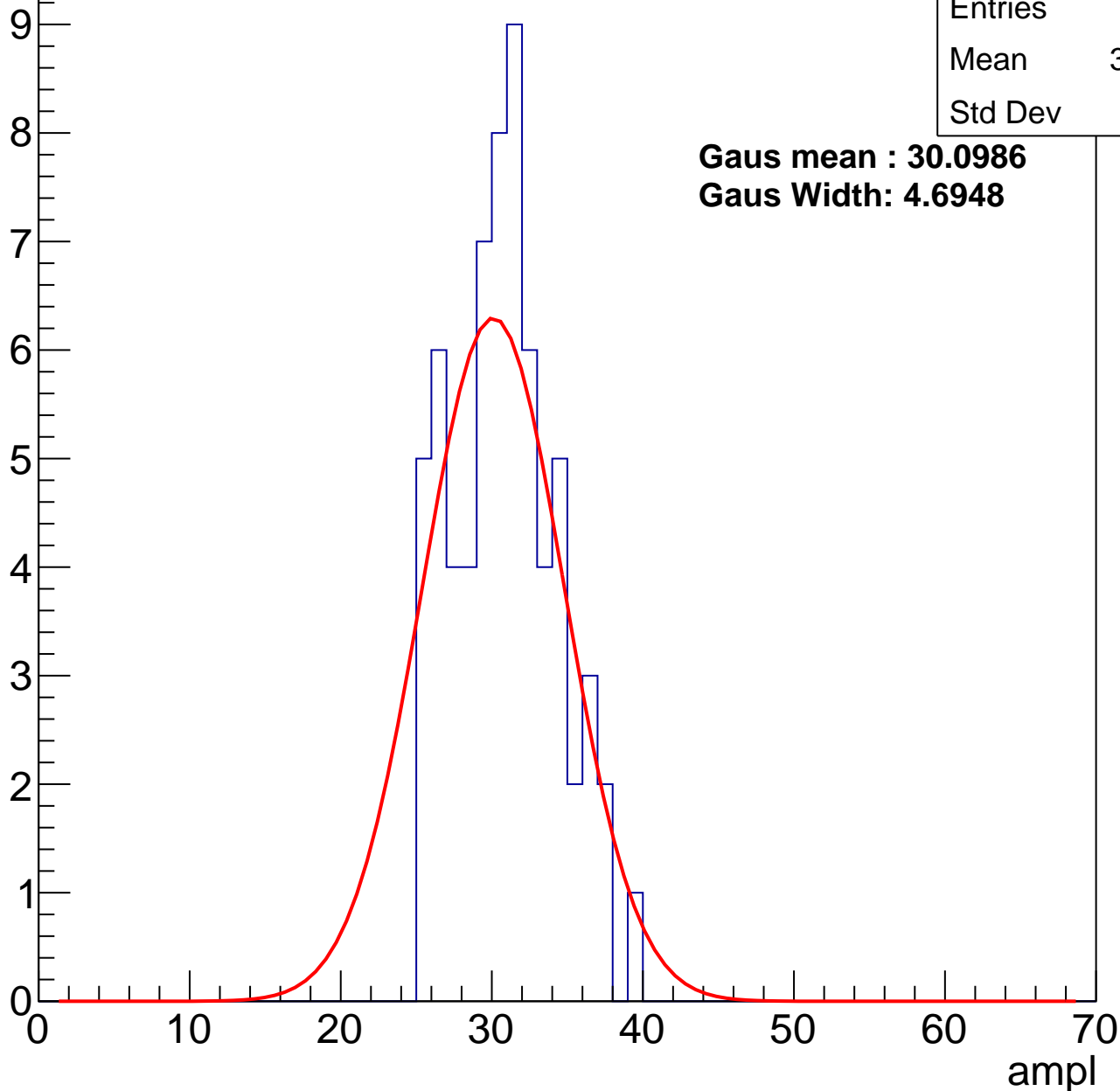
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	30.42
Std Dev	3.38

**Gaus mean : 30.0986**

**Gaus Width: 4.6948**



# B0L000S, U25-ch11, adc1

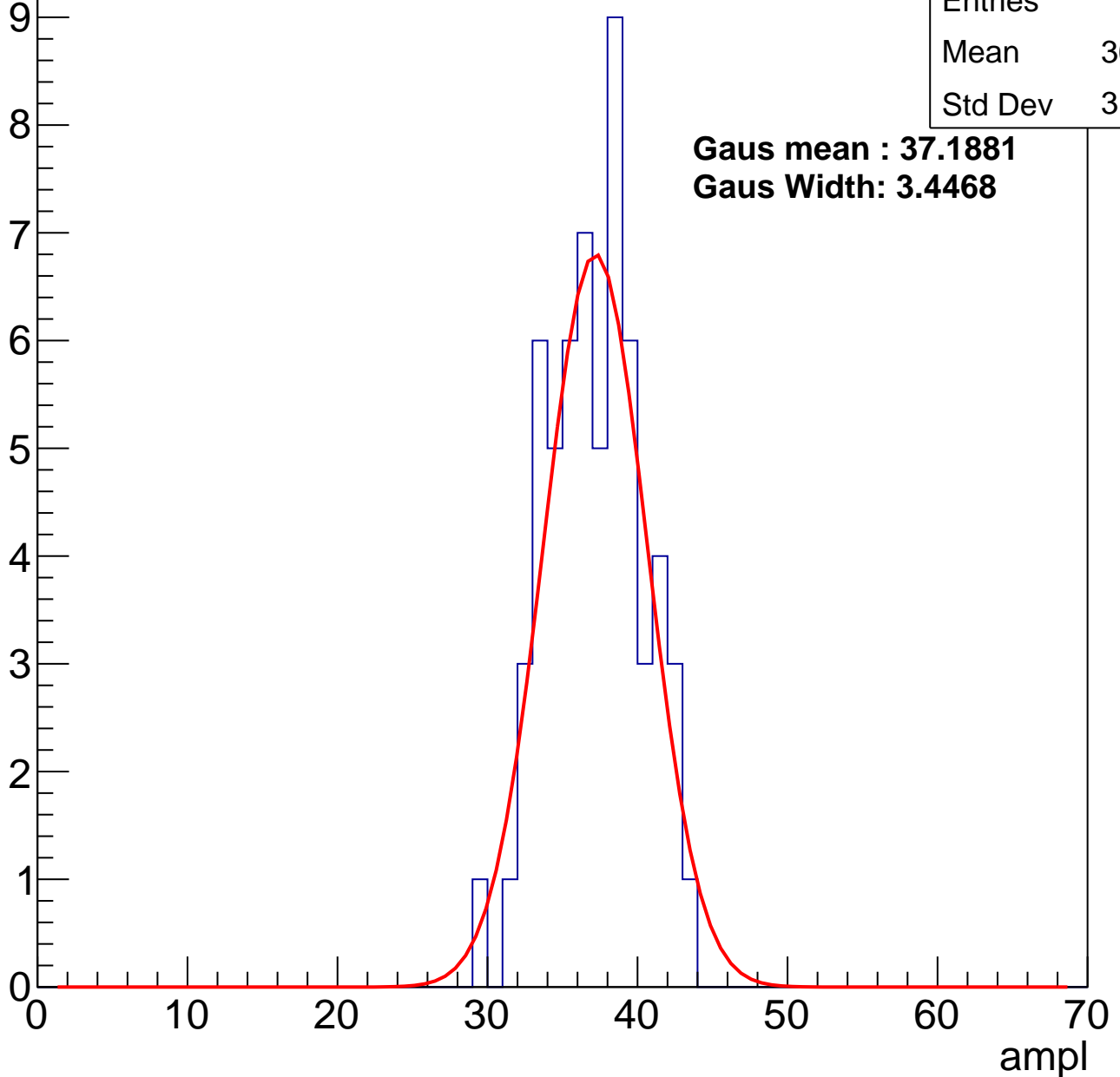
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.67
Std Dev	3.097

**Gaus mean : 37.1881**

**Gaus Width: 3.4468**



# B0L000S, U25-ch11, adc2

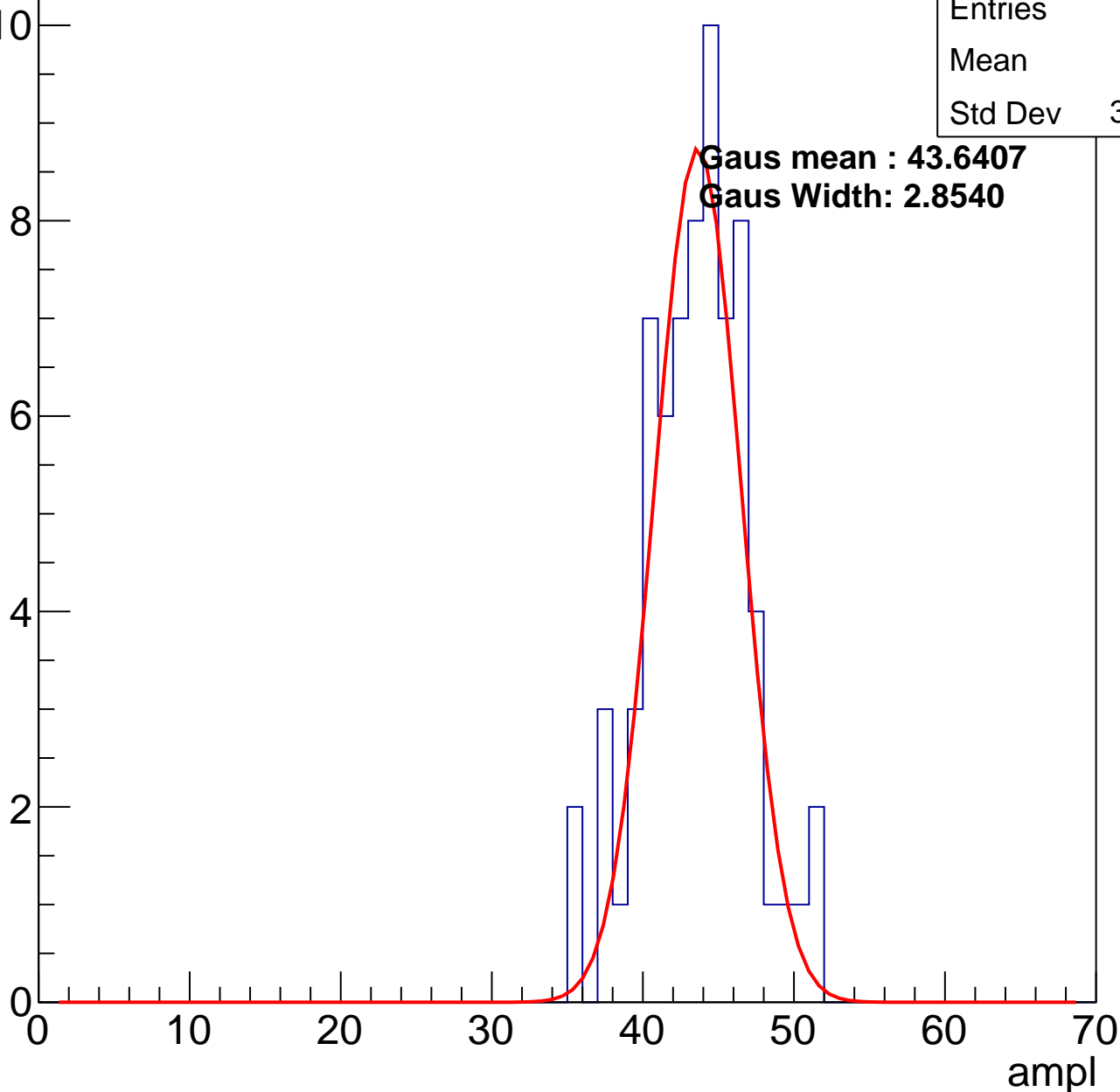
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	43.1
Std Dev	3.387

**Gaus mean : 43.6407**

**Gaus Width: 2.8540**

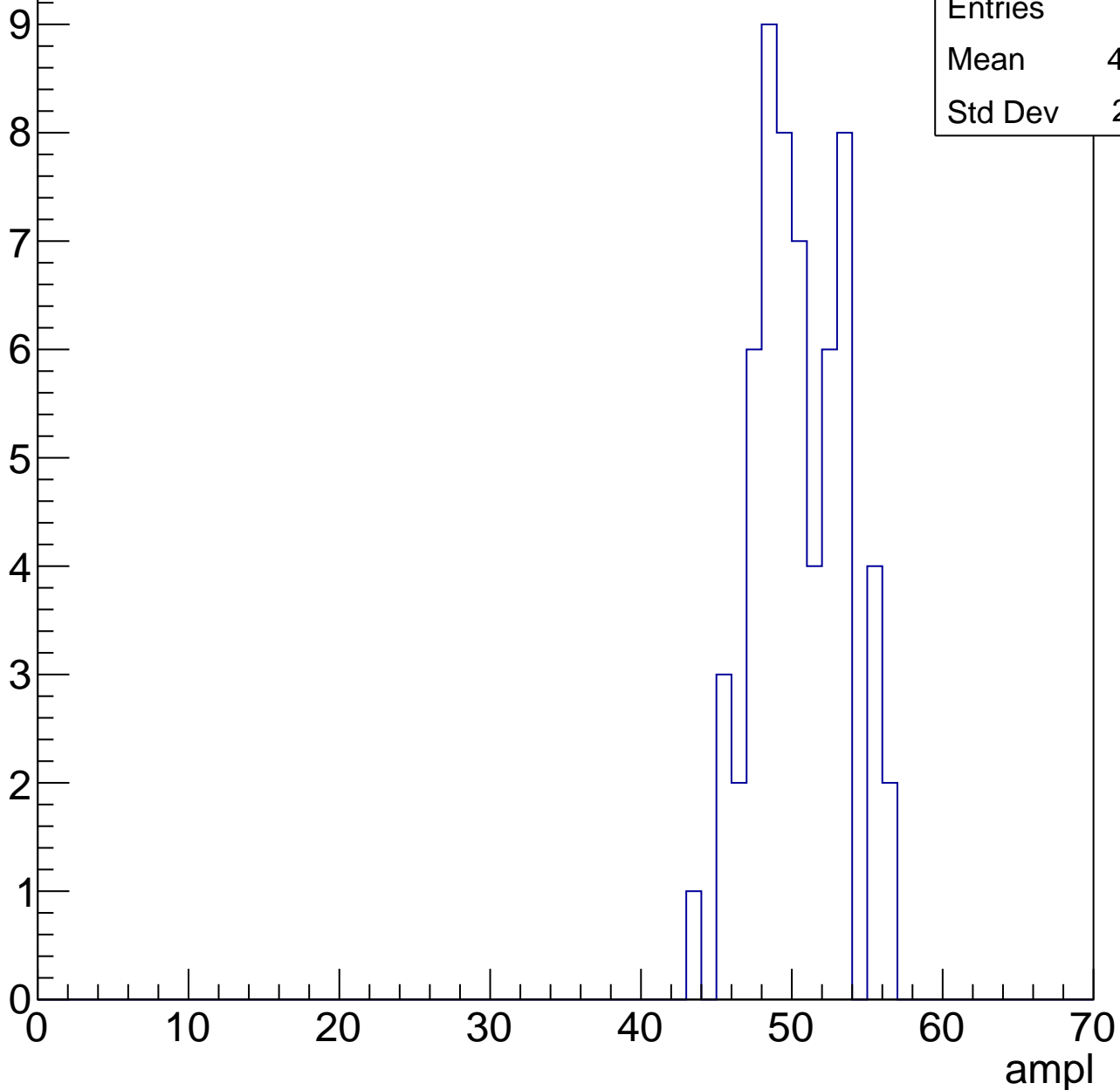


# B0L000S, U25-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	49.97
Std Dev	2.961



# B0L000S, U25-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

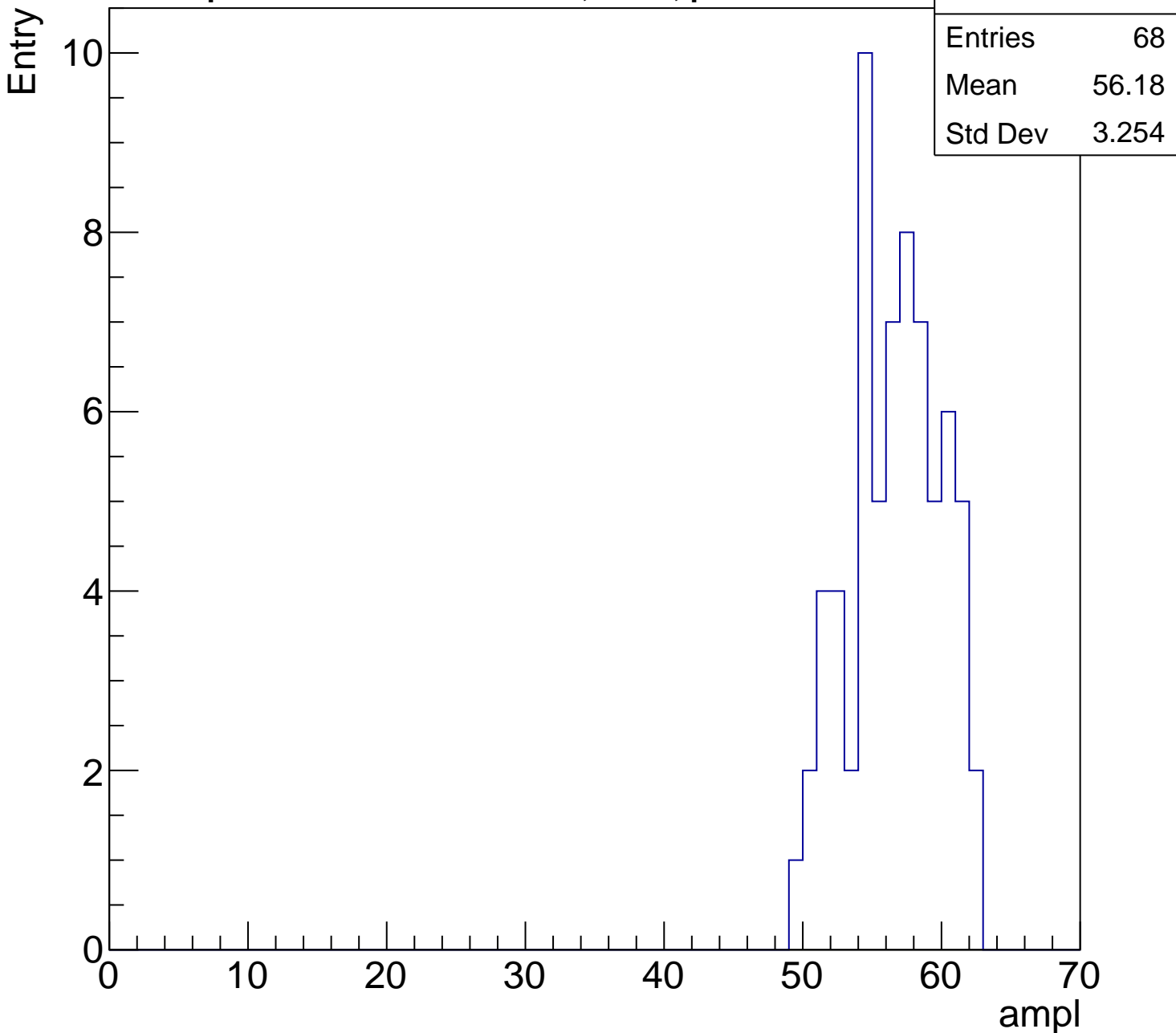
Entries	68
Mean	56.18
Std Dev	3.254

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

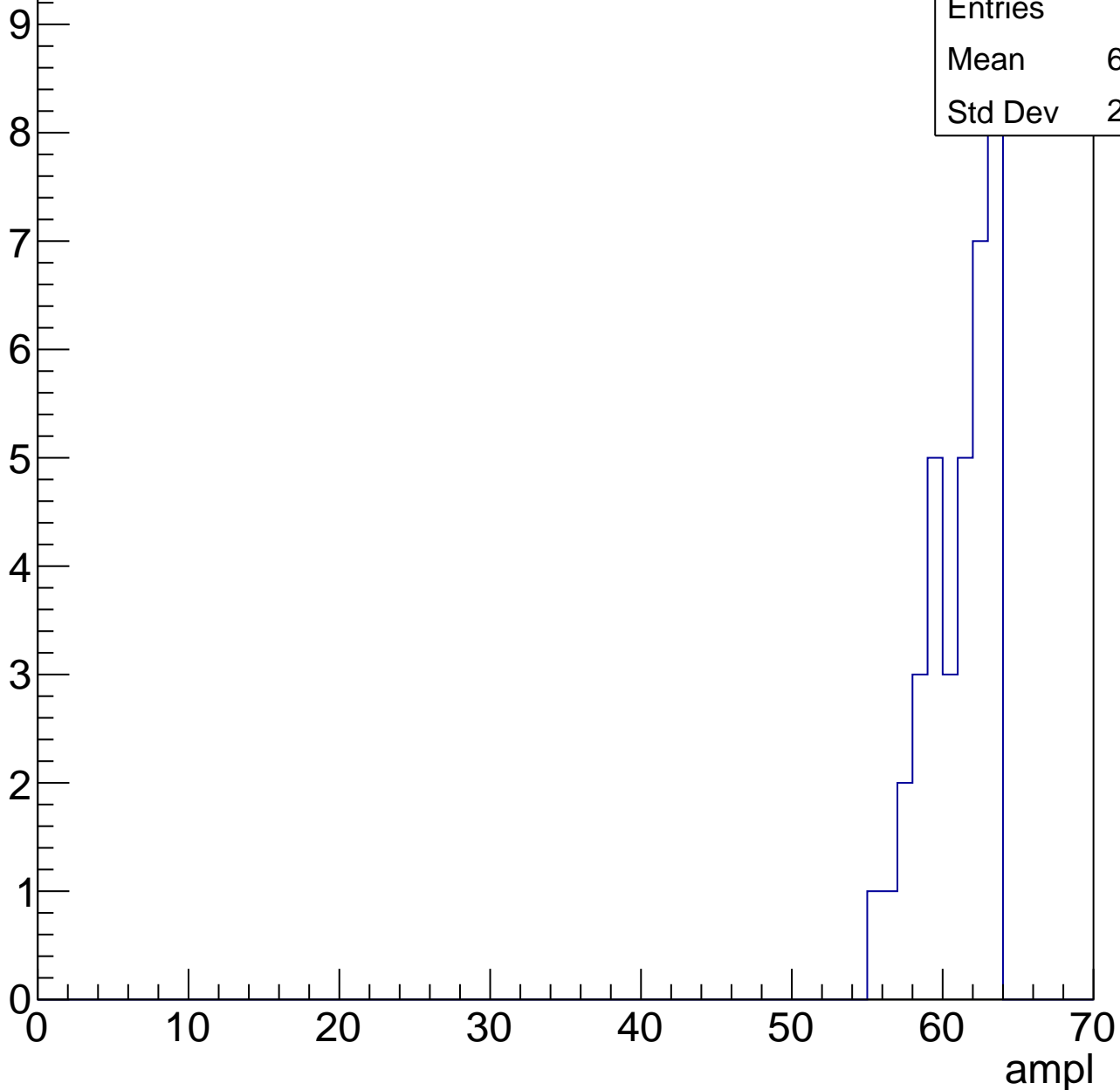


# B0L000S, U25-ch11, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

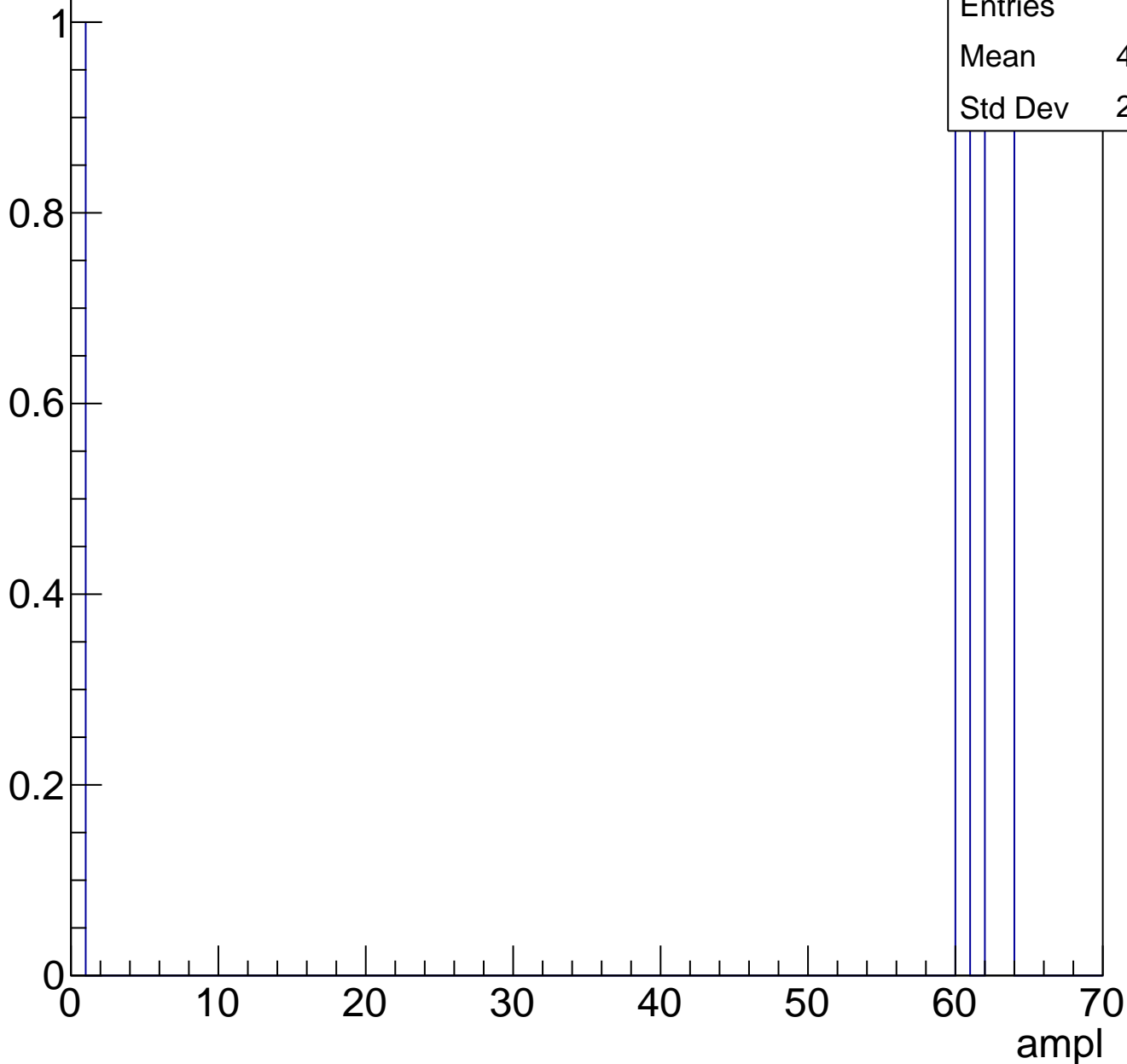
Entries	36
Mean	60.56
Std Dev	2.229



# B0L000S, U25-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U25-ch12, adc0

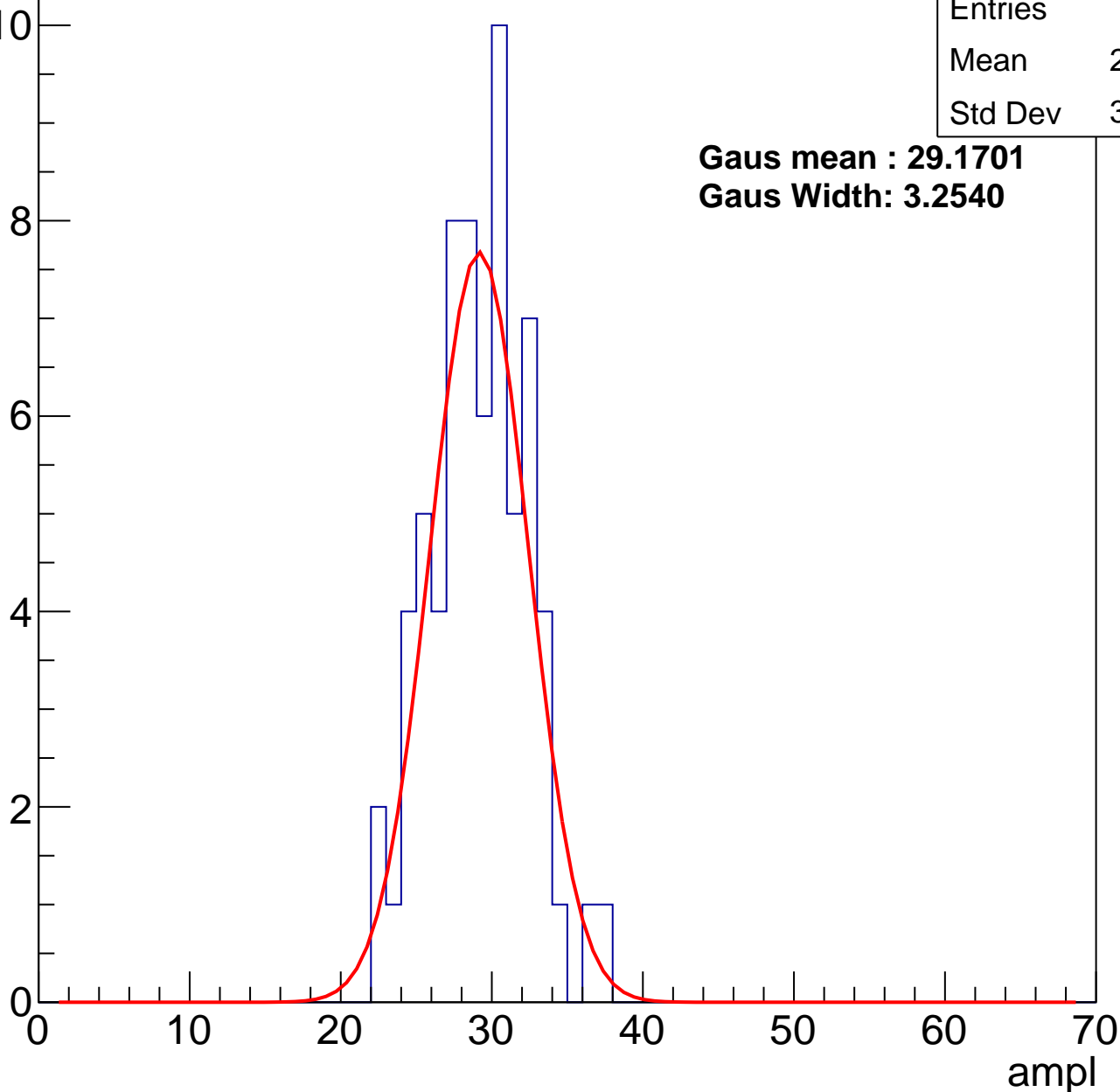
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.72
Std Dev	3.185

**Gaus mean : 29.1701**

**Gaus Width: 3.2540**



# B0L000S, U25-ch12, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	35.27
Std Dev	3.422

**Gaus mean : 35.6622**

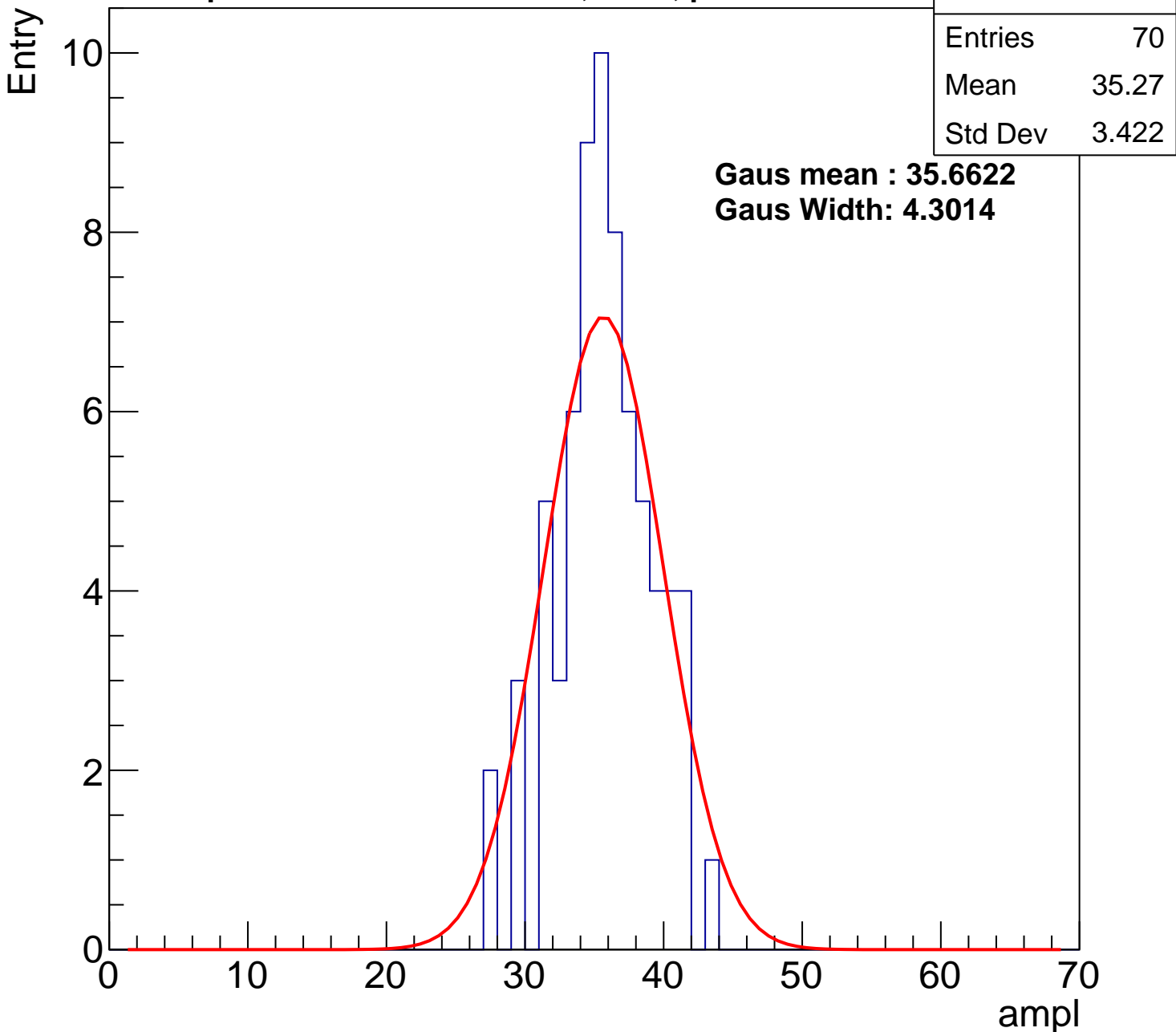
**Gaus Width: 4.3014**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch12, adc2

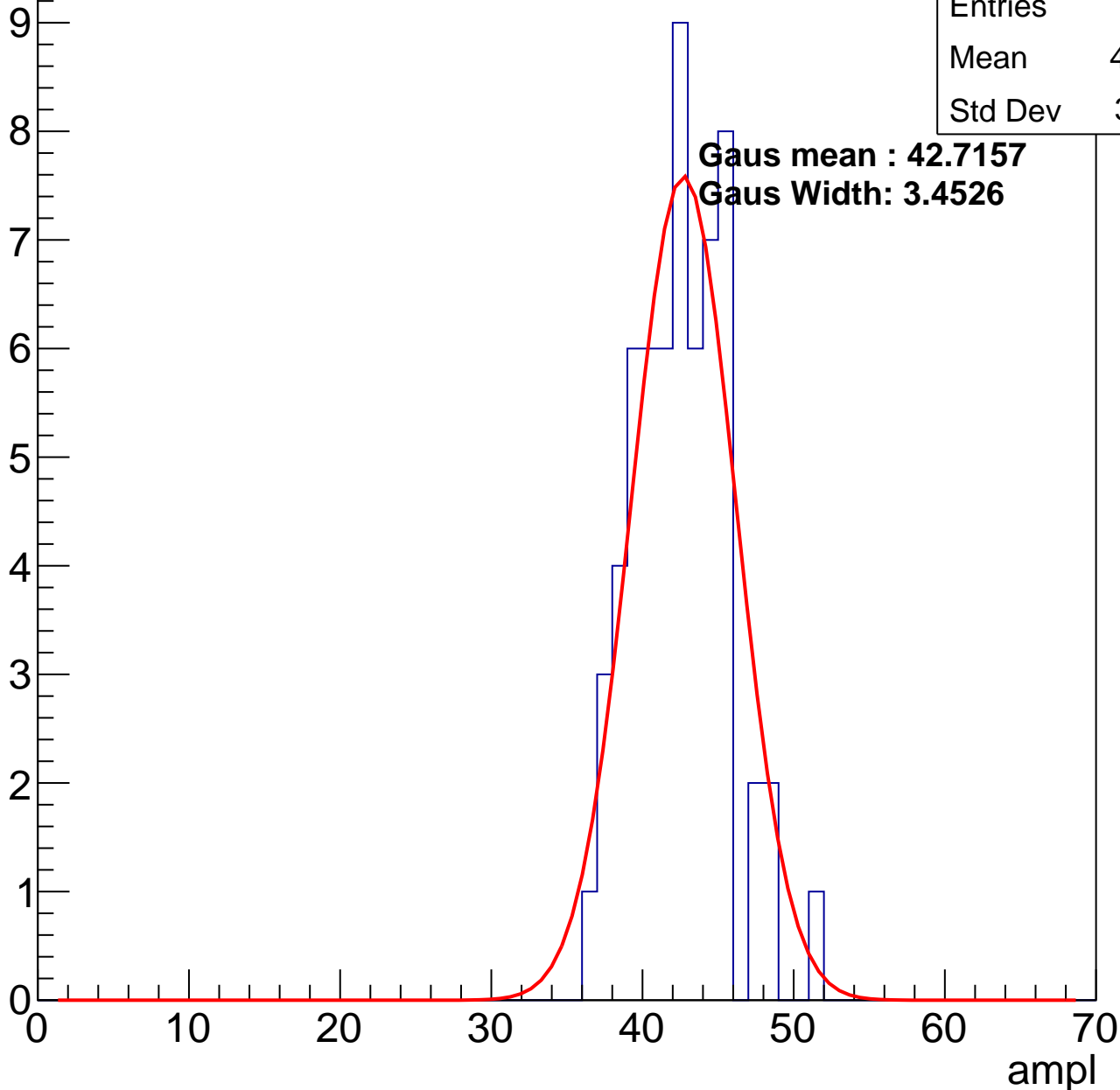
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.03
Std Dev	3.051

**Gaus mean : 42.7157**

**Gaus Width: 3.4526**



# B0L000S, U25-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

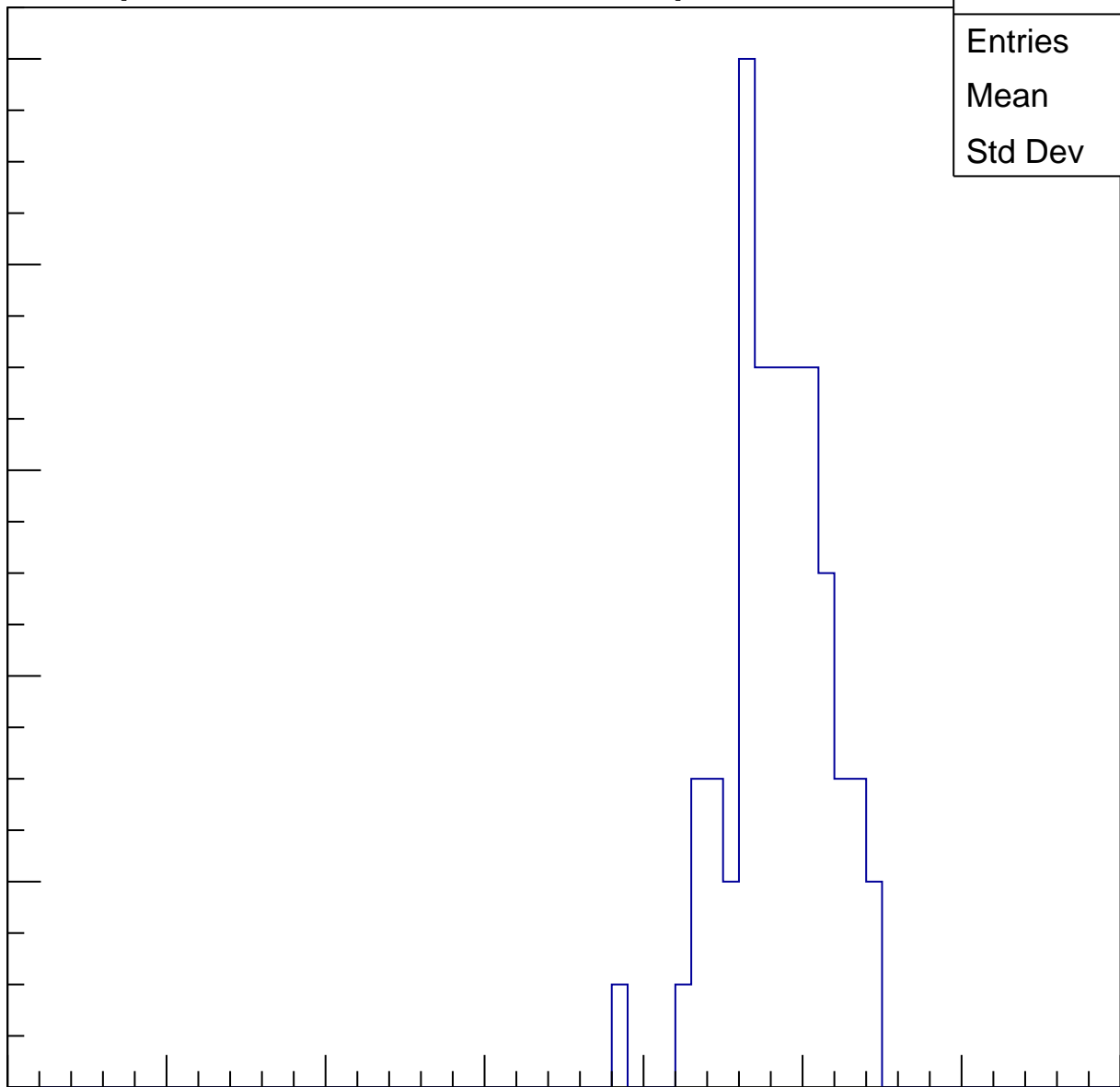
Entries	61
Mean	47.98
Std Dev	3.134

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

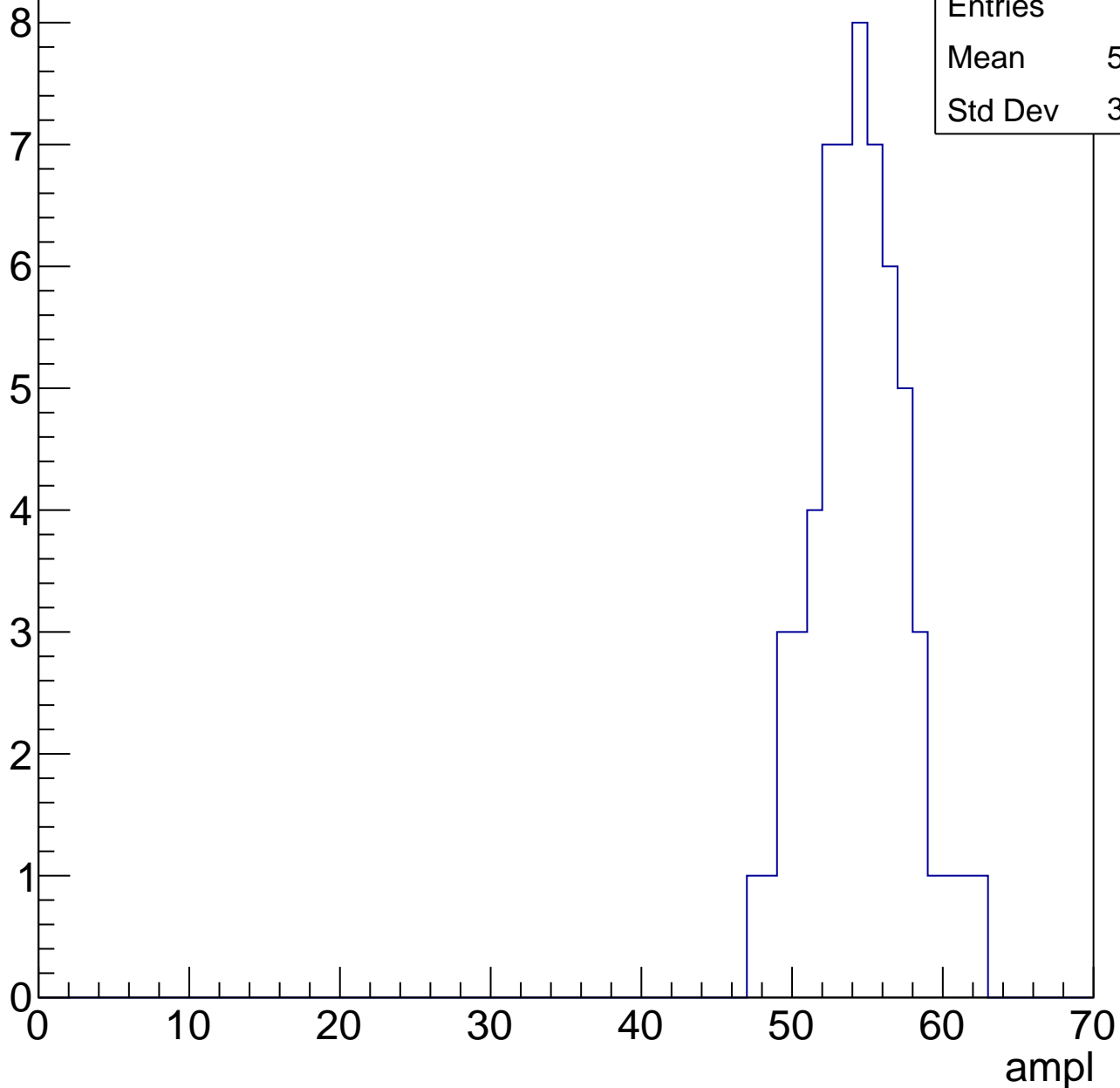


# B0L000S, U25-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	53.98
Std Dev	3.127

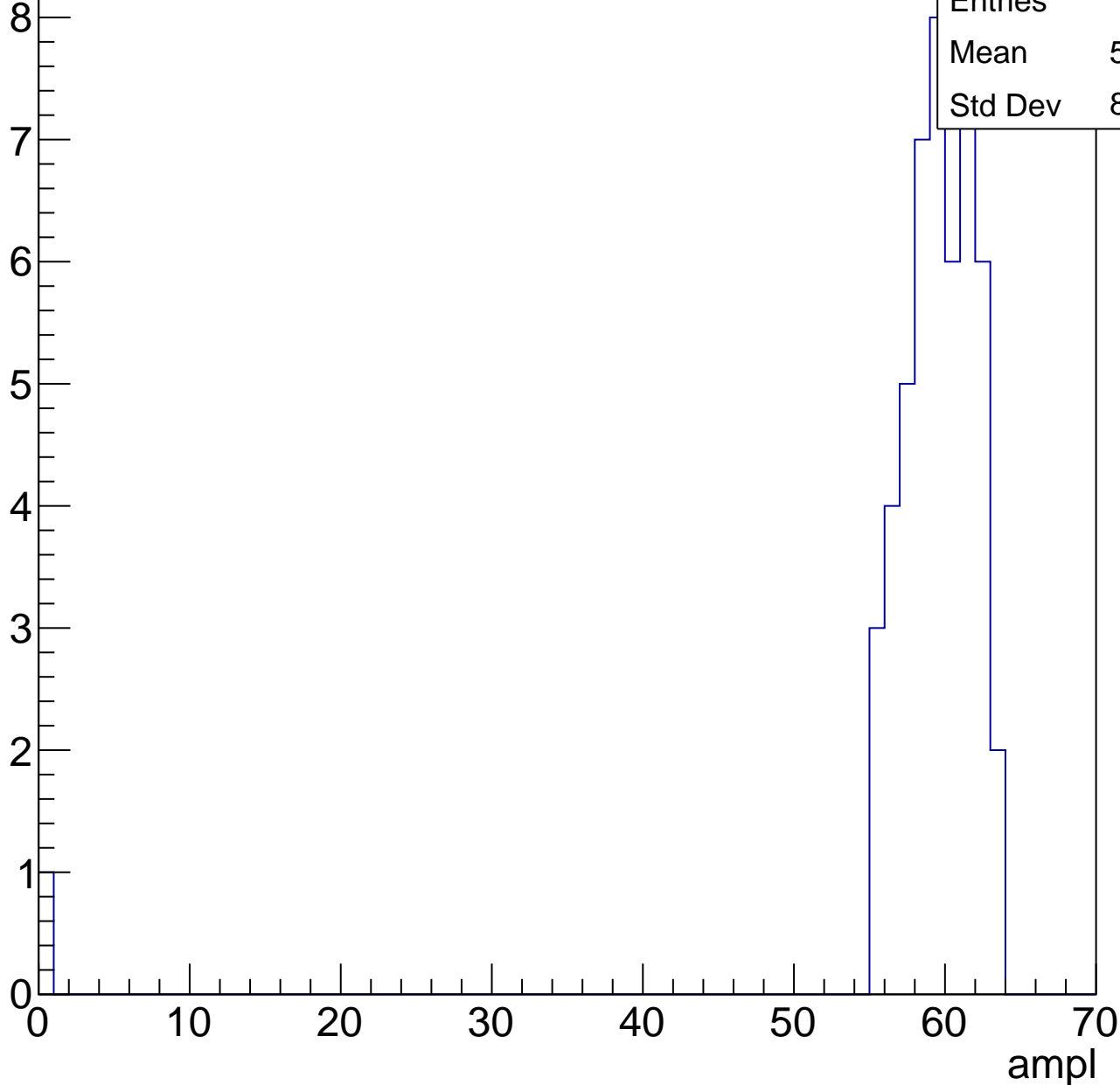


# B0L000S, U25-ch12, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	57.96
Std Dev	8.558

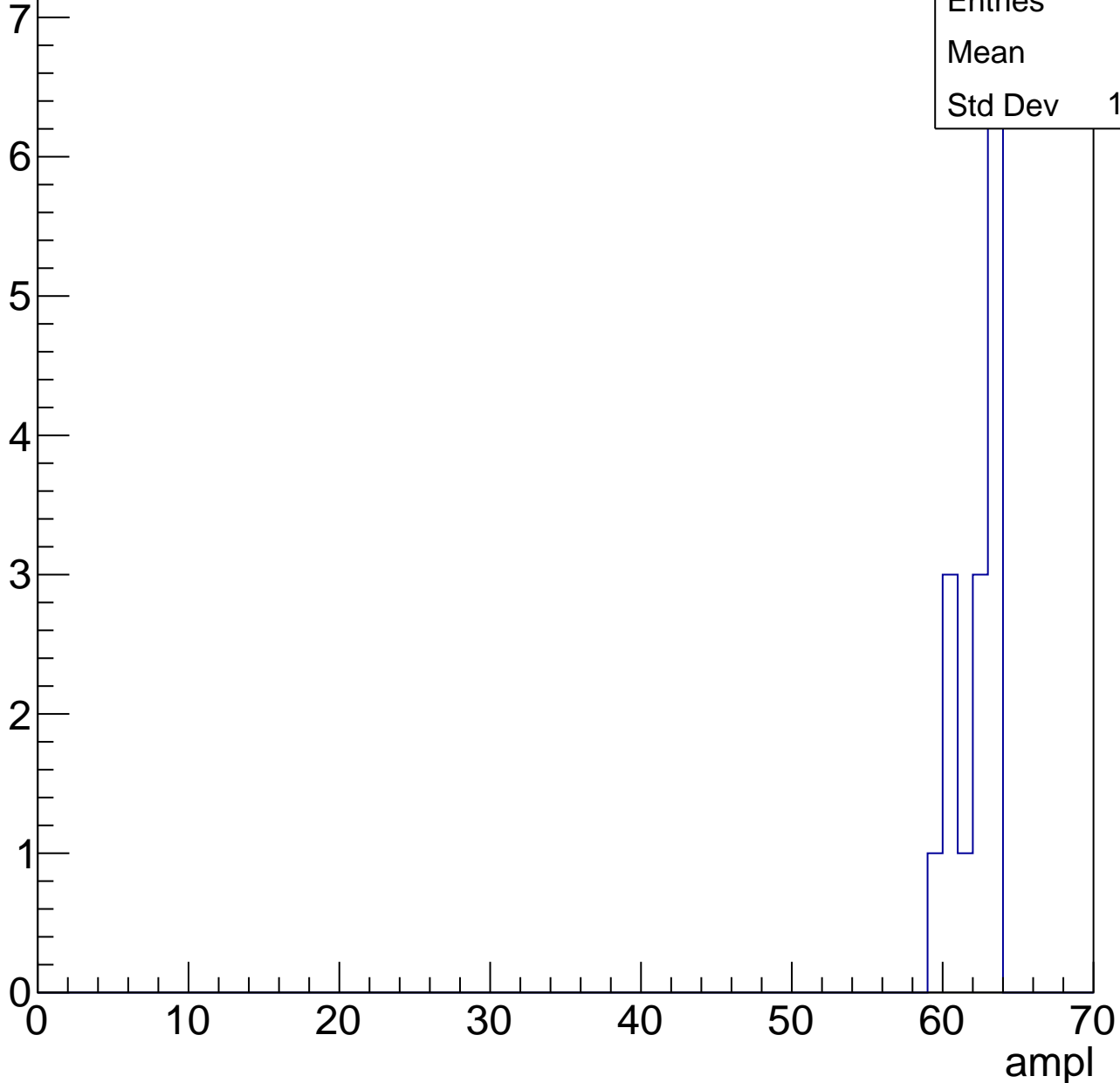


# B0L000S, U25-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	61.8
Std Dev	1.376





# B0L000S, U25-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch13, adc0

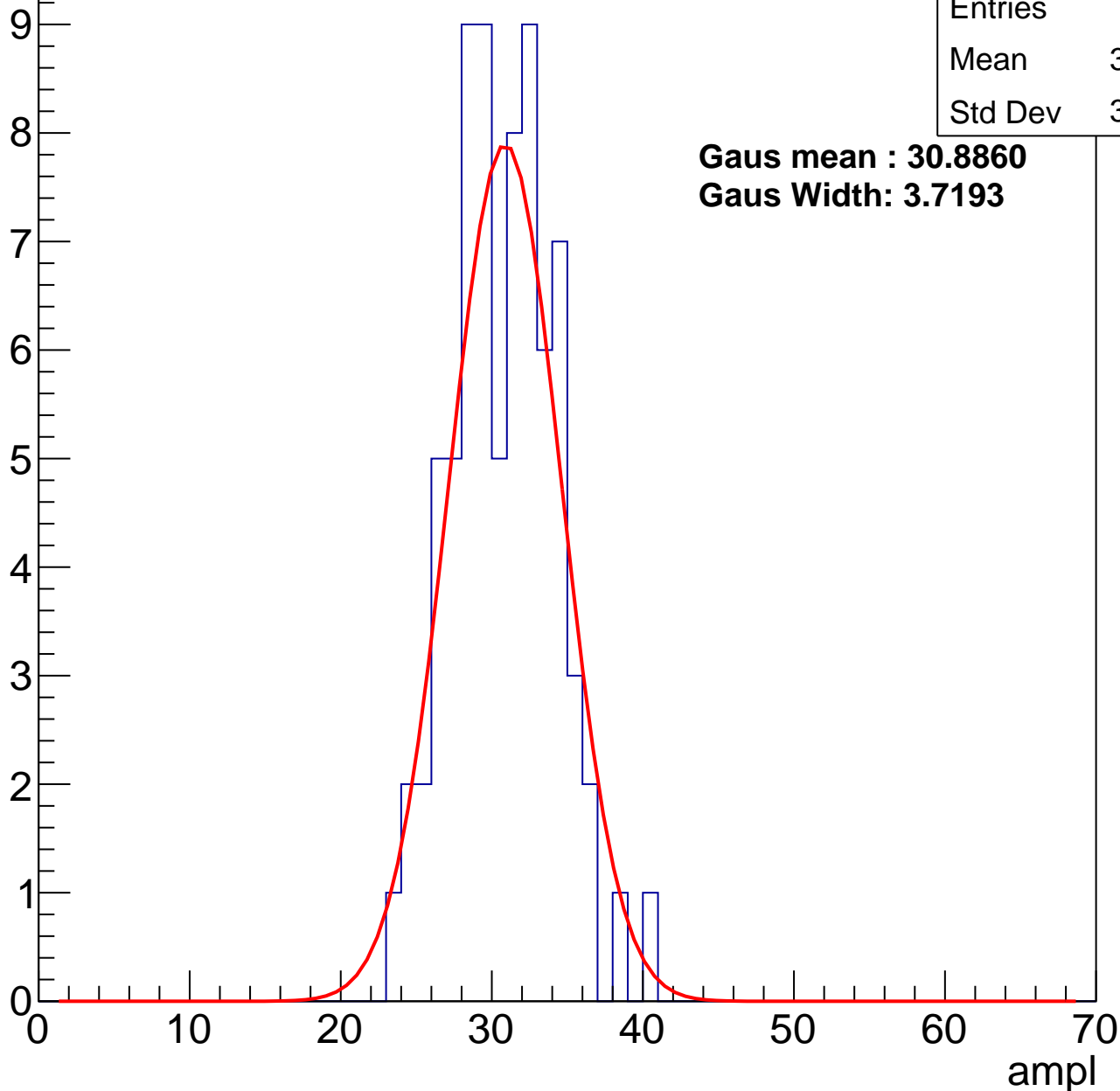
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	30.35
Std Dev	3.372

**Gaus mean : 30.8860**

**Gaus Width: 3.7193**



# B0L000S, U25-ch13, adc1

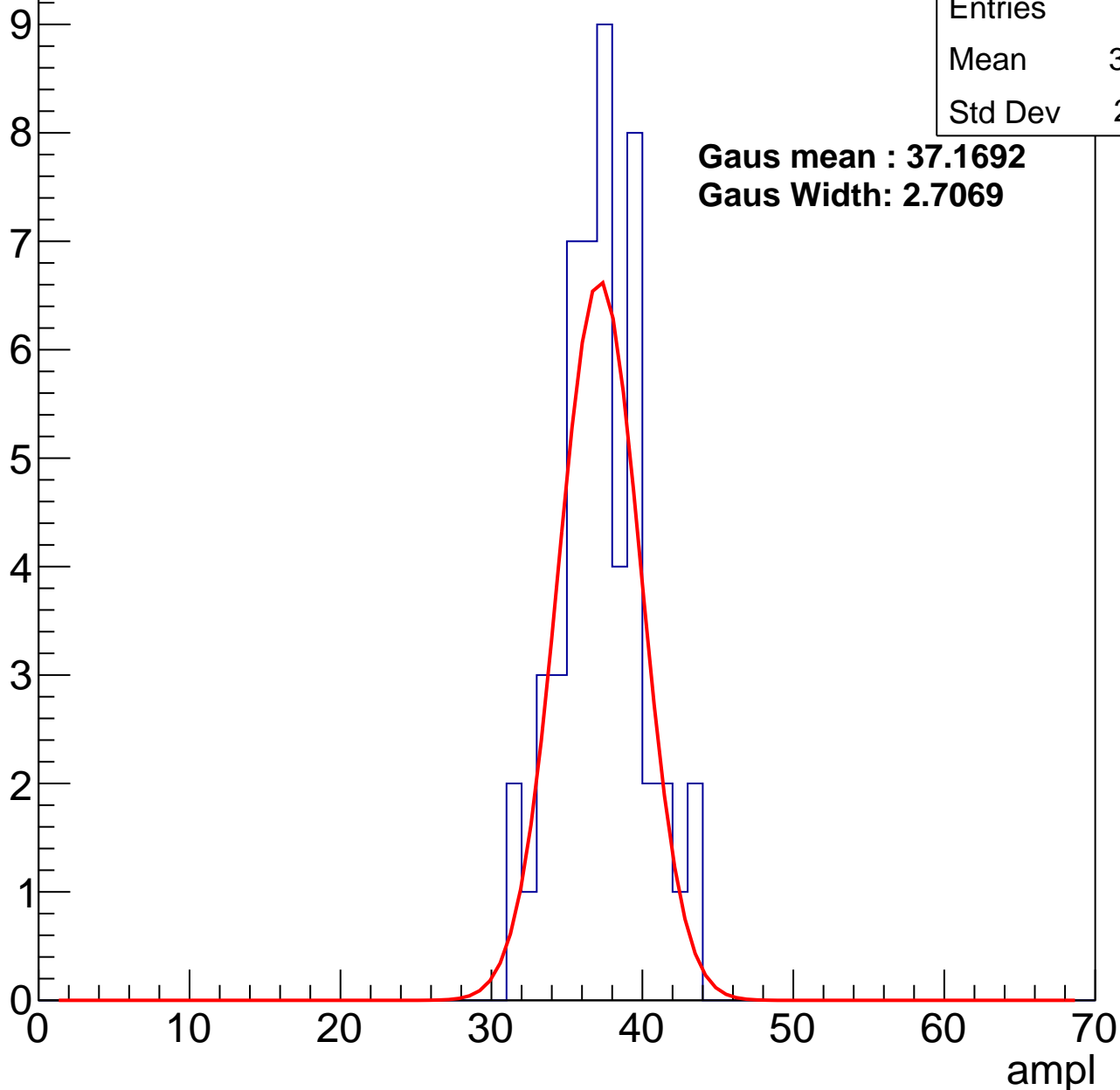
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	36.84
Std Dev	2.761

**Gaus mean : 37.1692**

**Gaus Width: 2.7069**



# B0L000S, U25-ch13, adc2

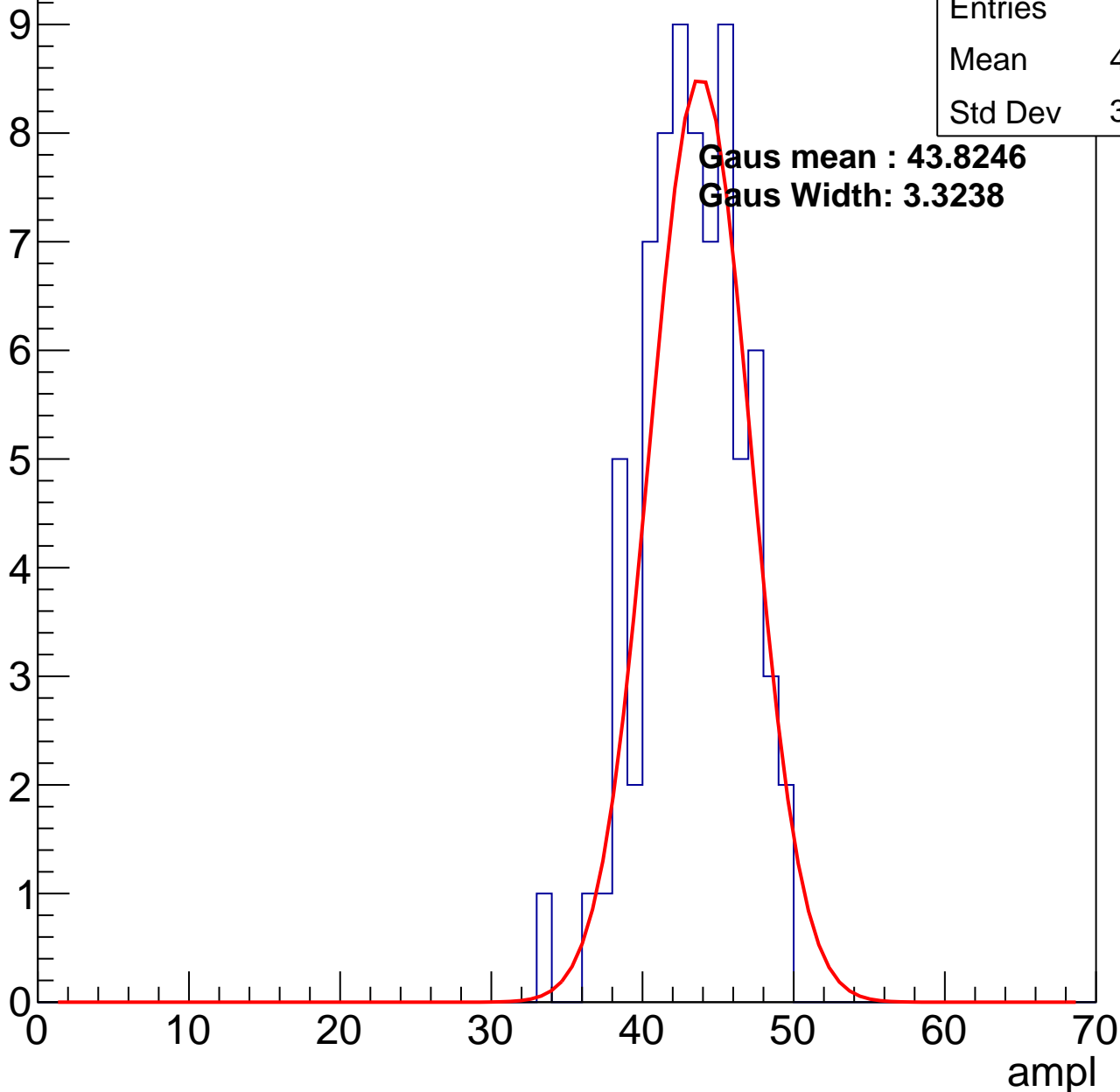
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	42.85
Std Dev	3.237

**Gaus mean : 43.8246**

**Gaus Width: 3.3238**

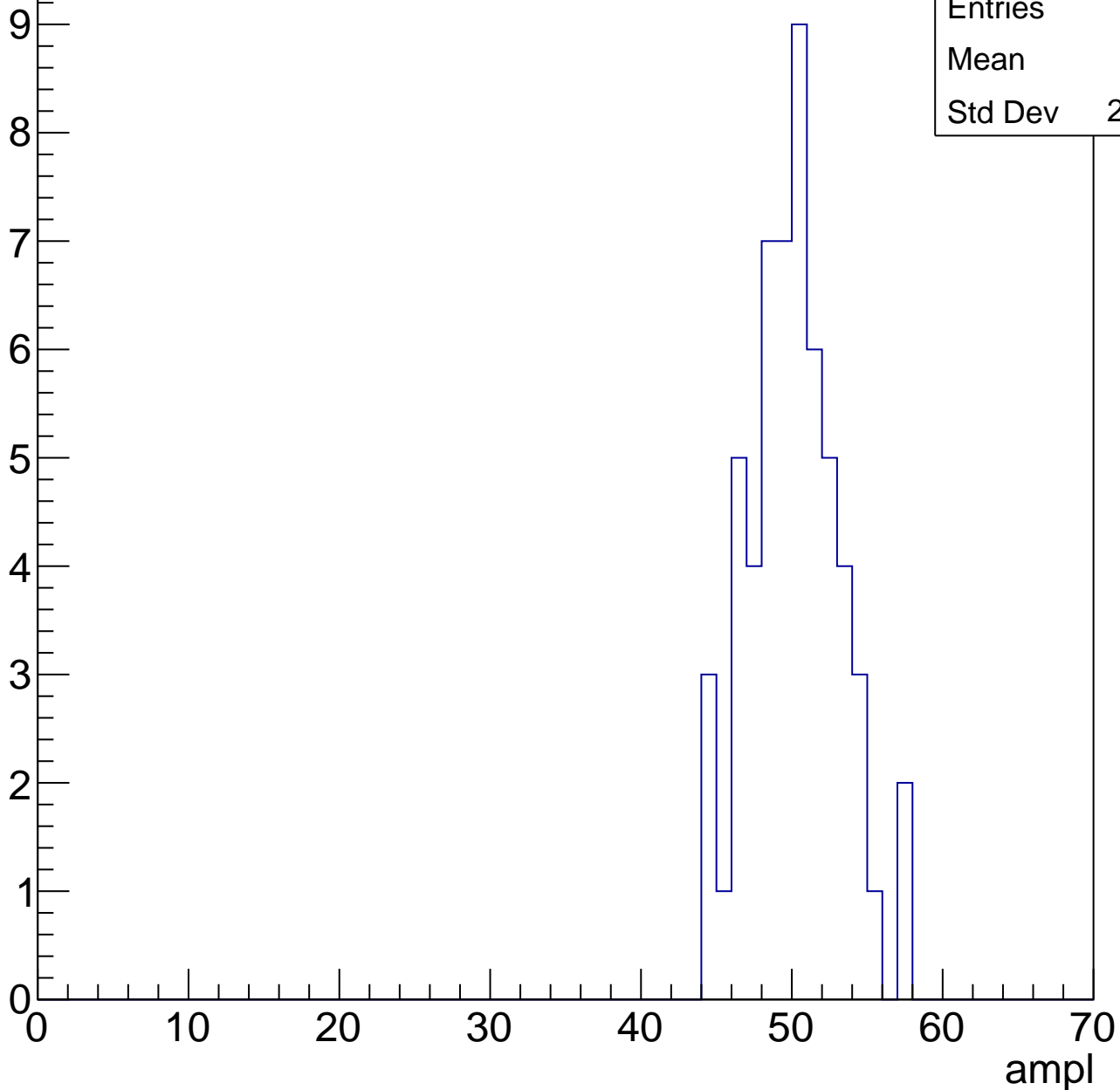


# B0L000S, U25-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

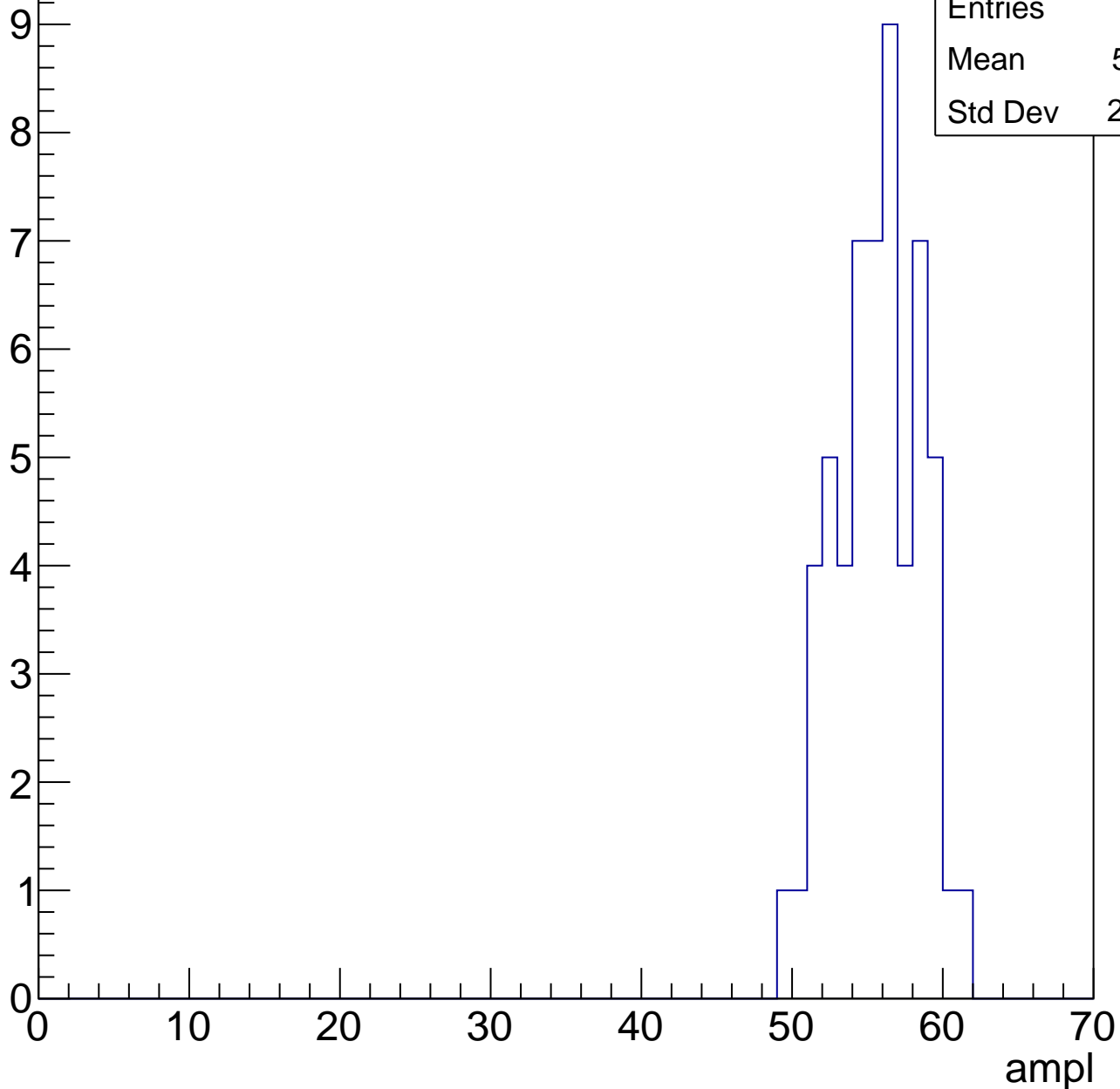
Entries	57
Mean	49.7
Std Dev	2.997



# B0L000S, U25-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

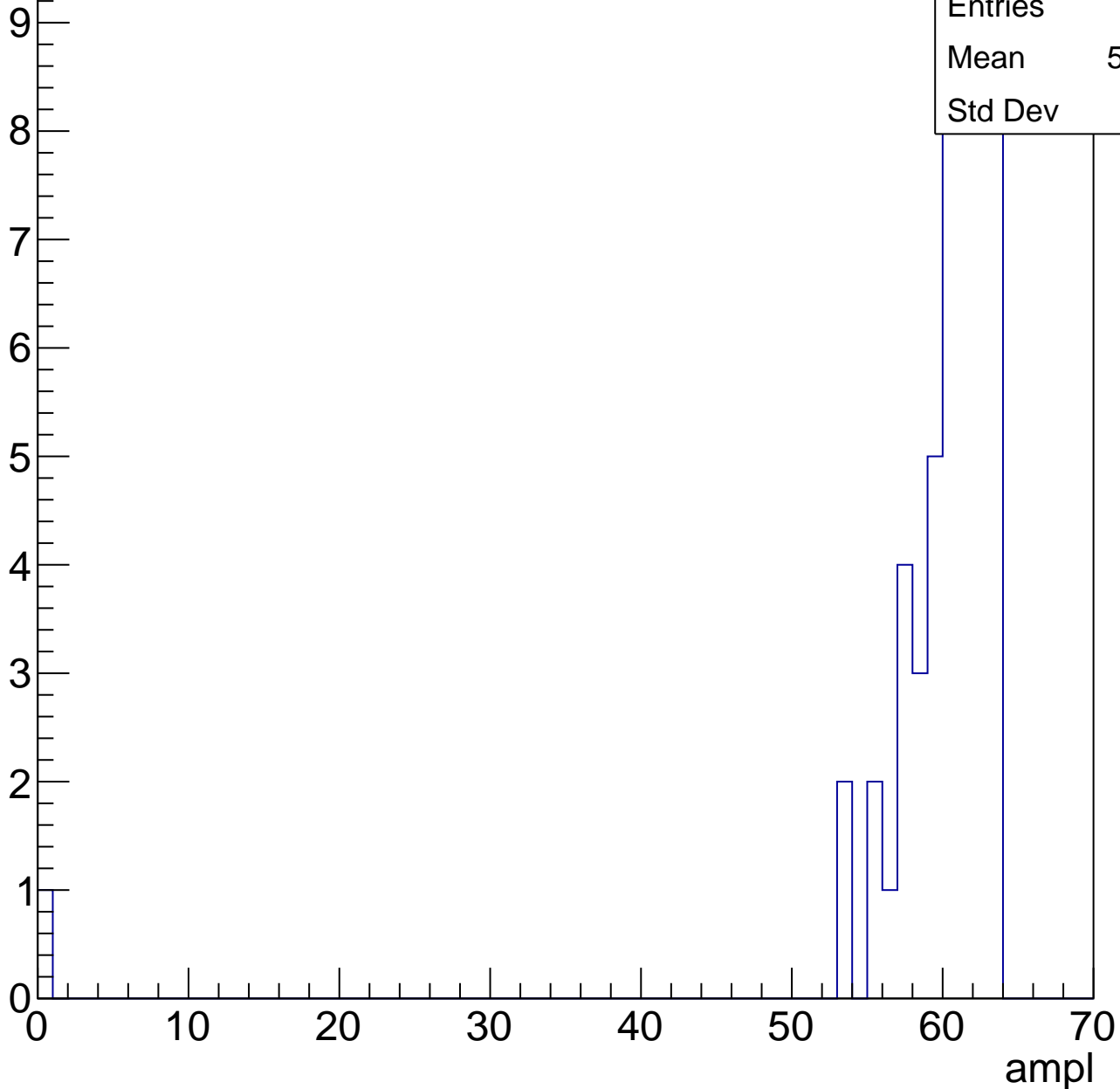


# B0L000S, U25-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	58.76
Std Dev	8.69



# B0L000S, U25-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

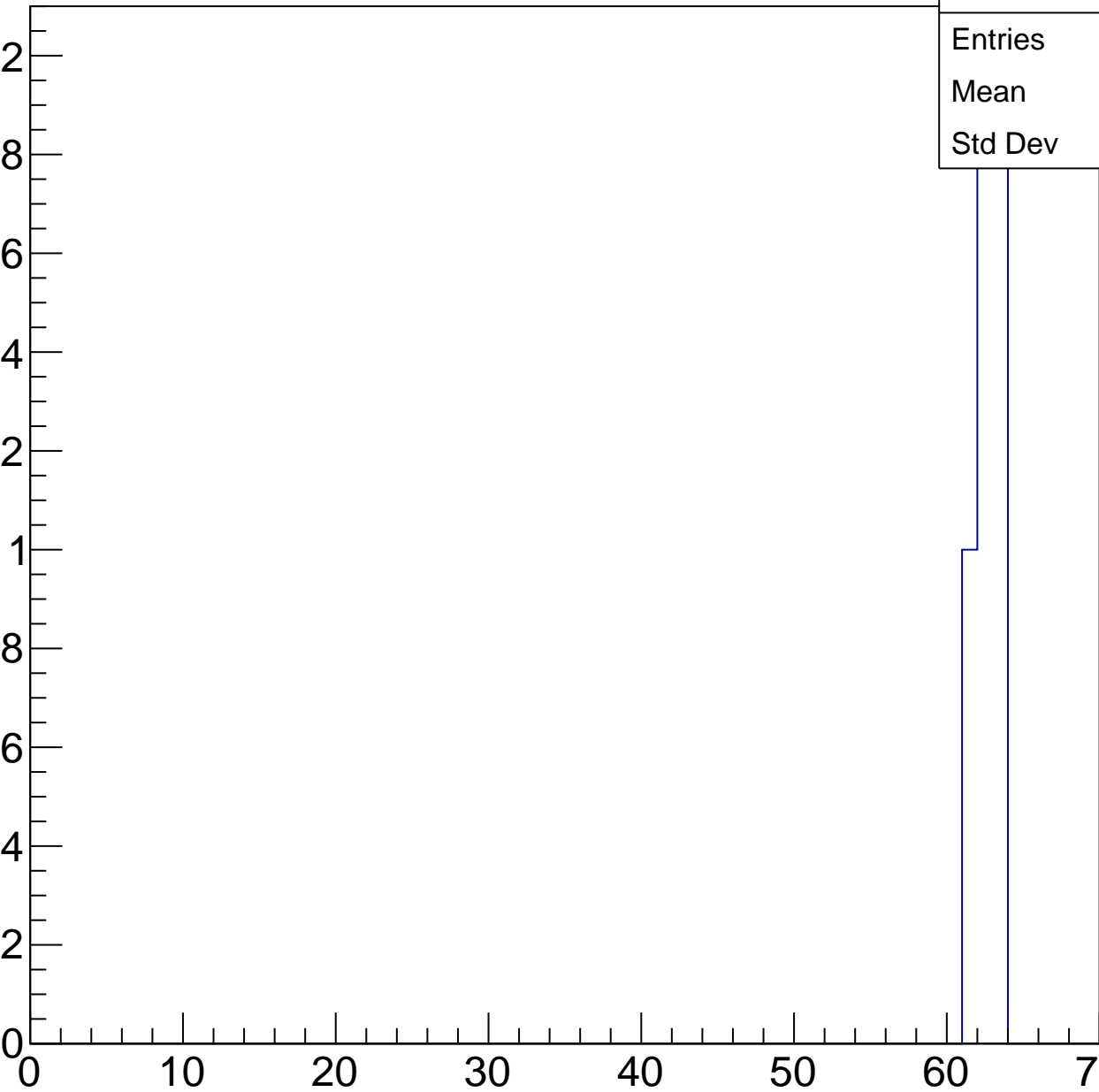
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch14, adc0

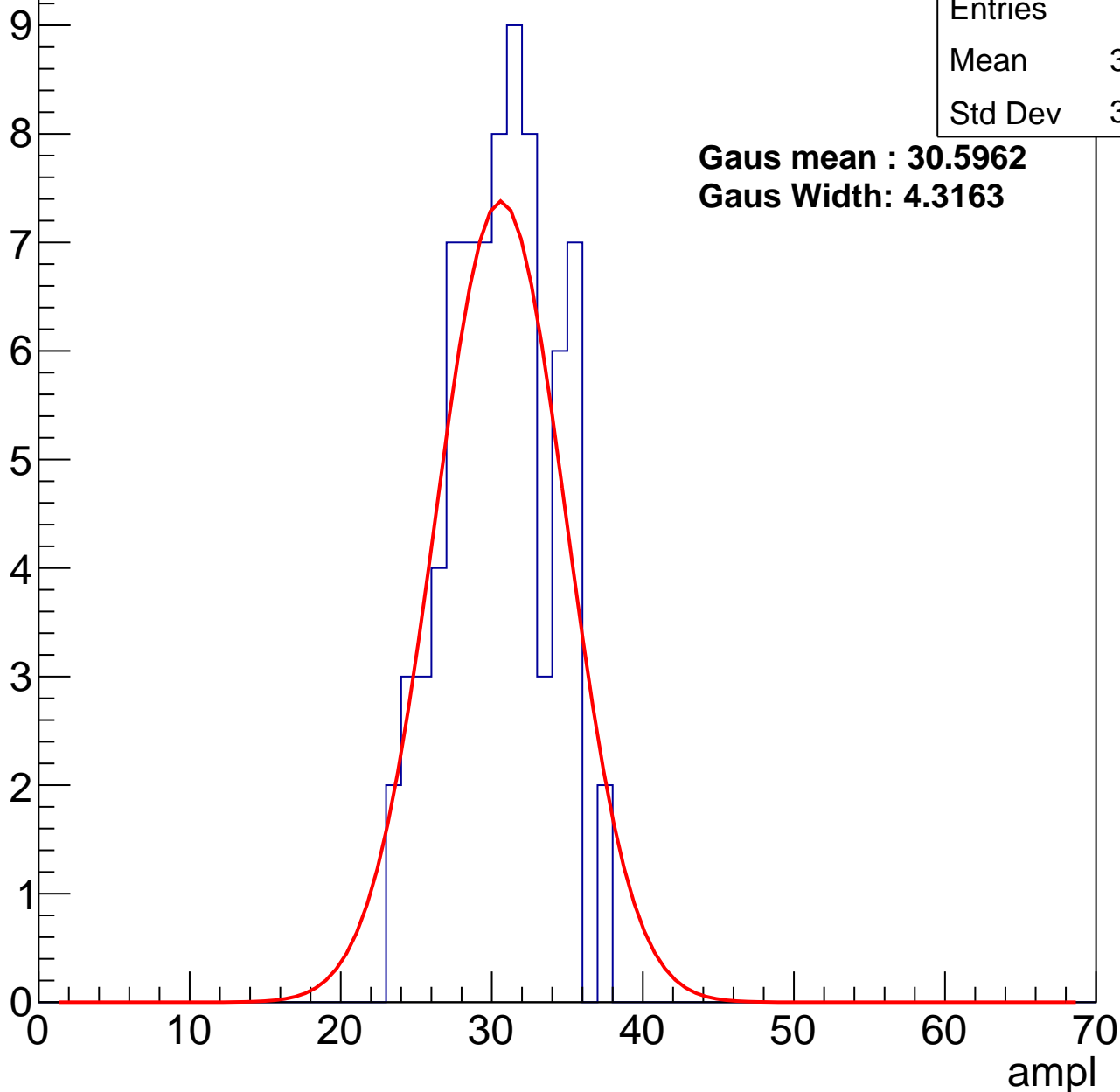
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	30.03
Std Dev	3.403

**Gaus mean : 30.5962**

**Gaus Width: 4.3163**



# B0L000S, U25-ch14, adc1

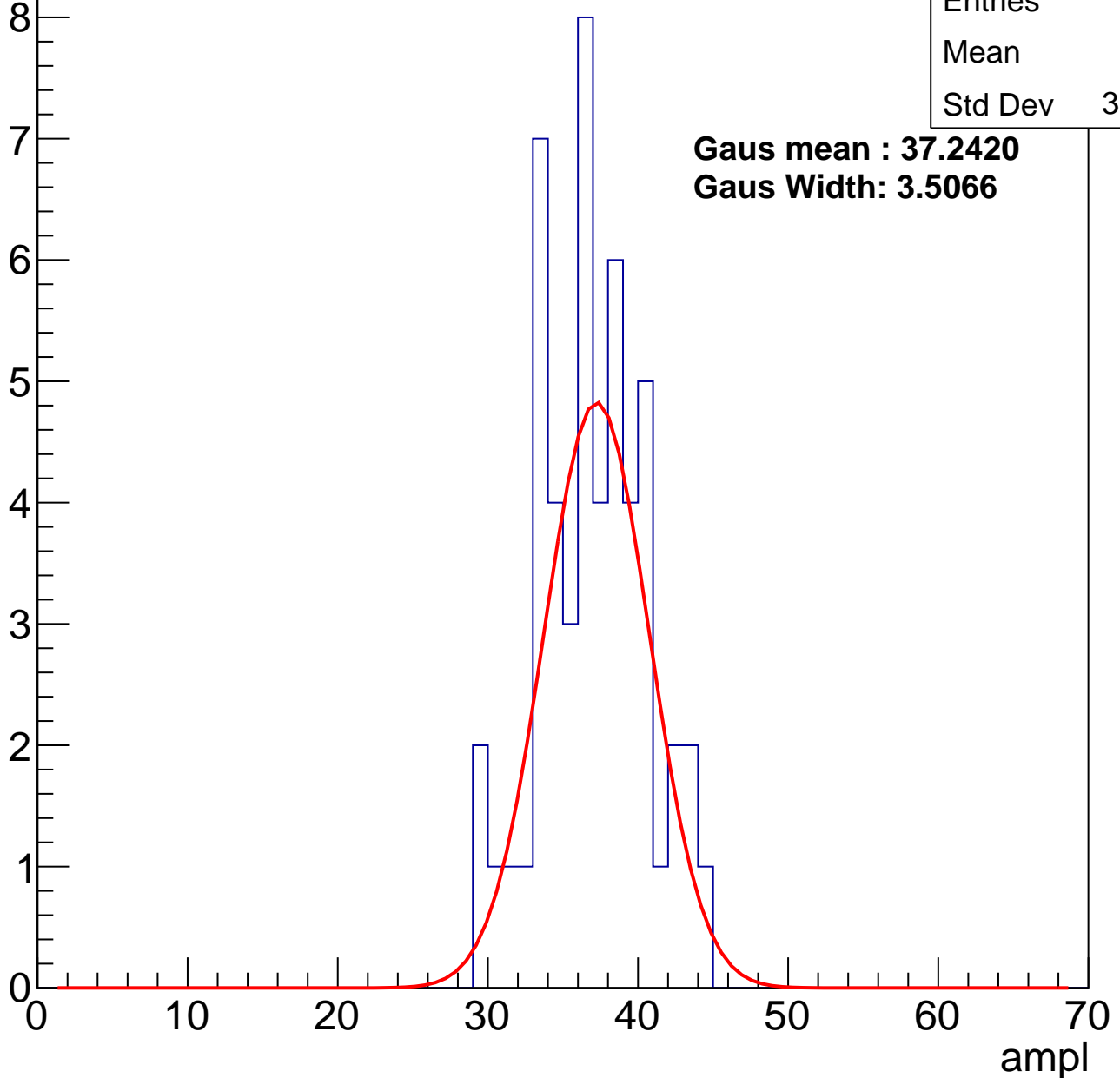
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	36.5
Std Dev	3.527

**Gaus mean : 37.2420**

**Gaus Width: 3.5066**



# B0L000S, U25-ch14, adc2

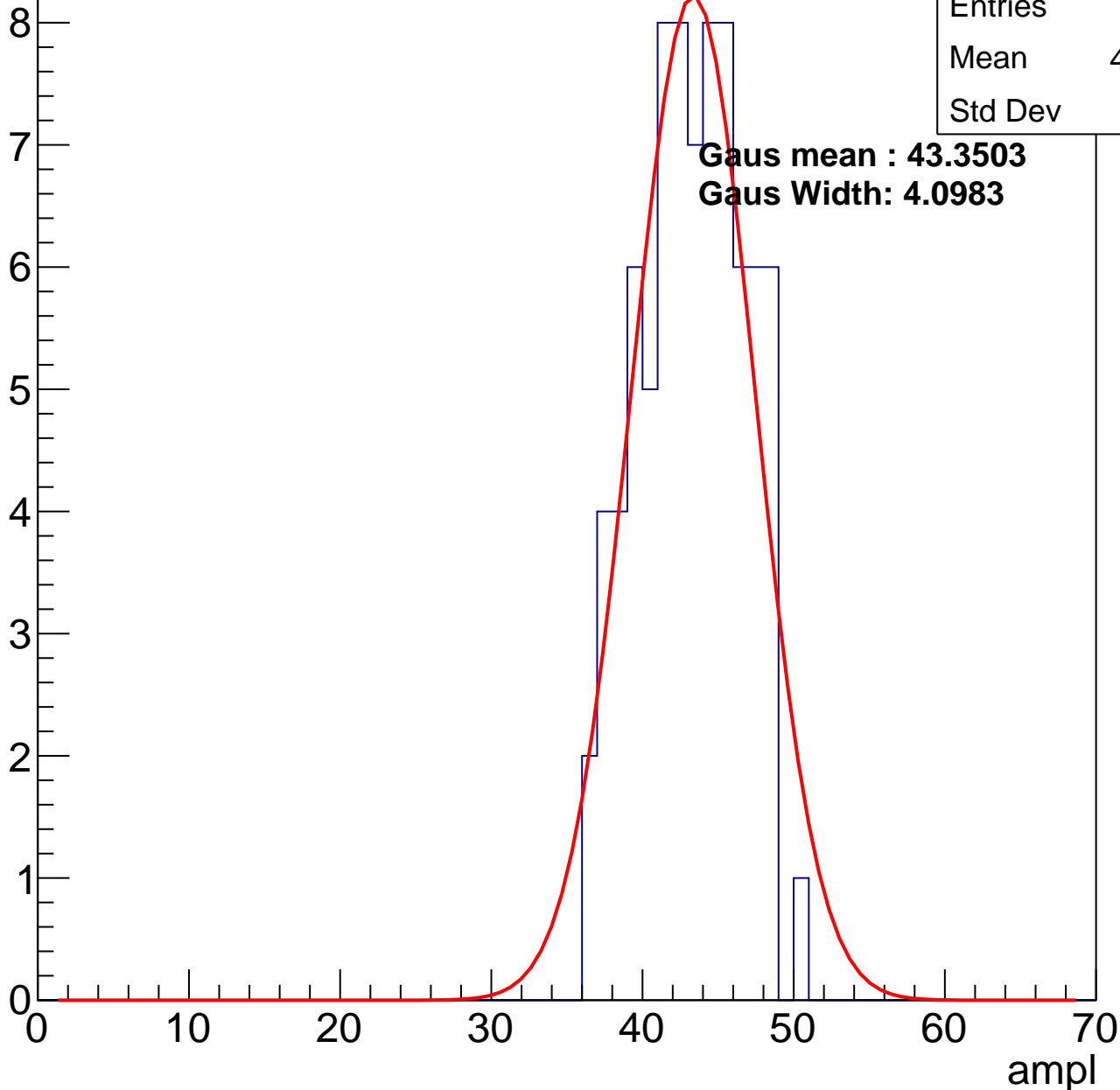
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	42.77
Std Dev	3.39

**Gaus mean : 43.3503**

**Gaus Width: 4.0983**

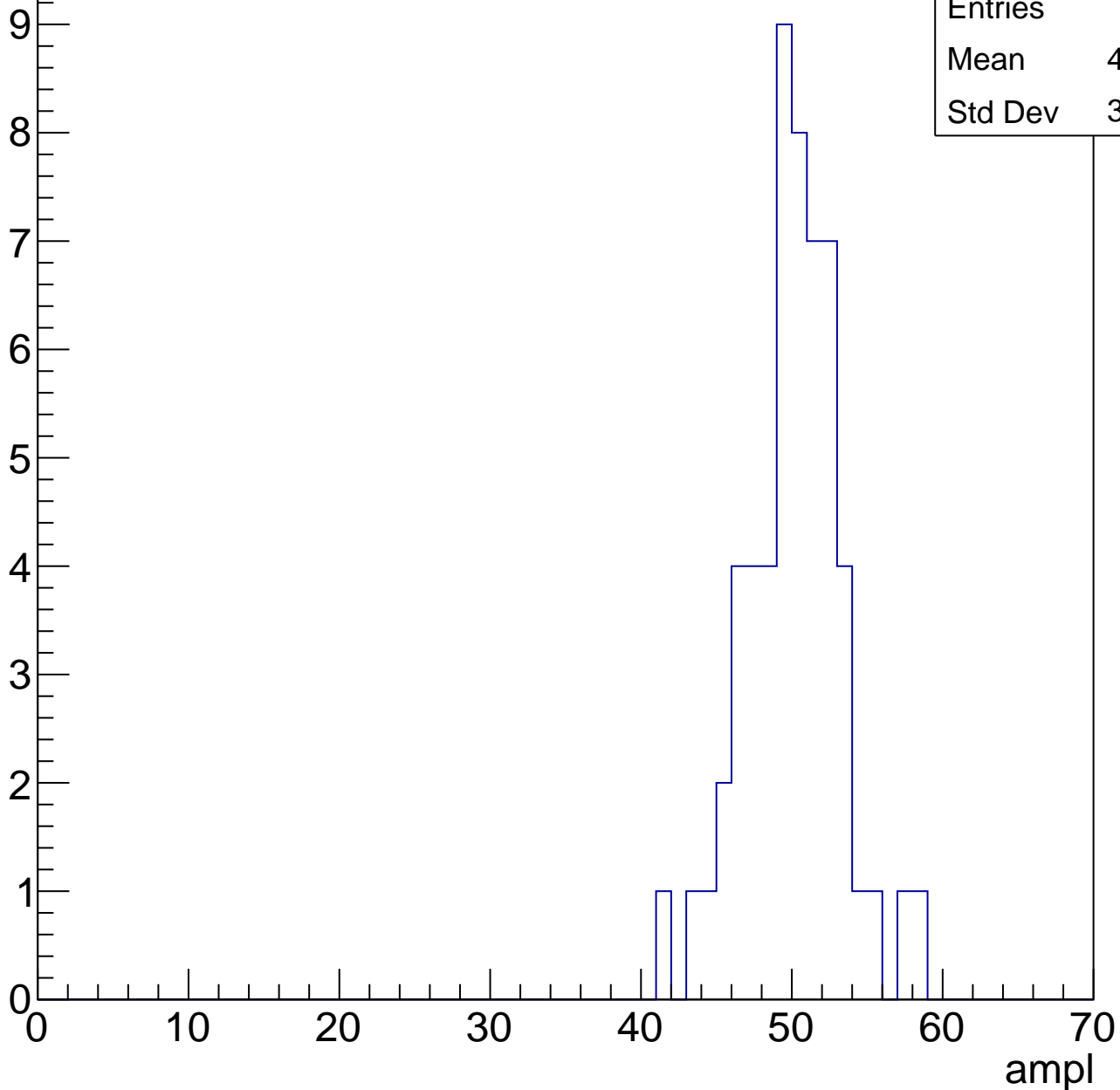


# B0L000S, U25-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	49.64
Std Dev	3.159

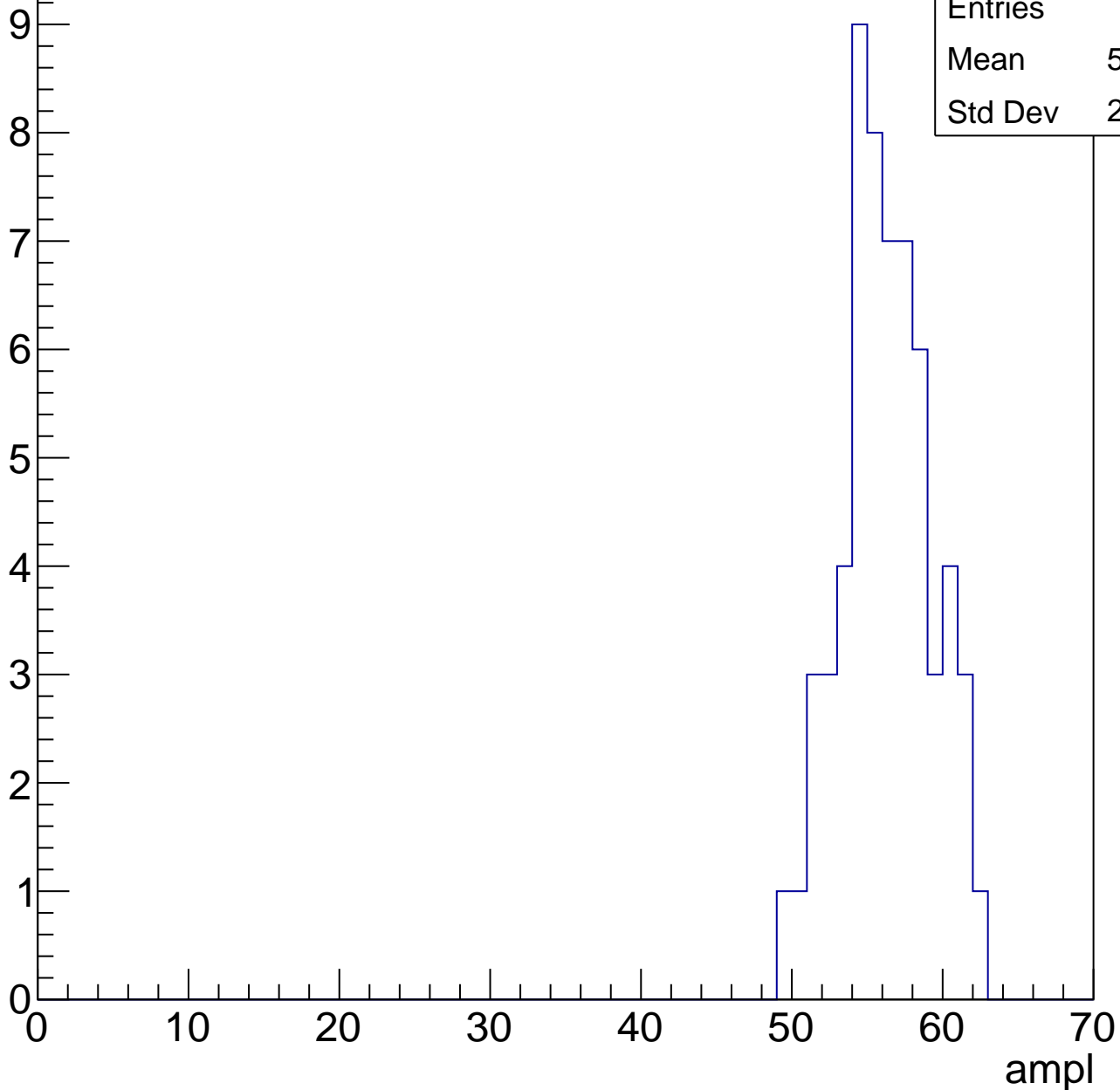


# B0L000S, U25-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

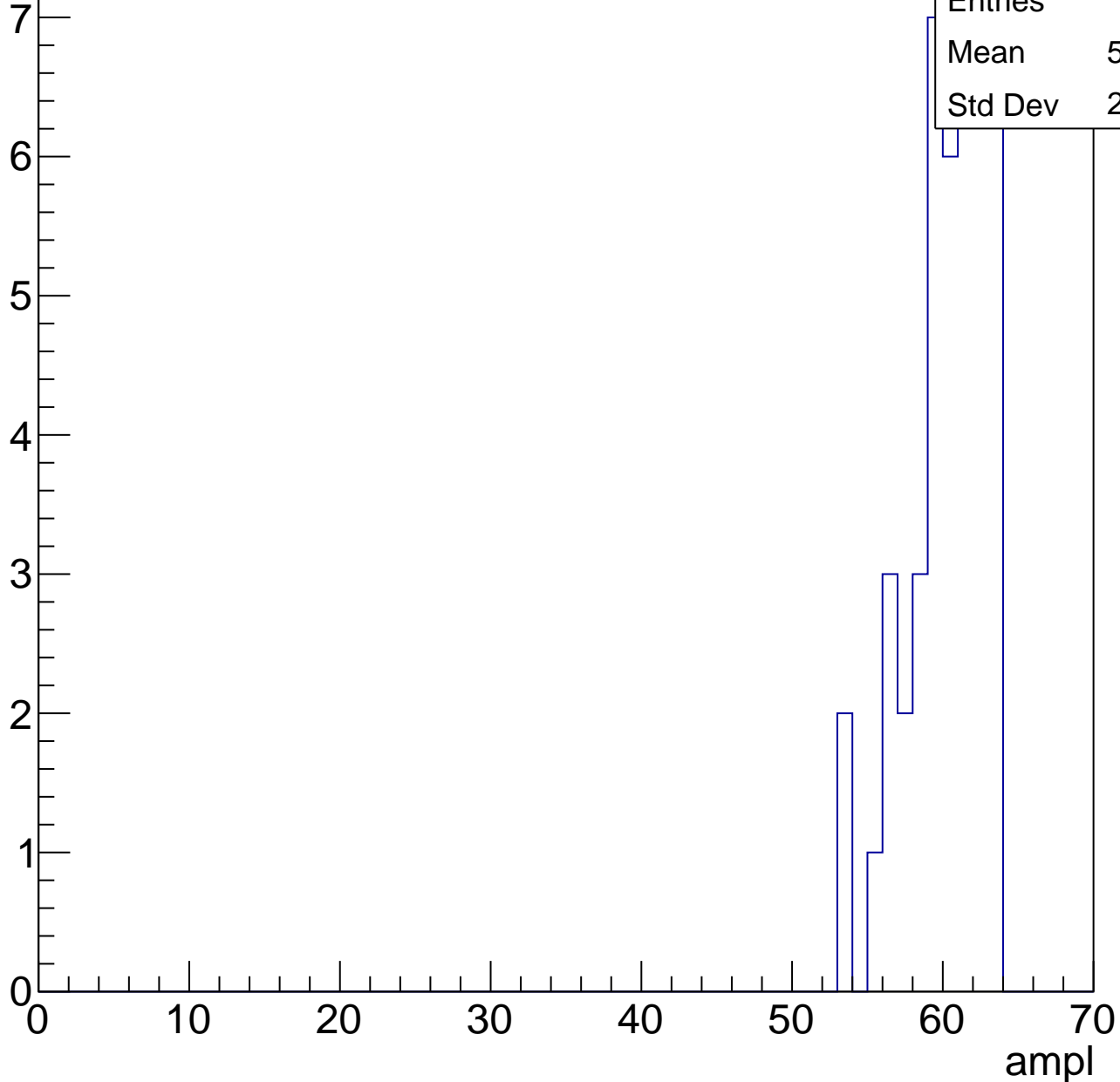
Entries	60
Mean	55.78
Std Dev	2.939



# B0L000S, U25-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

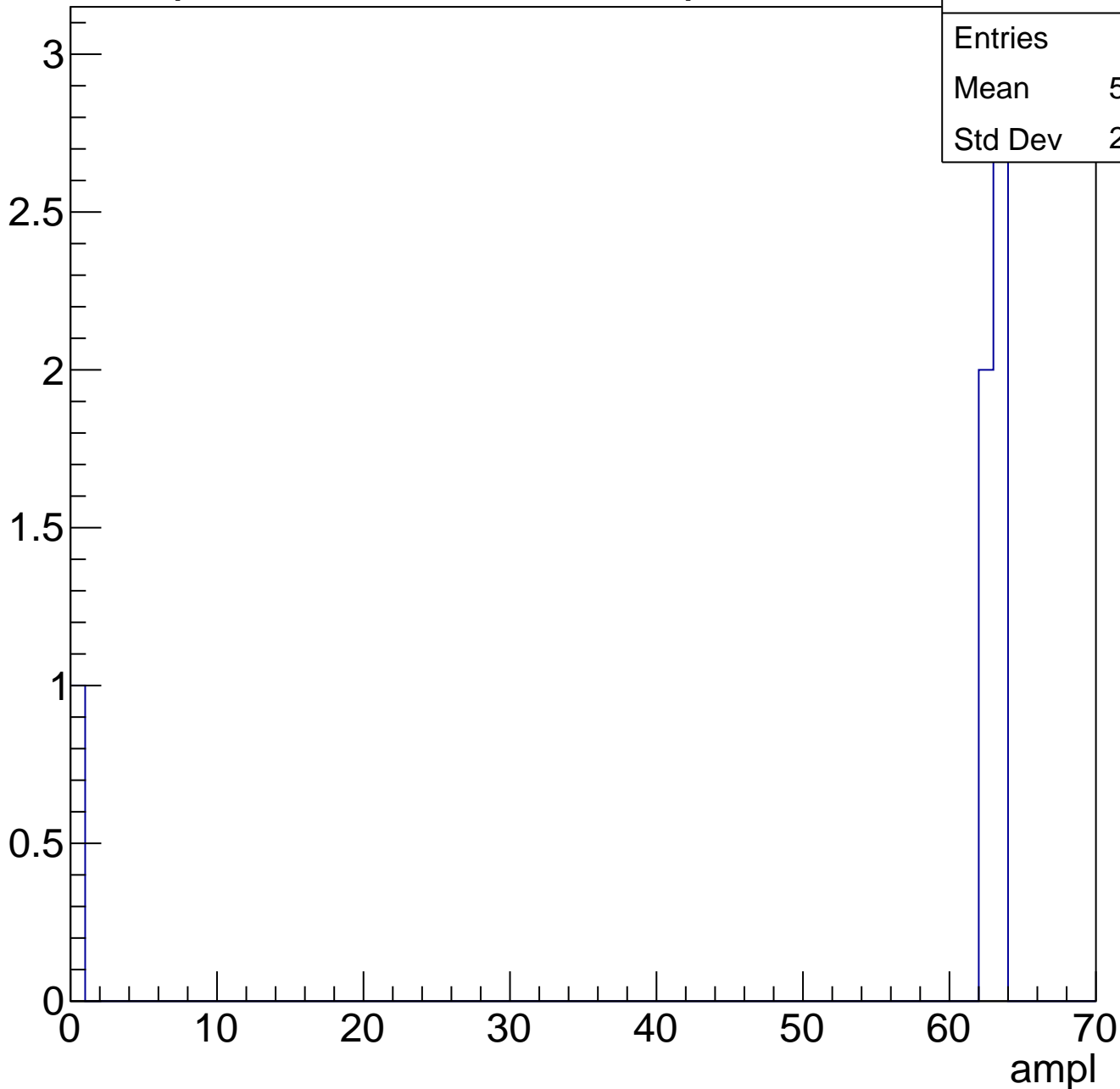
Entry



# B0L000S, U25-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch15, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.88
Std Dev	6.011

**Gaus mean : 30.5275**

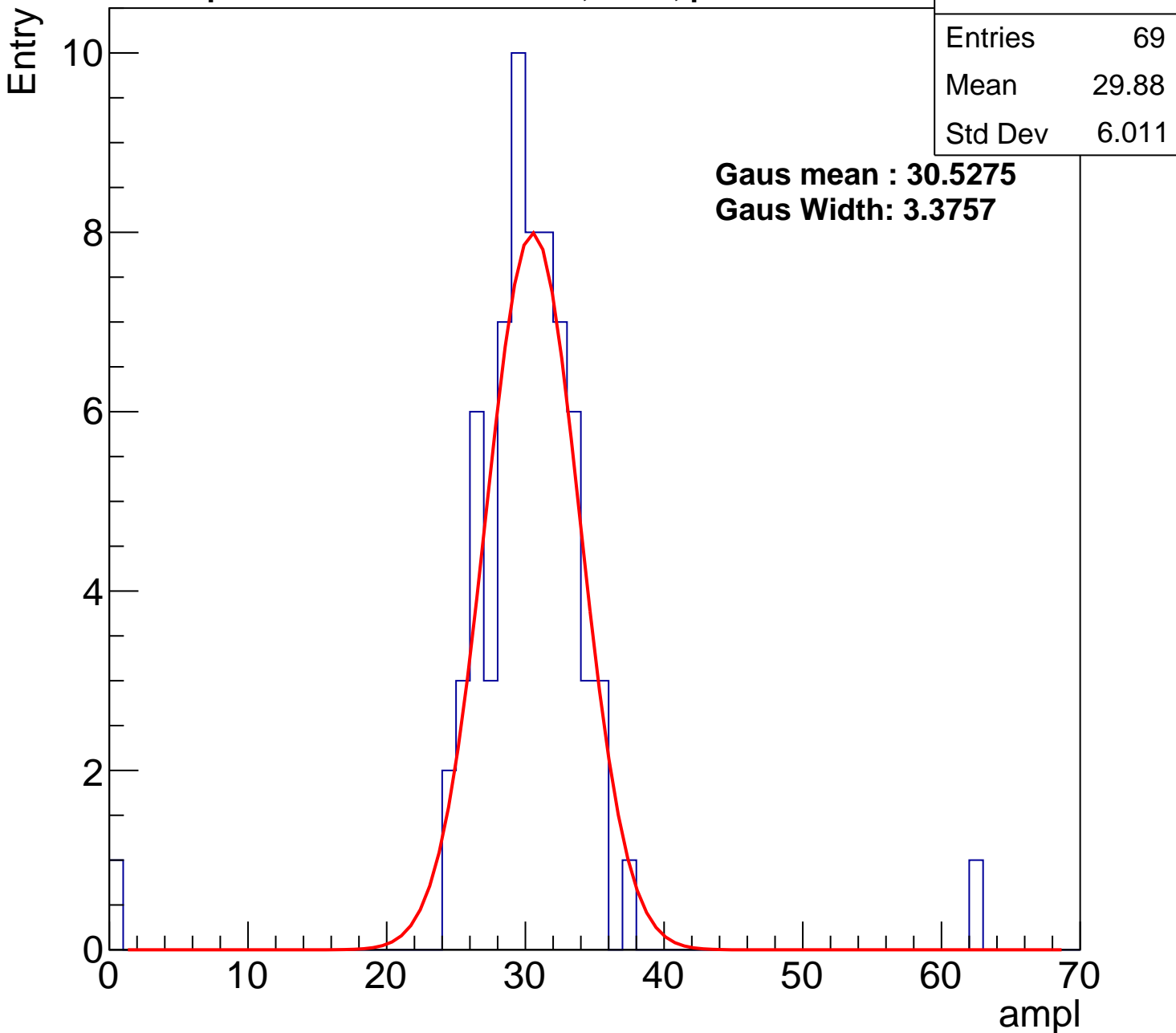
**Gaus Width: 3.3757**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

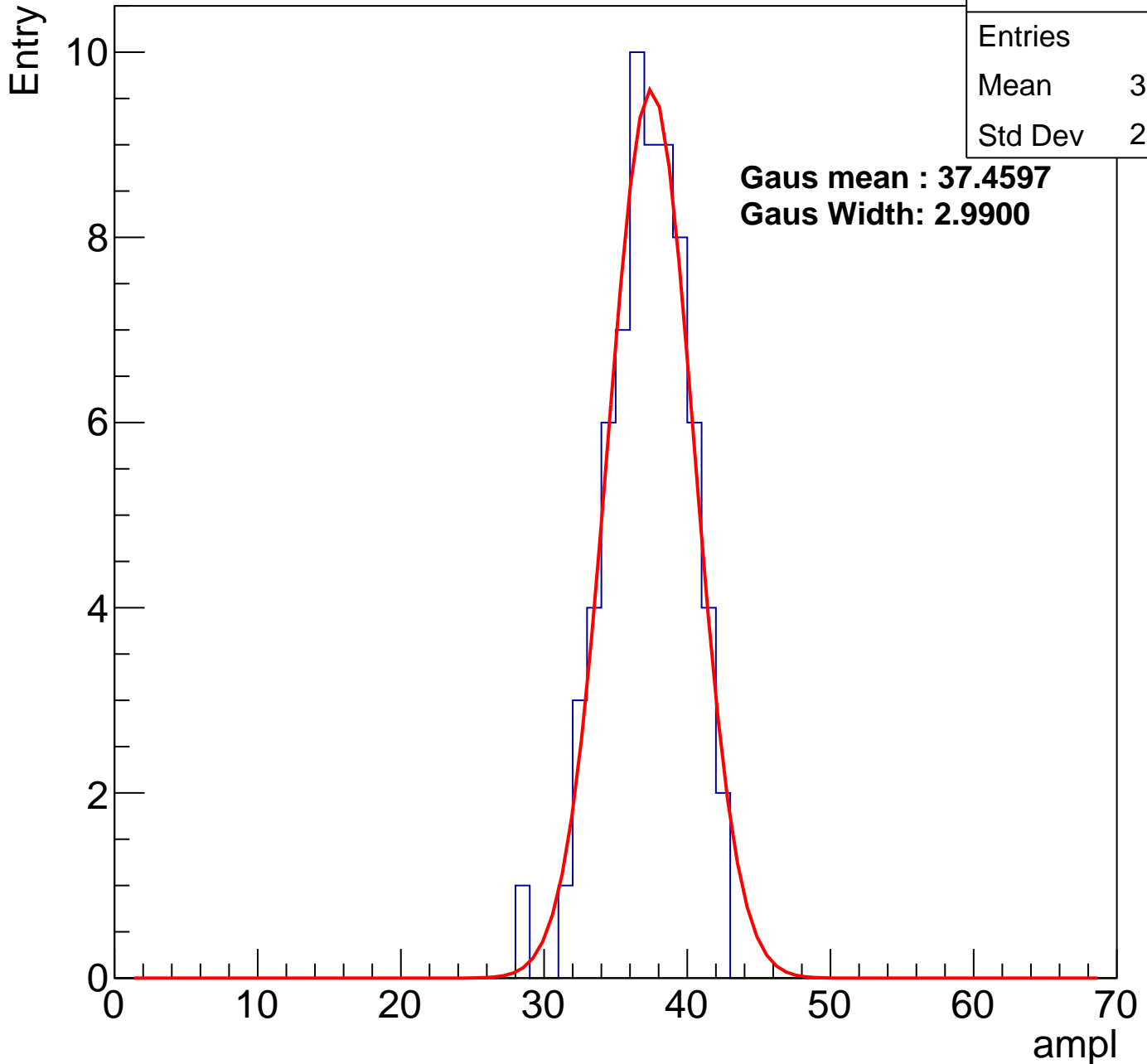


# B0L000S, U25-ch15, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	36.73
Std Dev	2.808

**Gaus mean : 37.4597**  
**Gaus Width: 2.9900**



# B0L000S, U25-ch15, adc2

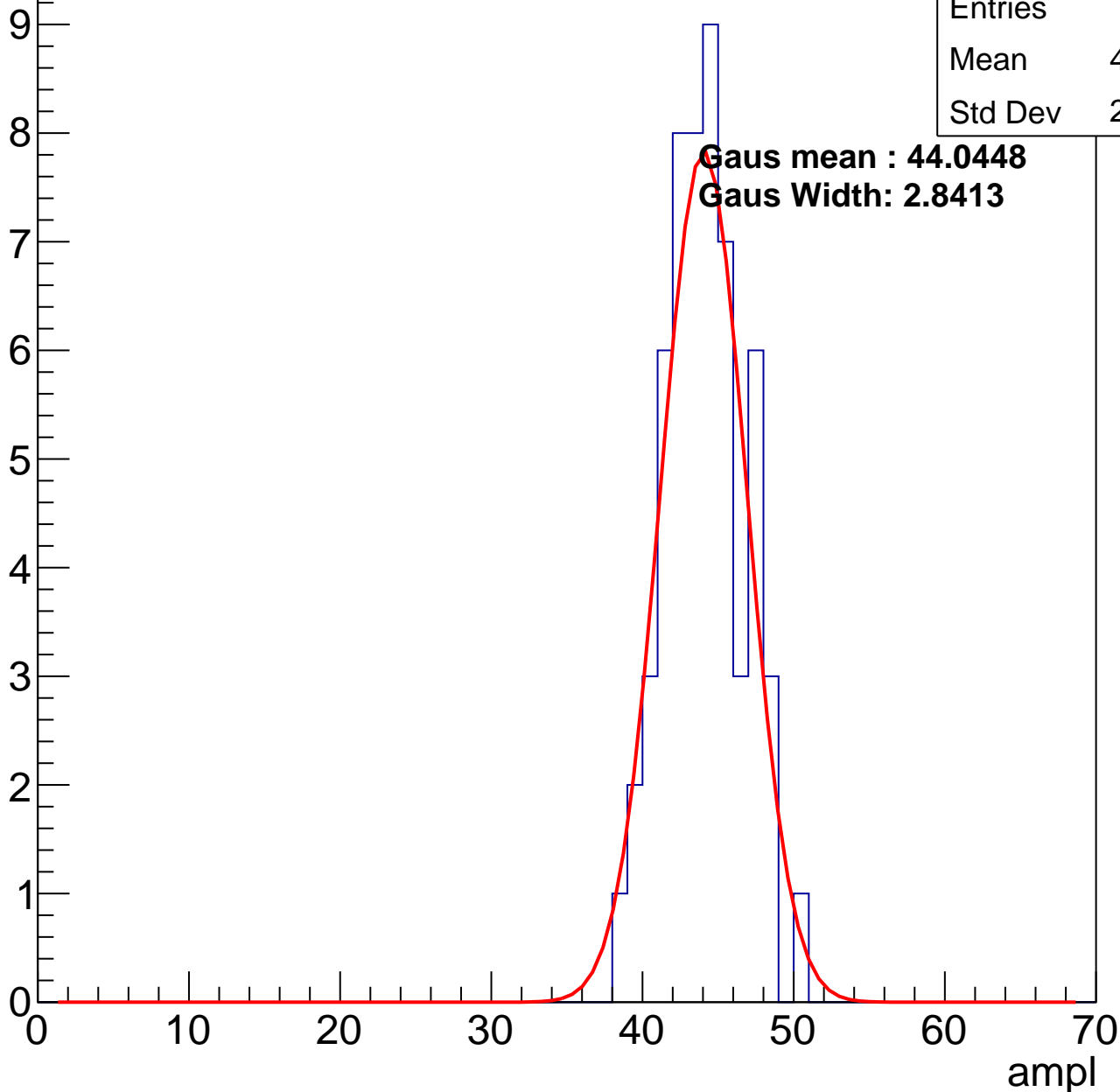
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.63
Std Dev	2.573

**Gaus mean : 44.0448**

**Gaus Width: 2.8413**



# B0L000S, U25-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

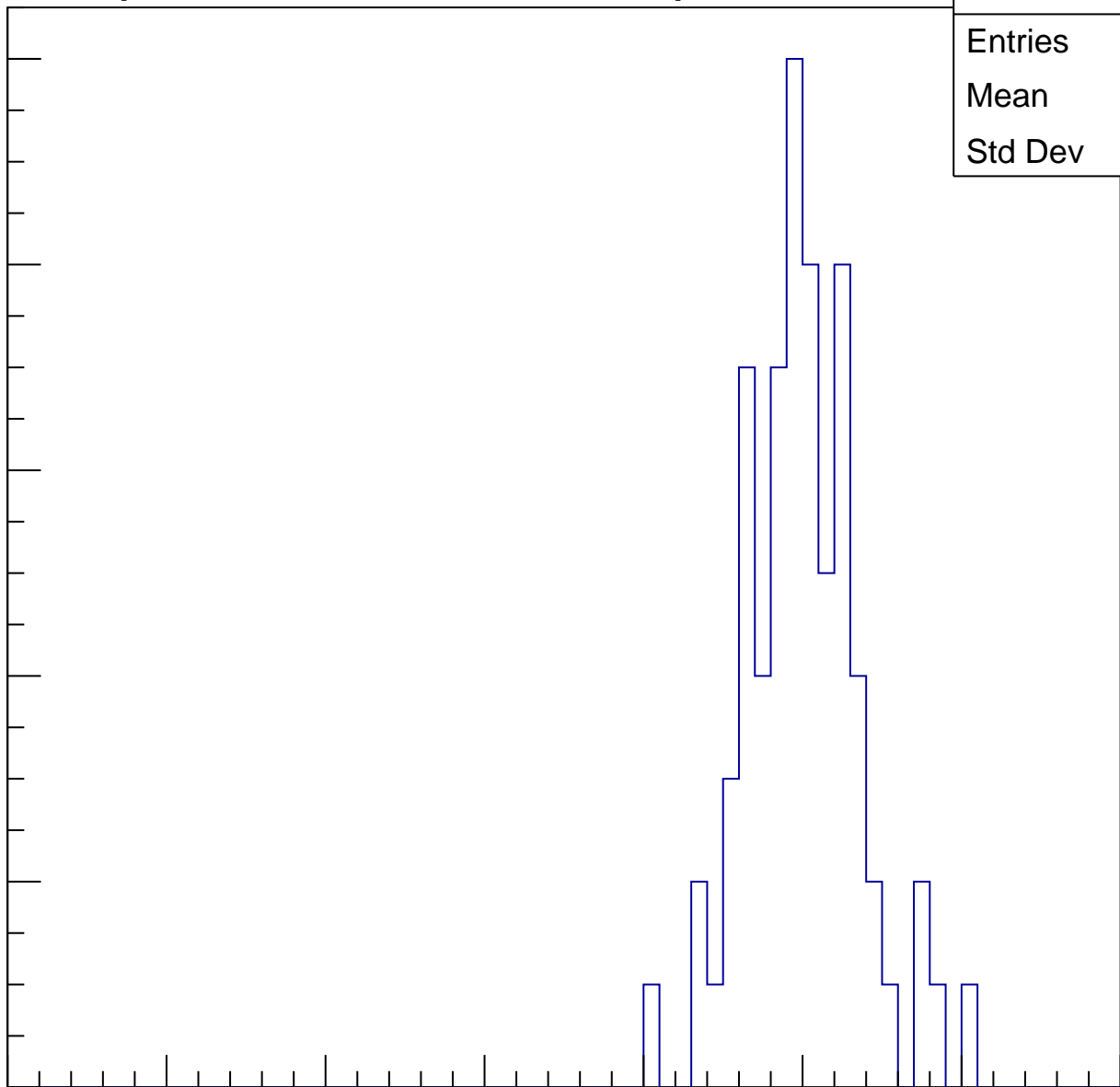
Entries	67
Mean	49.54
Std Dev	3.609

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

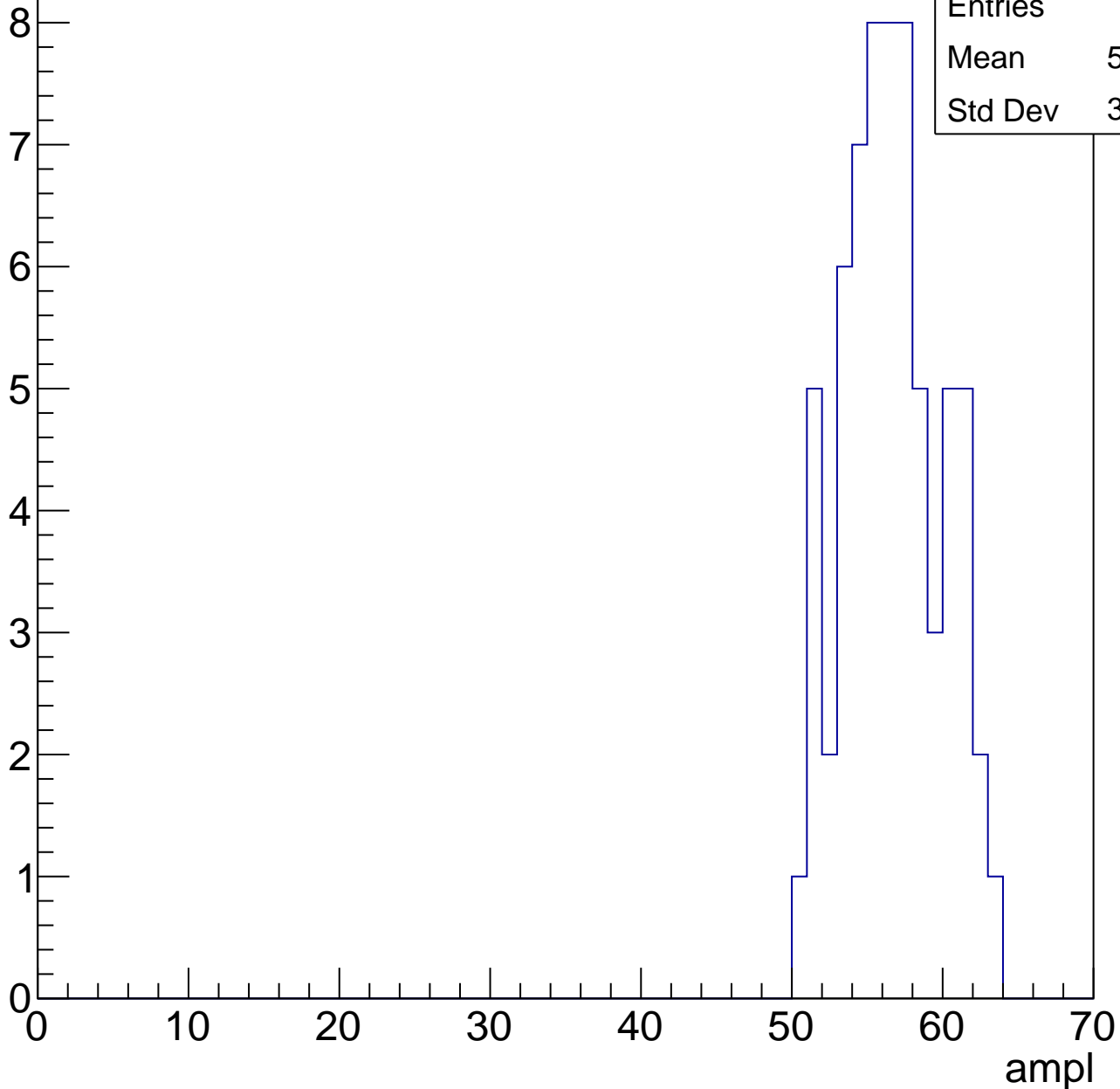


# B0L000S, U25-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.18
Std Dev	3.167



# B0L000S, U25-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 36

Mean 60.08

Std Dev 2.373

ampl

0

10

20

30

40

50

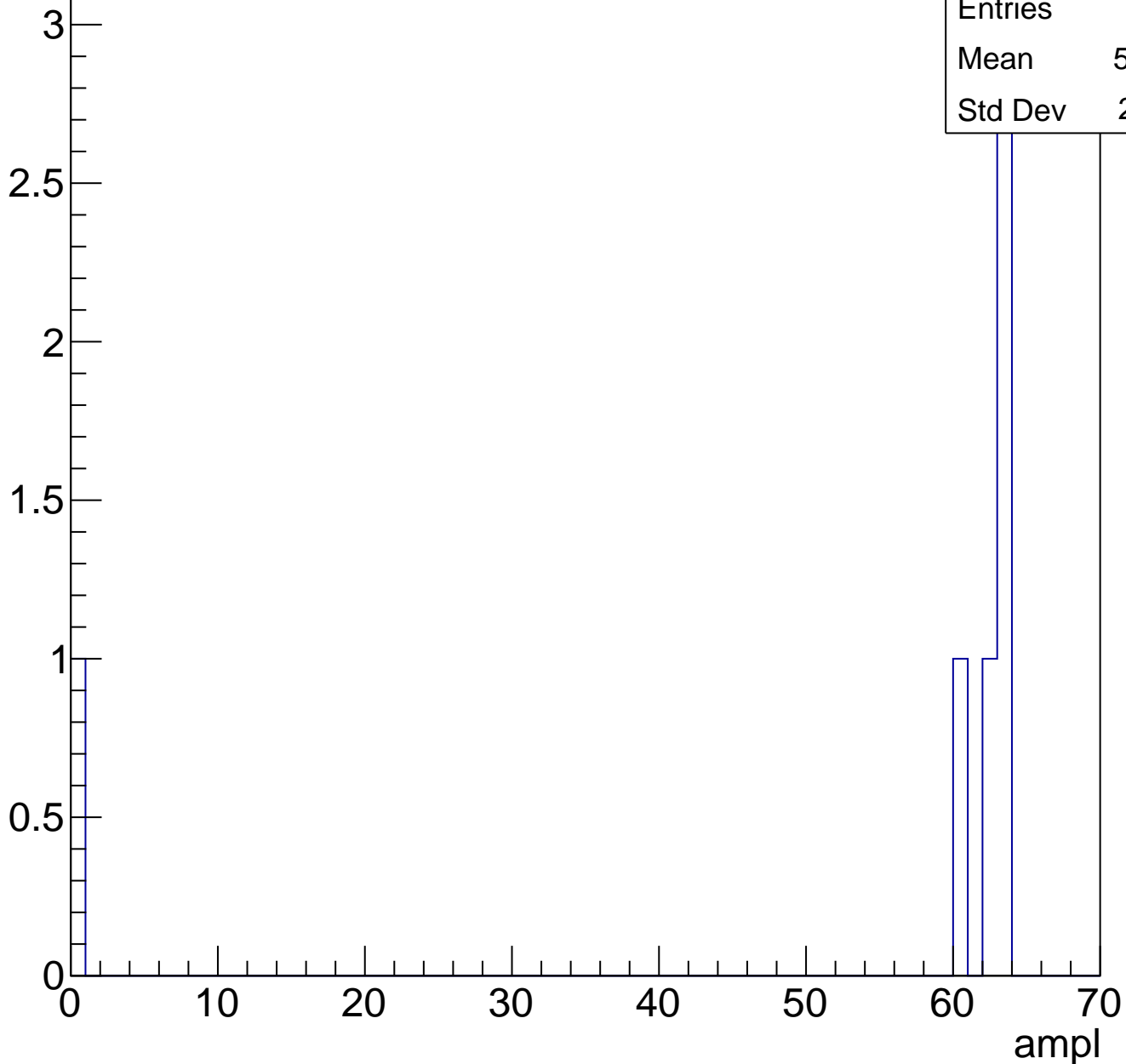
60

70

# B0L000S, U25-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch16, adc0

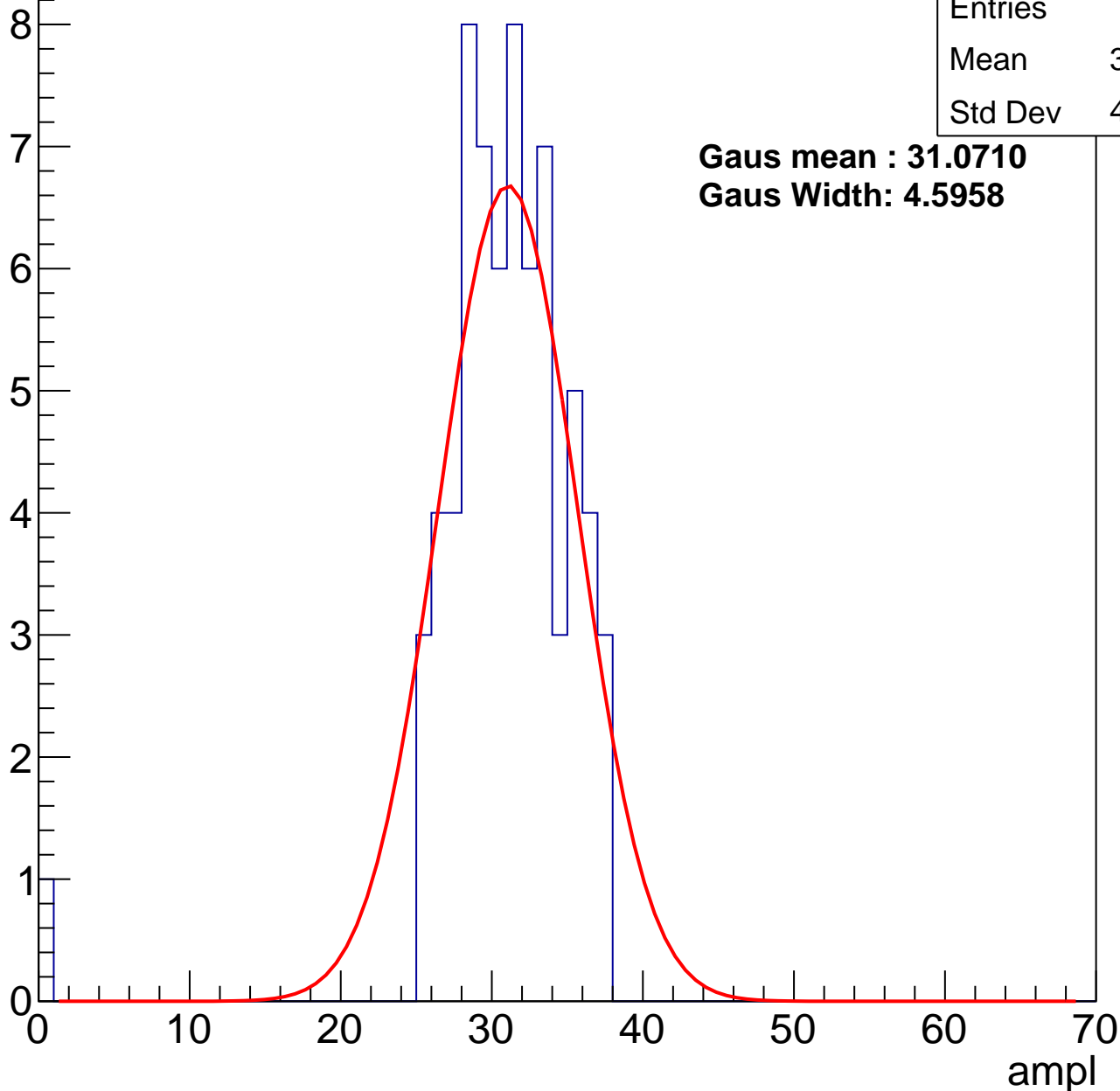
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.39
Std Dev	4.908

**Gaus mean : 31.0710**

**Gaus Width: 4.5958**



# B0L000S, U25-ch16, adc1

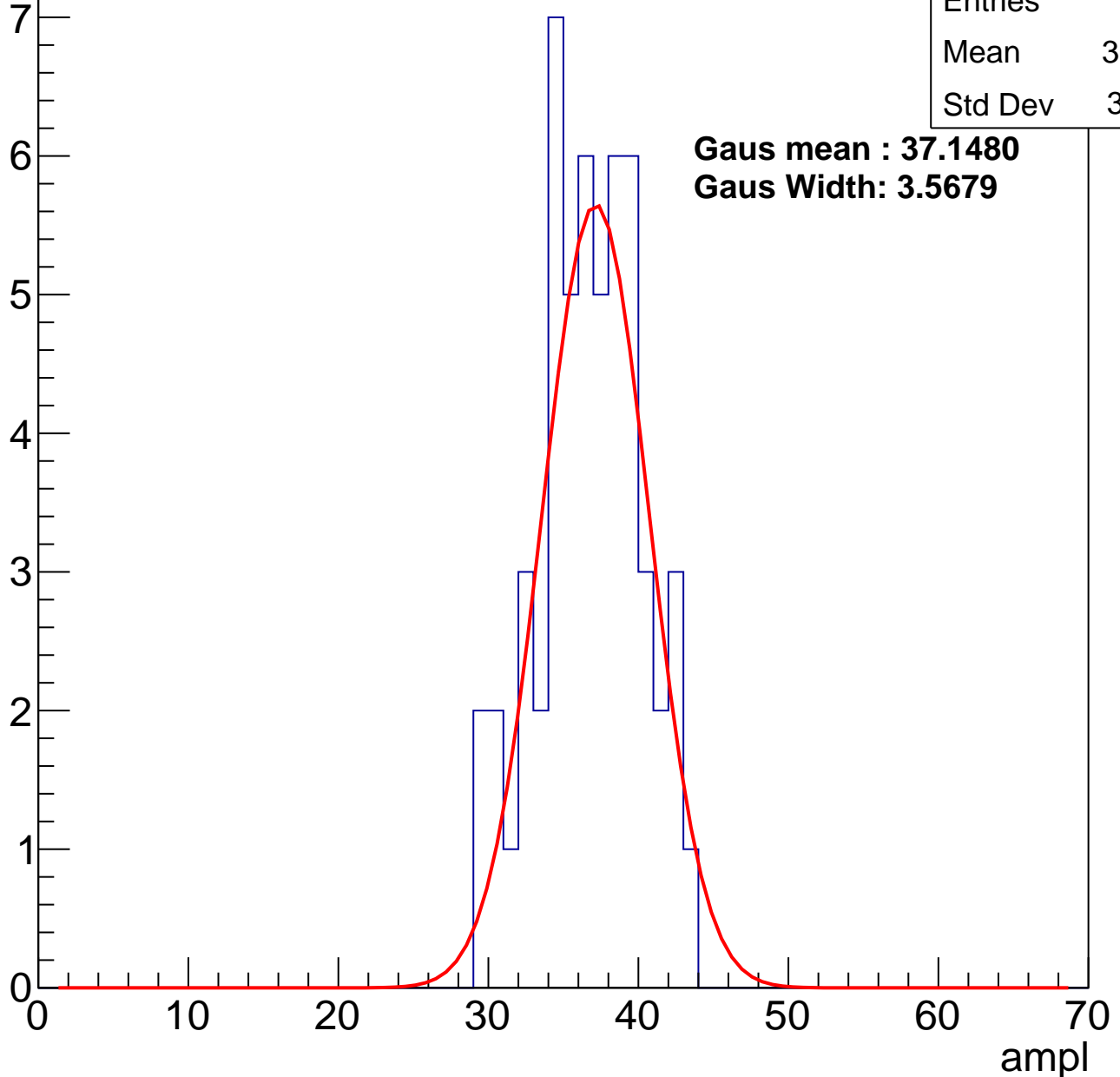
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	36.26
Std Dev	3.411

**Gaus mean : 37.1480**

**Gaus Width: 3.5679**



# B0L000S, U25-ch16, adc2

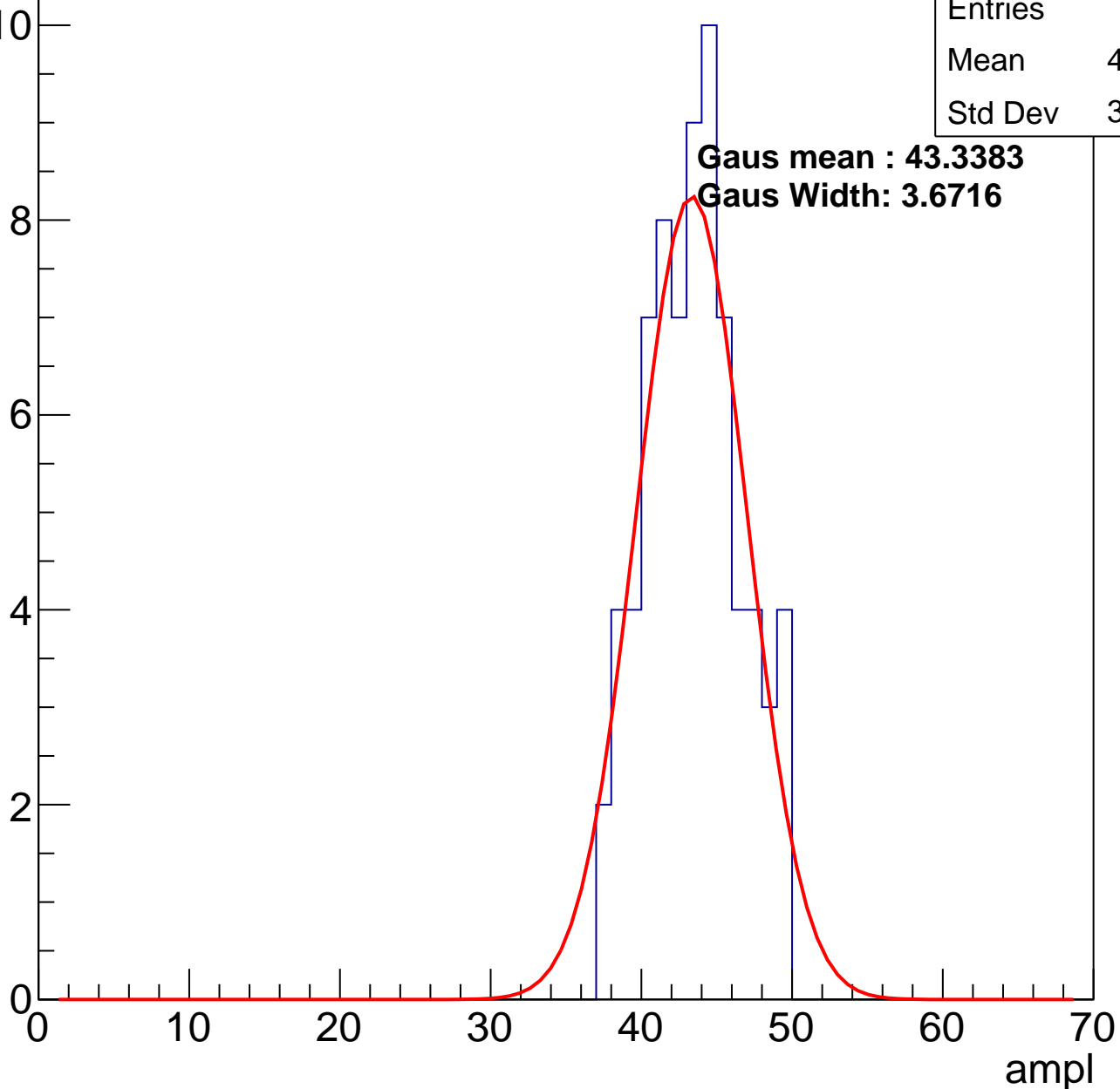
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	42.99
Std Dev	3.086

**Gaus mean : 43.3383**

**Gaus Width: 3.6716**

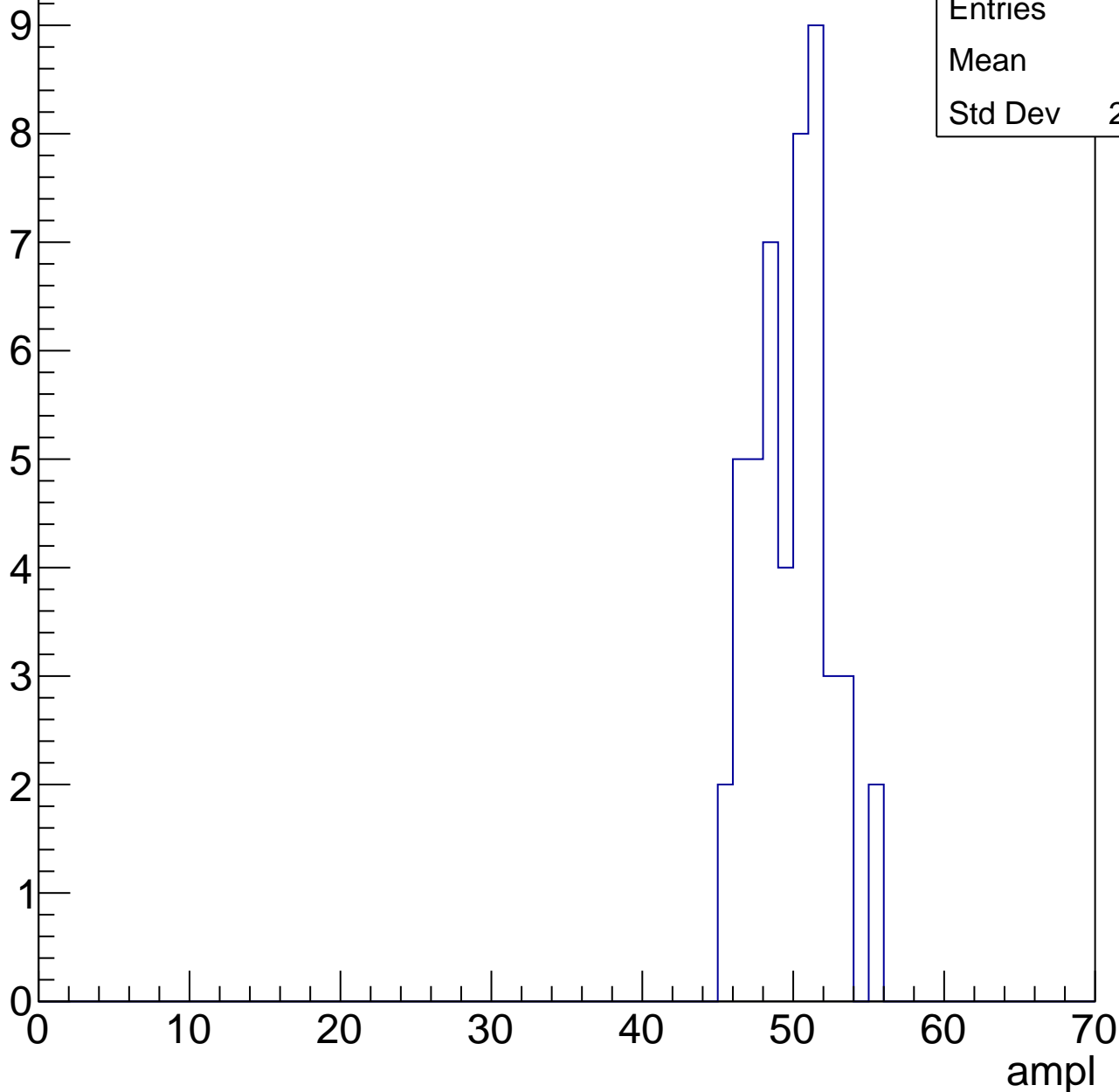


# B0L000S, U25-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	49.4
Std Dev	2.447

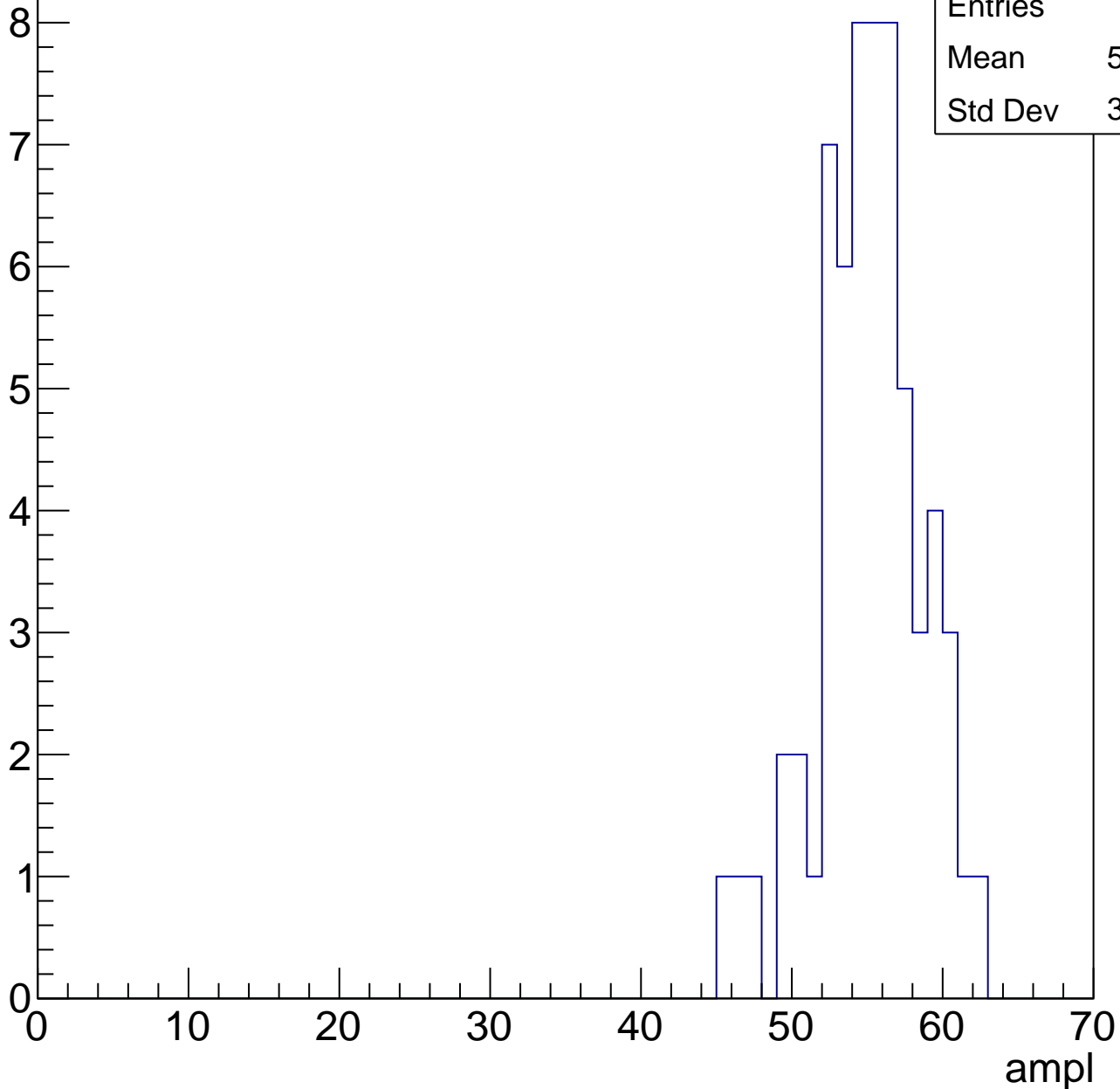


# B0L000S, U25-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	54.63
Std Dev	3.474

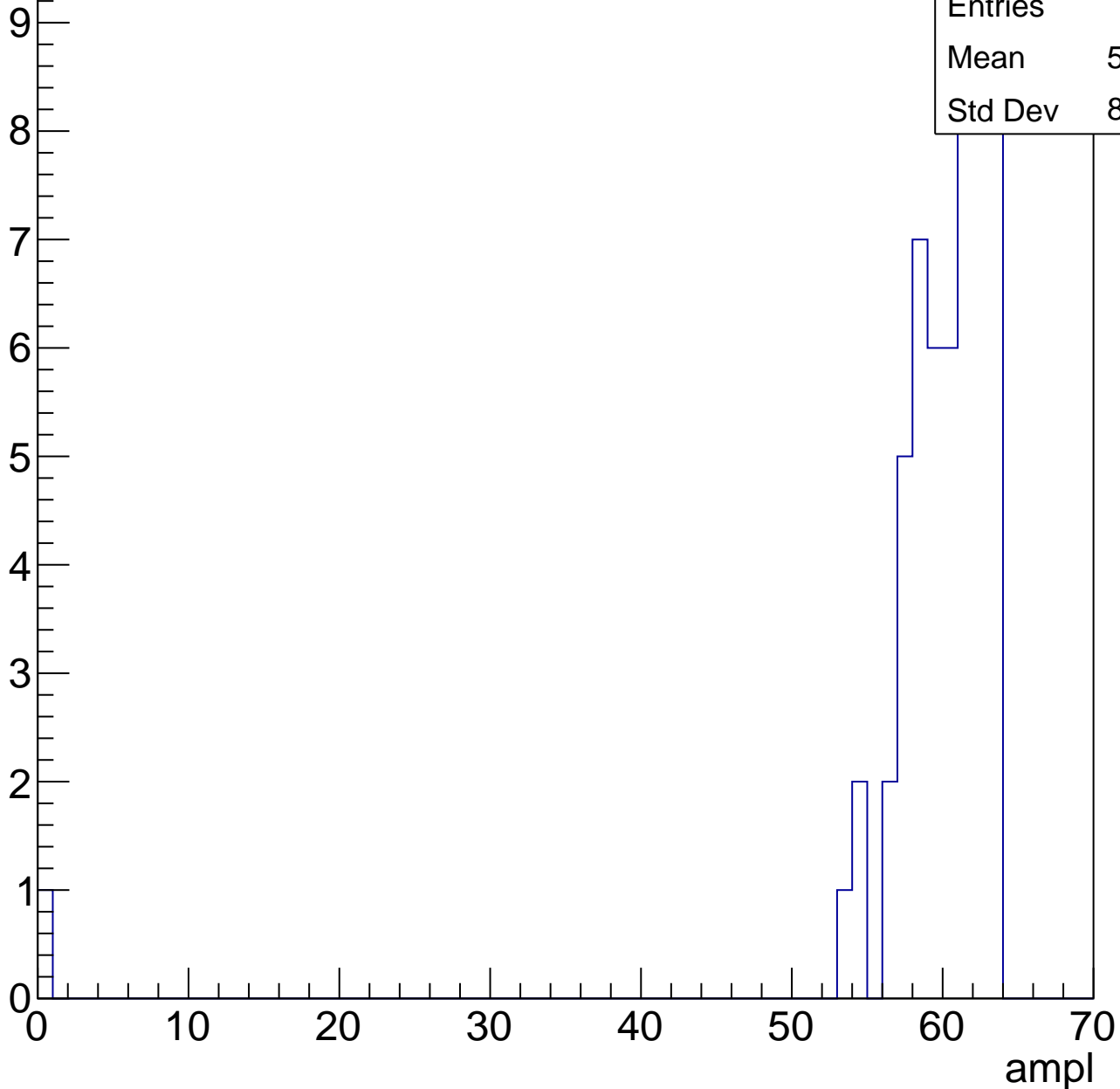


# B0L000S, U25-ch16, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

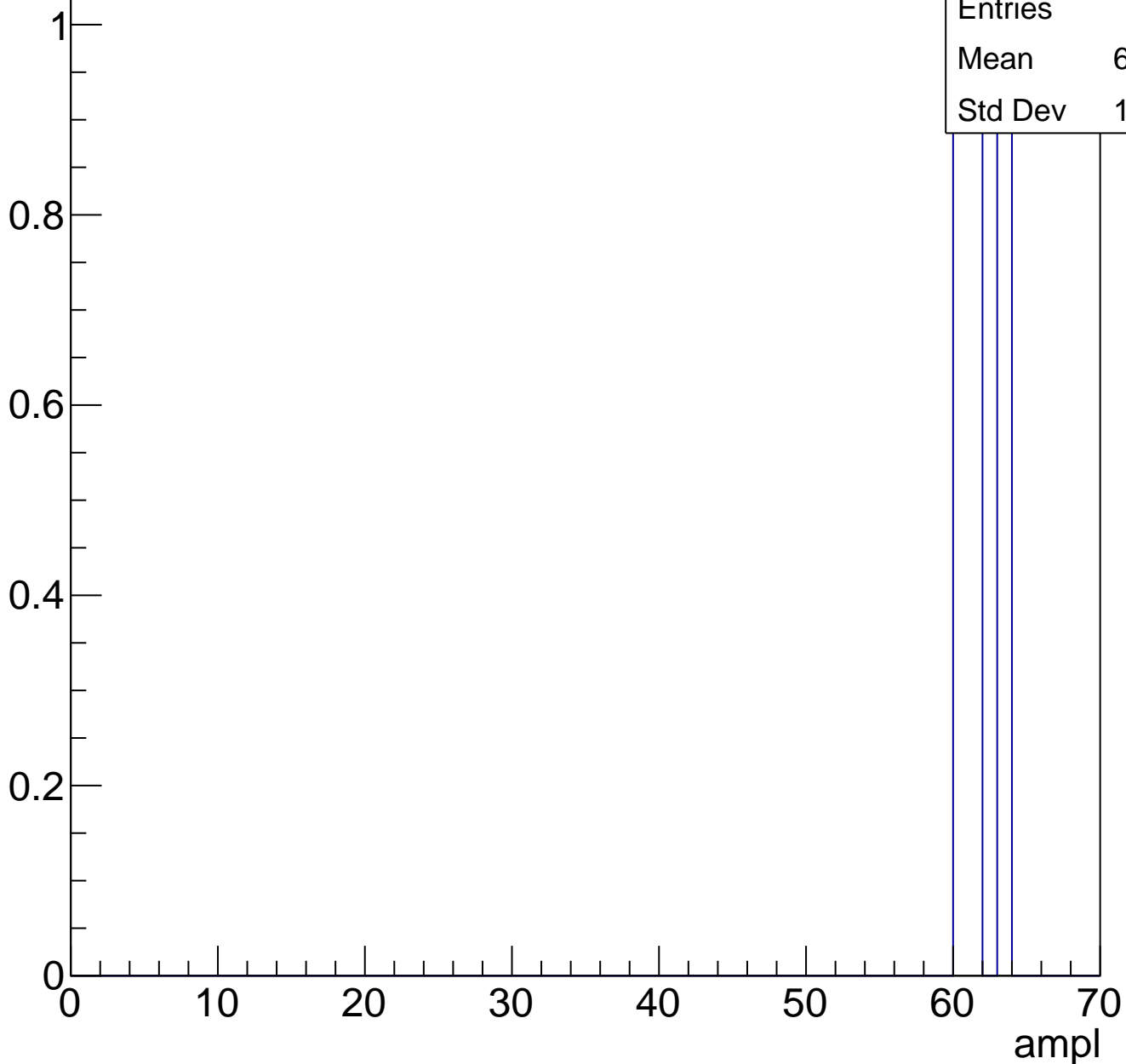
Entries	56
Mean	58.77
Std Dev	8.315



# B0L000S, U25-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch16, adc7

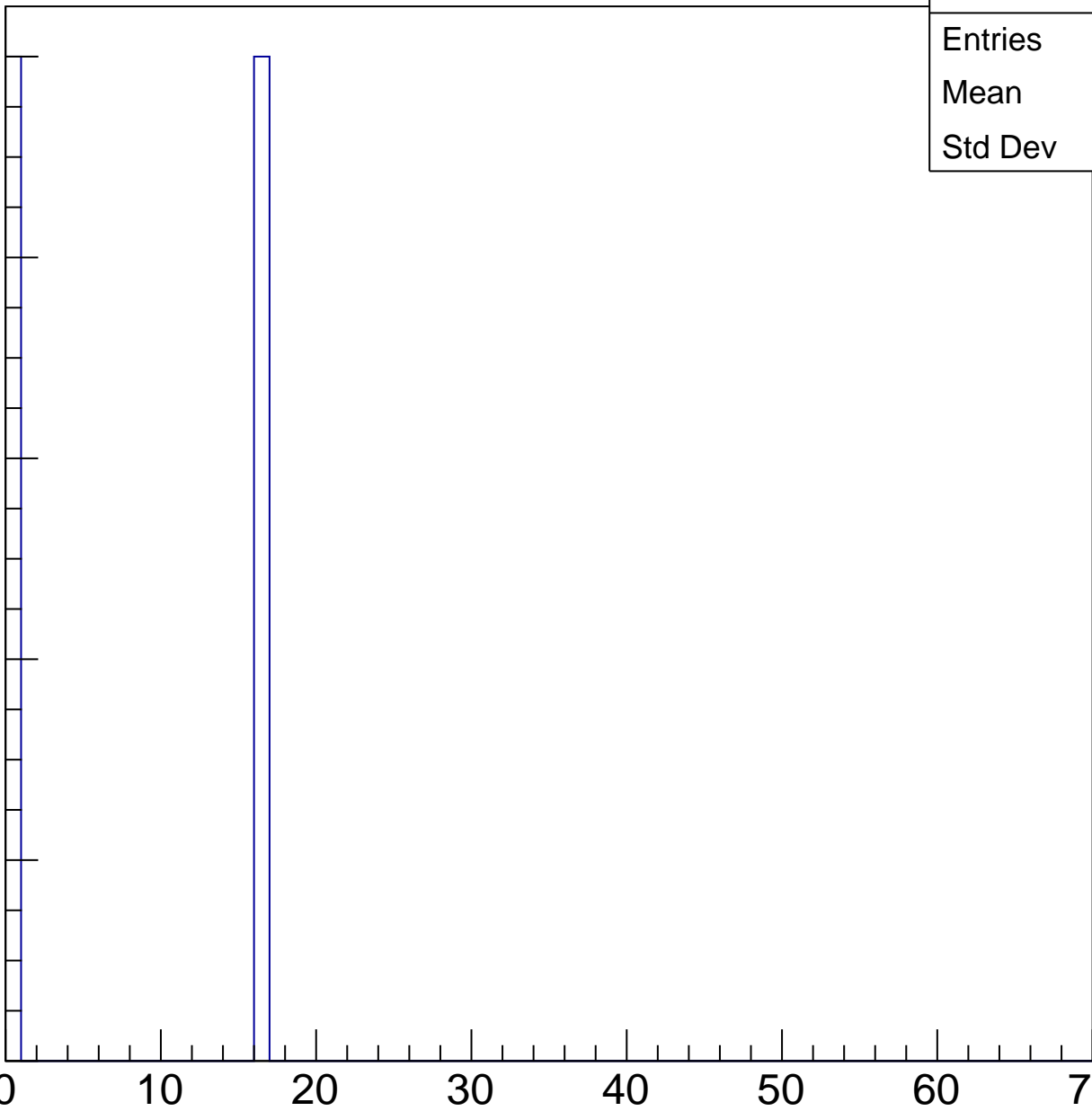
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	8
Std Dev	8

ampl



# B0L000S, U25-ch17, adc0

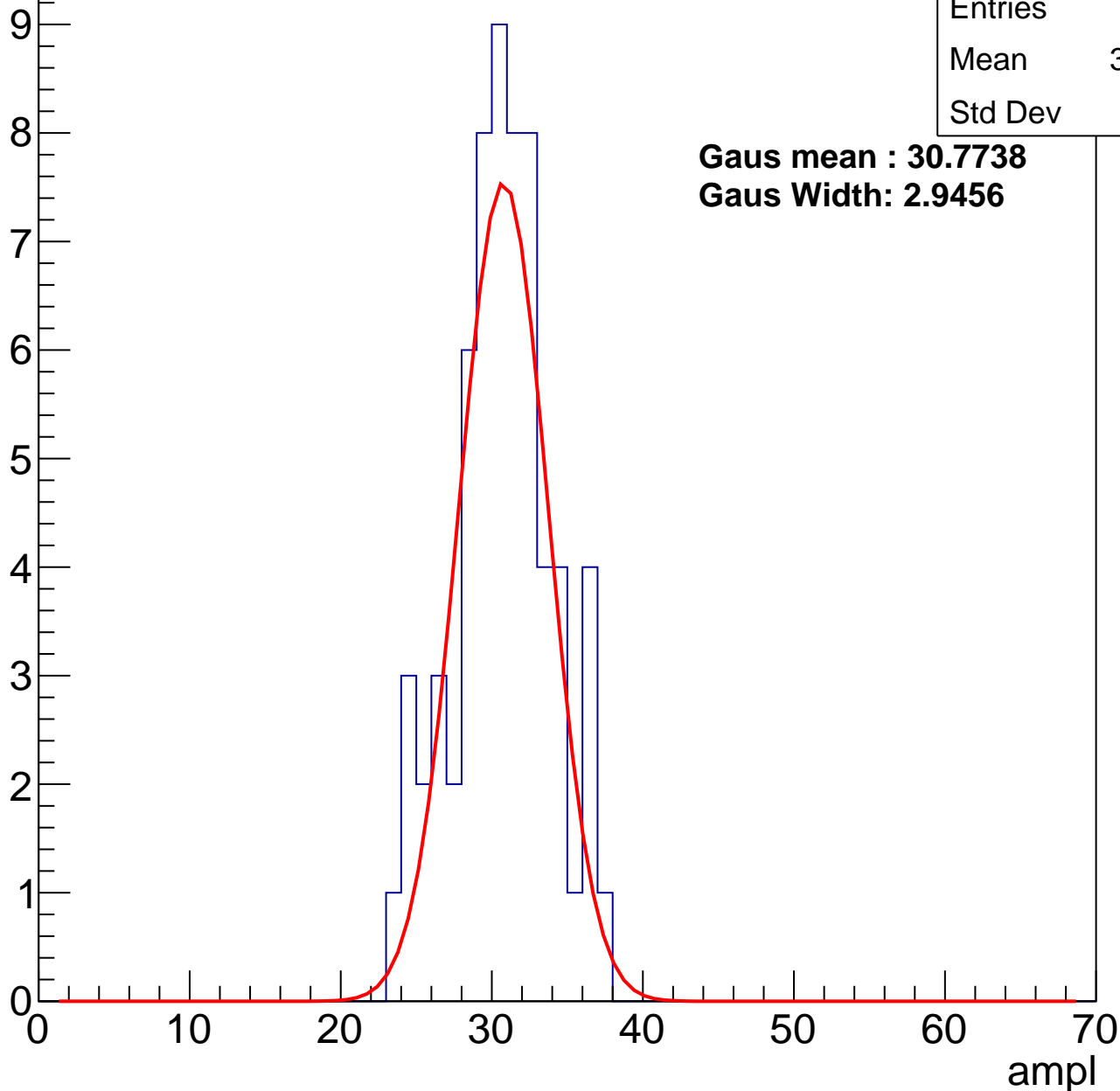
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.23
Std Dev	3.21

**Gaus mean : 30.7738**

**Gaus Width: 2.9456**



# B0L000S, U25-ch17, adc1

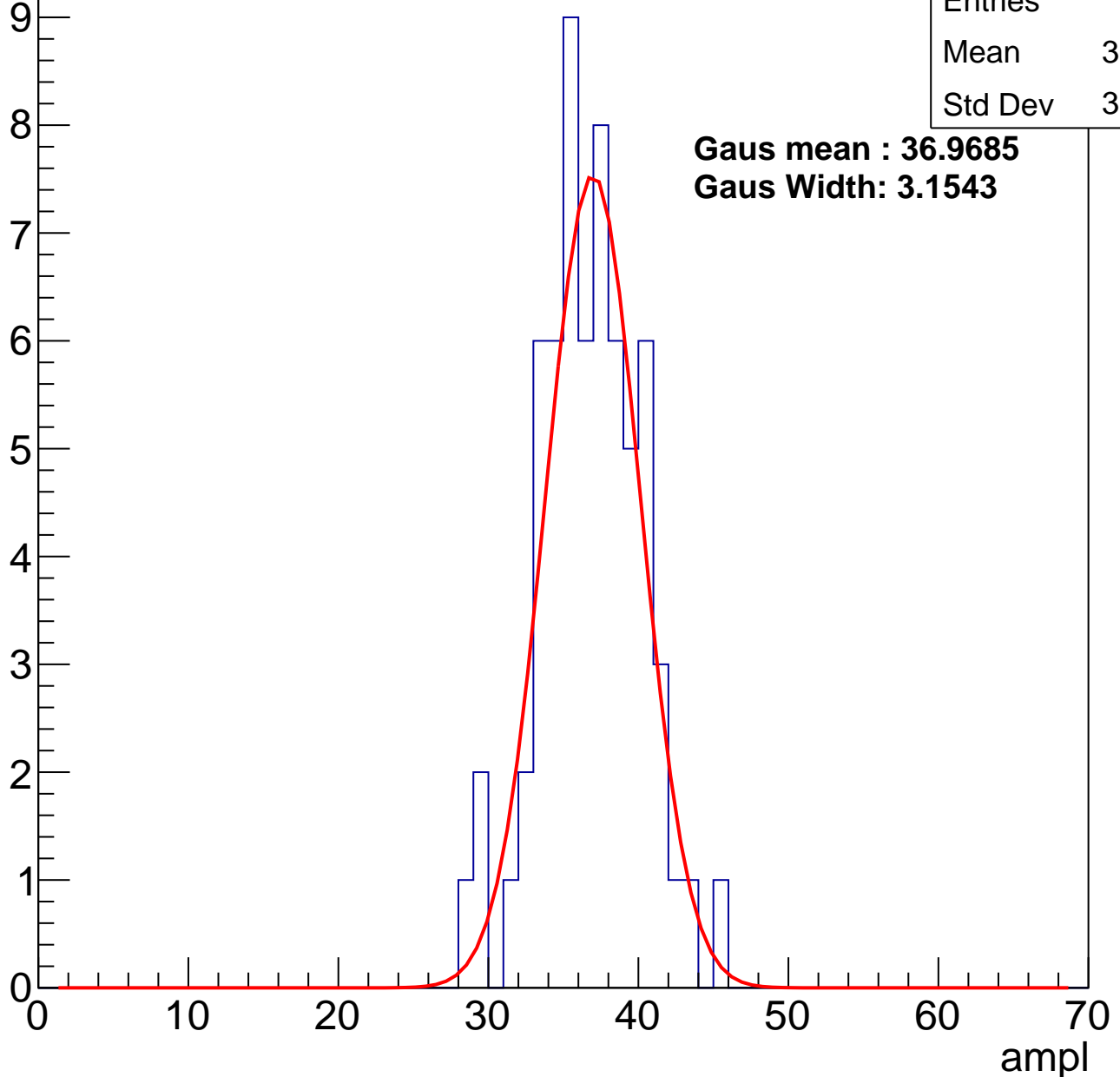
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	36.34
Std Dev	3.336

**Gaus mean : 36.9685**

**Gaus Width: 3.1543**



# B0L000S, U25-ch17, adc2

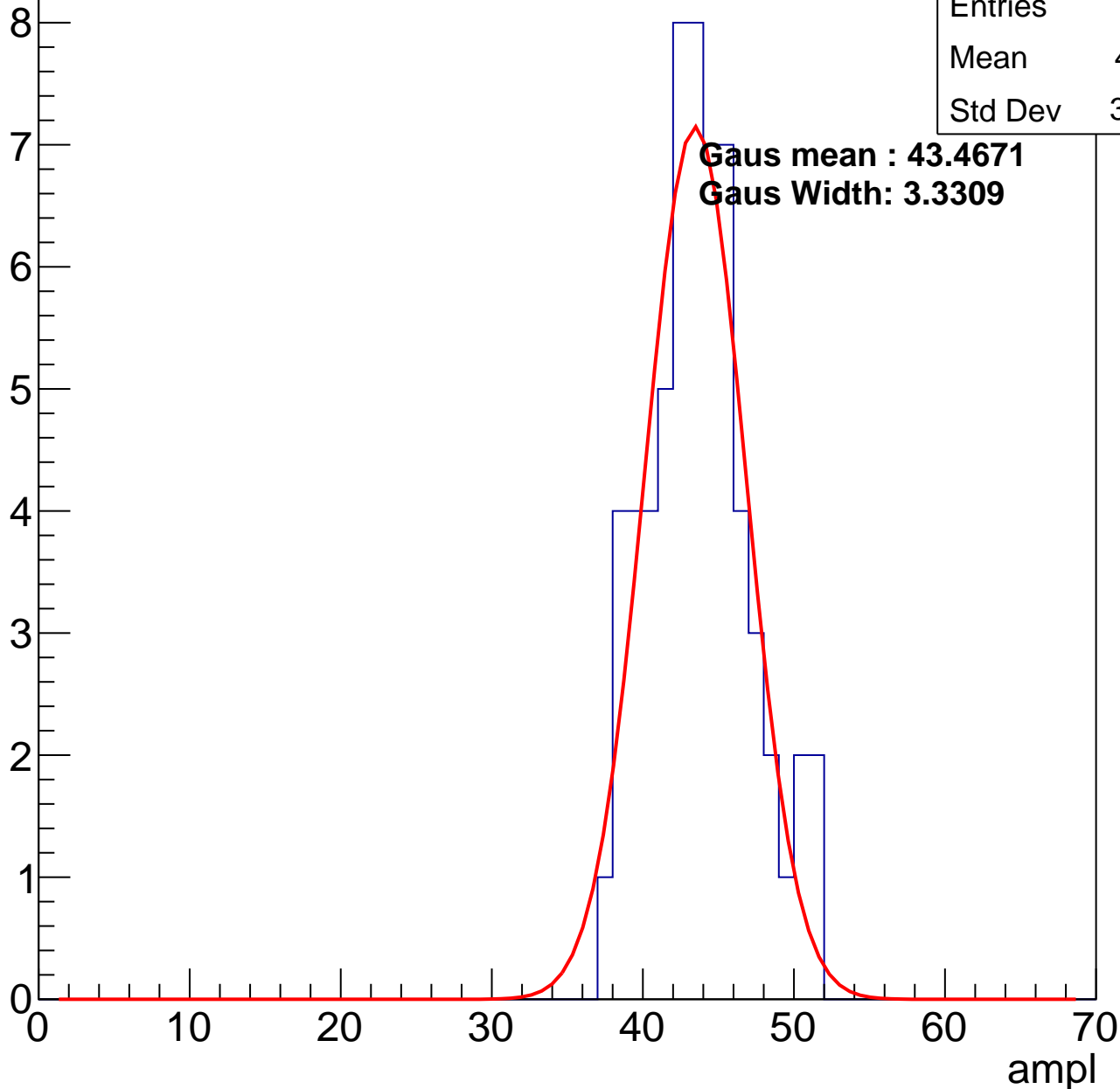
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.31
Std Dev	3.334

**Gaus mean : 43.4671**

**Gaus Width: 3.3309**

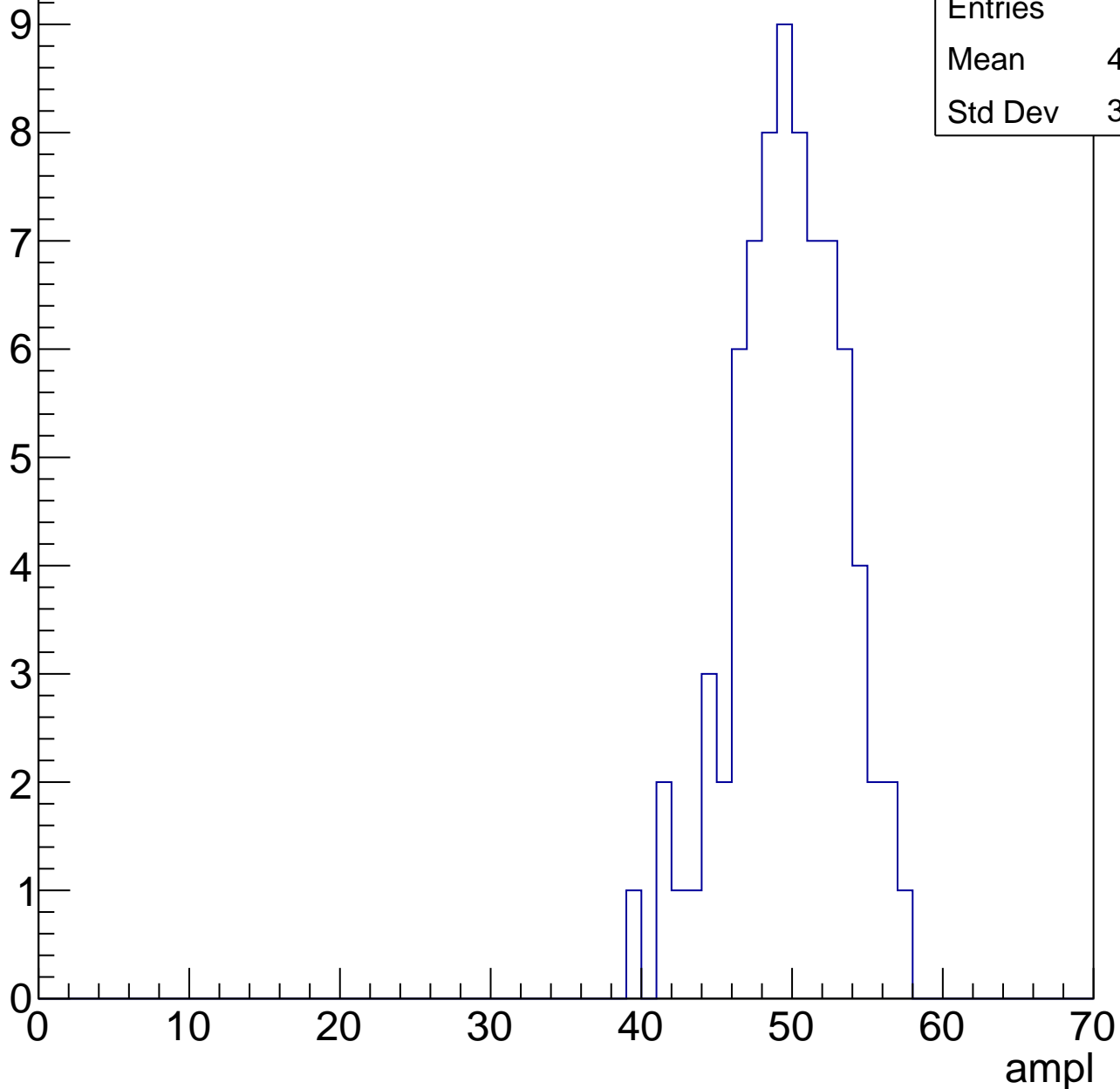


# B0L000S, U25-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	49.25
Std Dev	3.665

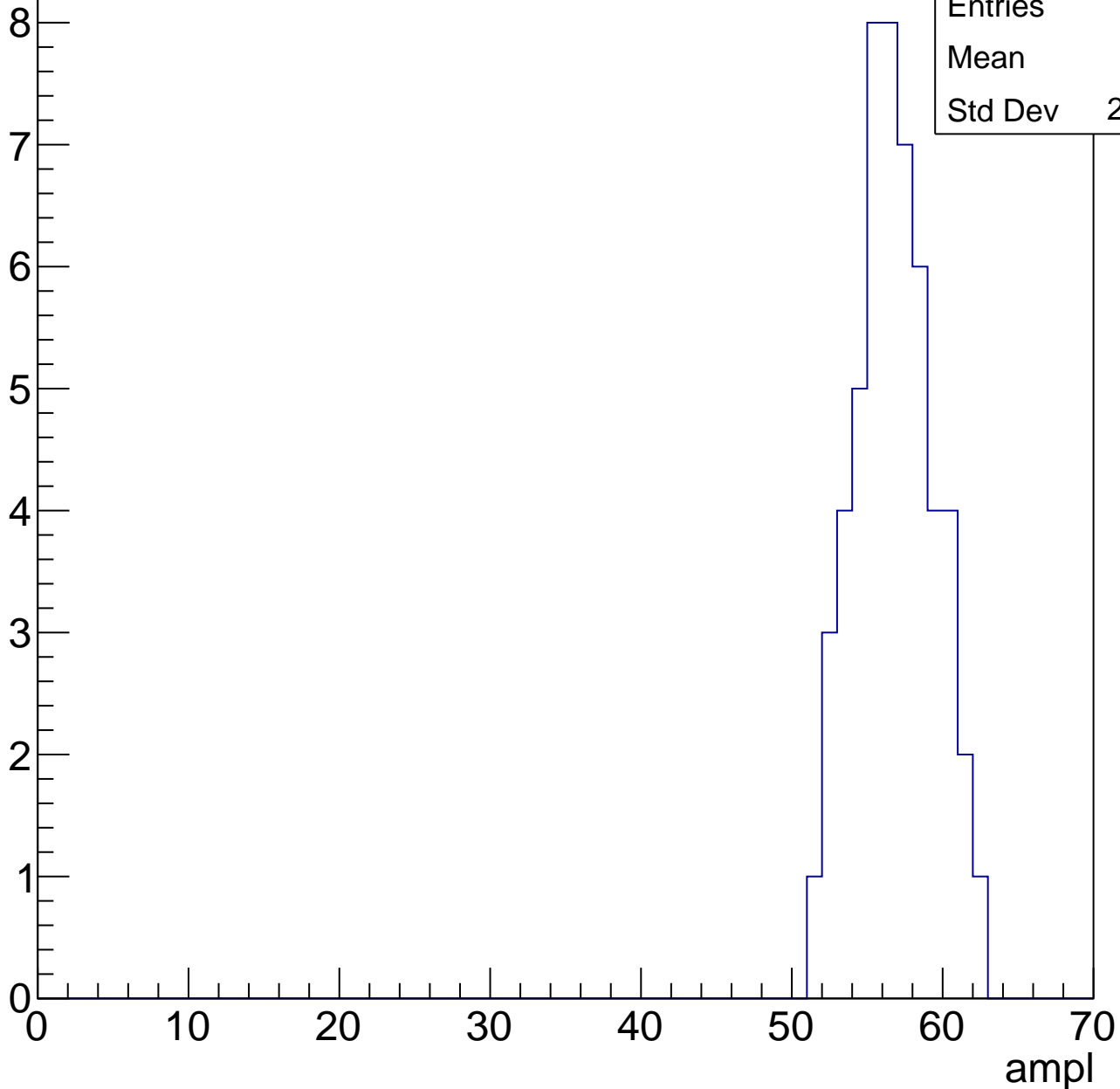


# B0L000S, U25-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	56.3
Std Dev	2.567

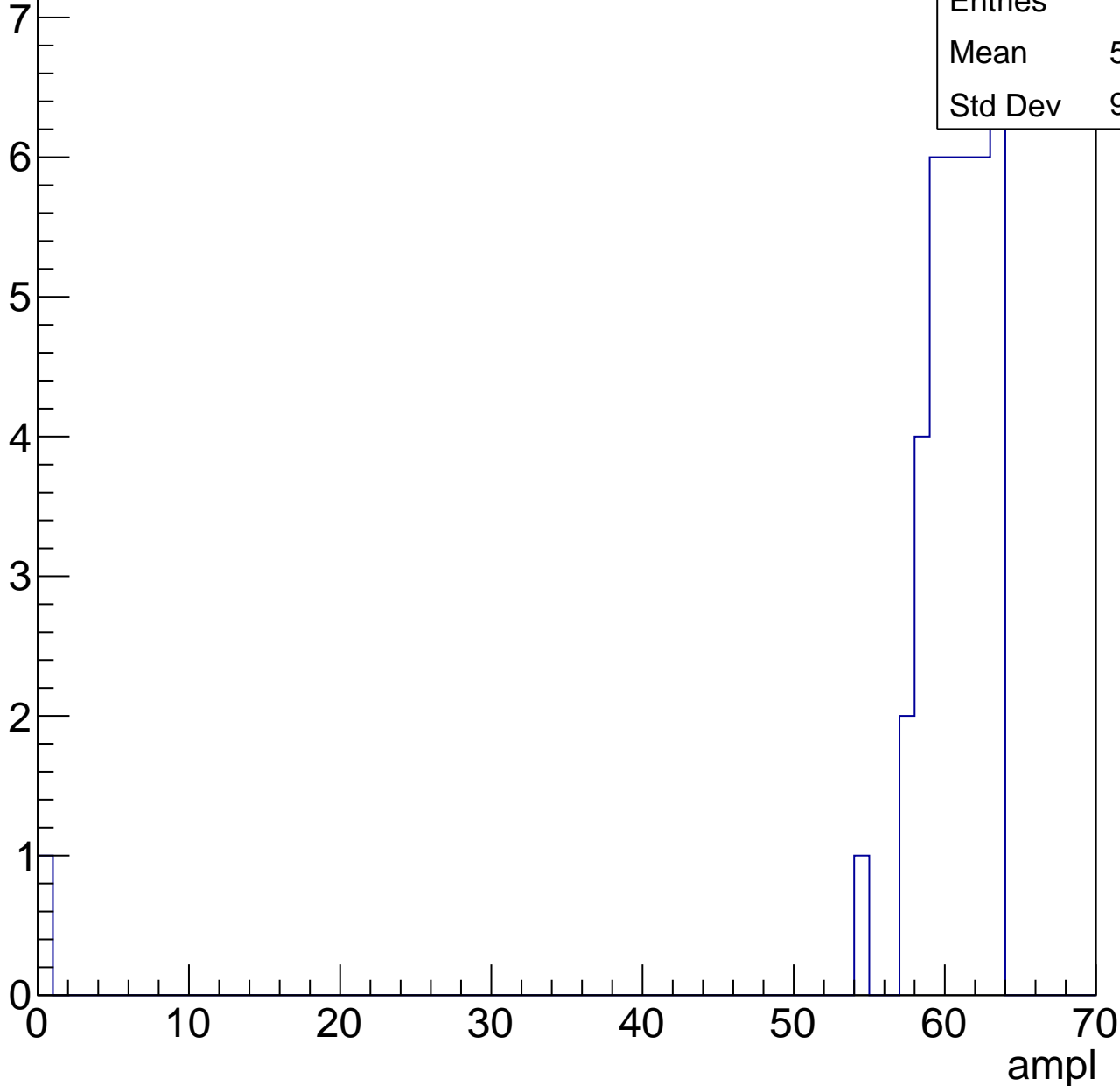


# B0L000S, U25-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

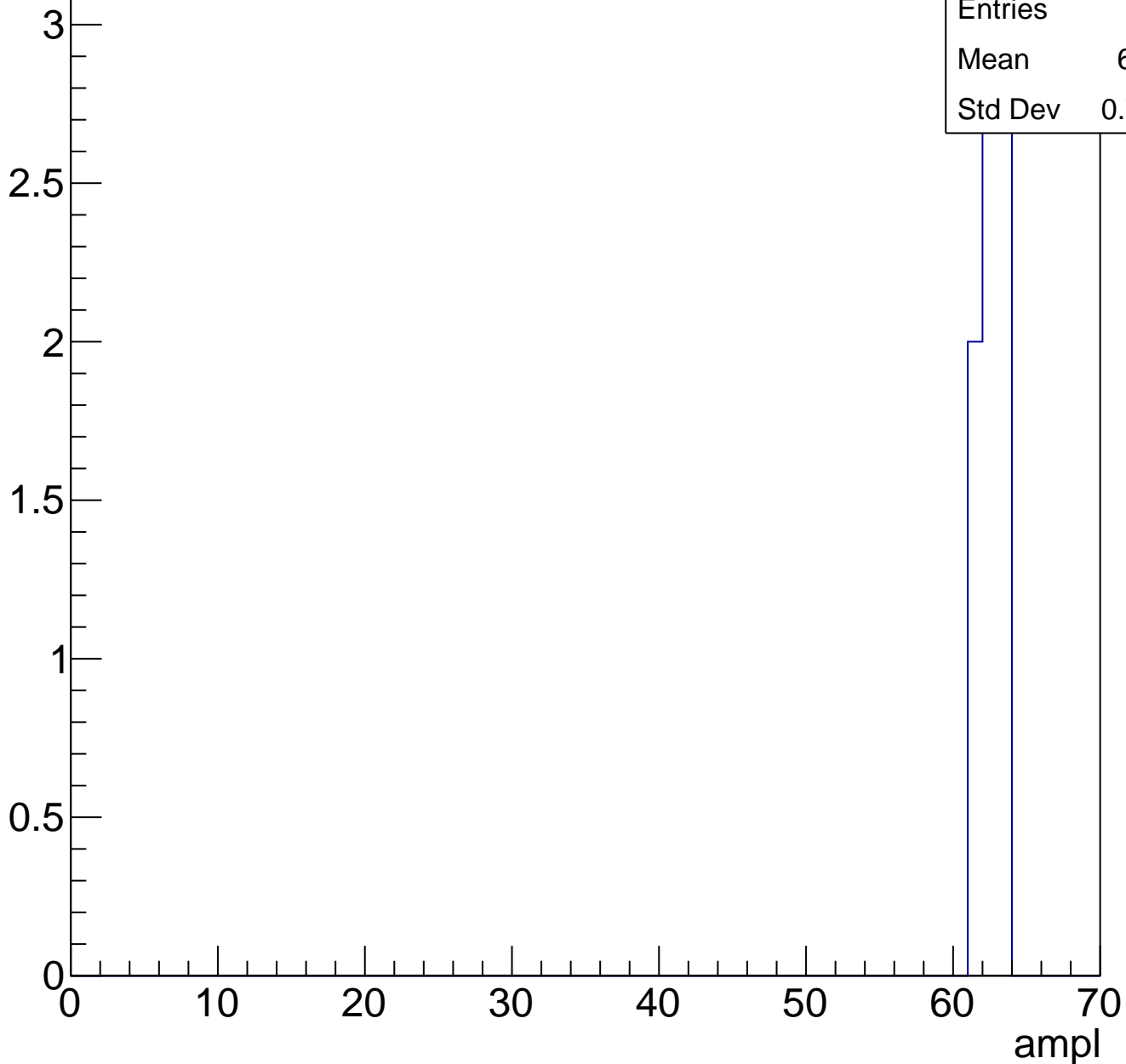
Entries	39
Mean	58.79
Std Dev	9.756



# B0L000S, U25-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch18, adc0

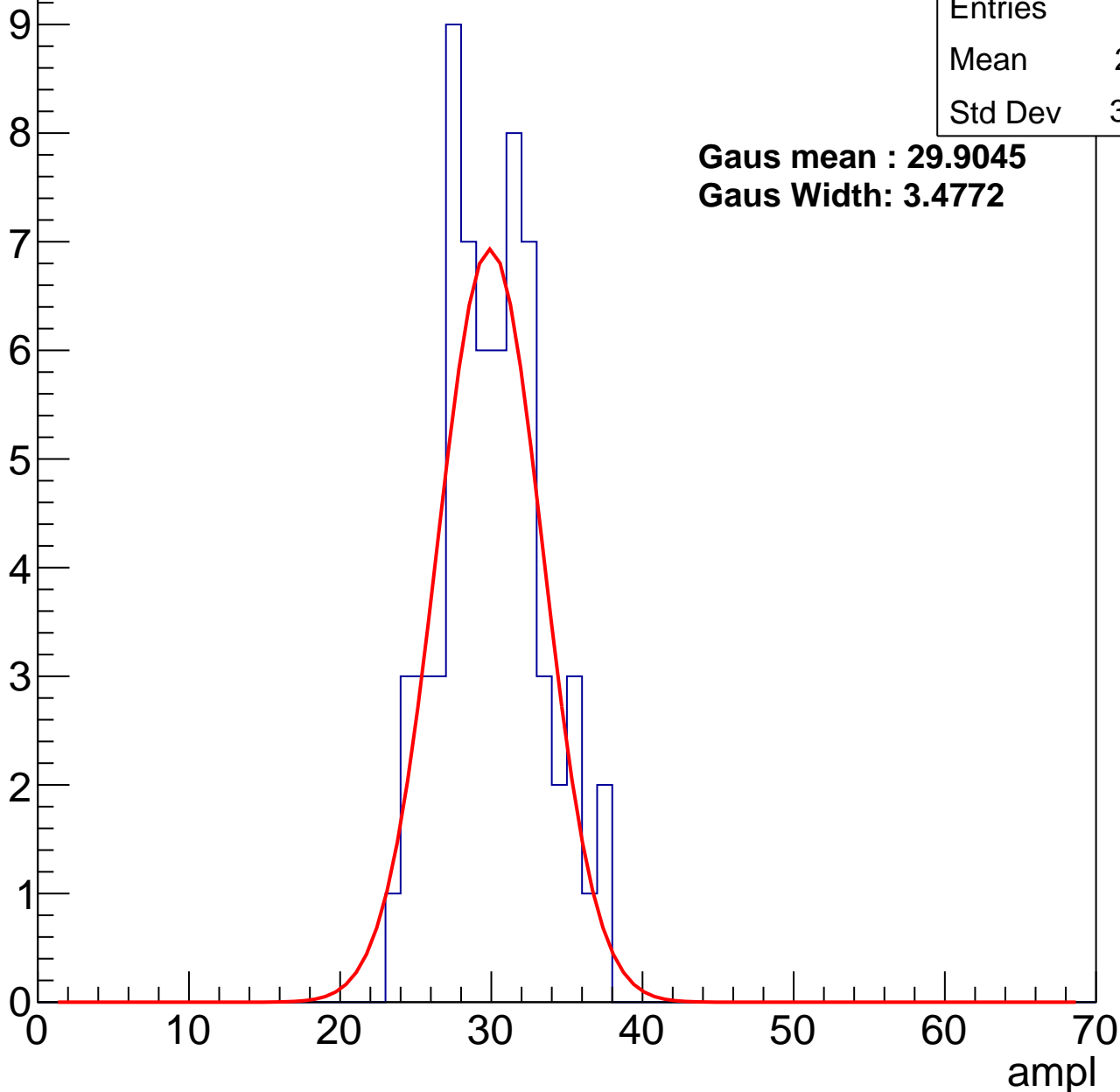
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	29.61
Std Dev	3.282

**Gaus mean : 29.9045**

**Gaus Width: 3.4772**



# B0L000S, U25-ch18, adc1

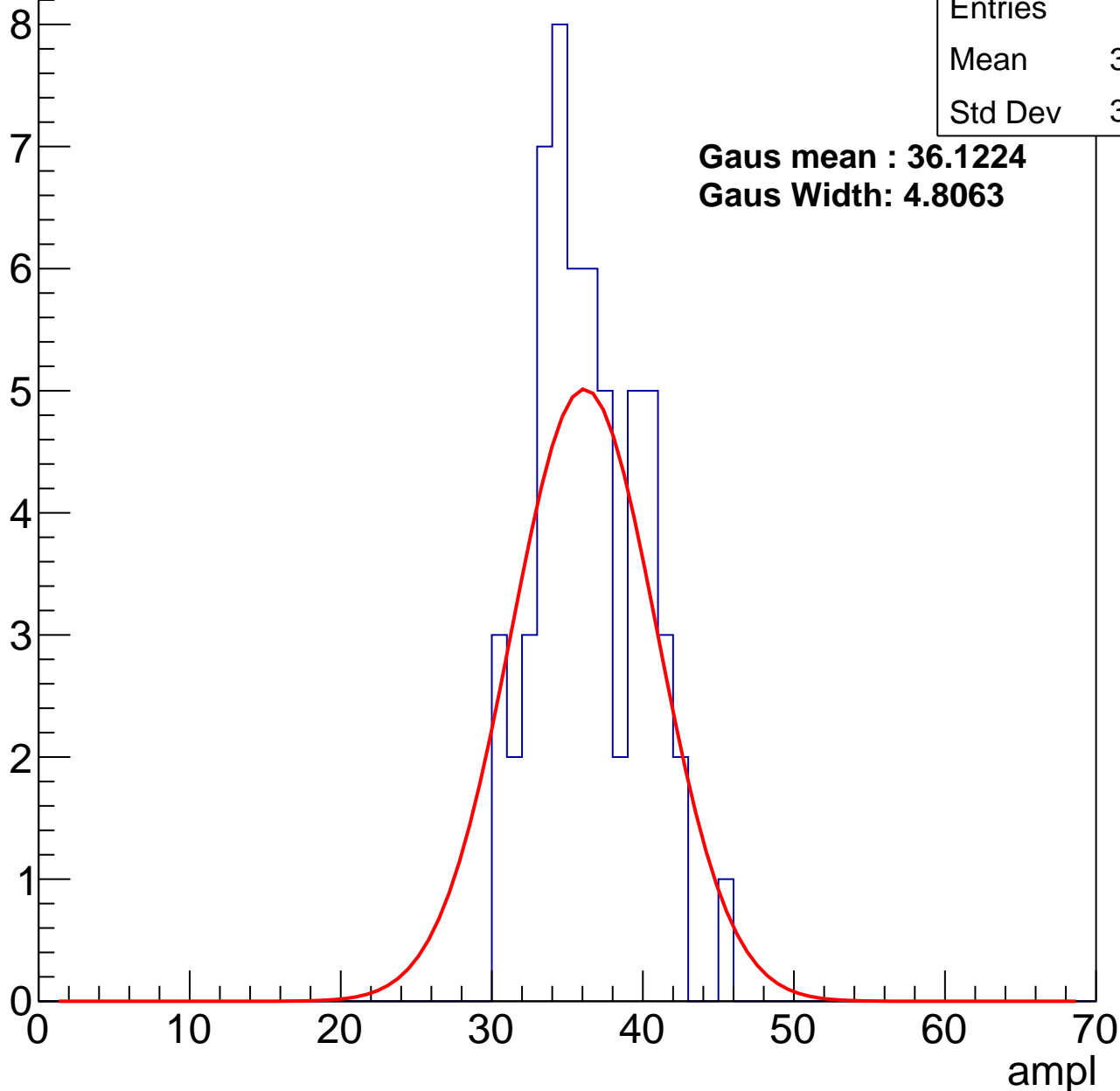
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	35.95
Std Dev	3.406

**Gaus mean : 36.1224**

**Gaus Width: 4.8063**



# B0L000S, U25-ch18, adc2

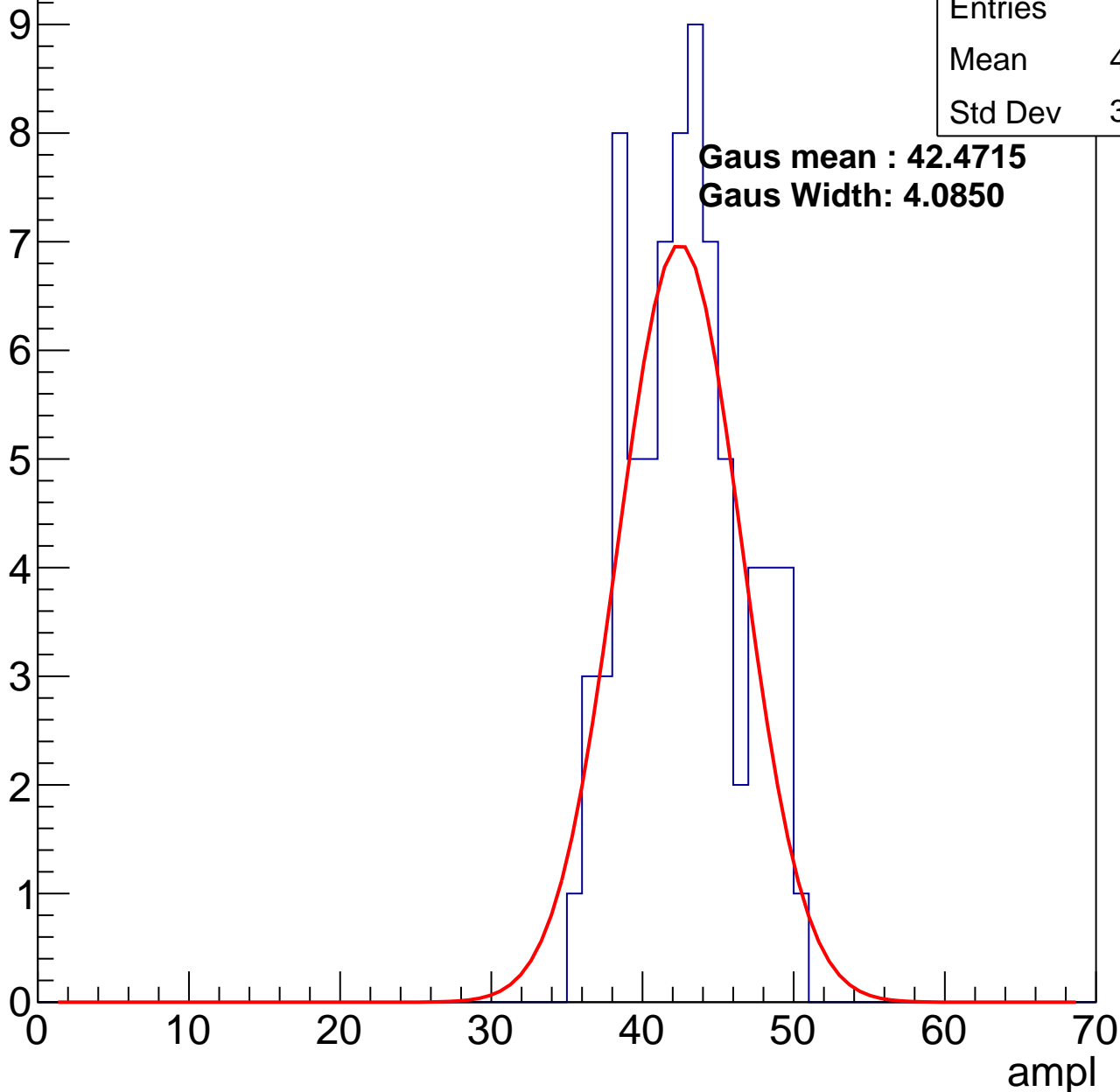
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.29
Std Dev	3.706

**Gaus mean : 42.4715**

**Gaus Width: 4.0850**



# B0L000S, U25-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

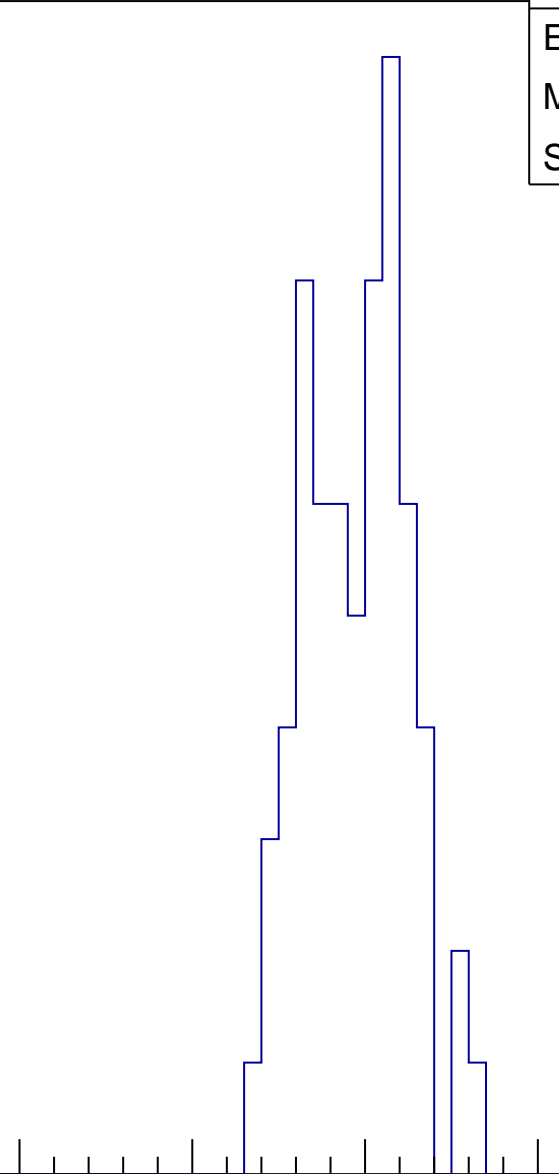
Entries	64
Mean	49.03
Std Dev	2.968

Entry

10  
8  
6  
4  
2  
0

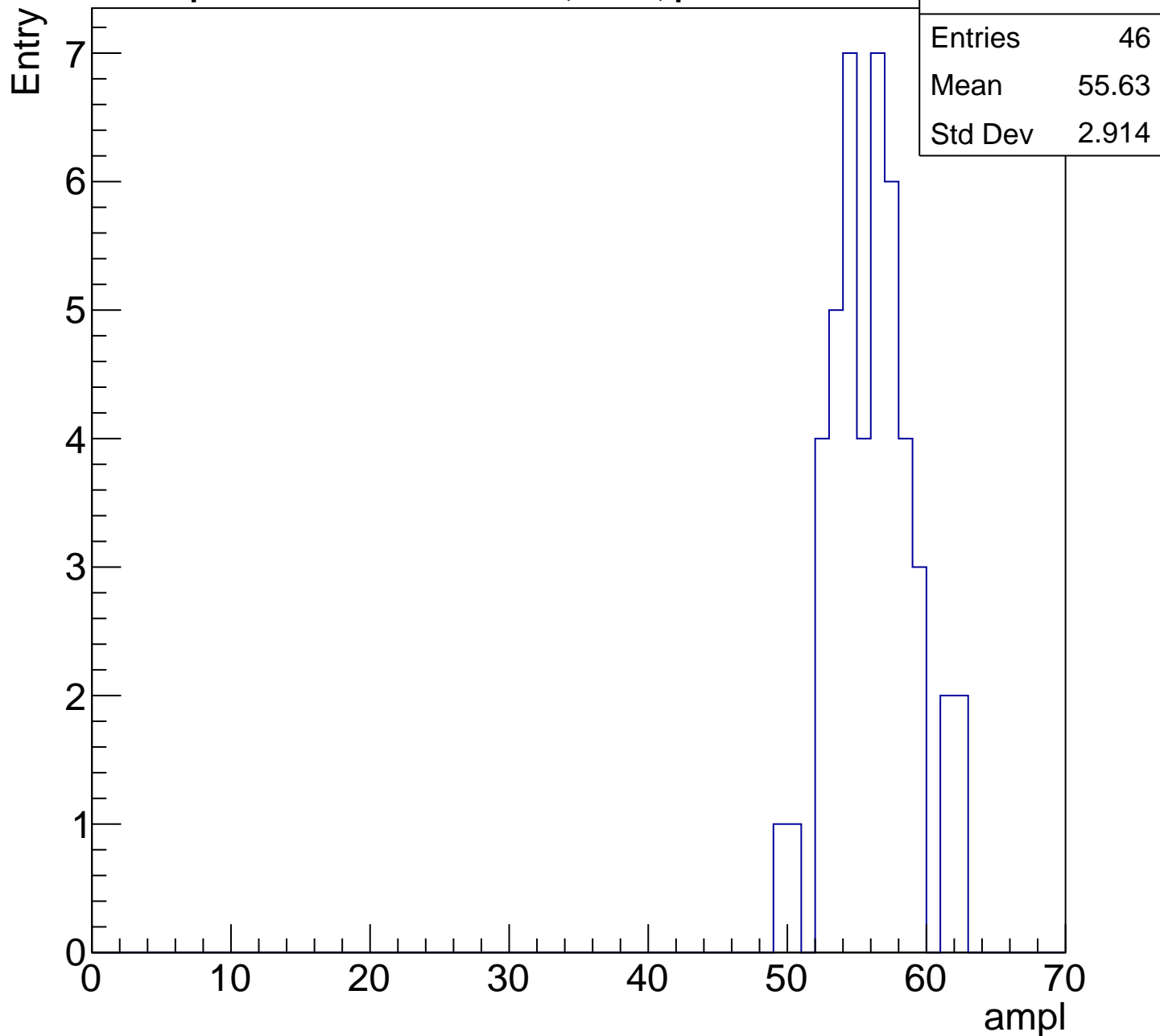
0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

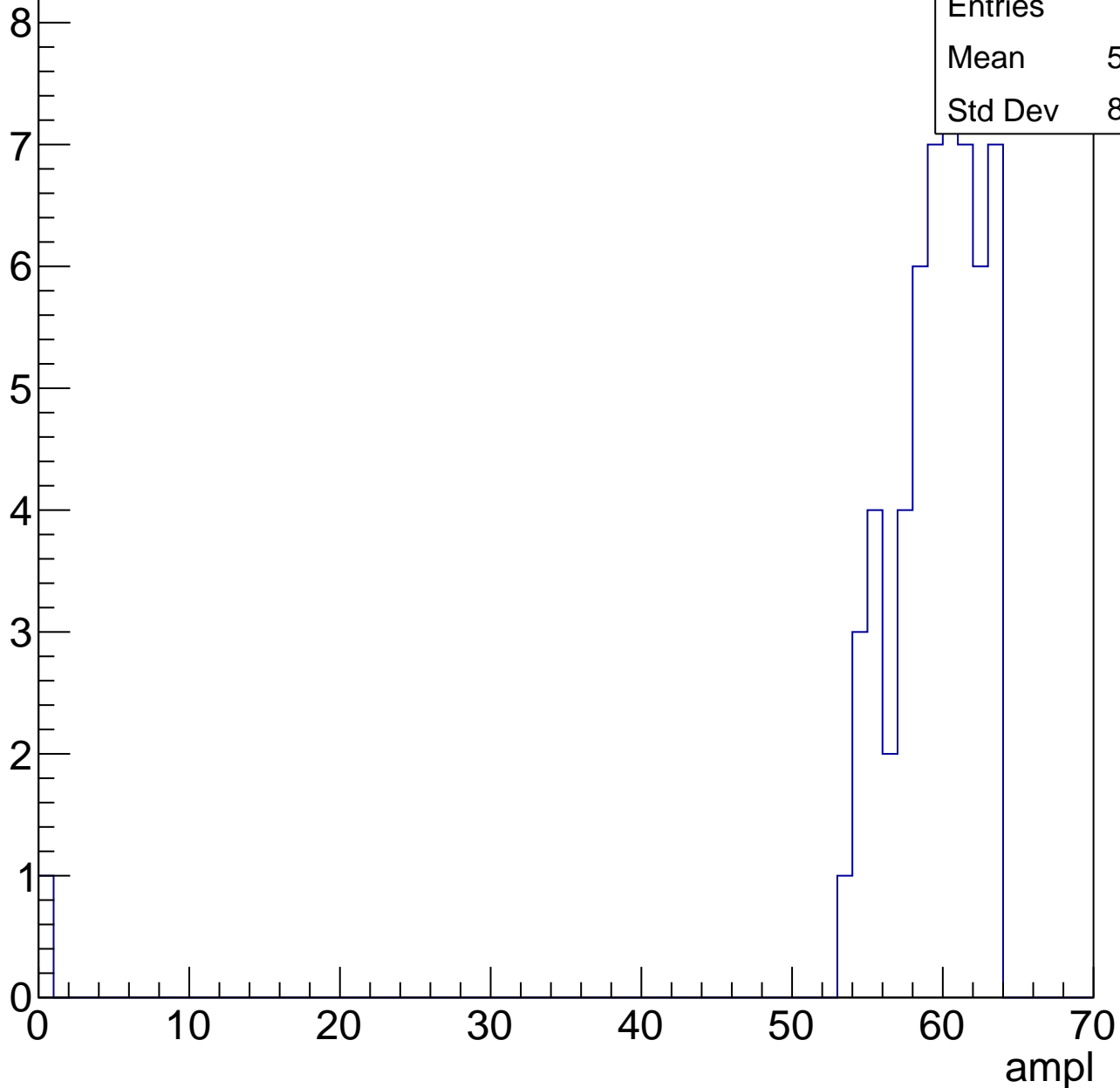


# B0L000S, U25-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

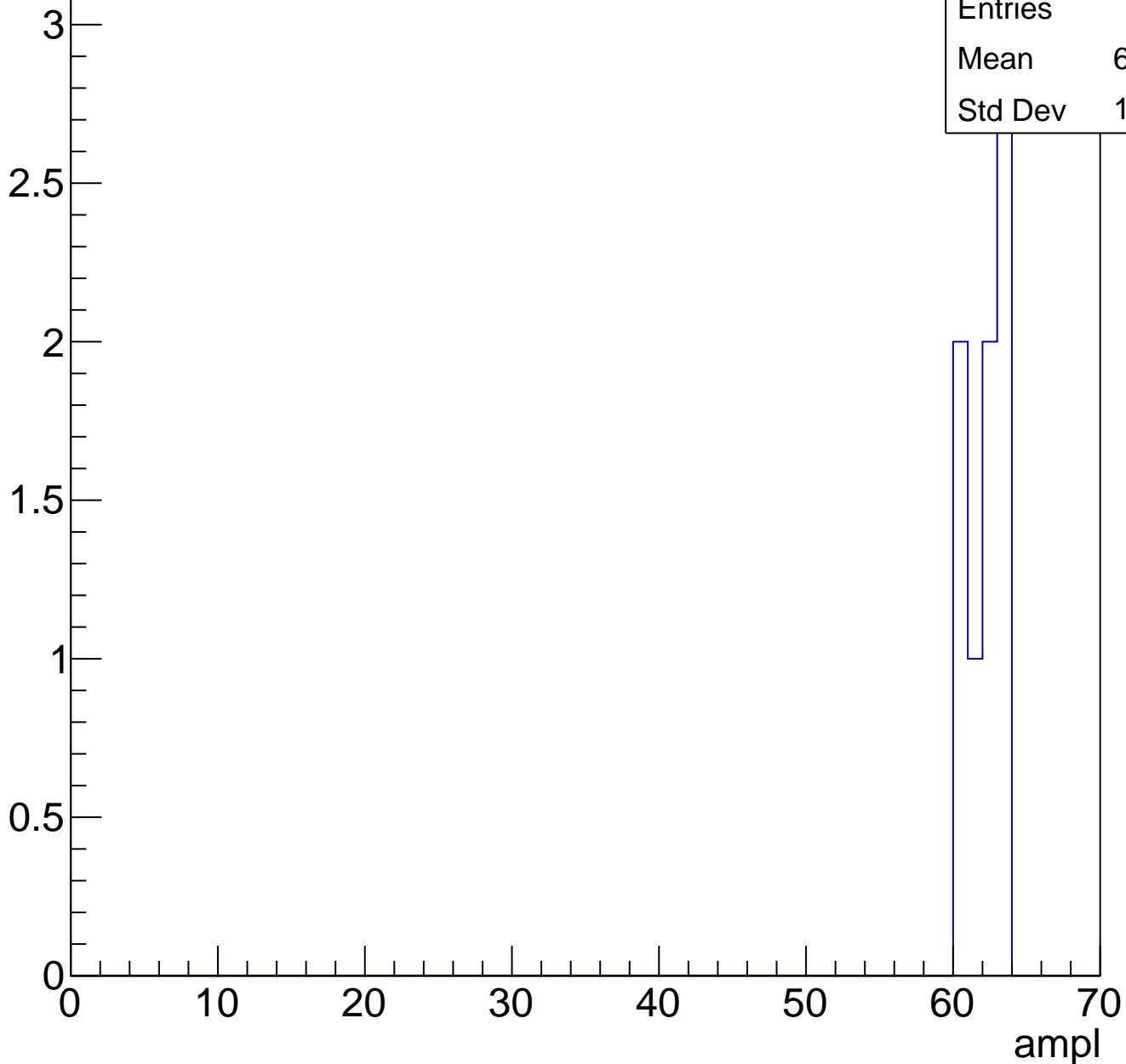
Entries	56
Mean	58.14
Std Dev	8.299



# B0L000S, U25-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

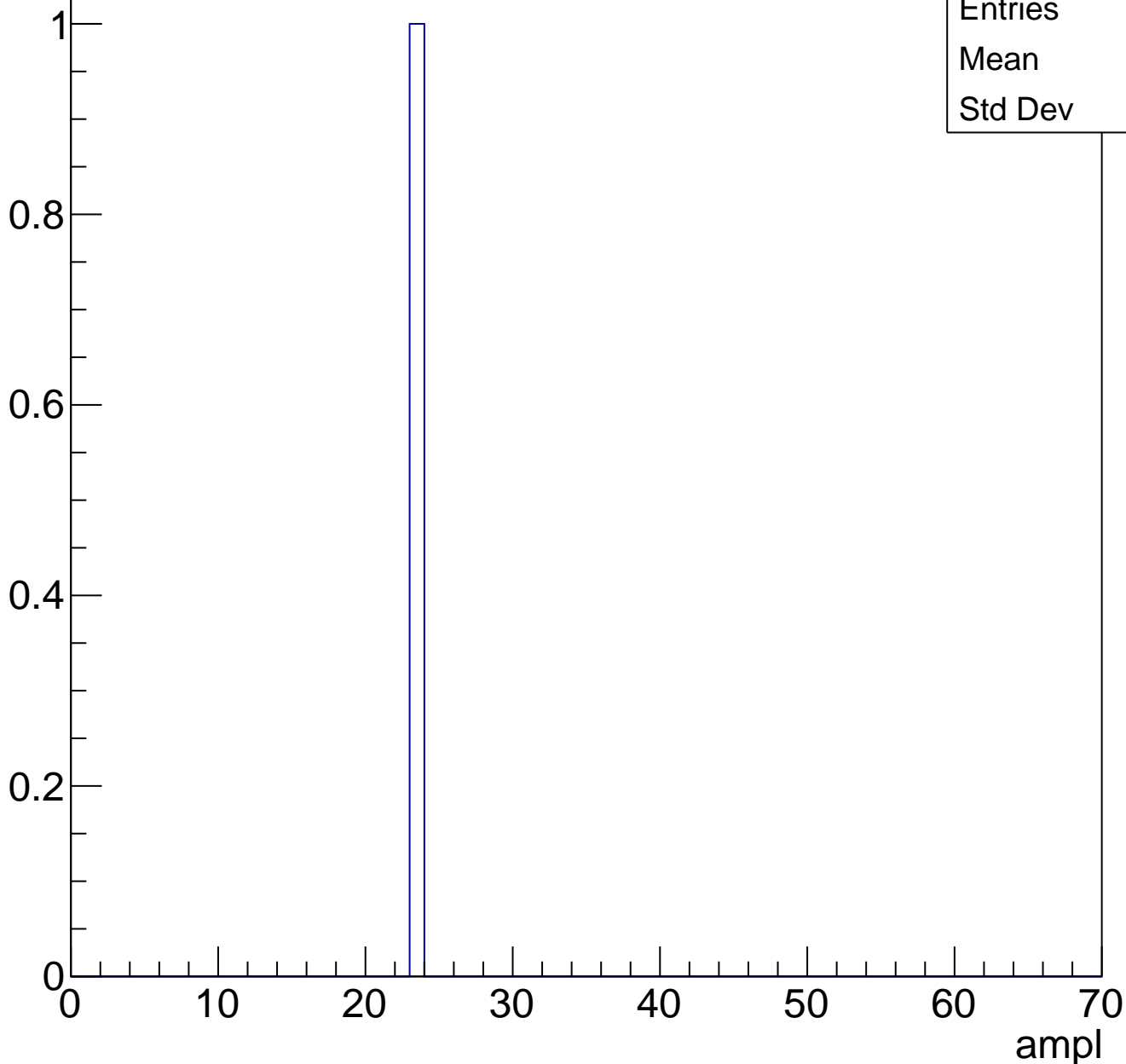




# B0L000S, U25-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U25-ch19, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	70
Mean	30.64
Std Dev	3.278

**Gaus mean : 31.2234**

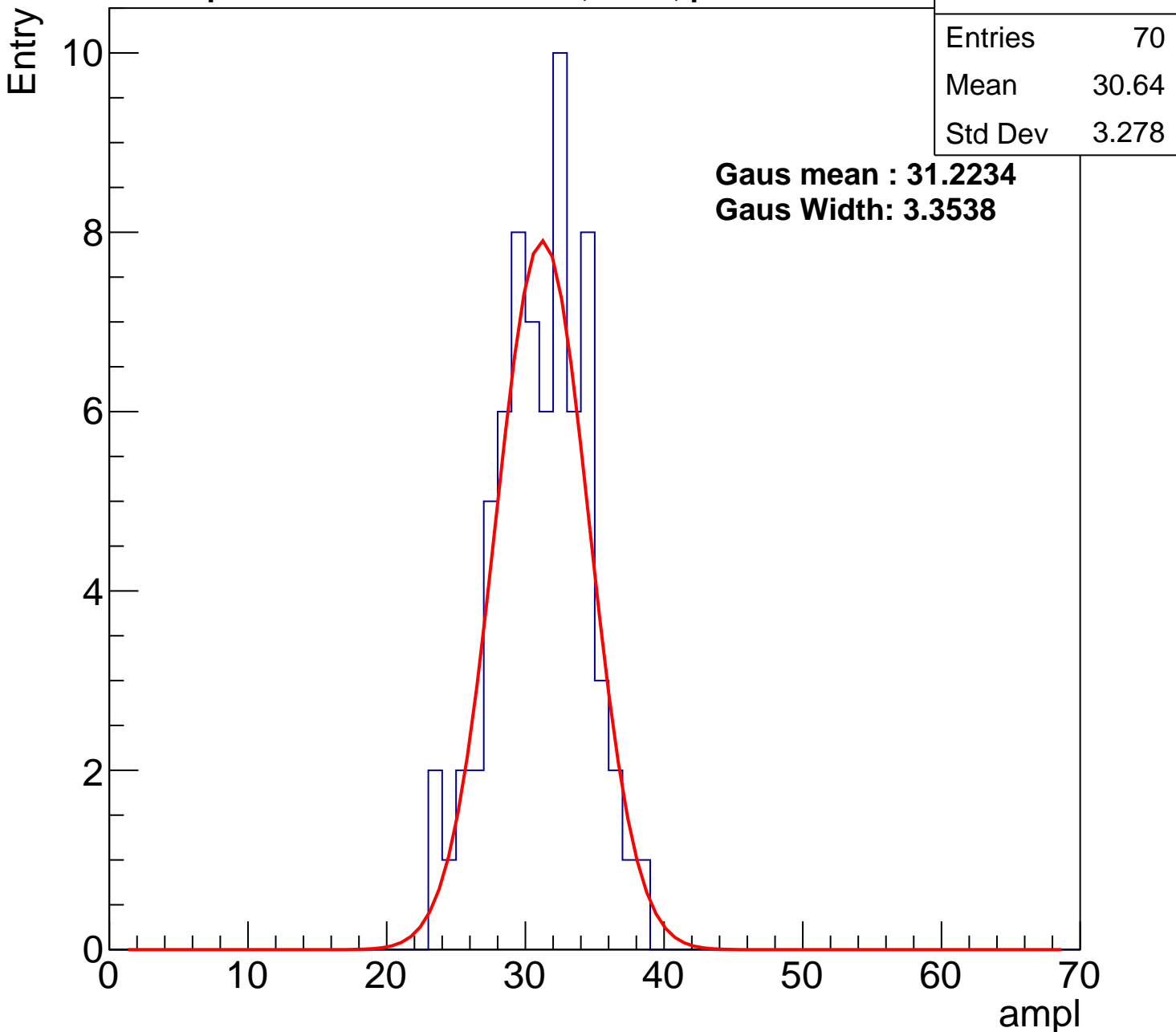
**Gaus Width: 3.3538**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch19, adc1

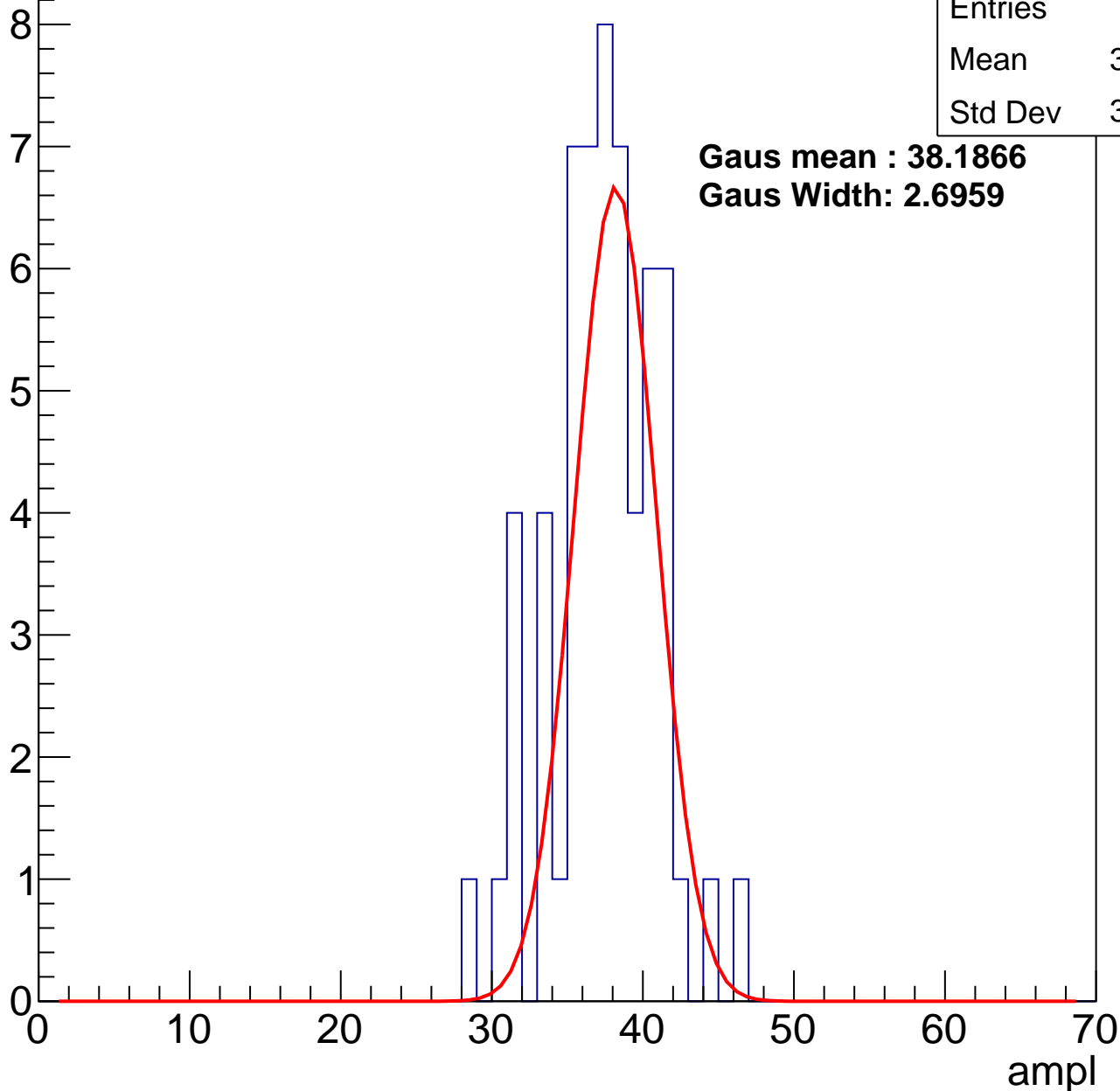
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.97
Std Dev	3.469

**Gaus mean : 38.1866**

**Gaus Width: 2.6959**



# B0L000S, U25-ch19, adc2

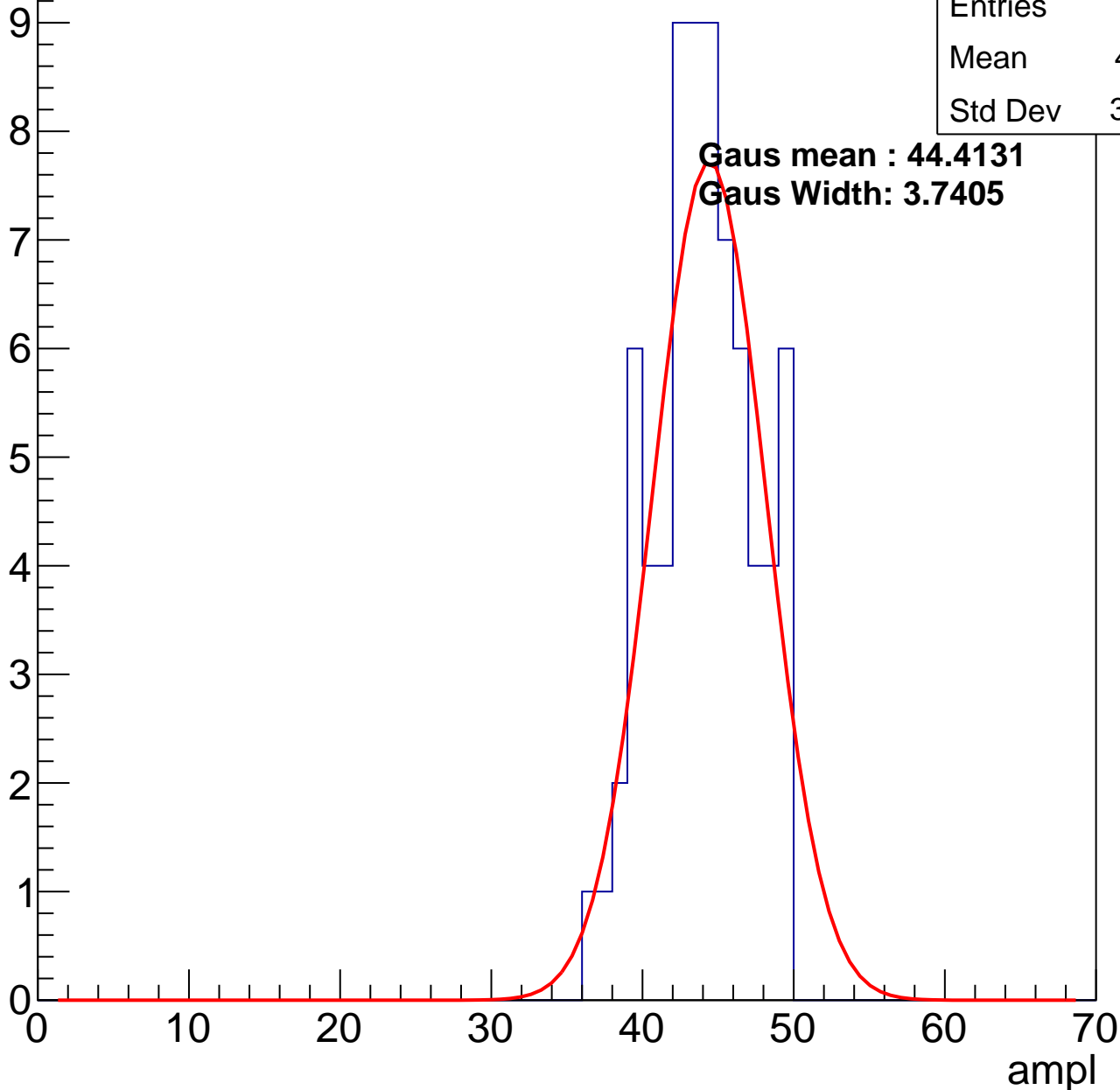
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	43.51
Std Dev	3.215

**Gaus mean : 44.4131**

**Gaus Width: 3.7405**

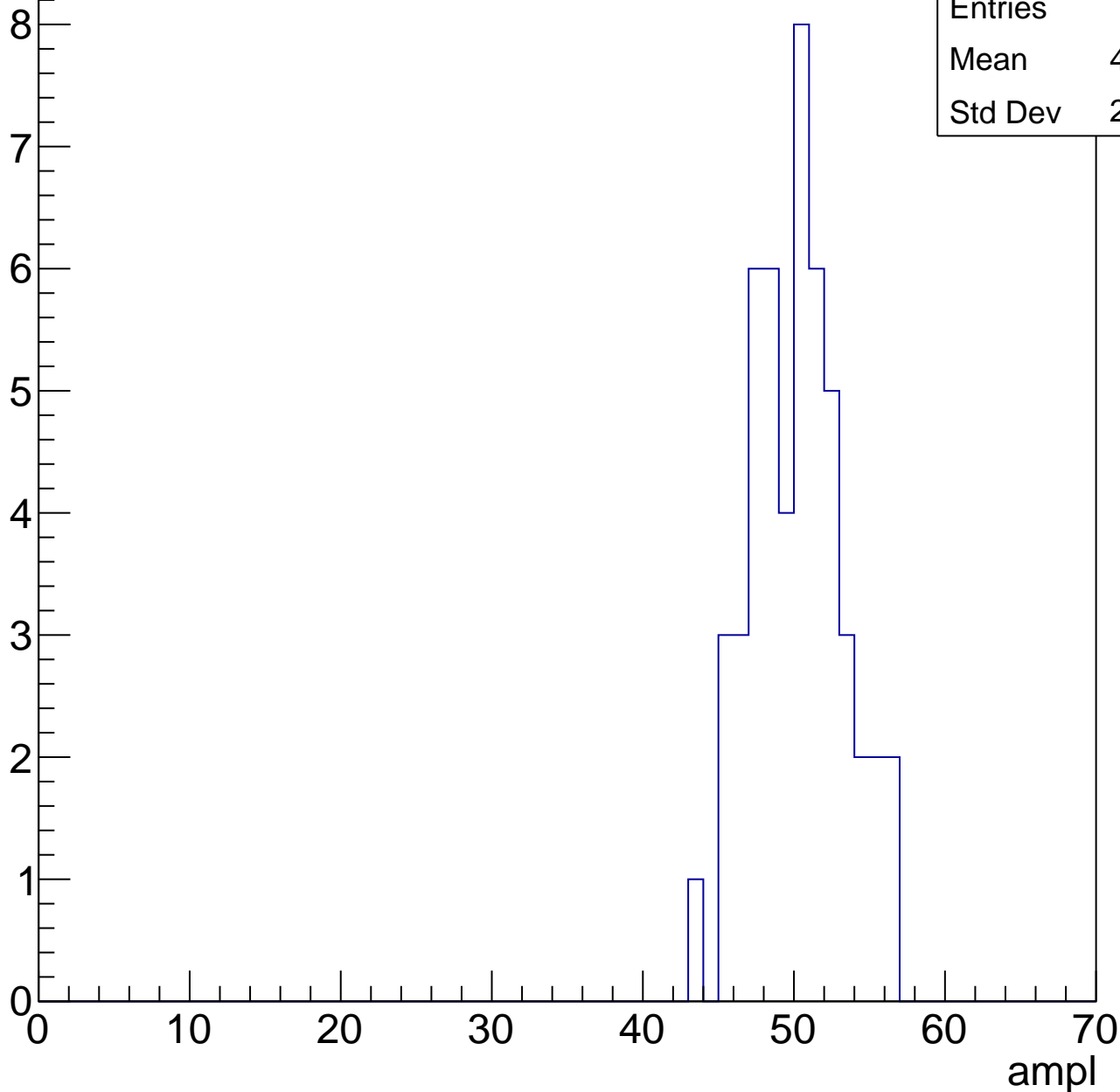


# B0L000S, U25-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

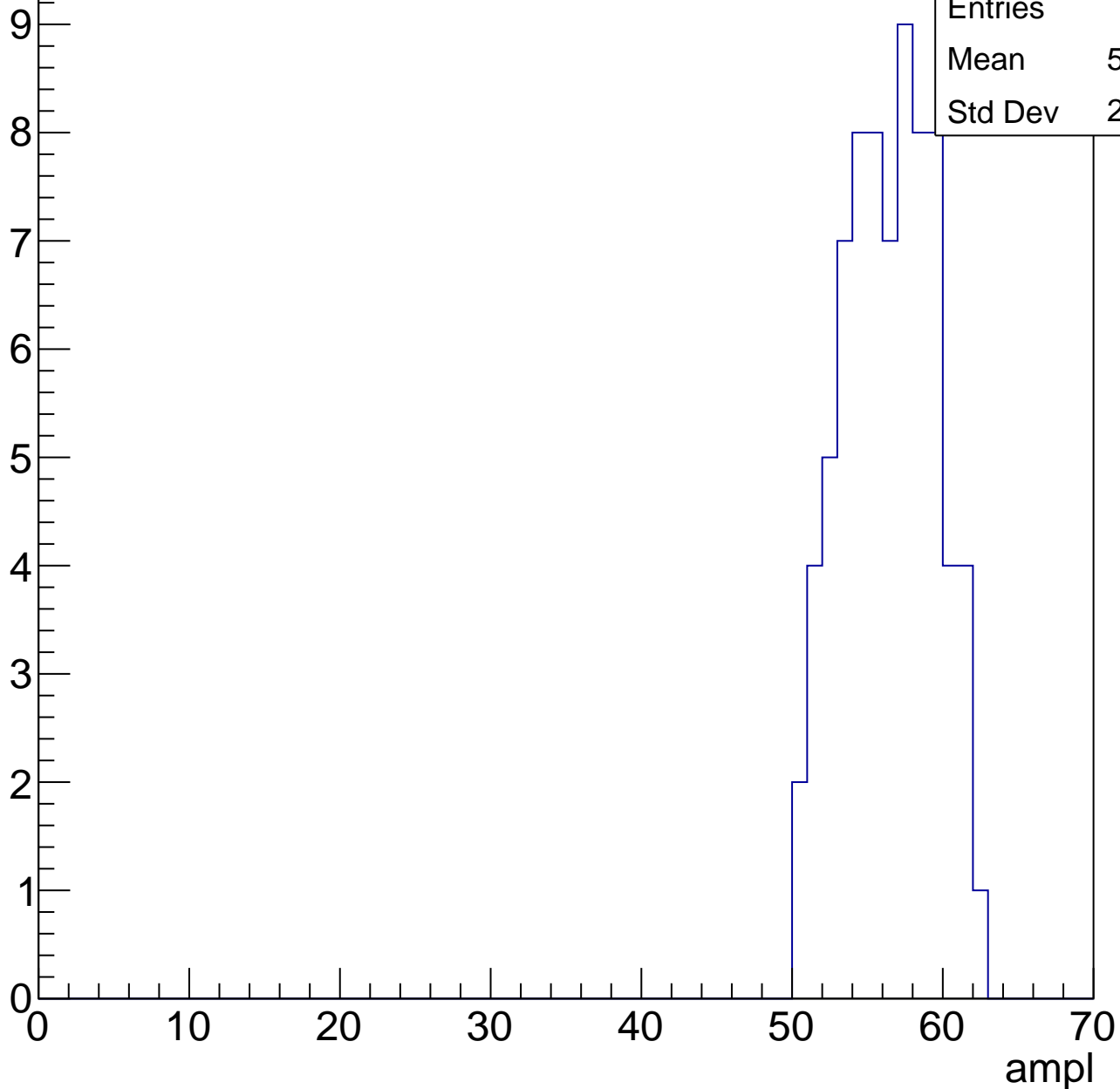
Entries	51
Mean	49.75
Std Dev	2.996



# B0L000S, U25-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



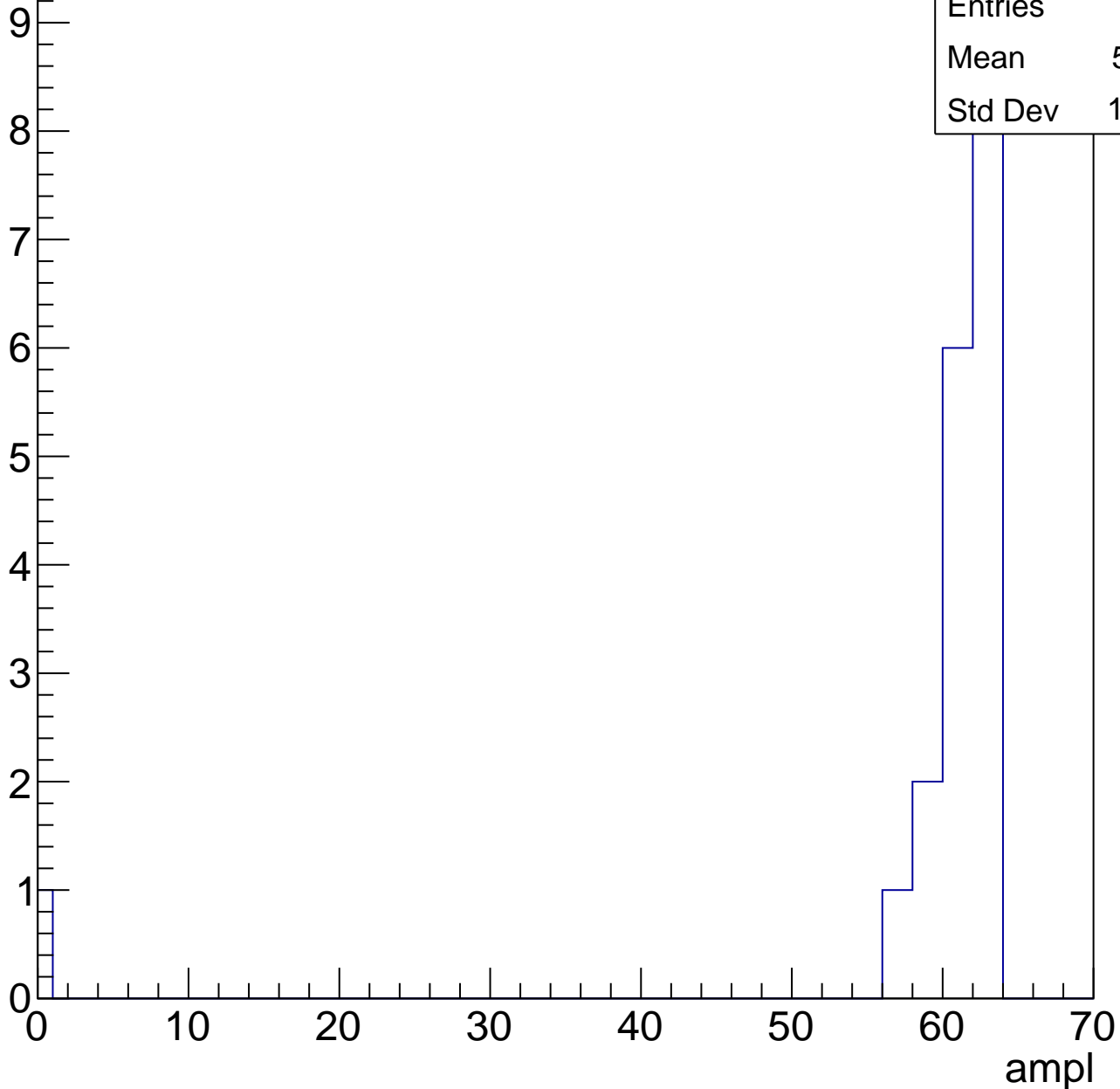
Entries	75
Mean	55.92
Std Dev	2.983

# B0L000S, U25-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	59.31
Std Dev	10.18



# B0L000S, U25-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch20, adc0

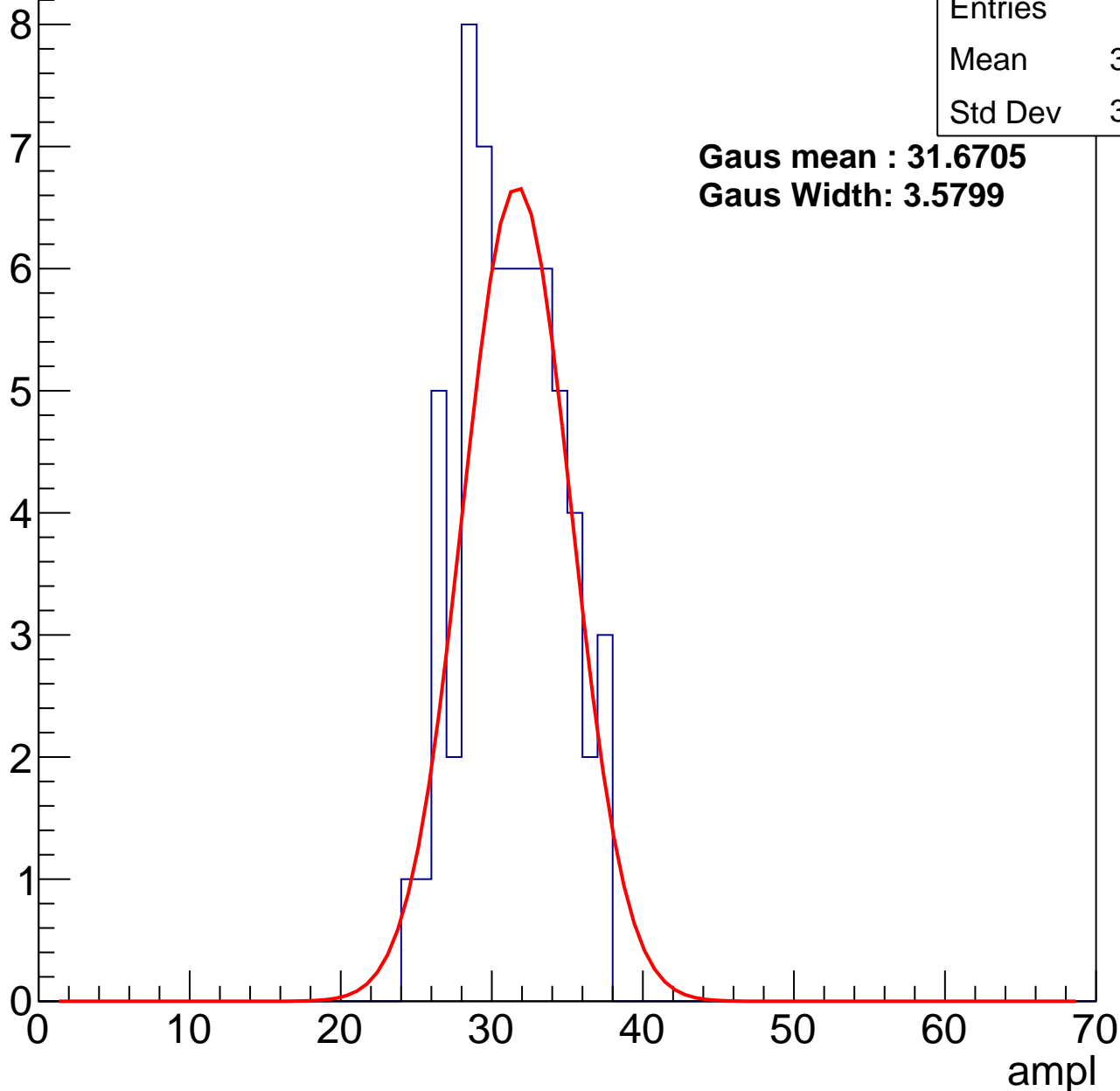
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.79
Std Dev	3.219

**Gaus mean : 31.6705**

**Gaus Width: 3.5799**



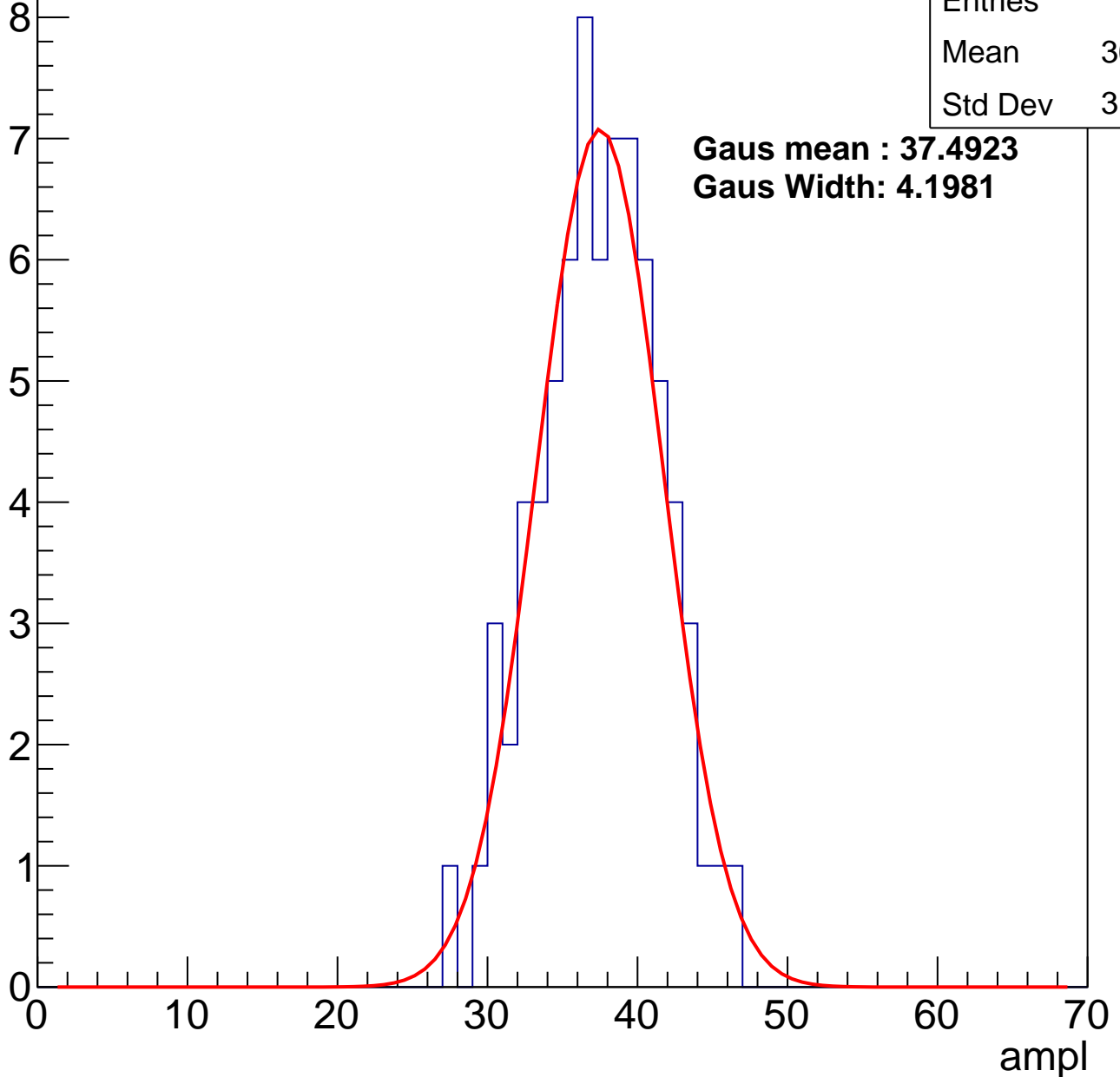
# B0L000S, U25-ch20, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	36.99
Std Dev	3.992

**Gaus mean : 37.4923**  
**Gaus Width: 4.1981**



# B0L000S, U25-ch20, adc2

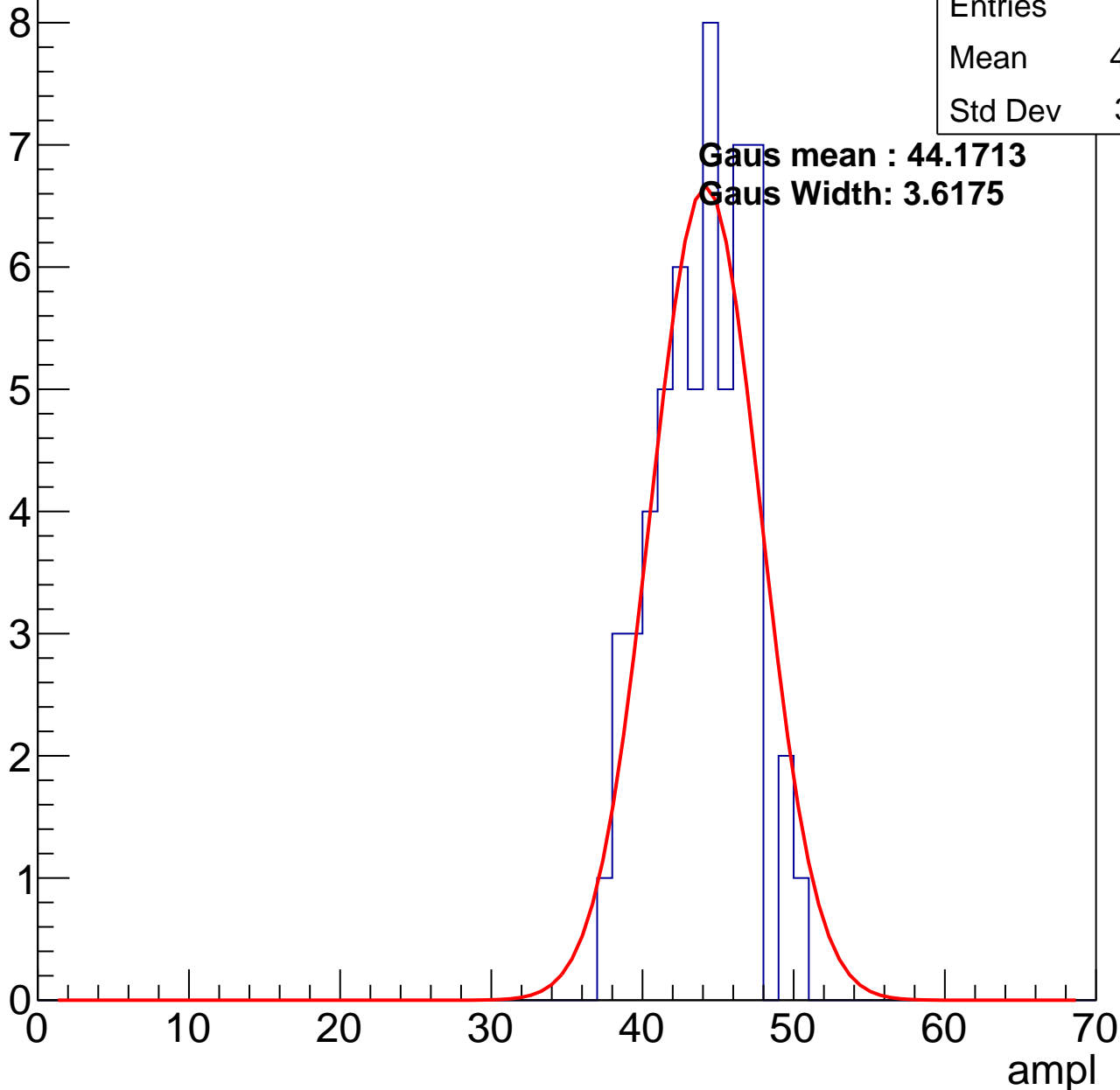
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.44
Std Dev	3.061

**Gaus mean : 44.1713**

**Gaus Width: 3.6175**



# B0L000S, U25-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

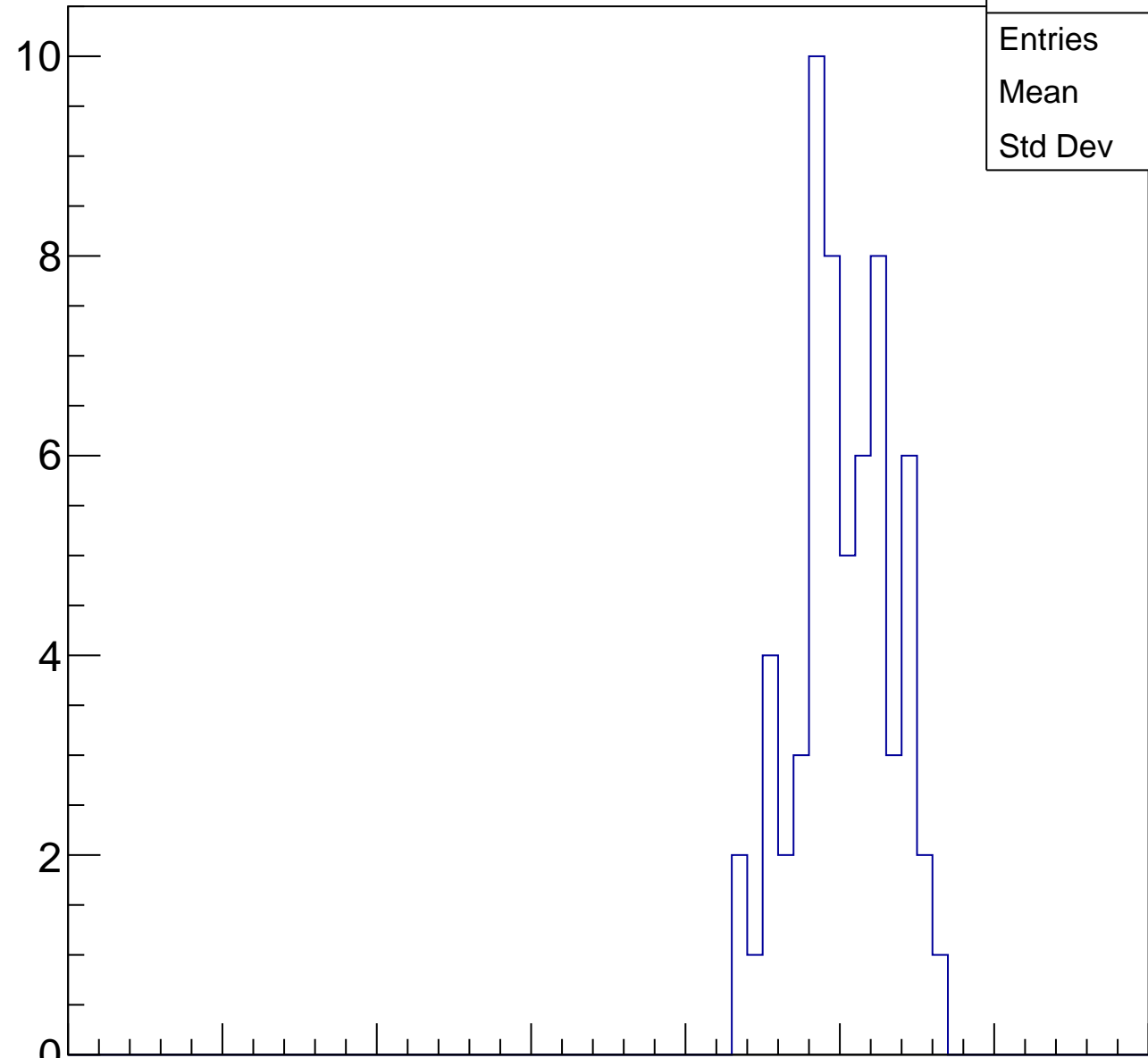
Entries	61
Mean	49.77
Std Dev	3.096

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

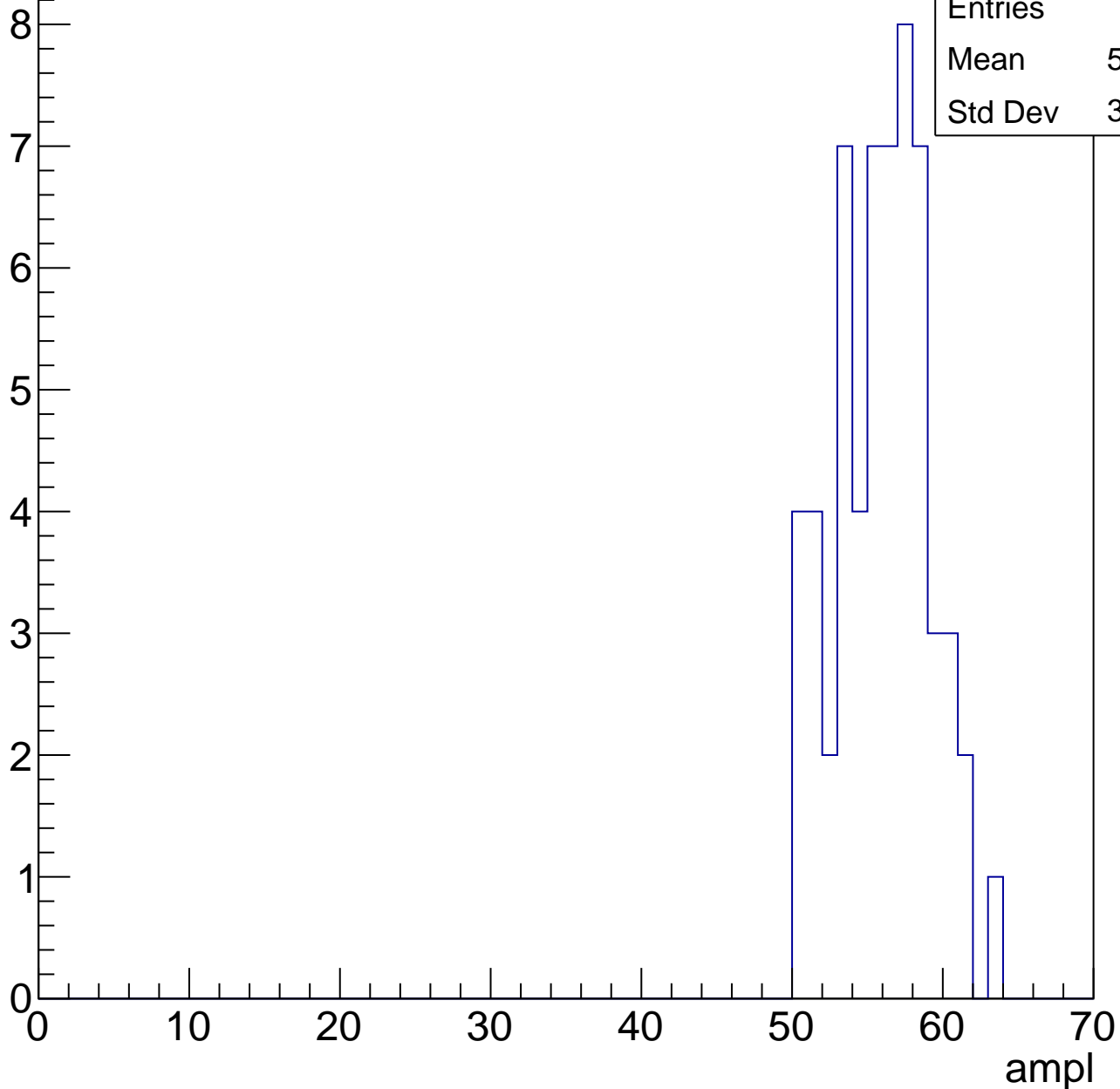


# B0L000S, U25-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.53
Std Dev	3.077

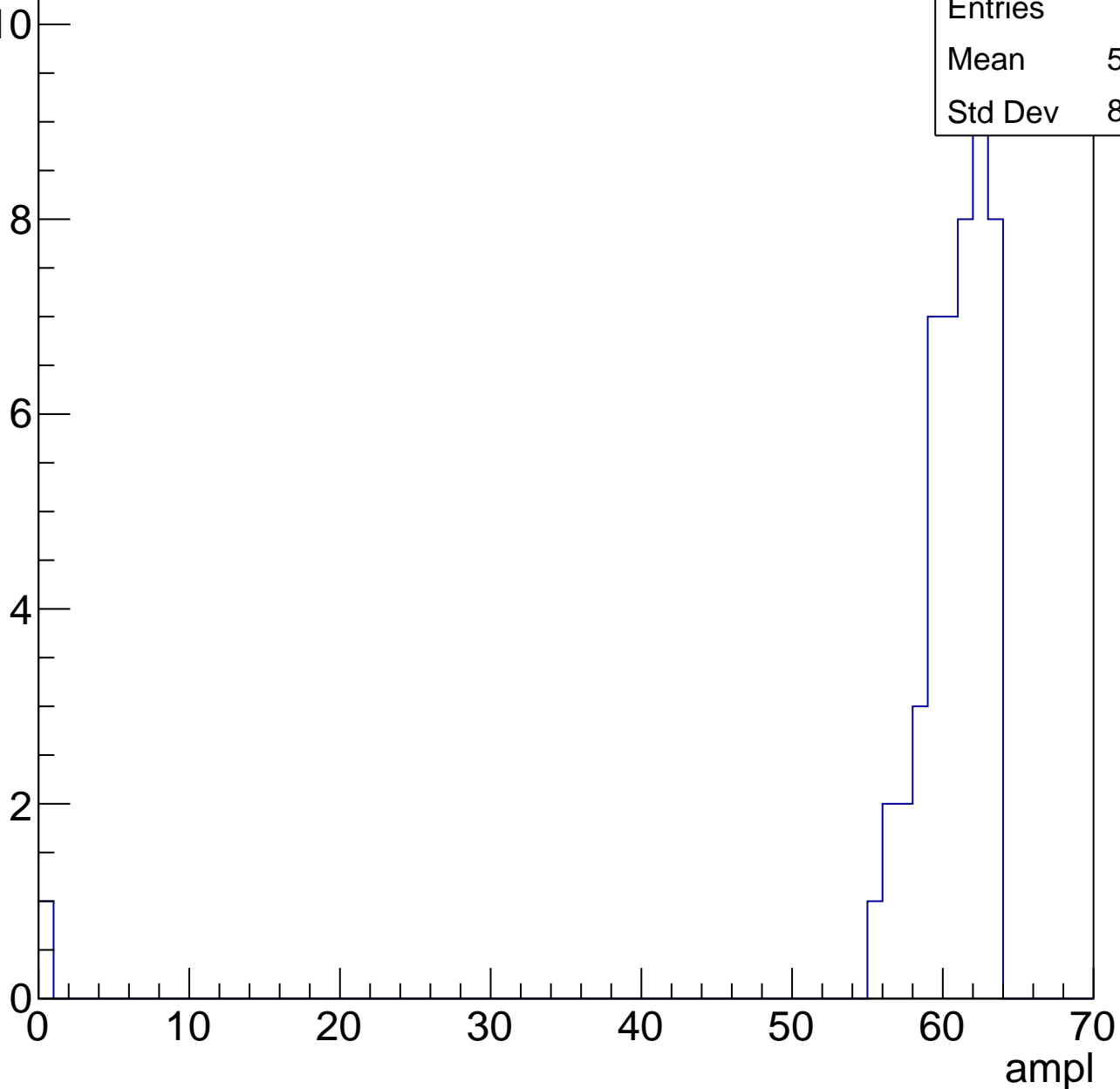


# B0L000S, U25-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	59.18
Std Dev	8.785



# B0L000S, U25-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U25-ch21, adc0

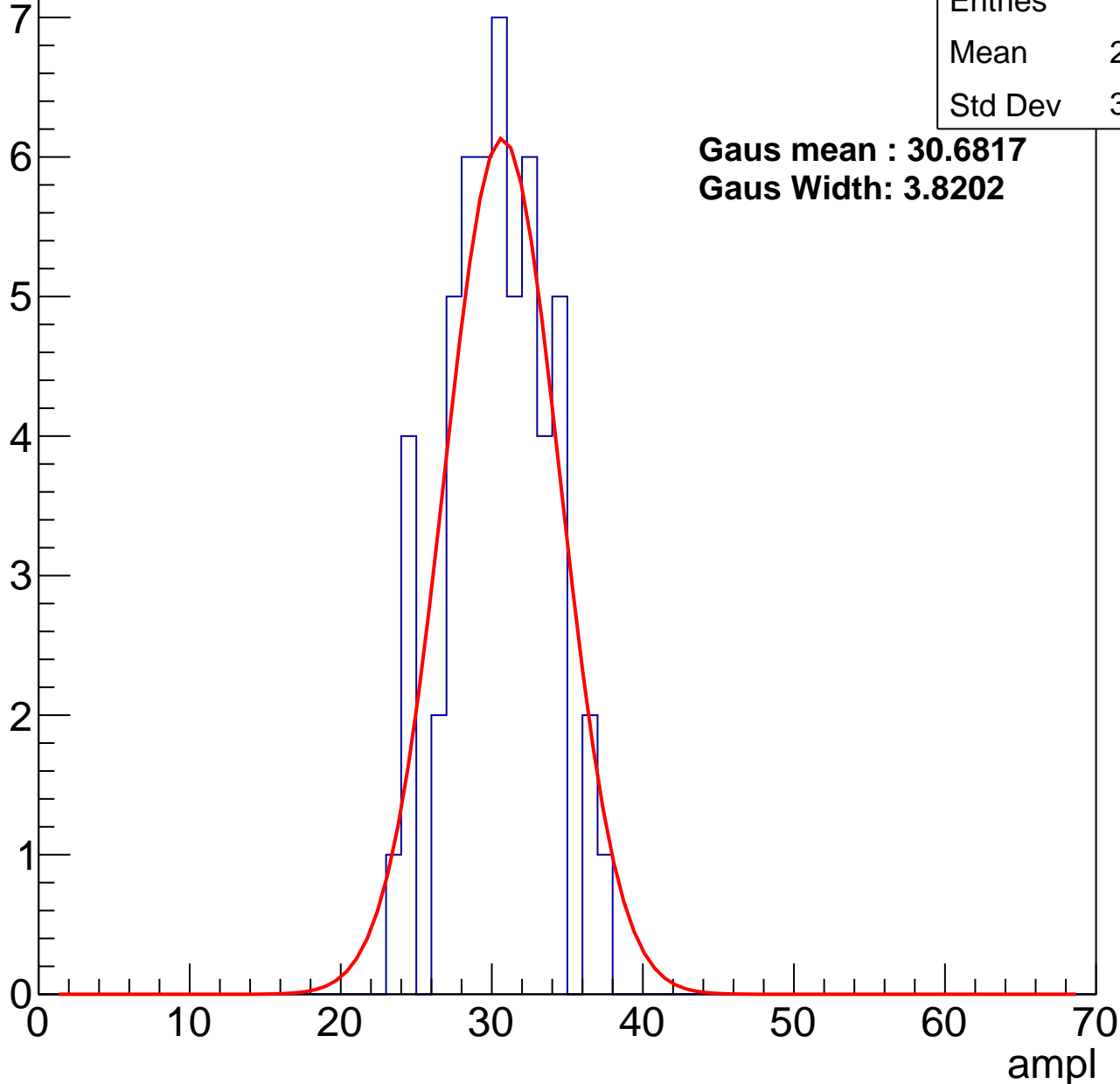
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	29.93
Std Dev	3.237

**Gaus mean : 30.6817**

**Gaus Width: 3.8202**



# B0L000S, U25-ch21, adc1

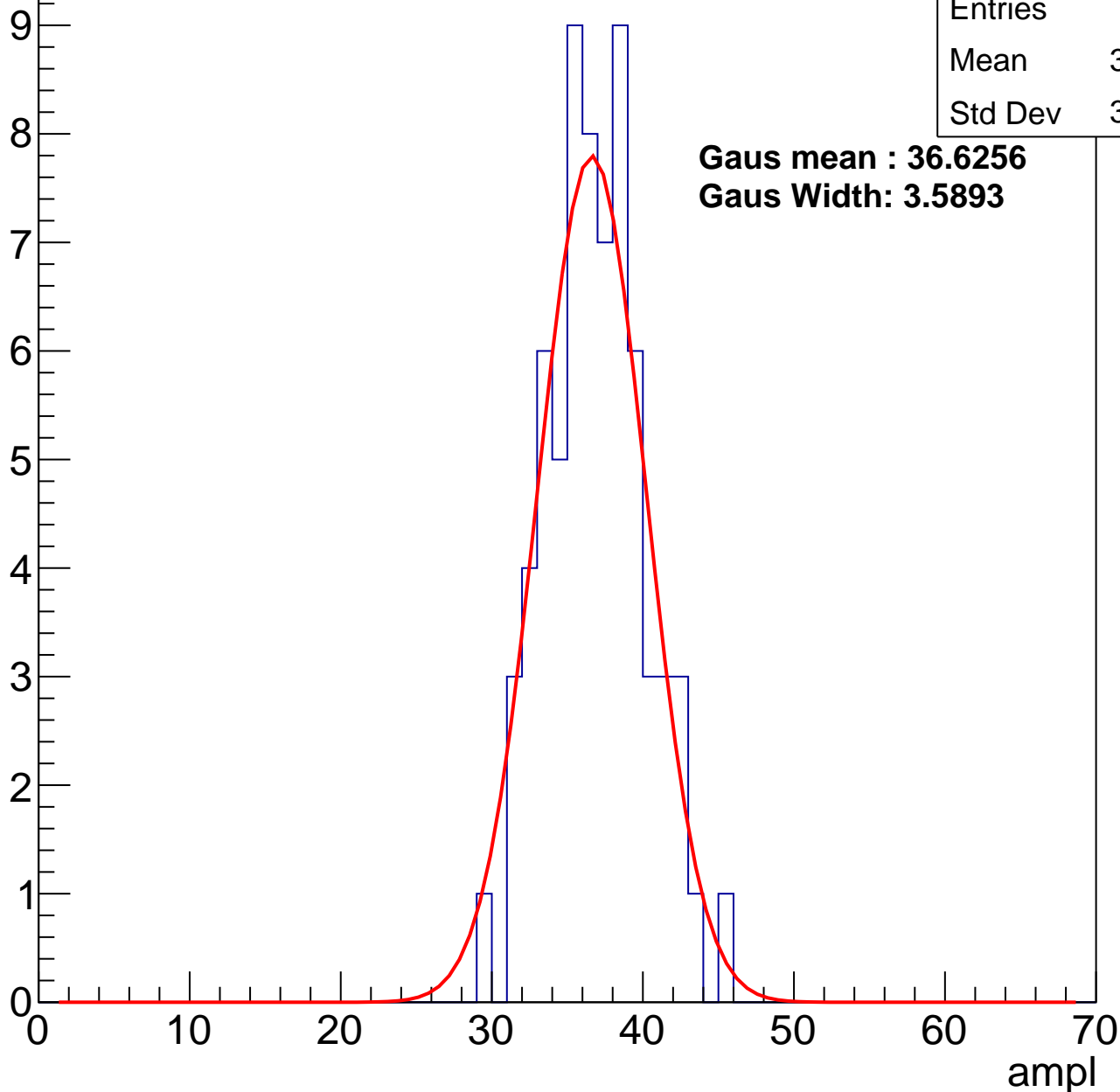
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.42
Std Dev	3.223

**Gaus mean : 36.6256**

**Gaus Width: 3.5893**



# B0L000S, U25-ch21, adc2

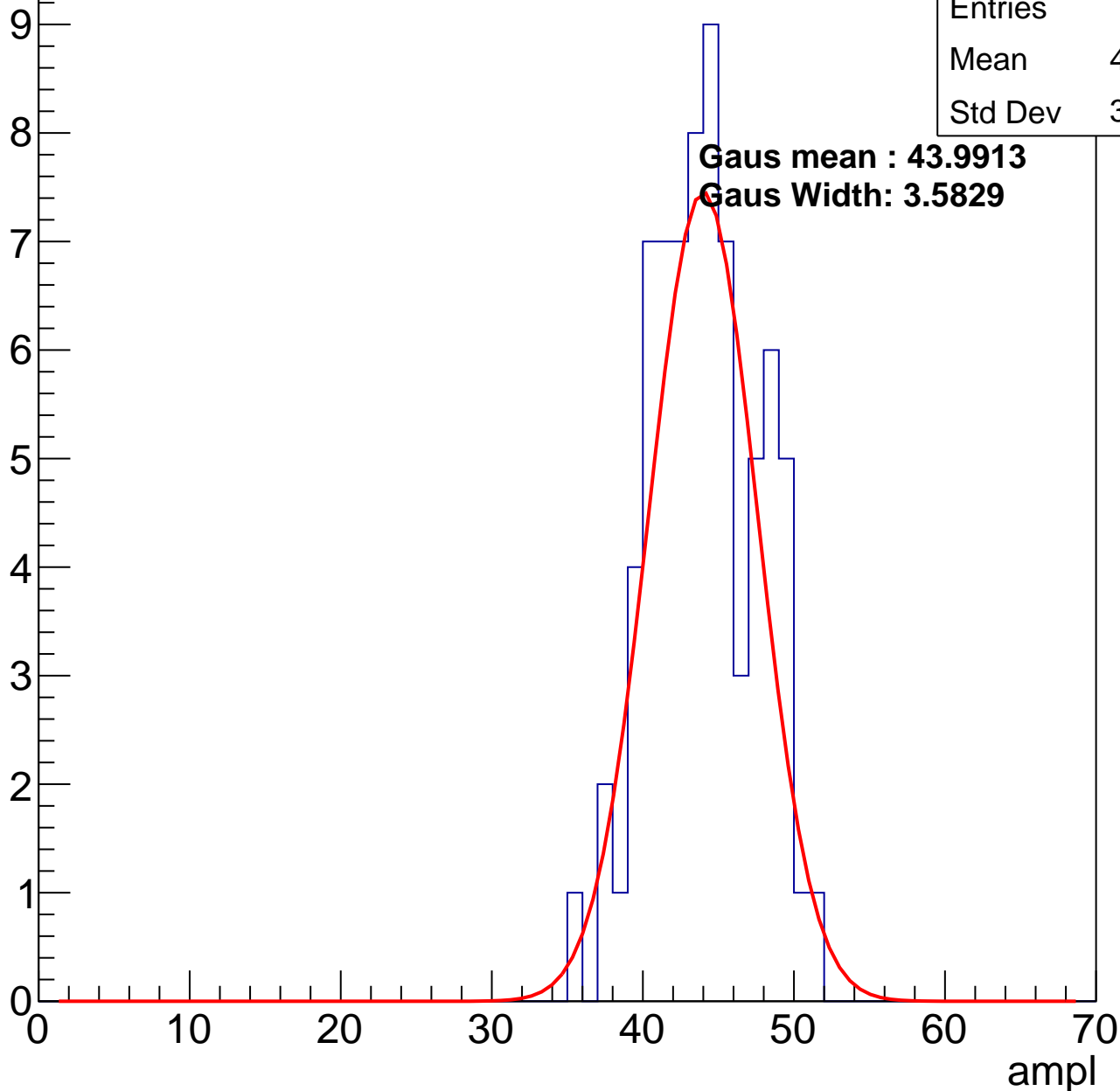
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.59
Std Dev	3.456

**Gaus mean : 43.9913**

**Gaus Width: 3.5829**

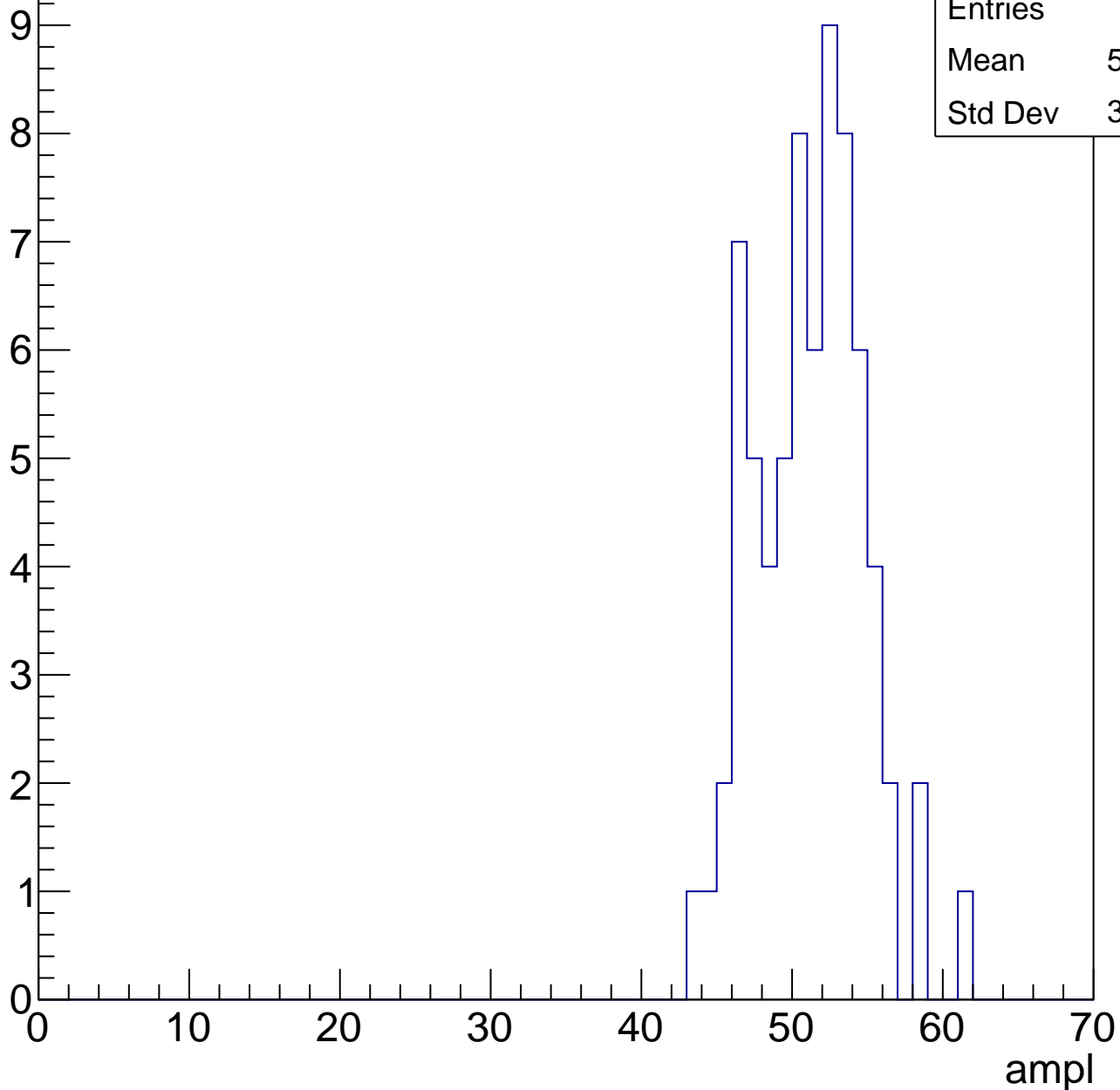


# B0L000S, U25-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	50.73
Std Dev	3.572

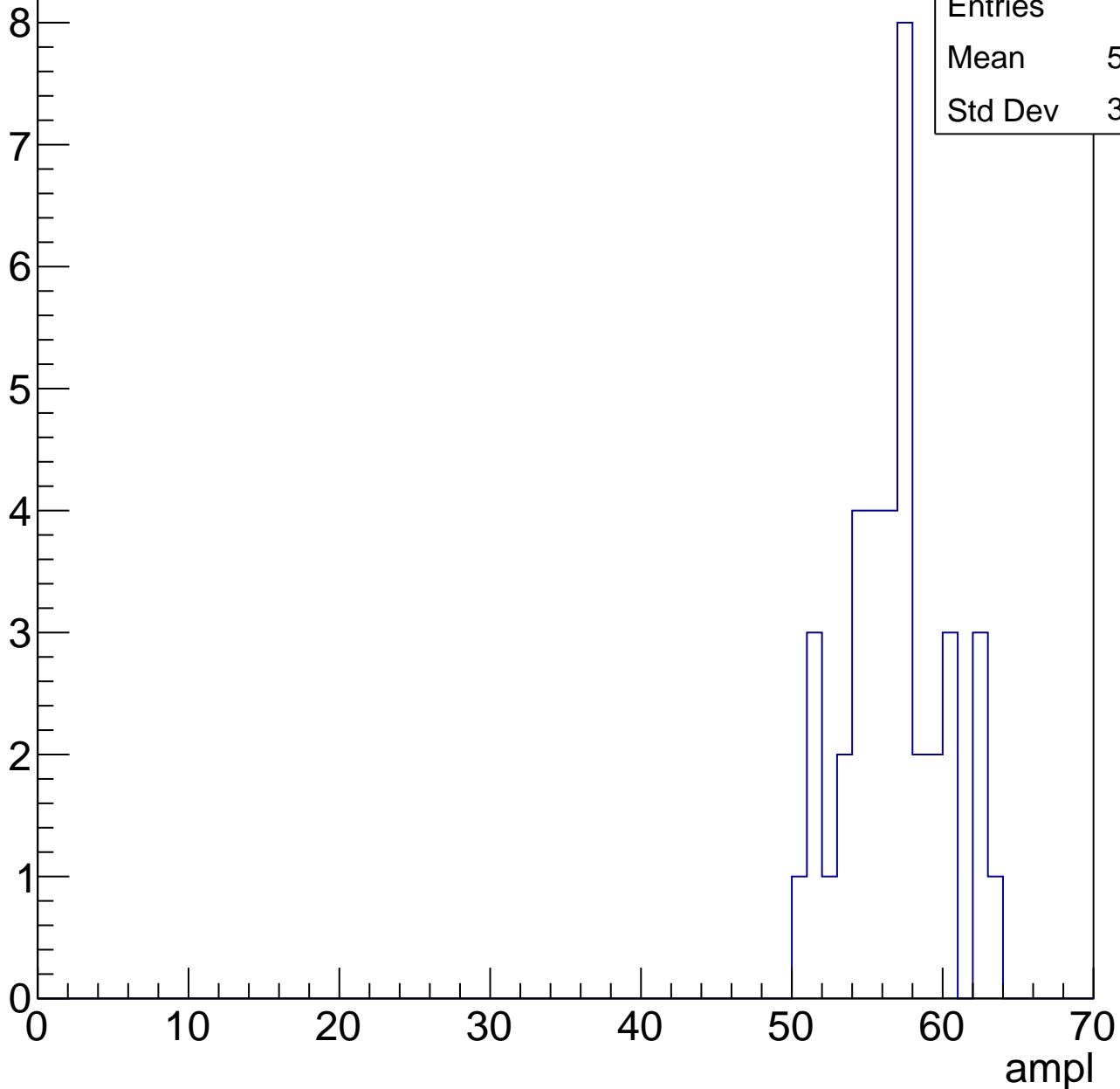


# B0L000S, U25-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

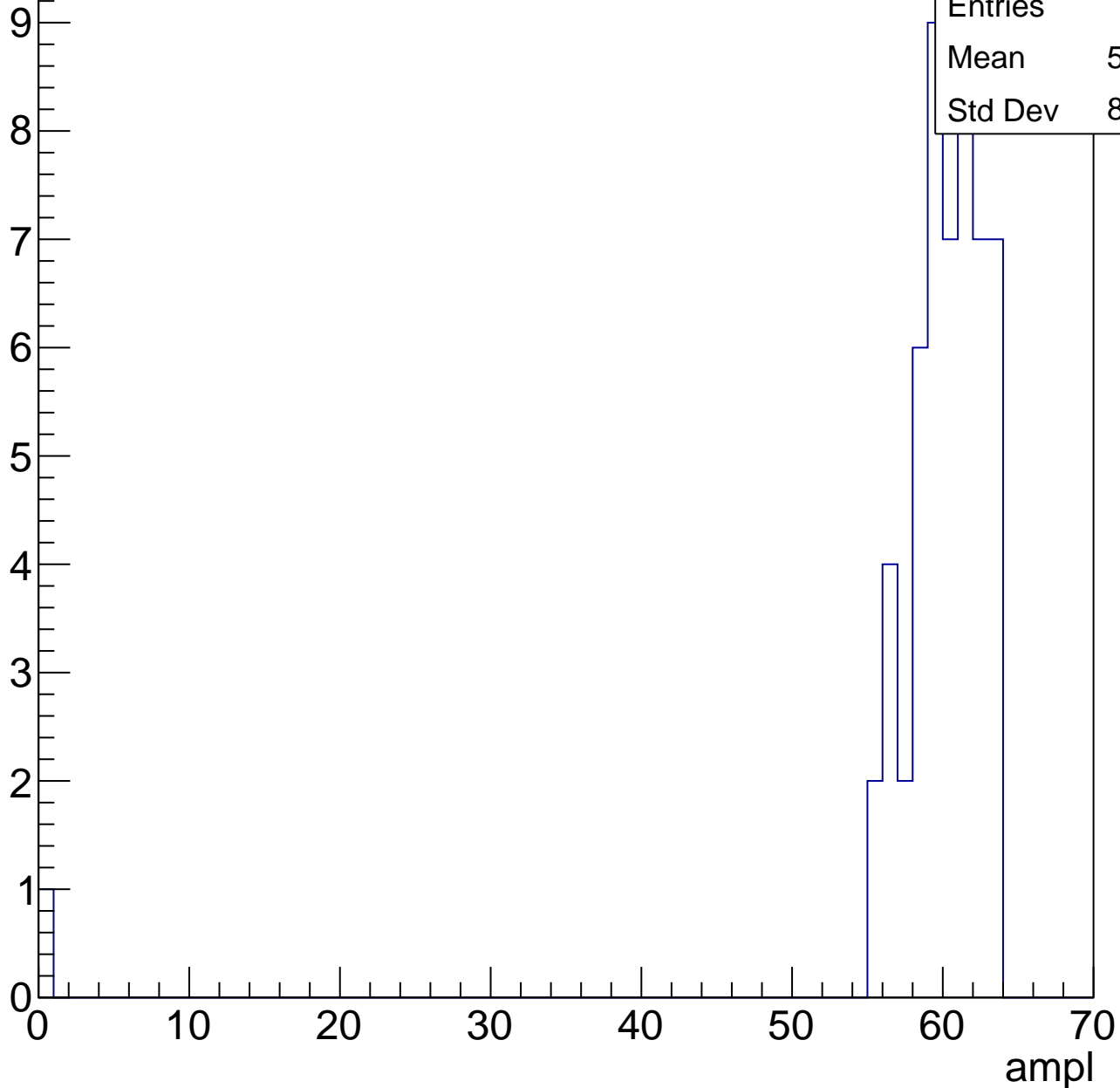
Entries	38
Mean	56.32
Std Dev	3.245



# B0L000S, U25-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U25-ch22, adc0

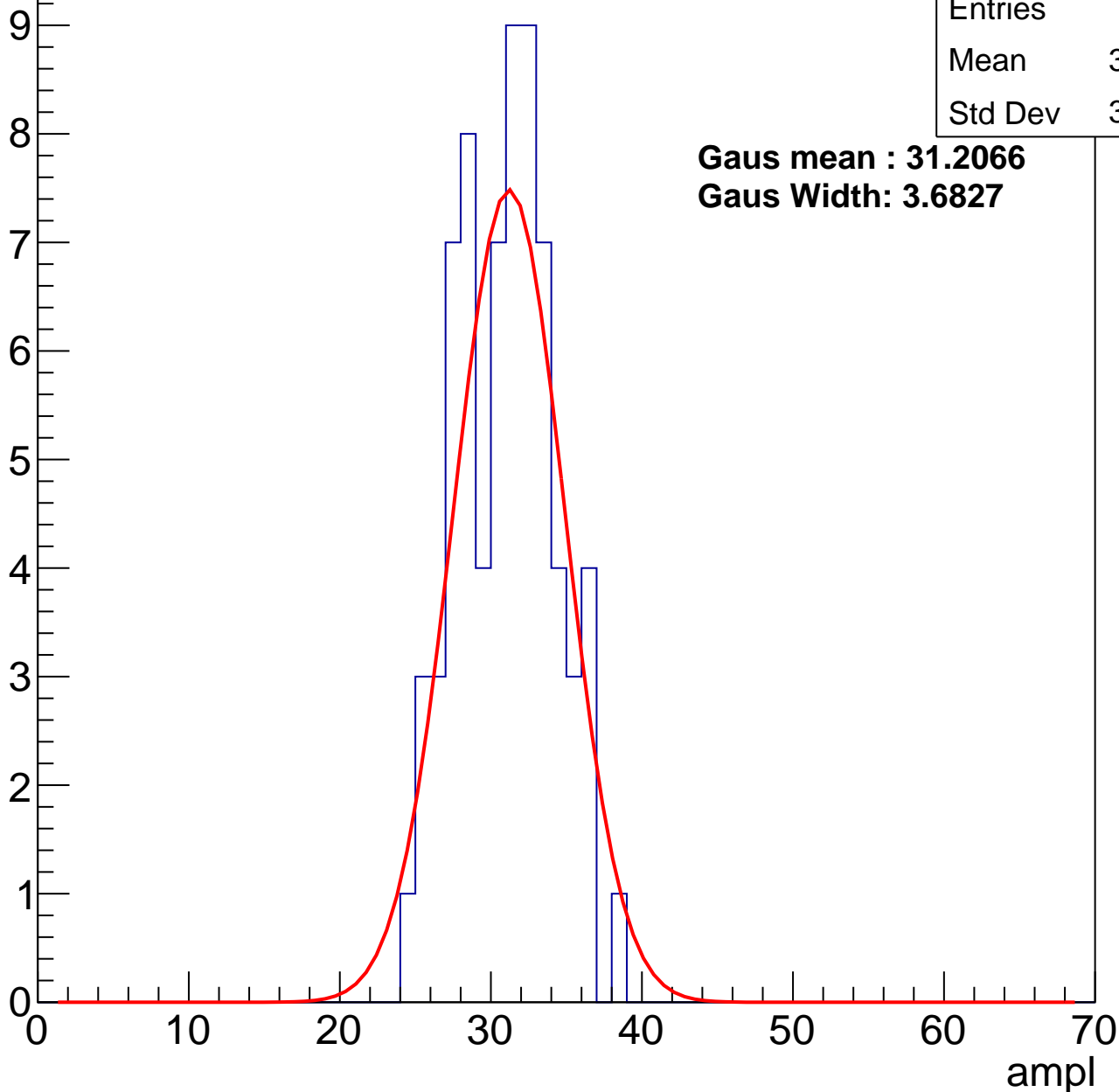
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.53
Std Dev	3.147

**Gaus mean : 31.2066**

**Gaus Width: 3.6827**



# B0L000S, U25-ch22, adc1

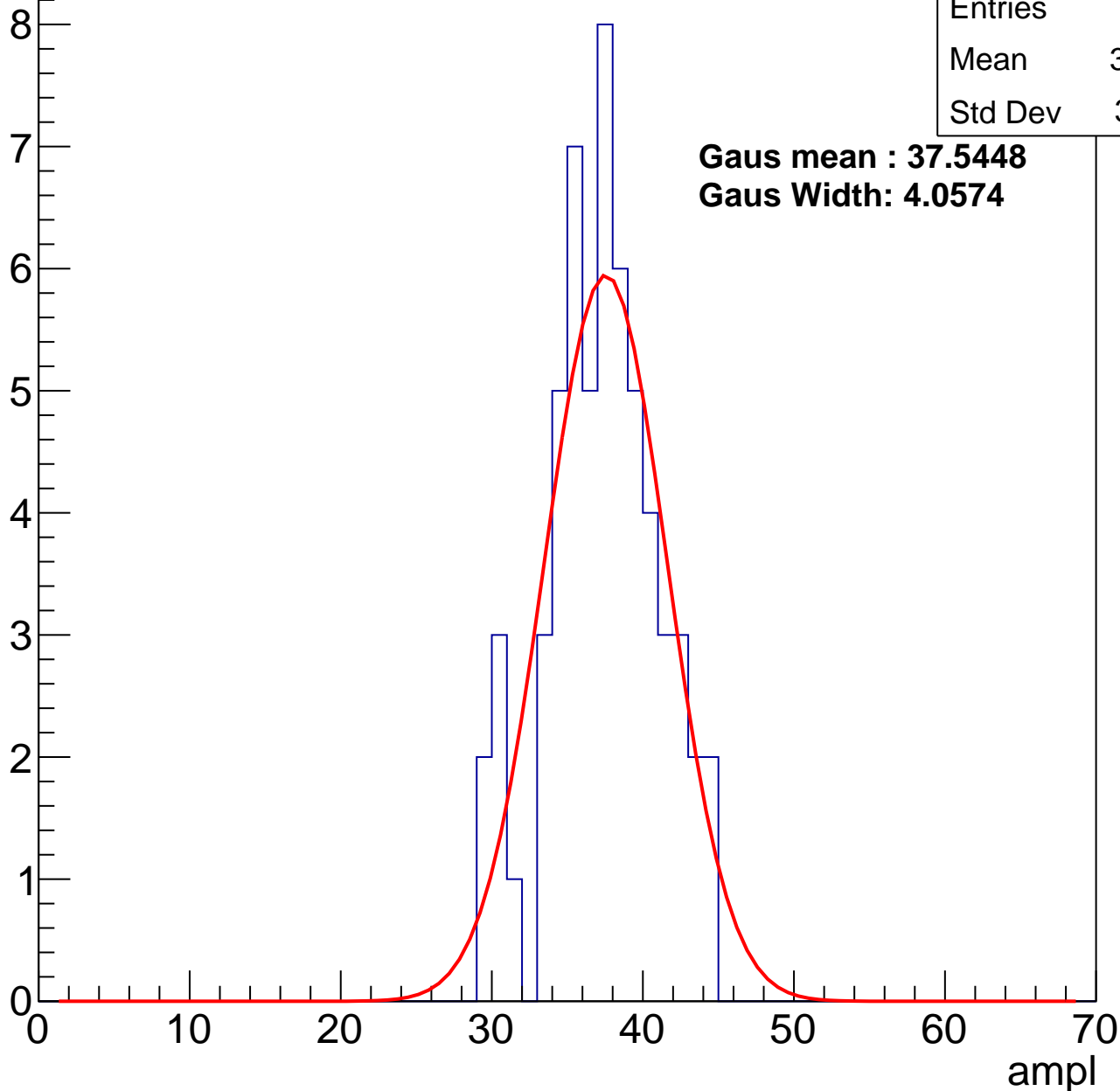
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	36.86
Std Dev	3.661

**Gaus mean : 37.5448**

**Gaus Width: 4.0574**



# B0L000S, U25-ch22, adc2

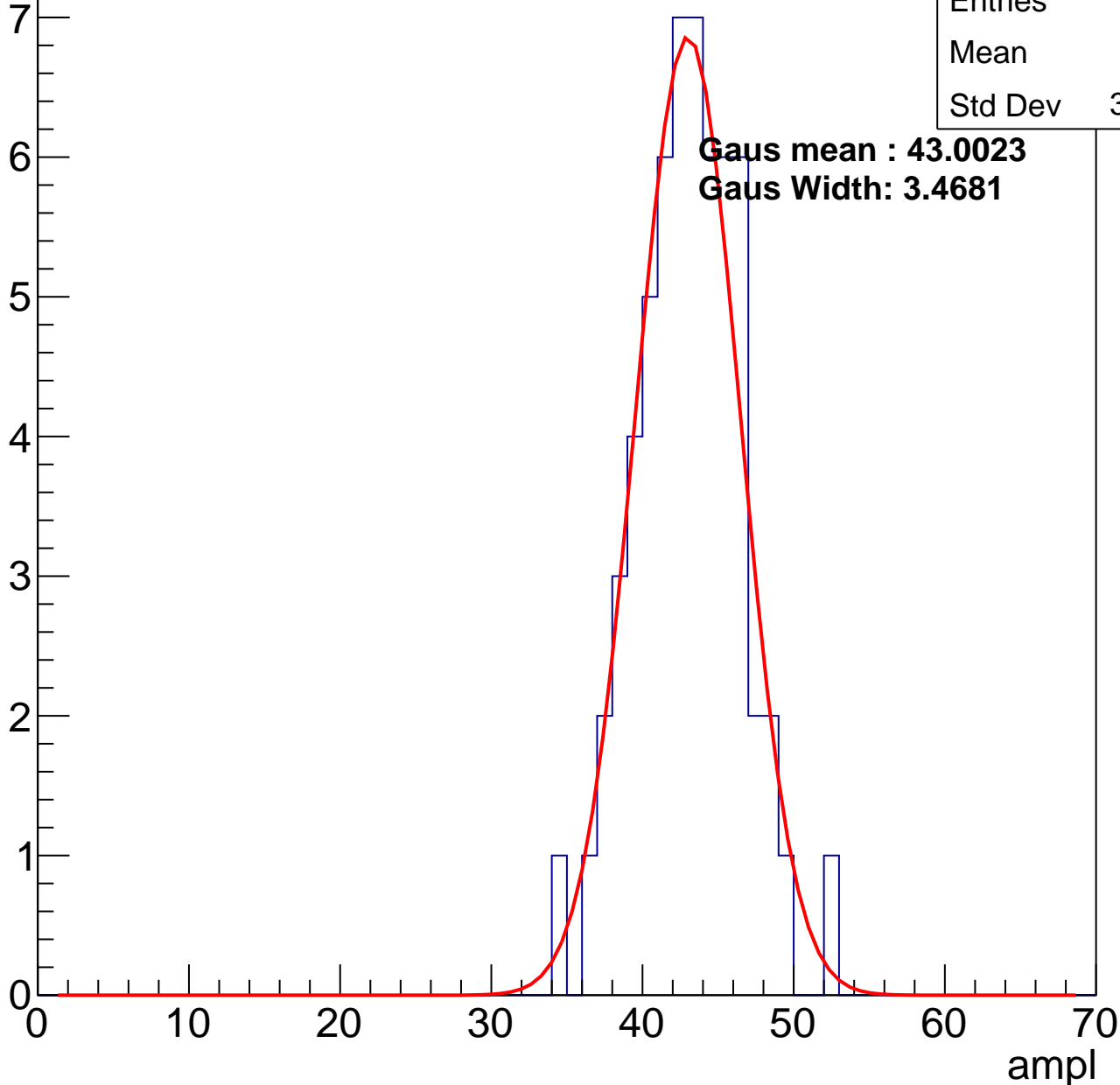
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.6
Std Dev	3.392

**Gaus mean : 43.0023**

**Gaus Width: 3.4681**

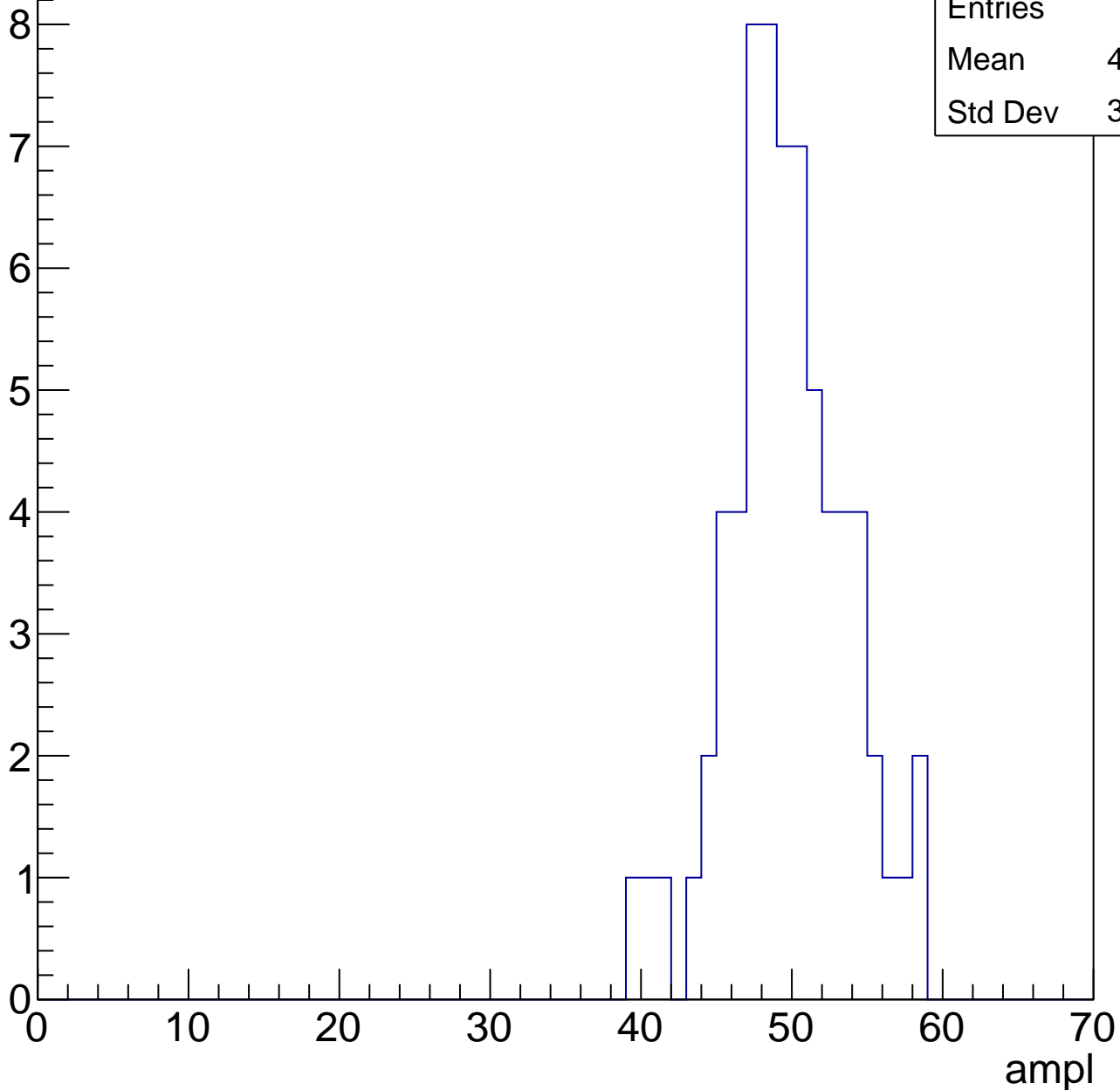


# B0L000S, U25-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.22
Std Dev	3.943

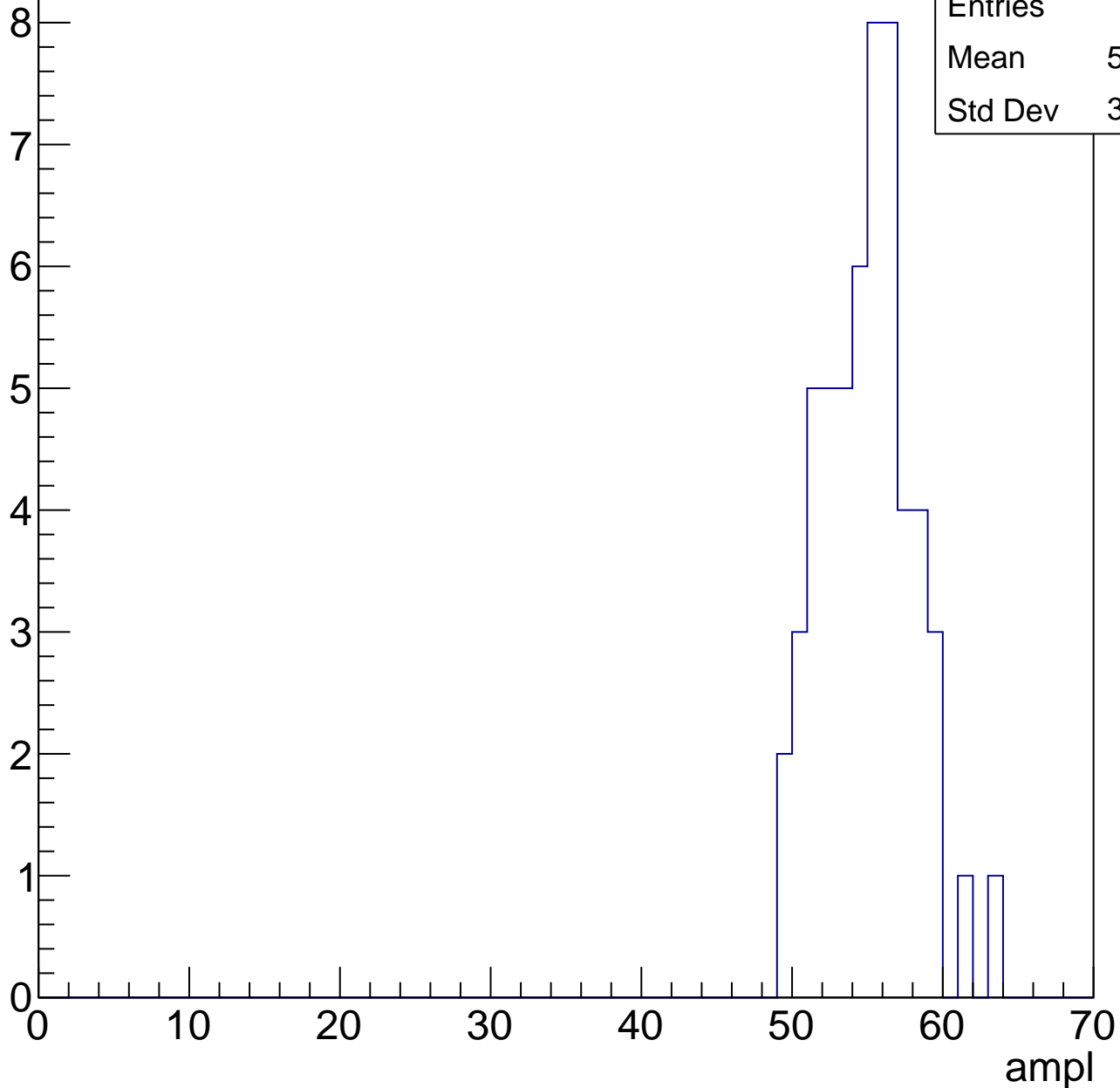


# B0L000S, U25-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.56
Std Dev	3.002

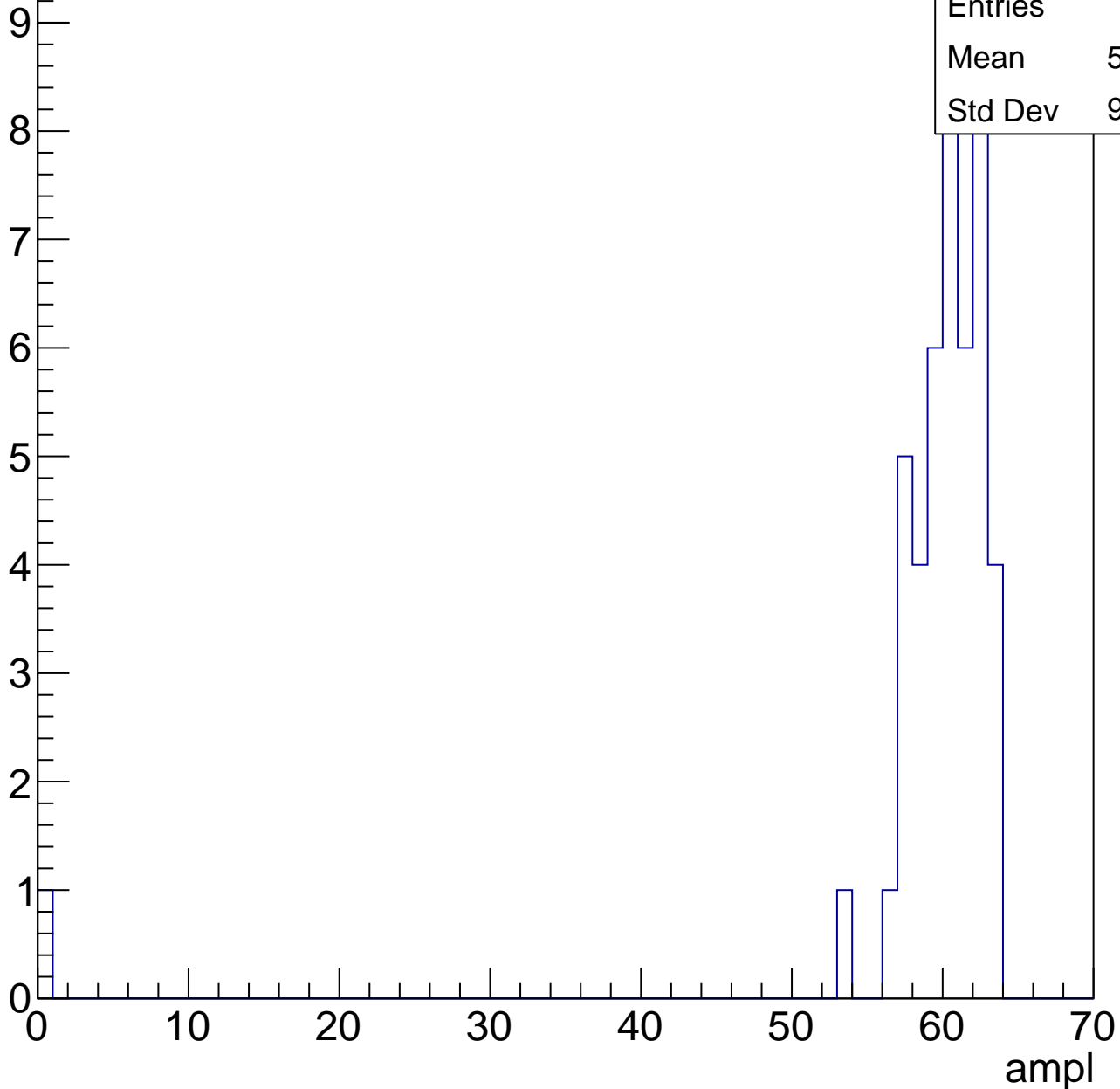


# B0L000S, U25-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.53
Std Dev	9.079

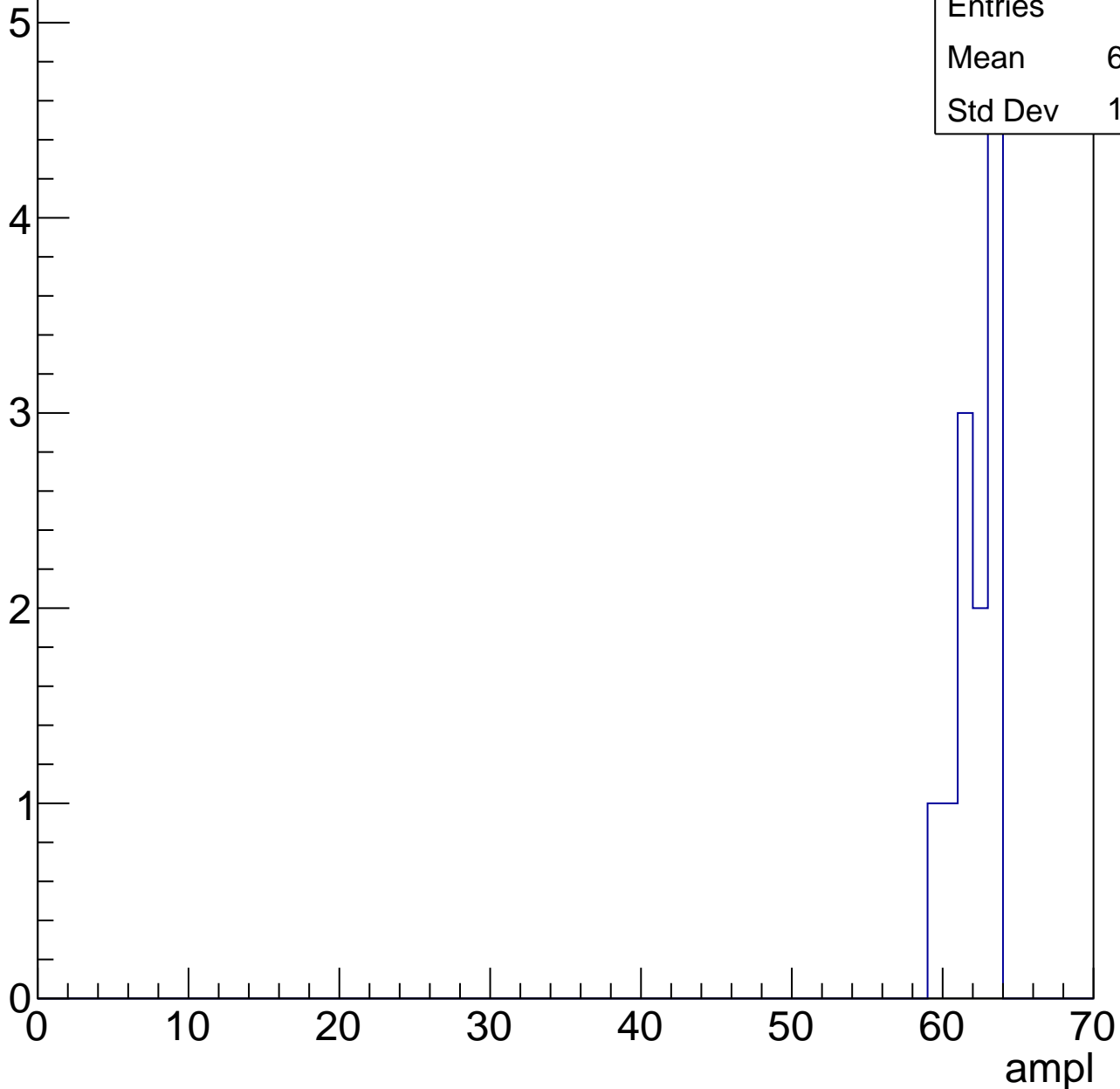


# B0L000S, U25-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	61.75
Std Dev	1.299





# B0L000S, U25-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch23, adc0

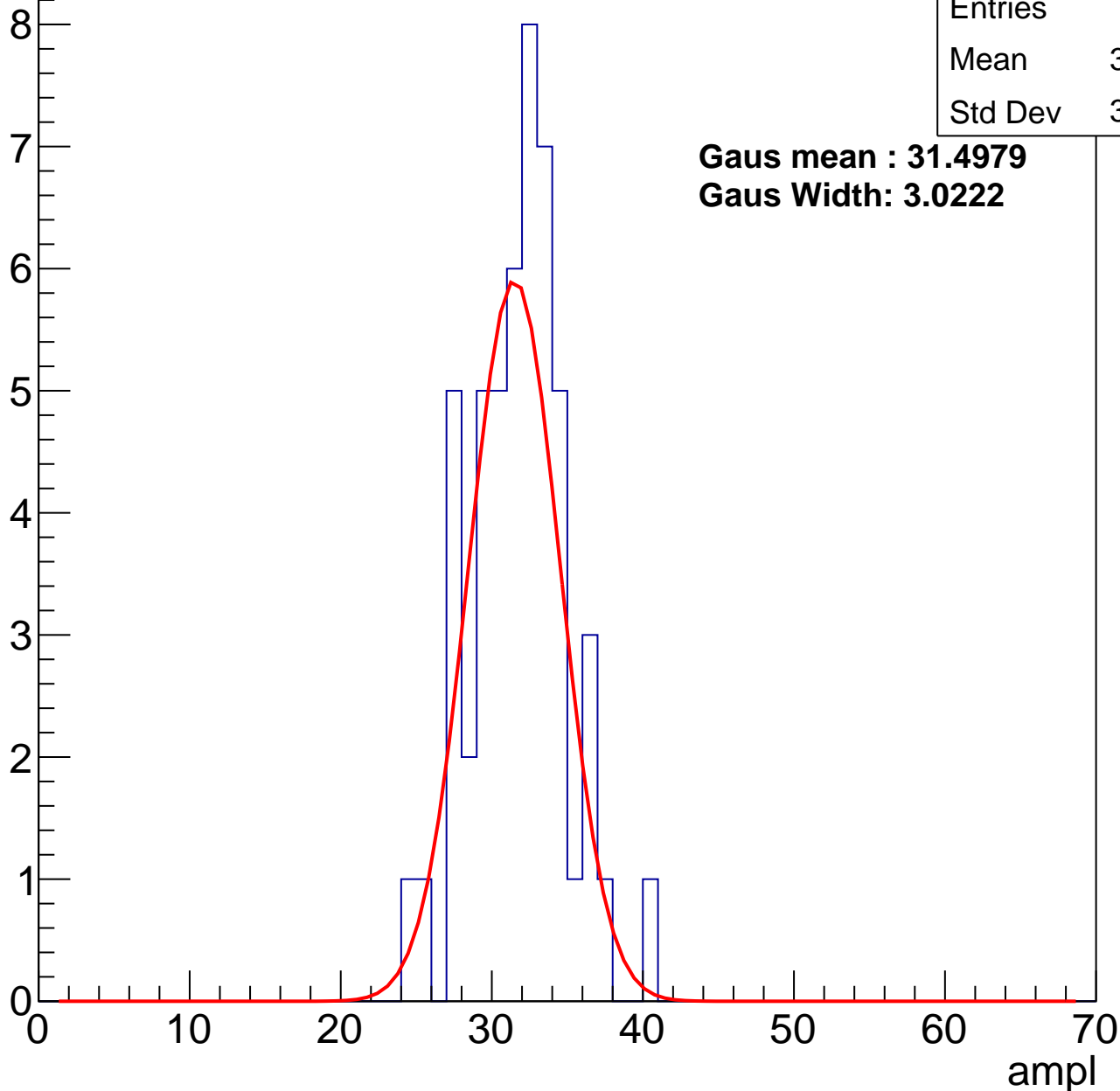
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	31.33
Std Dev	3.104

**Gaus mean : 31.4979**

**Gaus Width: 3.0222**



# B0L000S, U25-ch23, adc1

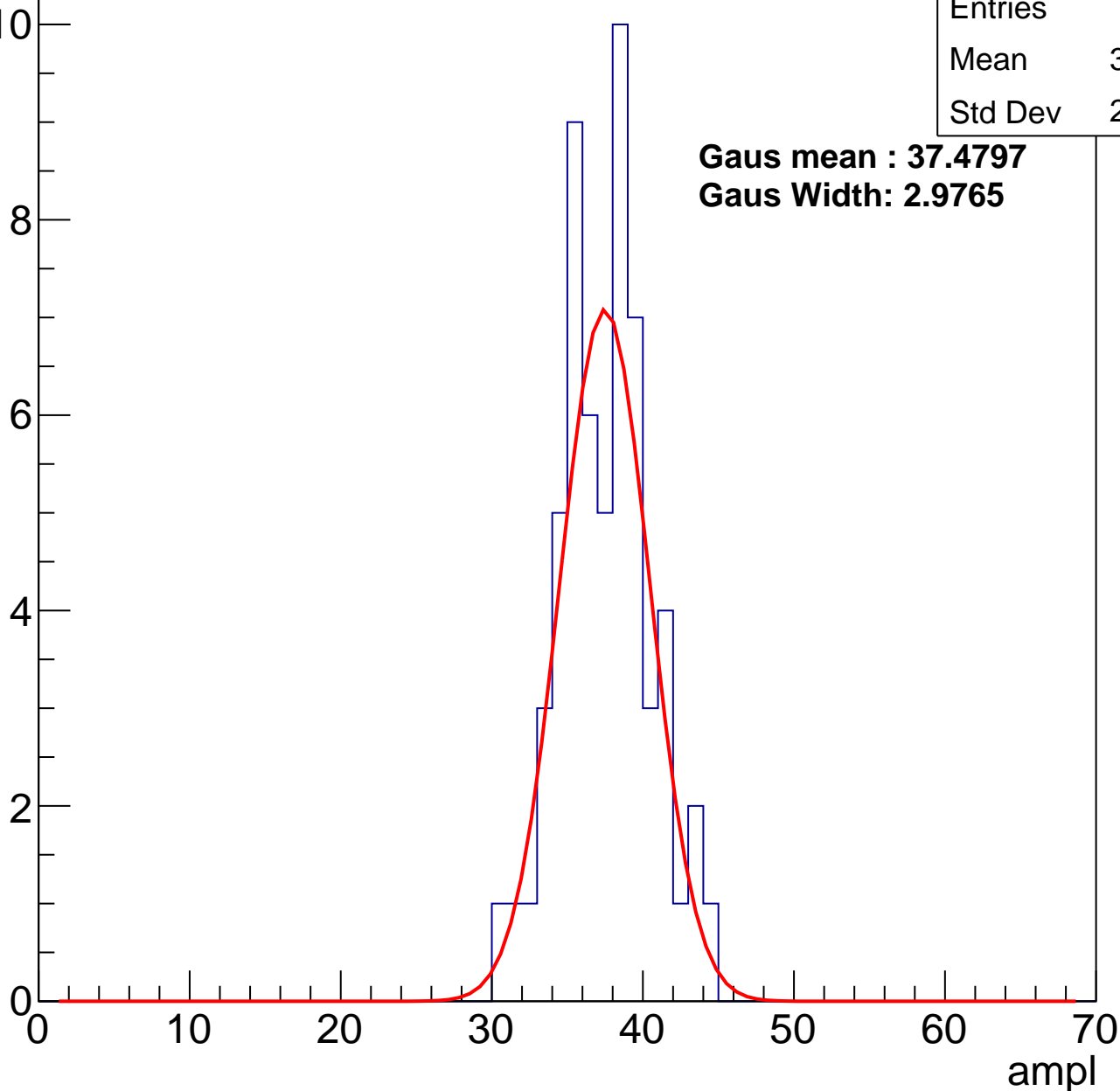
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	37.07
Std Dev	2.968

**Gaus mean : 37.4797**

**Gaus Width: 2.9765**



# B0L000S, U25-ch23, adc2

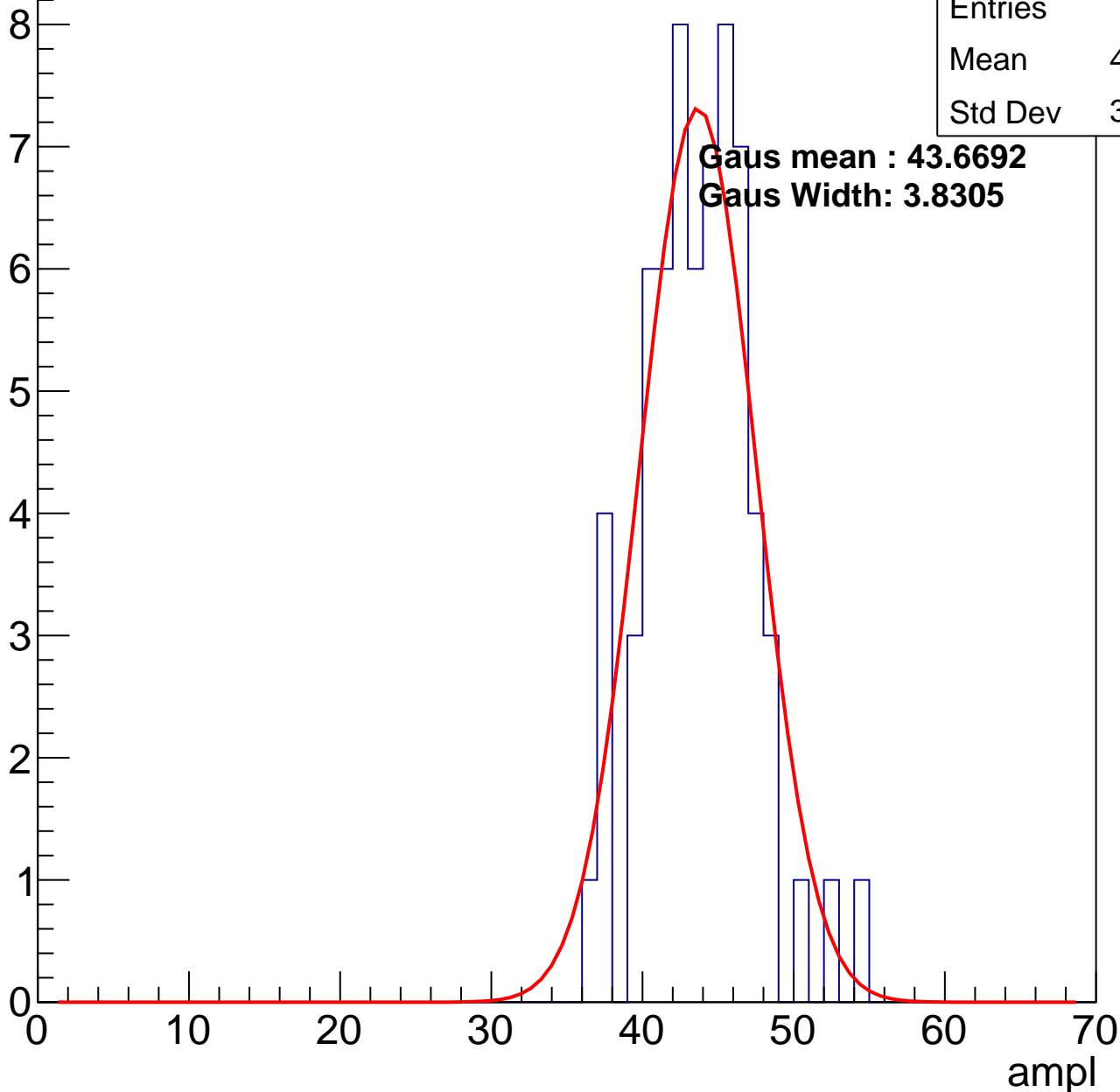
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.32
Std Dev	3.508

**Gaus mean : 43.6692**

**Gaus Width: 3.8305**

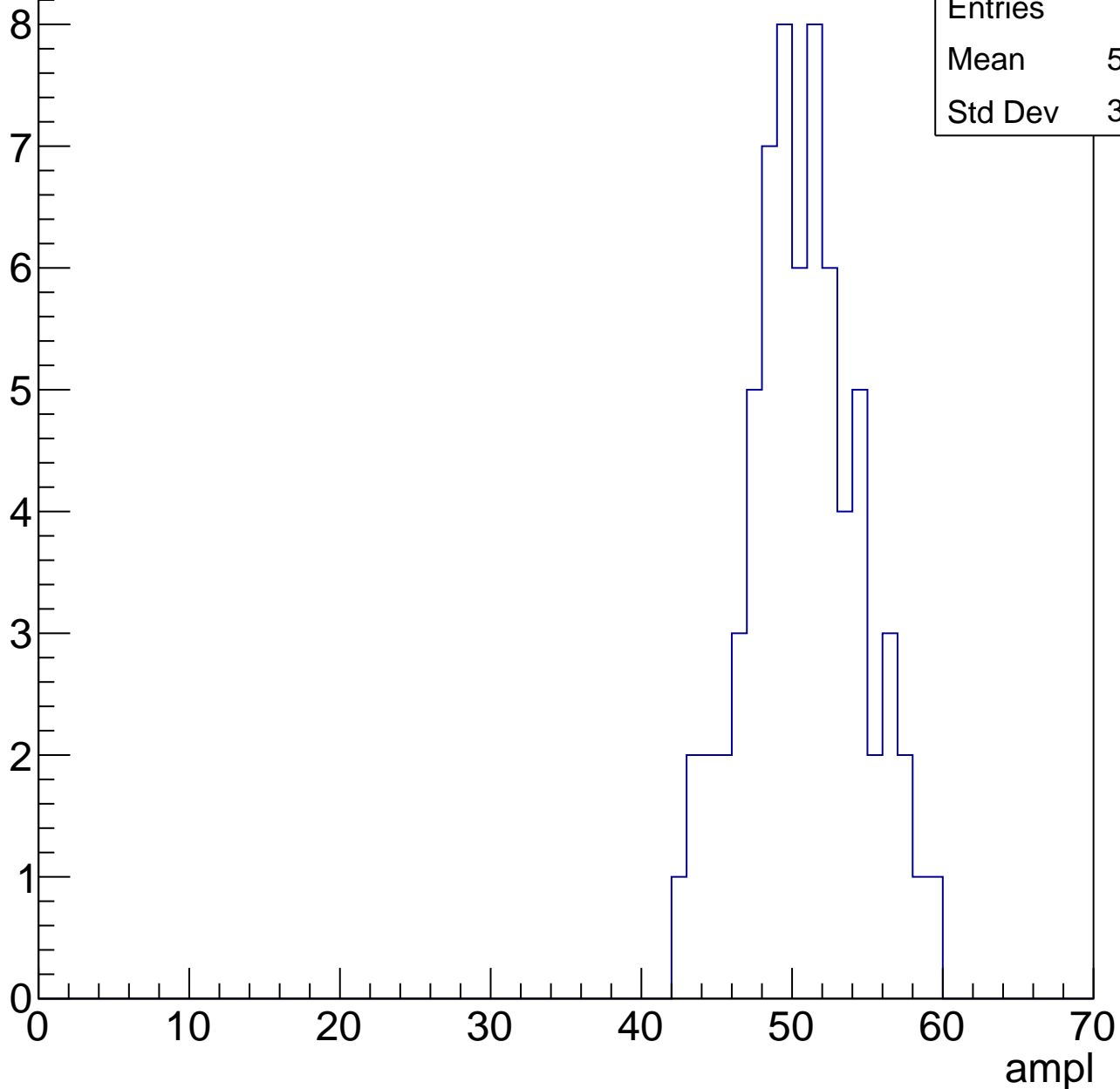


# B0L000S, U25-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.26
Std Dev	3.752

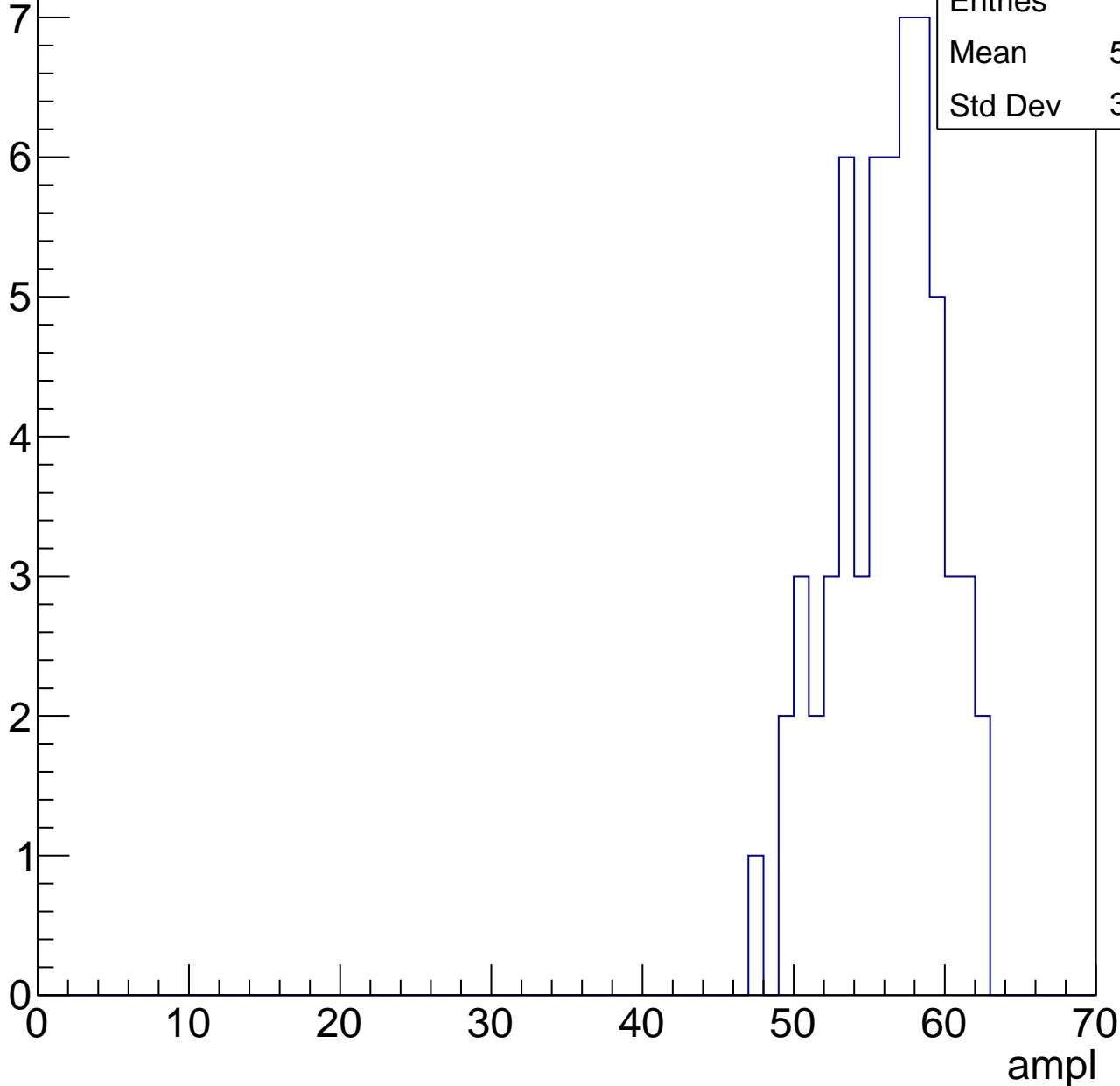


# B0L000S, U25-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	55.69
Std Dev	3.509

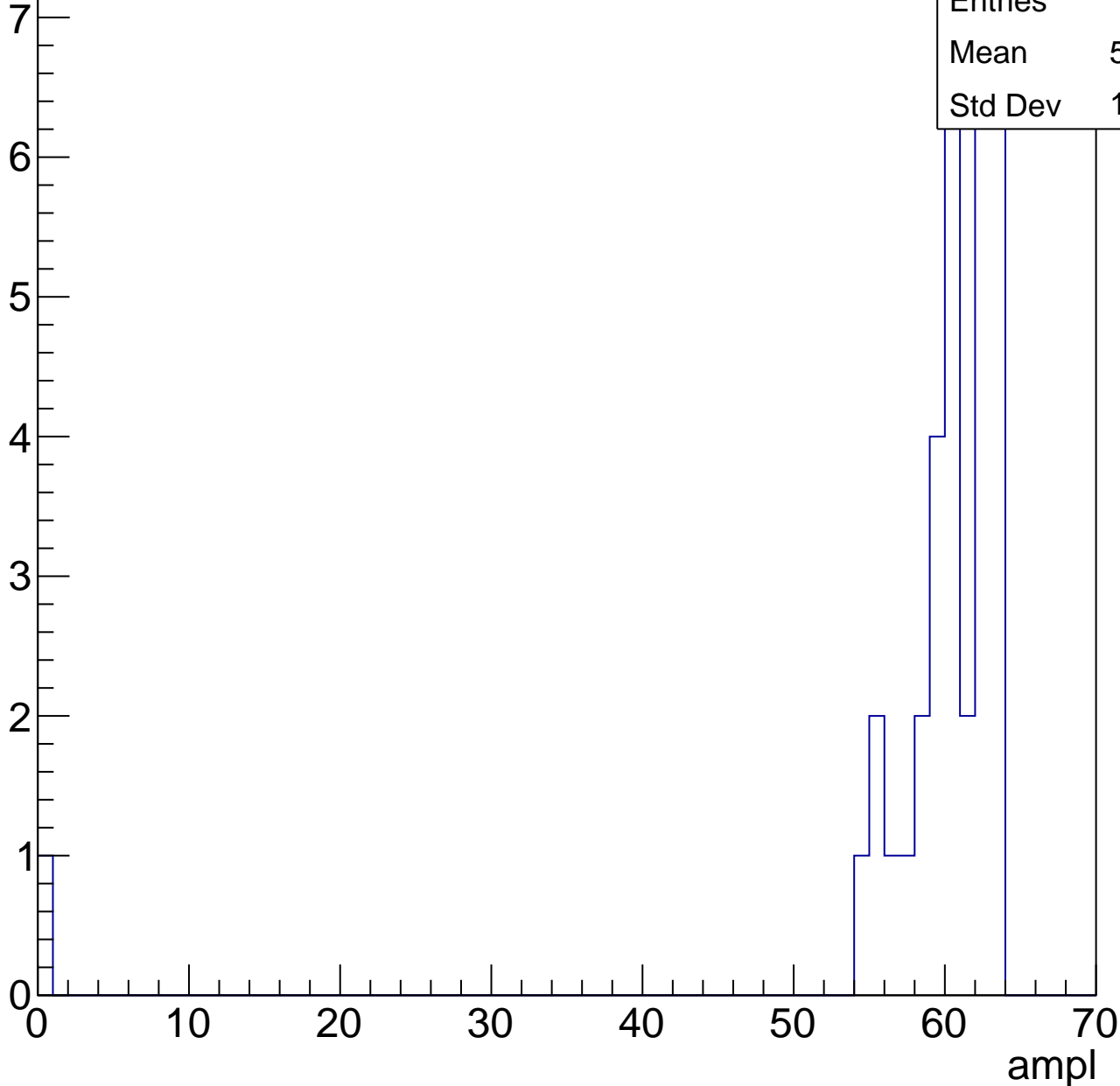


# B0L000S, U25-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	58.46
Std Dev	10.33

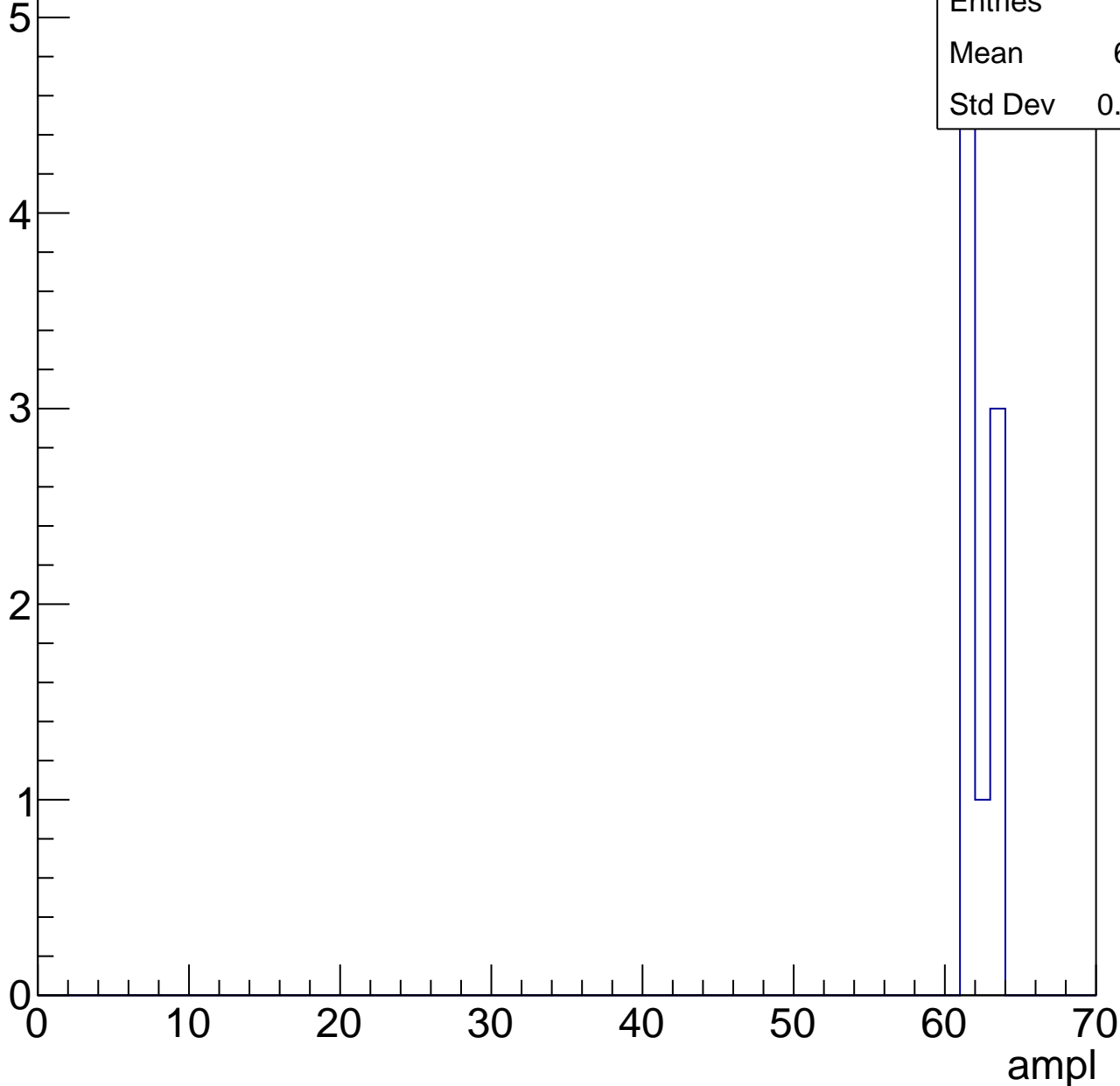


# B0L000S, U25-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	61.78
Std Dev	0.9162





# B0L000S, U25-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch24, adc0

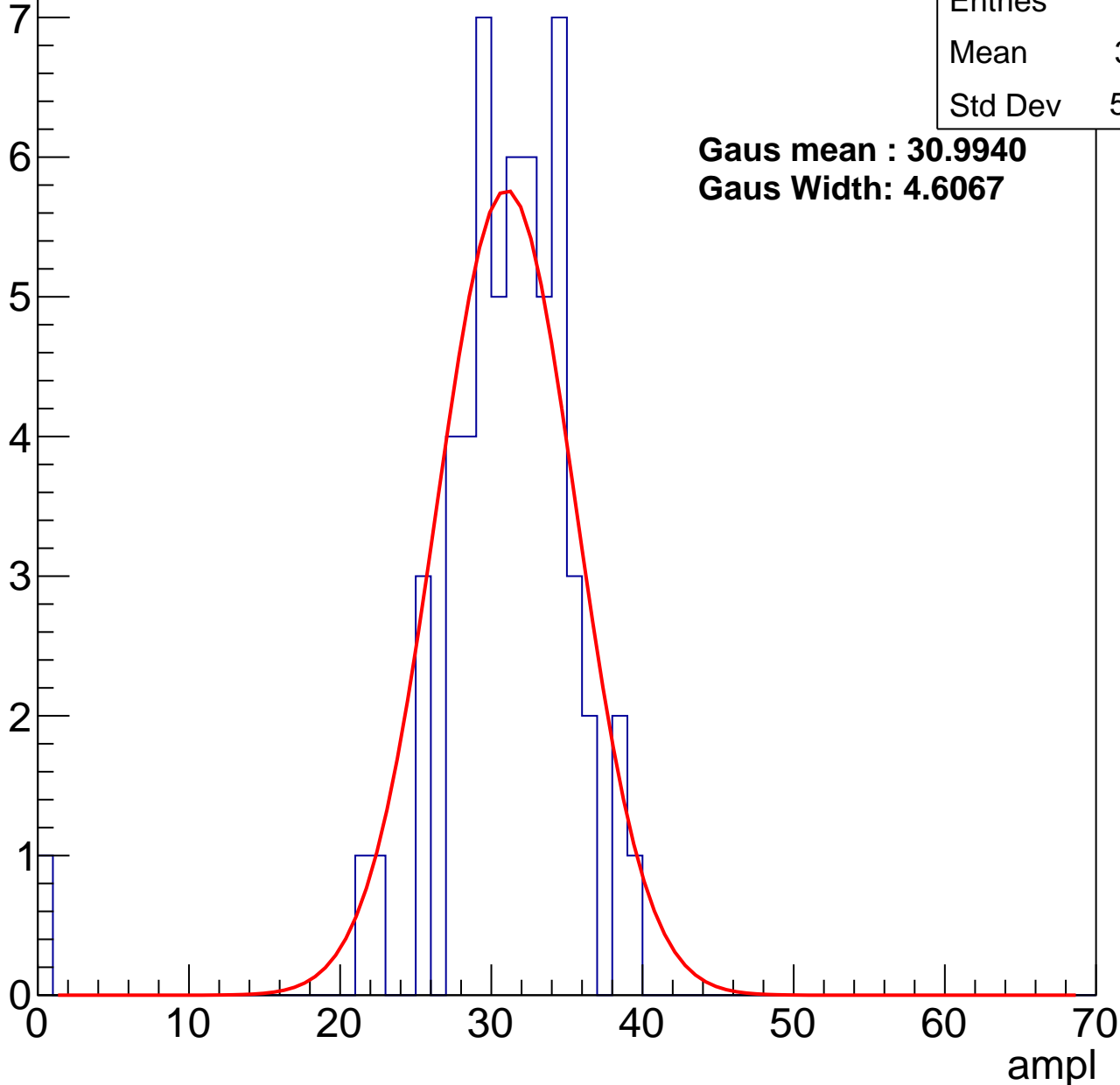
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	30.41
Std Dev	5.433

**Gaus mean : 30.9940**

**Gaus Width: 4.6067**



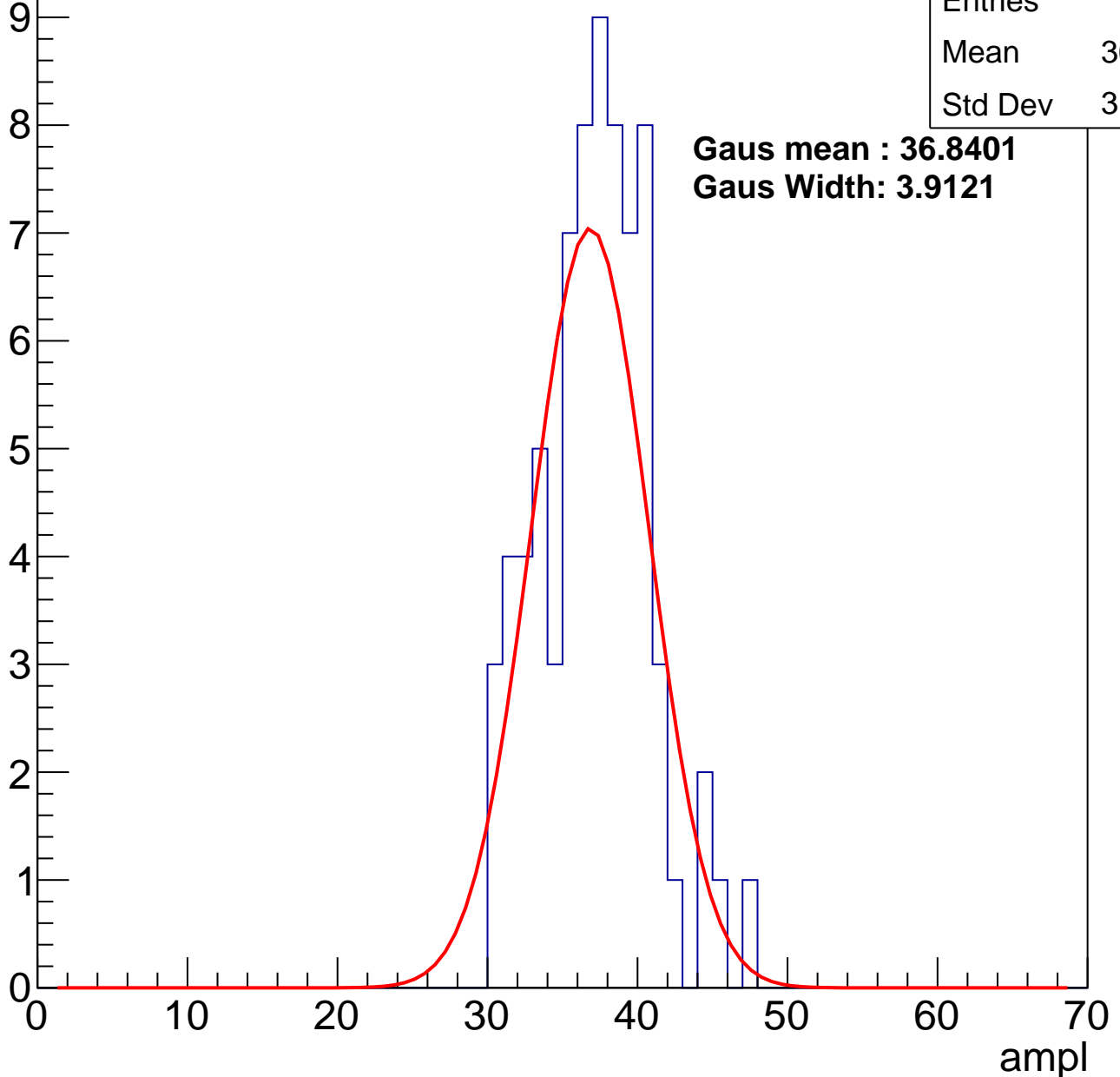
# B0L000S, U25-ch24, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	36.72
Std Dev	3.622

**Gaus mean : 36.8401**  
**Gaus Width: 3.9121**



# B0L000S, U25-ch24, adc2

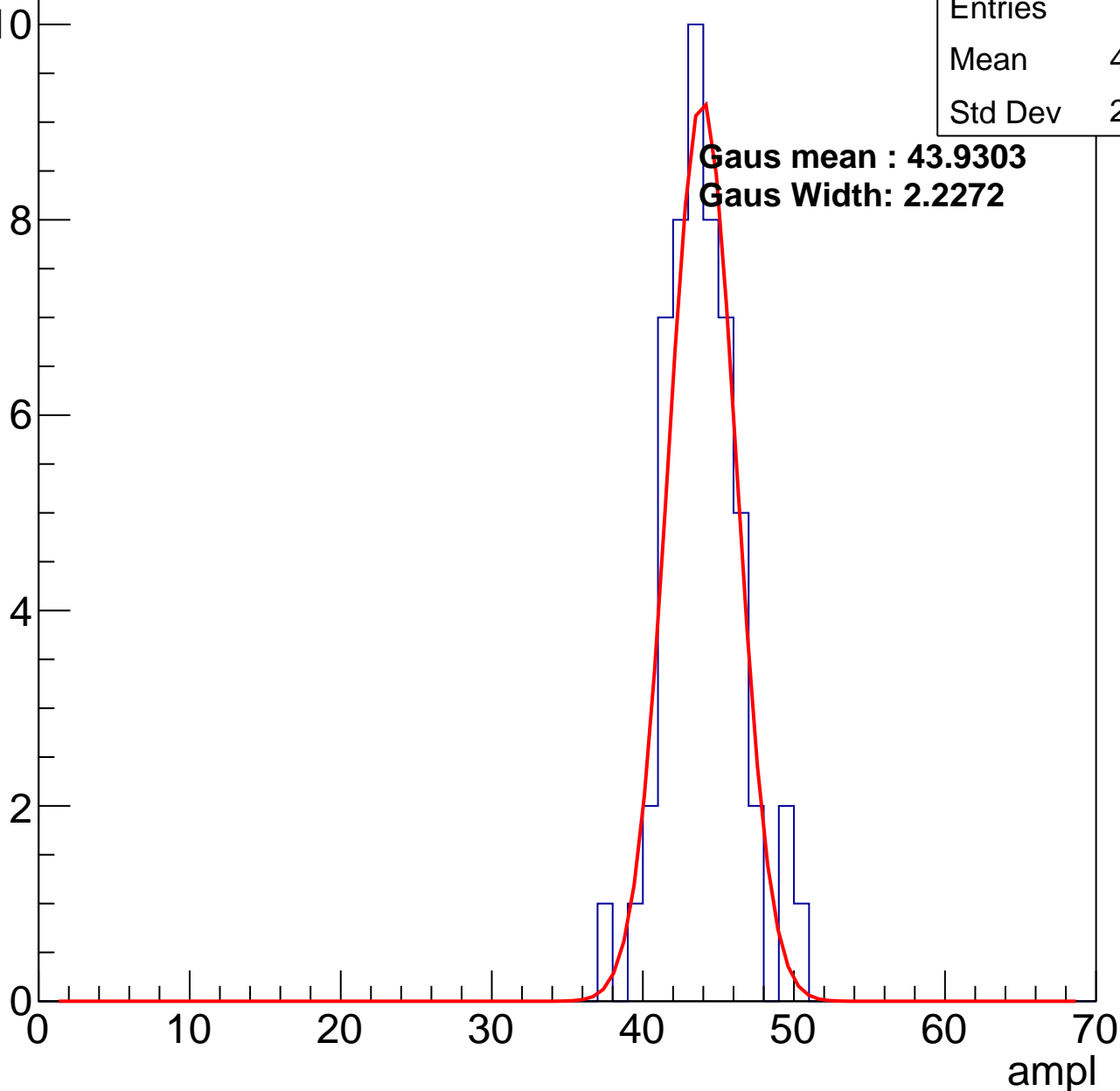
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	43.48
Std Dev	2.463

**Gaus mean : 43.9303**

**Gaus Width: 2.2272**

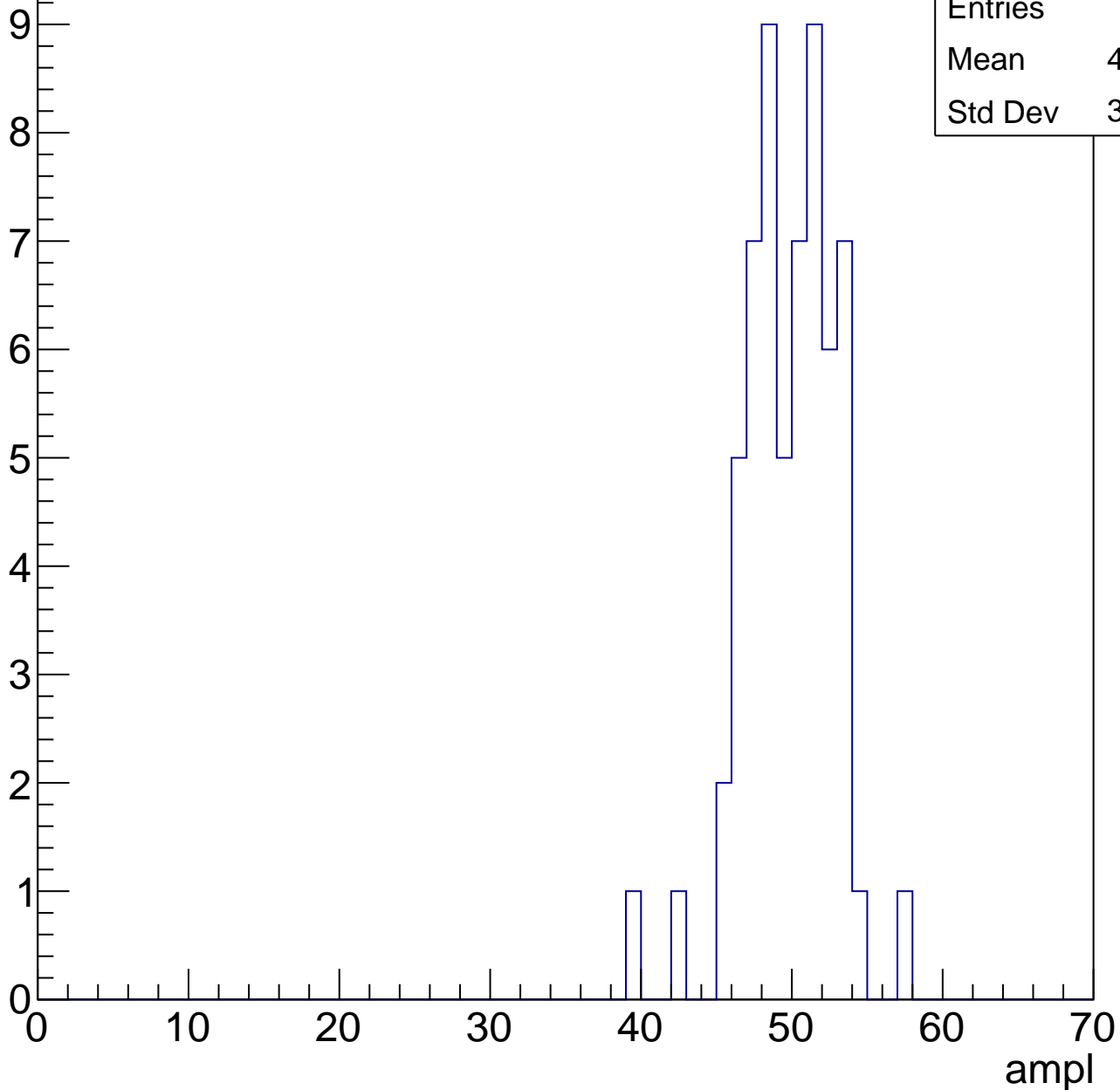


# B0L000S, U25-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	49.34
Std Dev	3.013

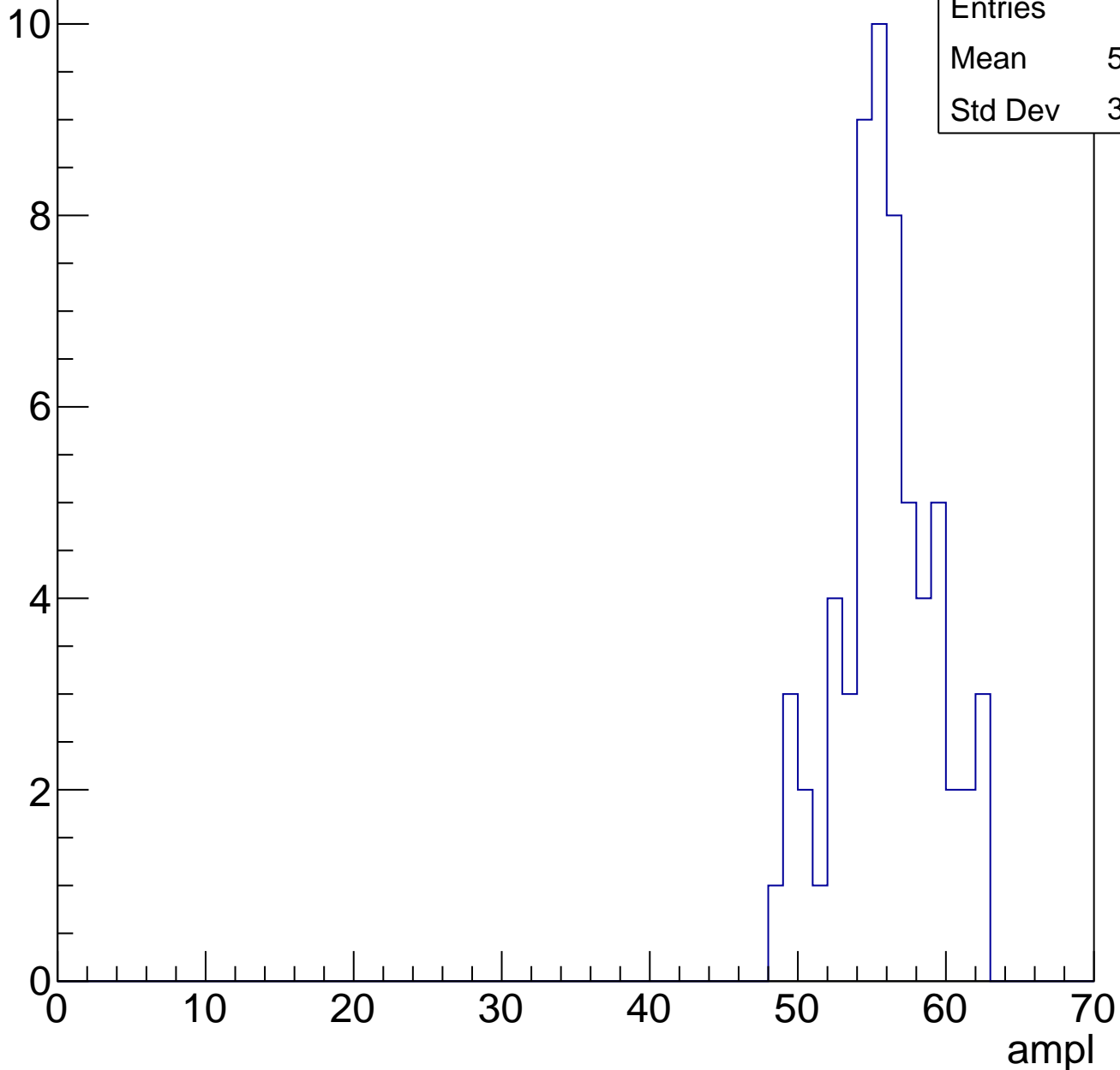


# B0L000S, U25-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	62
Mean	55.44
Std Dev	3.315

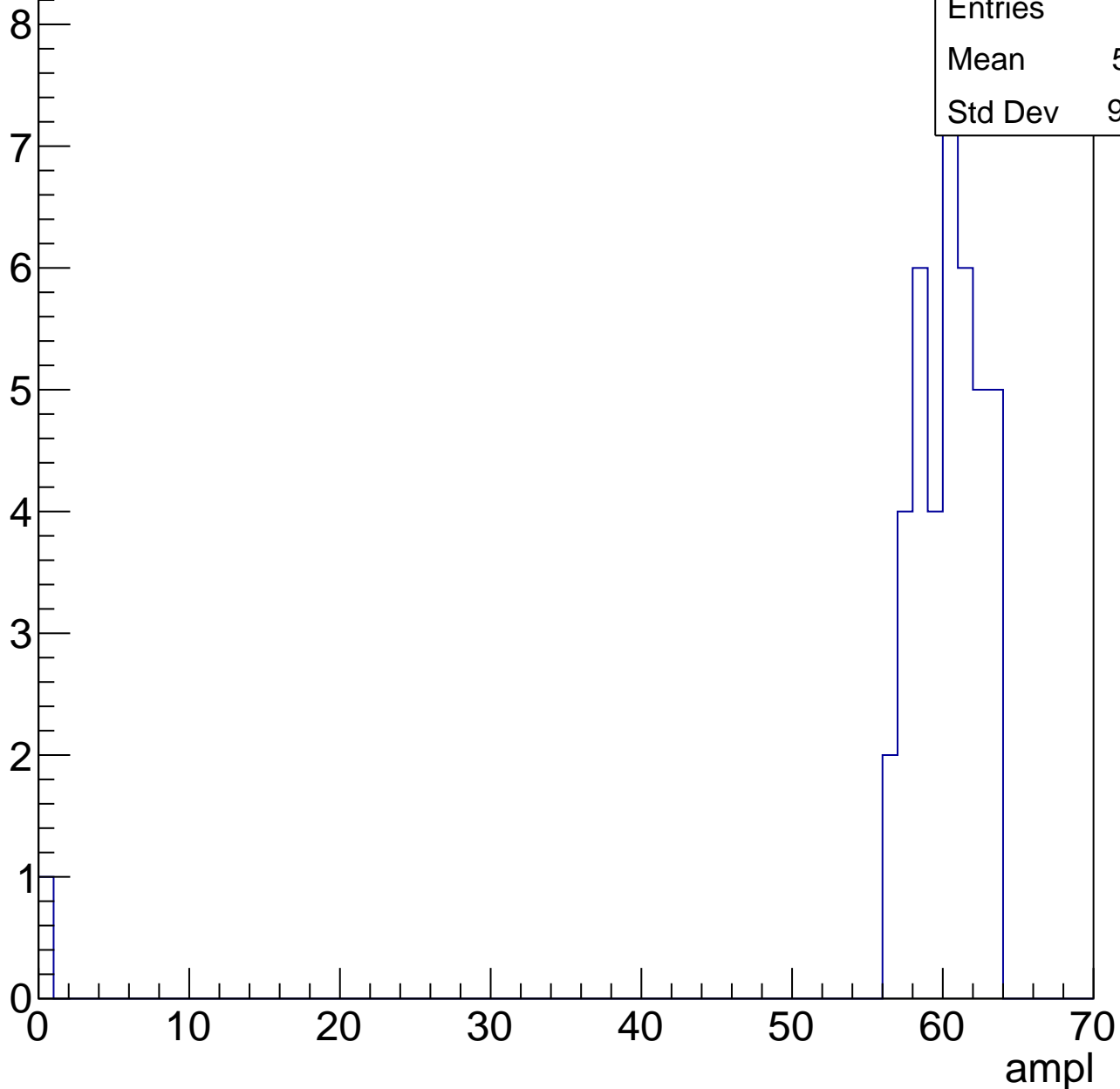
Entry



# B0L000S, U25-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

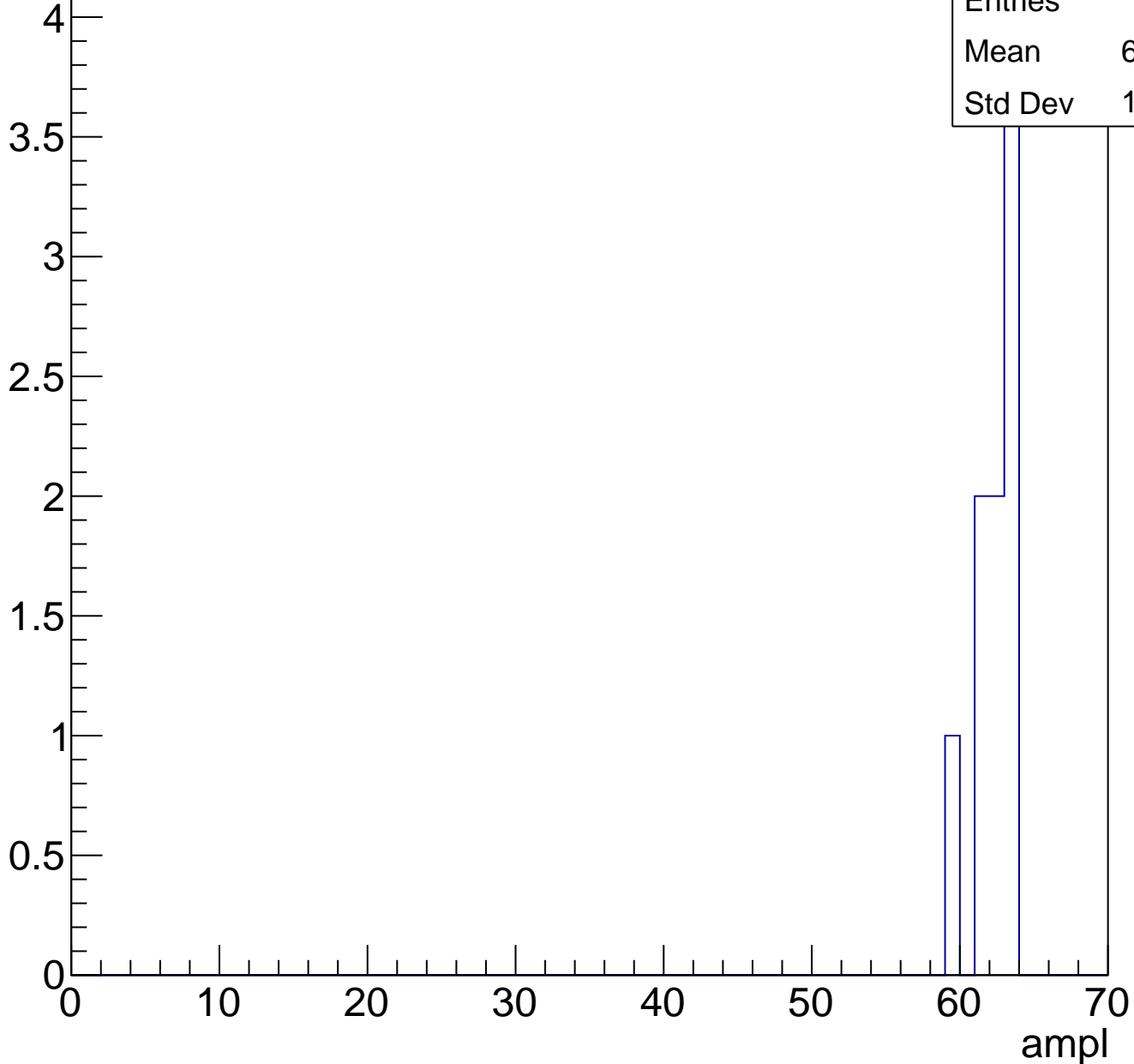
Entry



# B0L000S, U25-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch25, adc0

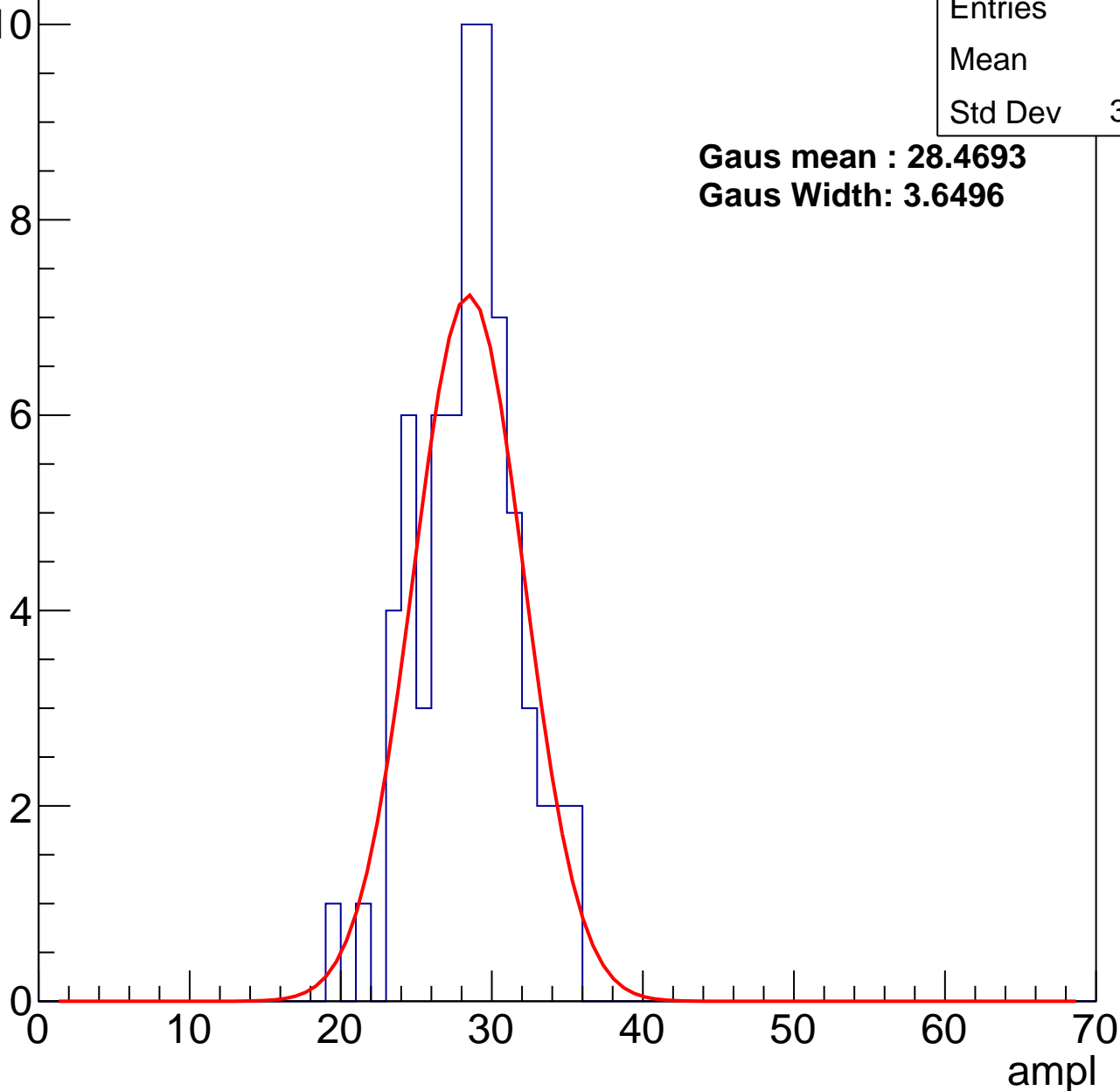
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28
Std Dev	3.285

**Gaus mean : 28.4693**

**Gaus Width: 3.6496**



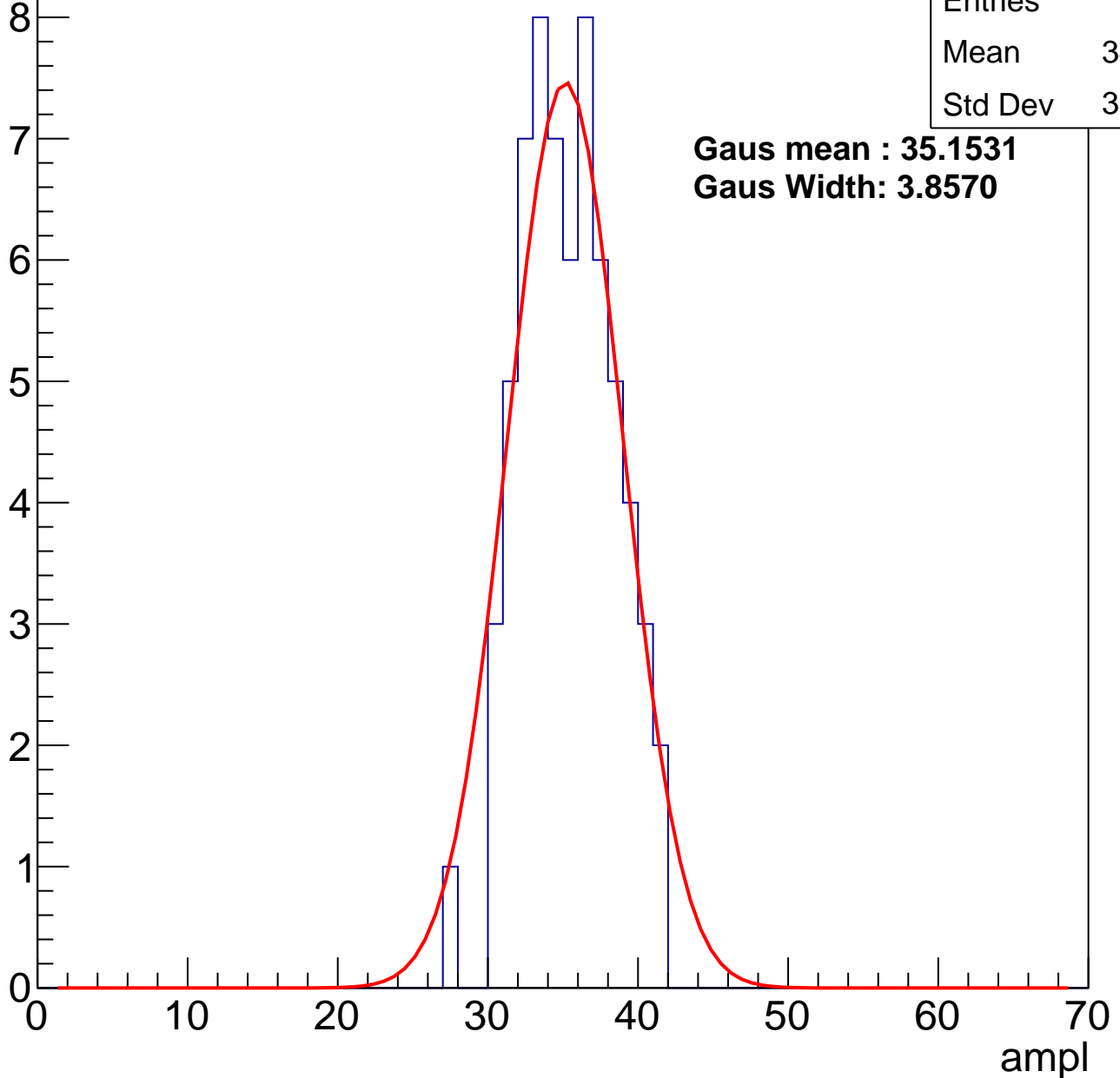
# B0L000S, U25-ch25, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	34.86
Std Dev	3.058

**Gaus mean : 35.1531**  
**Gaus Width: 3.8570**



# B0L000S, U25-ch25, adc2

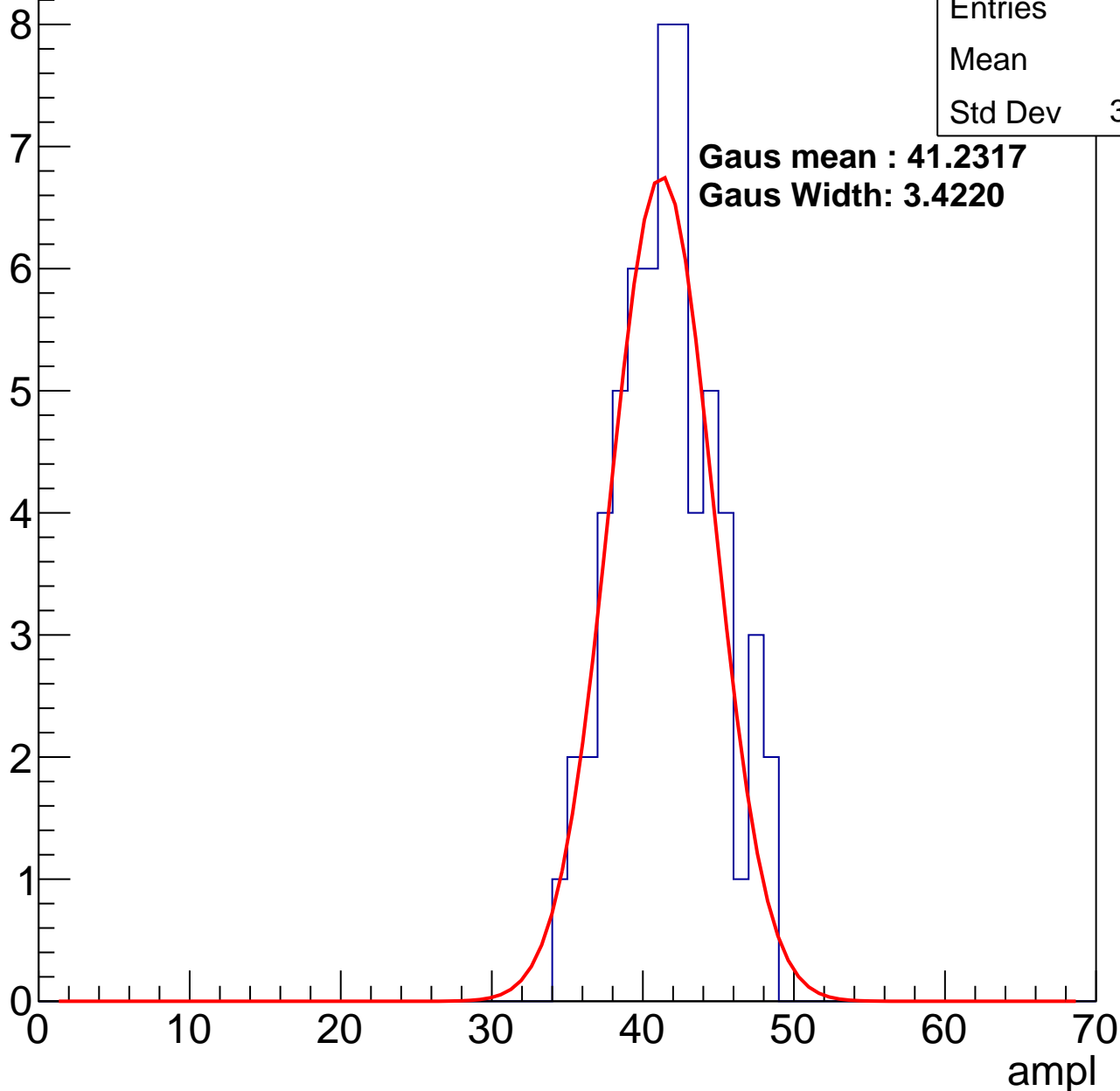
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	41.1
Std Dev	3.323

**Gaus mean : 41.2317**

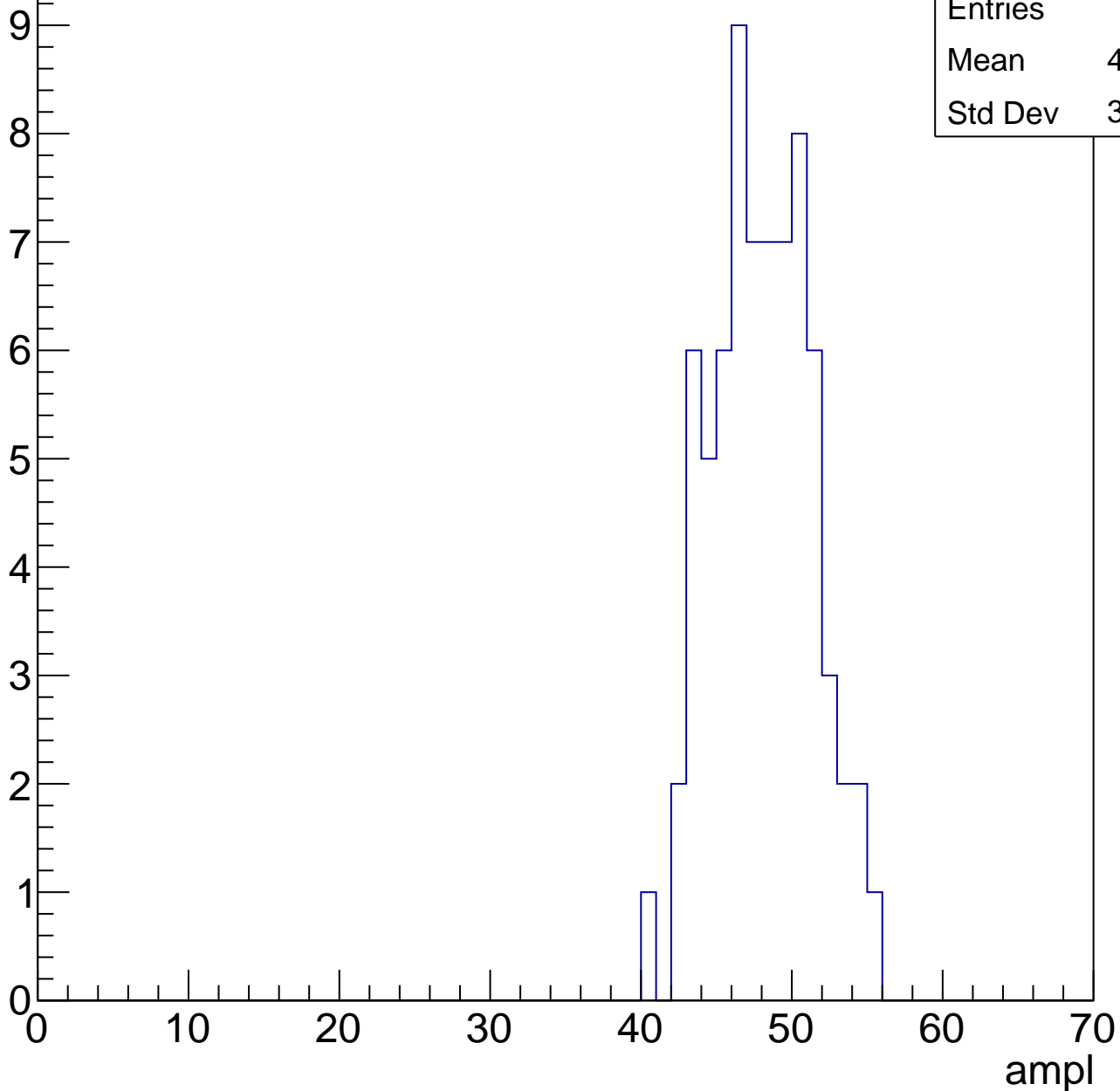
**Gaus Width: 3.4220**



# B0L000S, U25-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



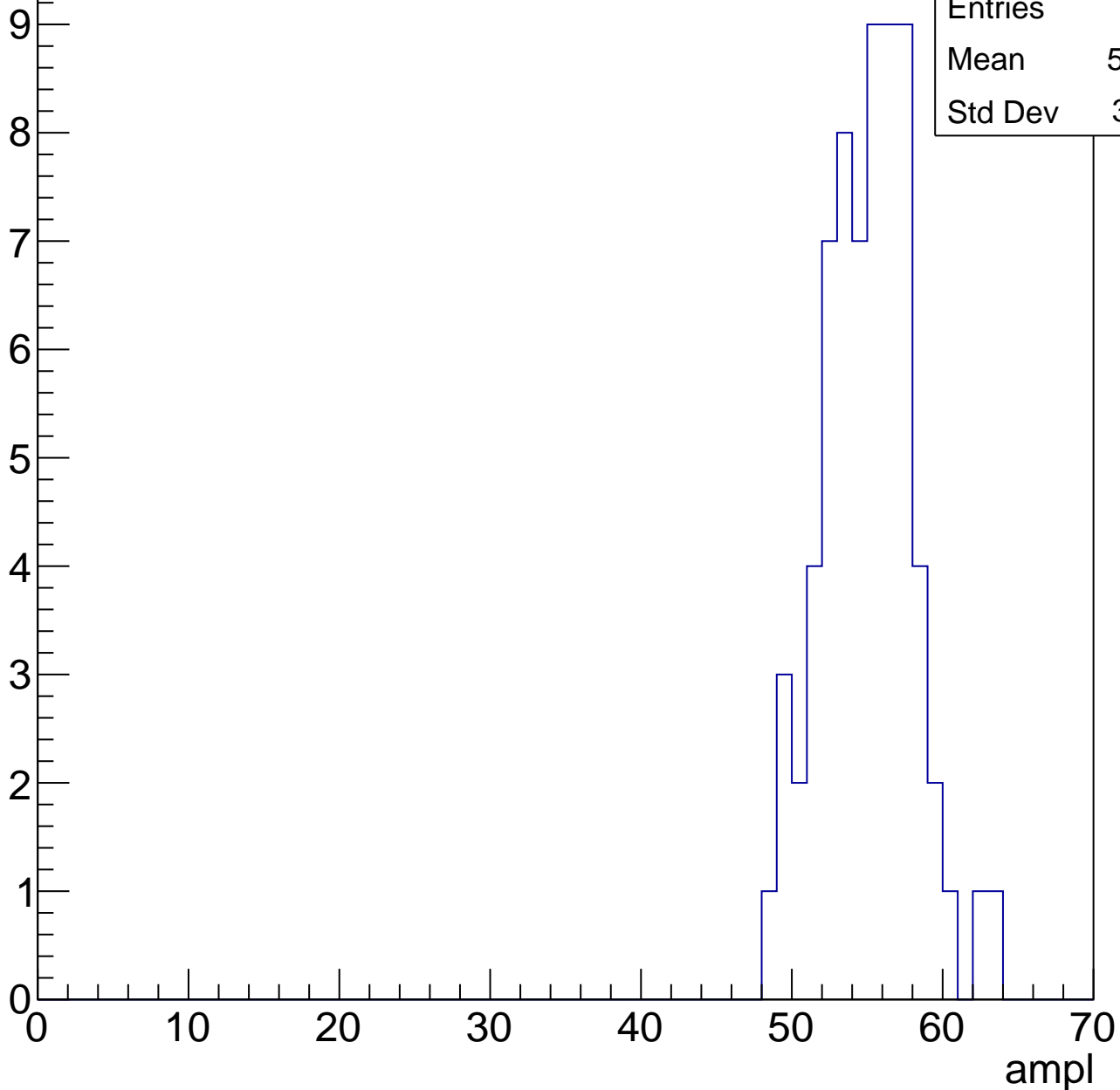
Entries	72
Mean	47.57
Std Dev	3.252

# B0L000S, U25-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	54.59
Std Dev	3.001

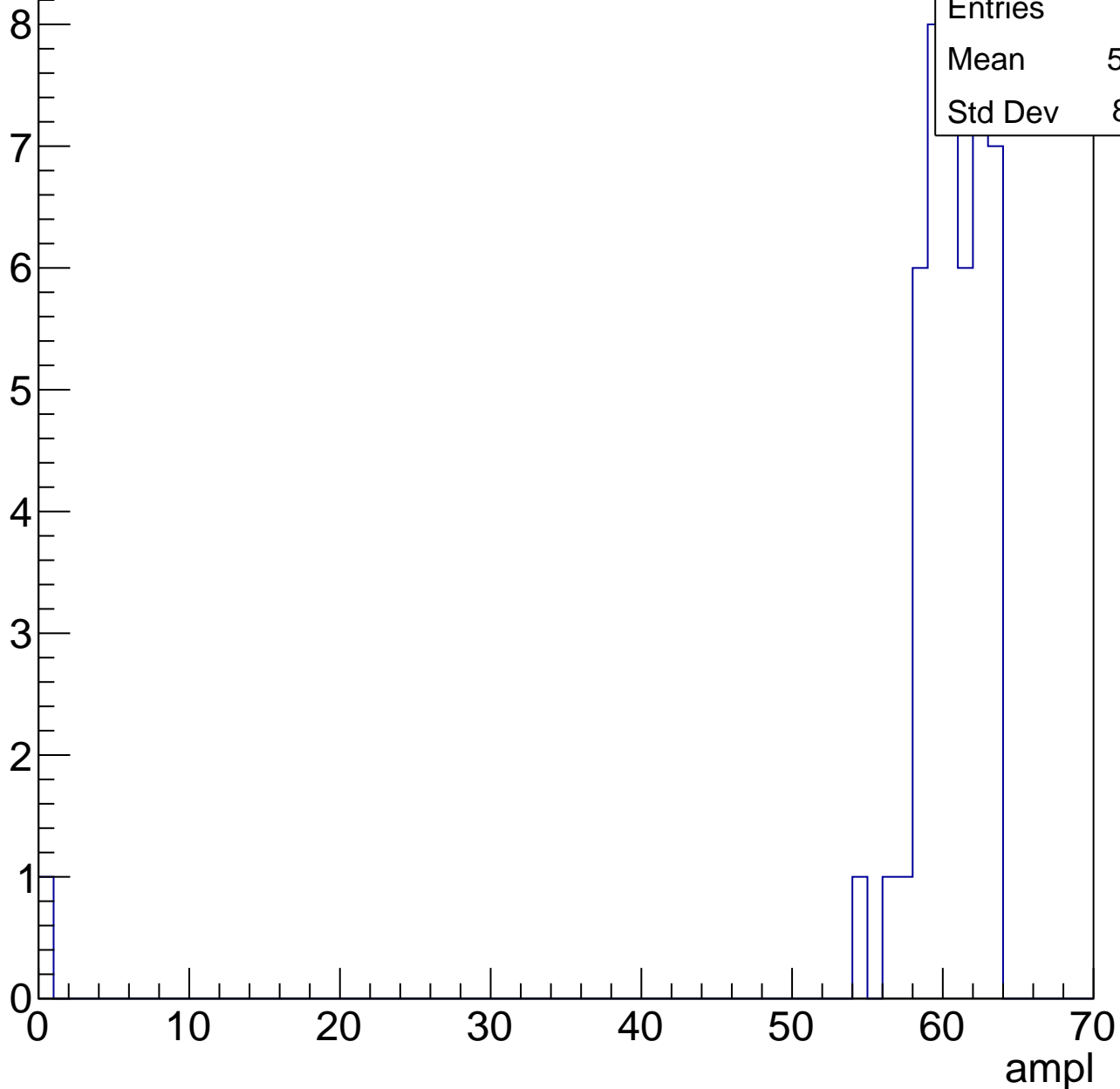


# B0L000S, U25-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

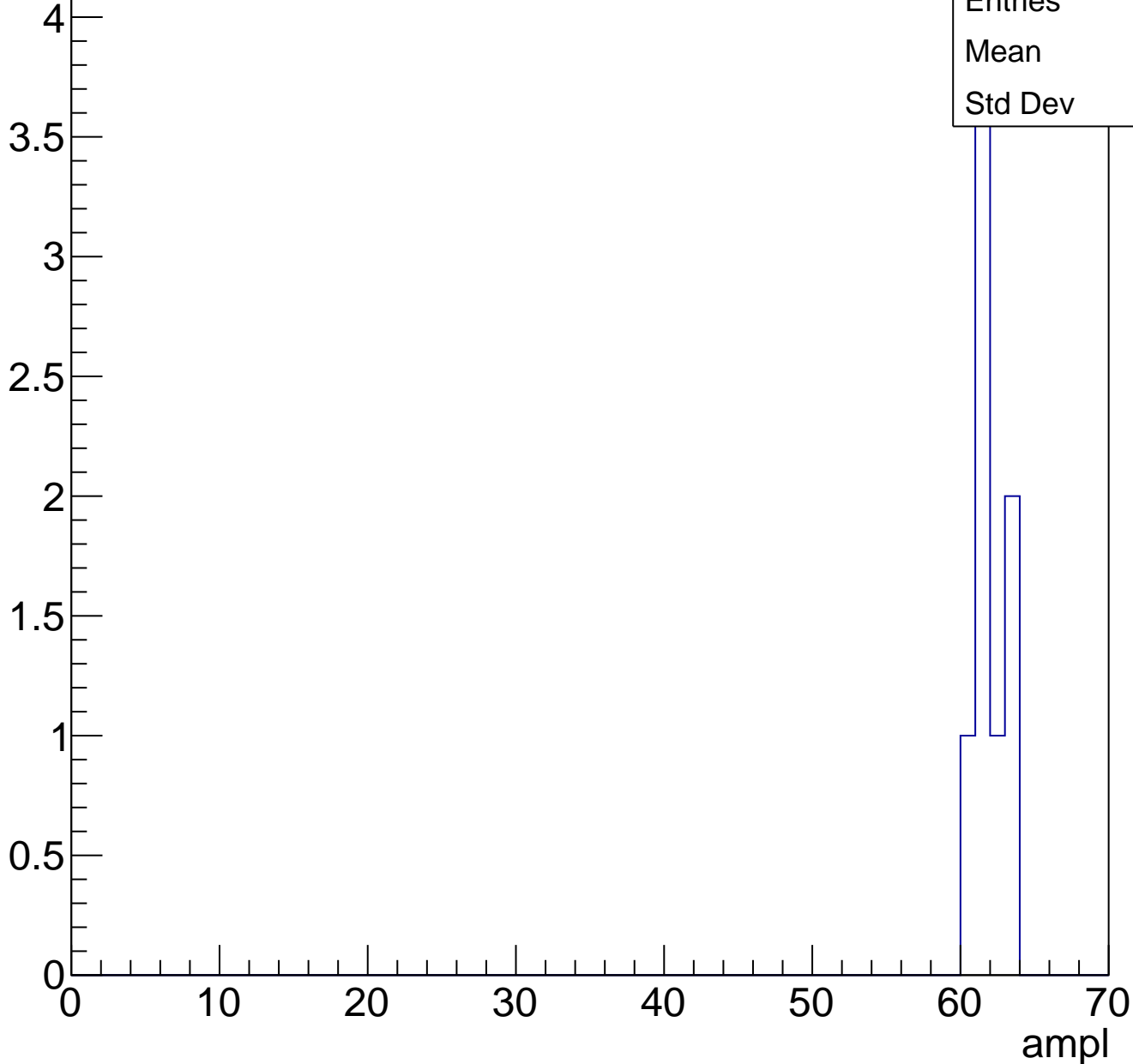
Entries	47
Mean	58.94
Std Dev	8.921



# B0L000S, U25-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch26, adc0

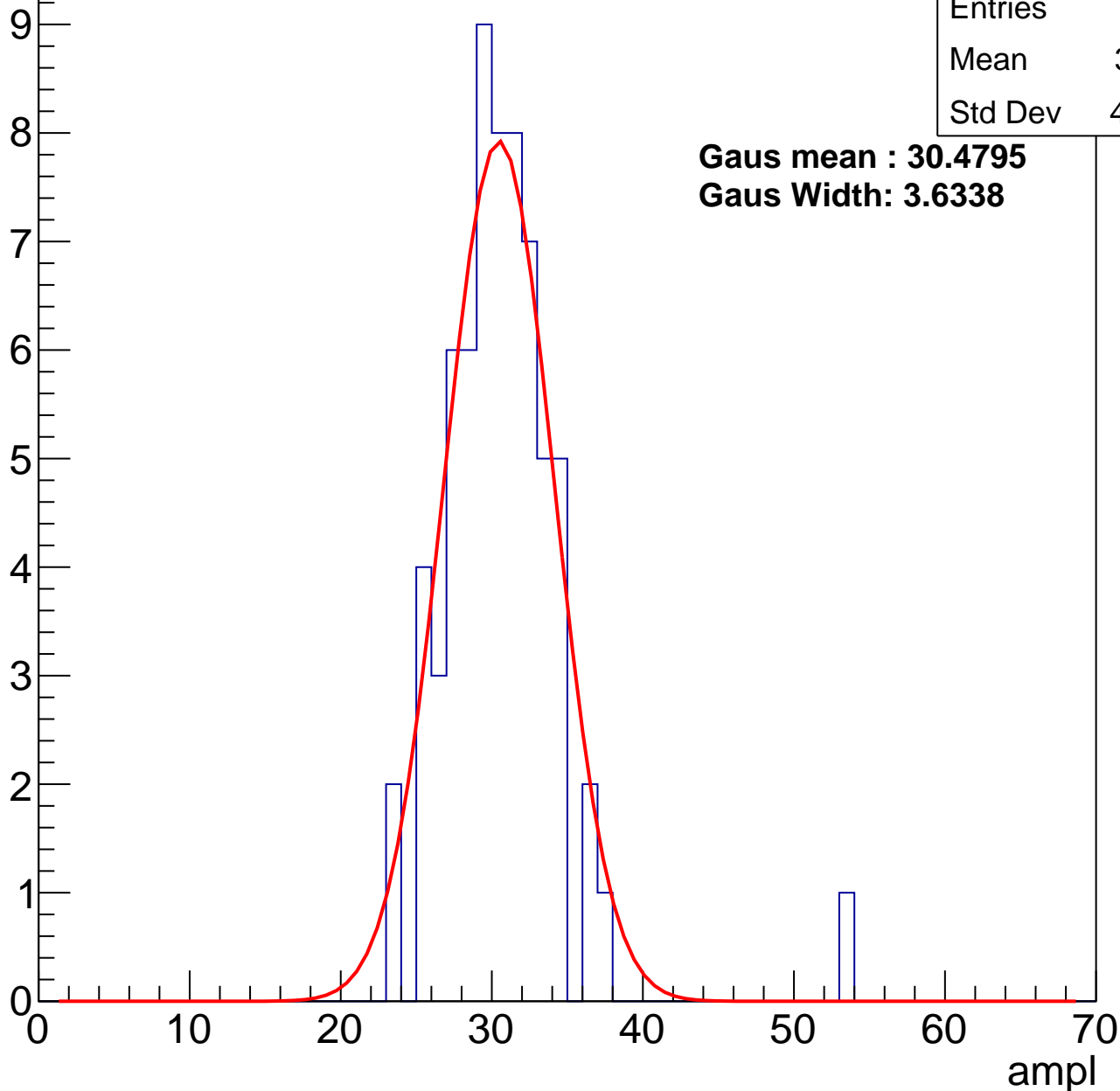
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.21
Std Dev	4.127

**Gaus mean : 30.4795**

**Gaus Width: 3.6338**



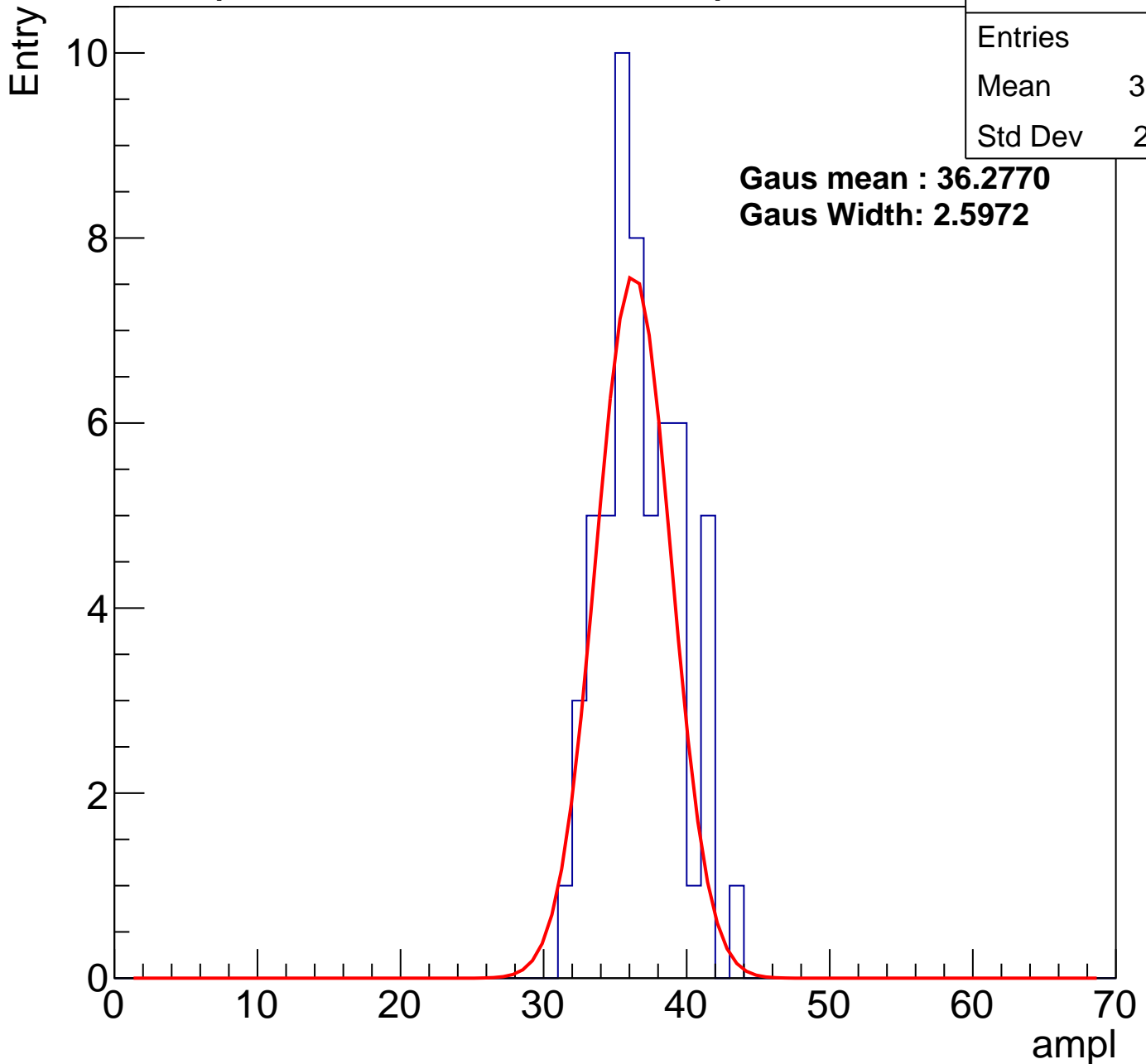
# B0L000S, U25-ch26, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	56
Mean	36.34
Std Dev	2.721

**Gaus mean : 36.2770**

**Gaus Width: 2.5972**



# B0L000S, U25-ch26, adc2

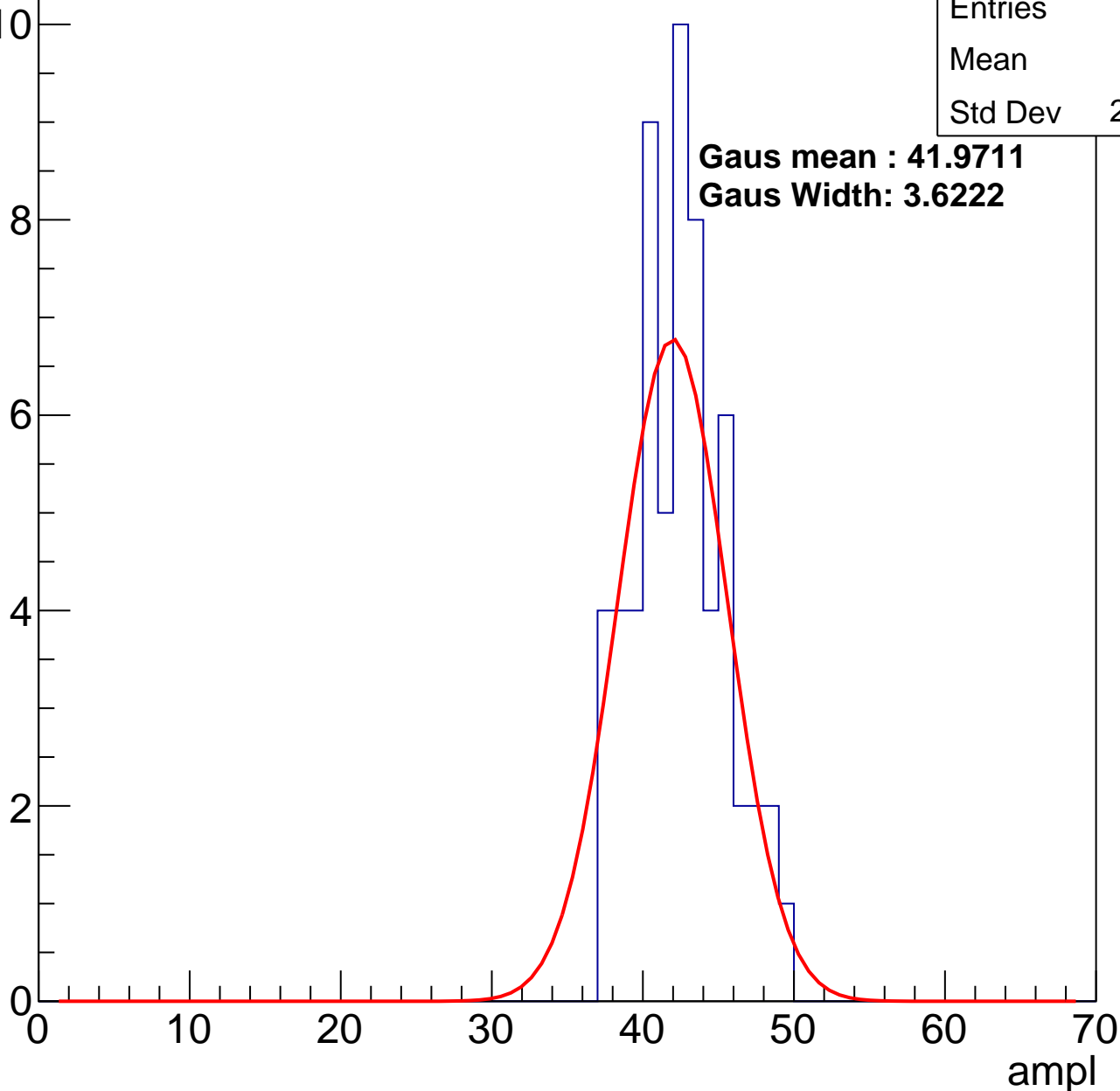
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42
Std Dev	2.925

**Gaus mean : 41.9711**

**Gaus Width: 3.6222**

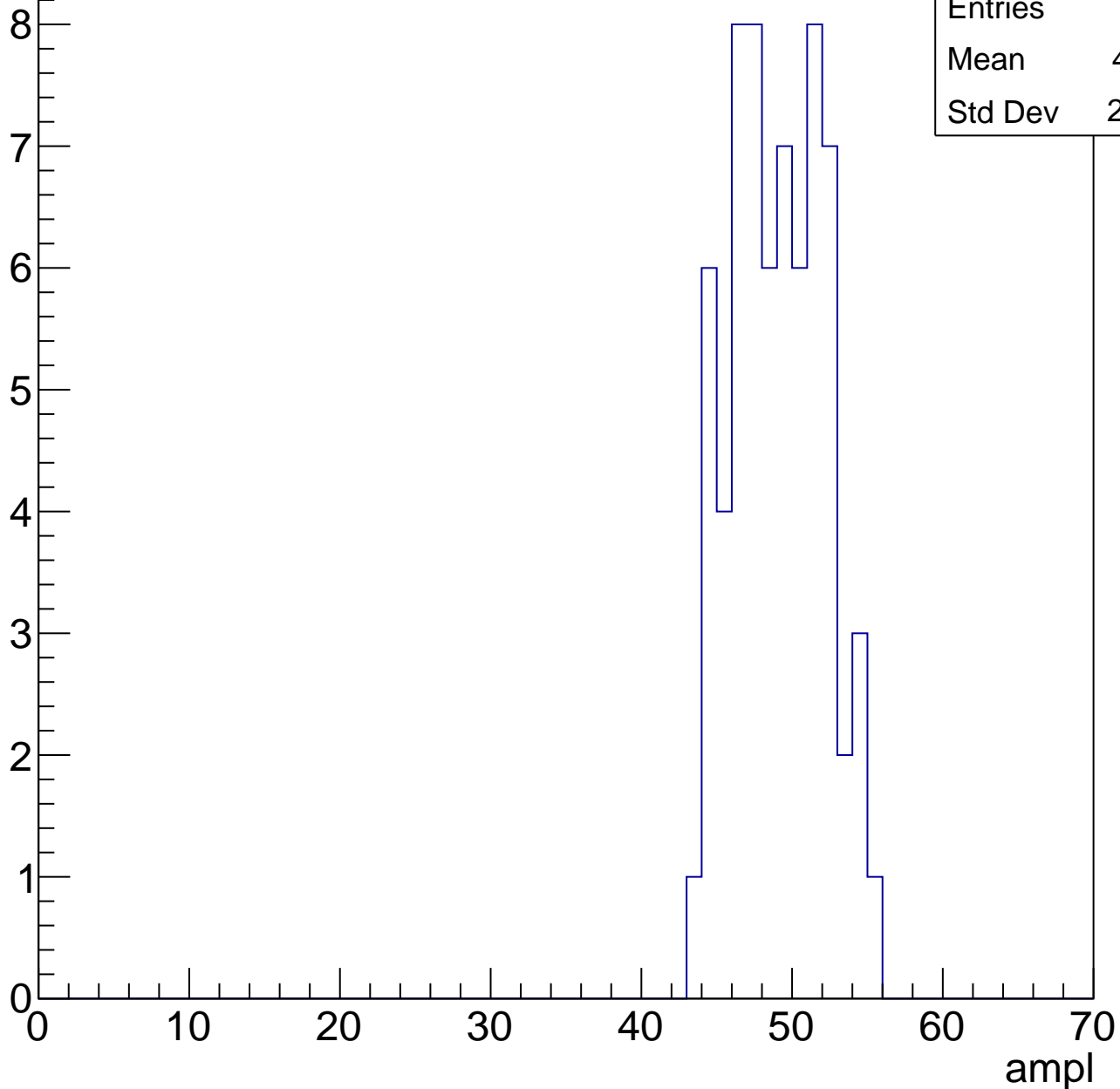


# B0L000S, U25-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.61
Std Dev	2.977

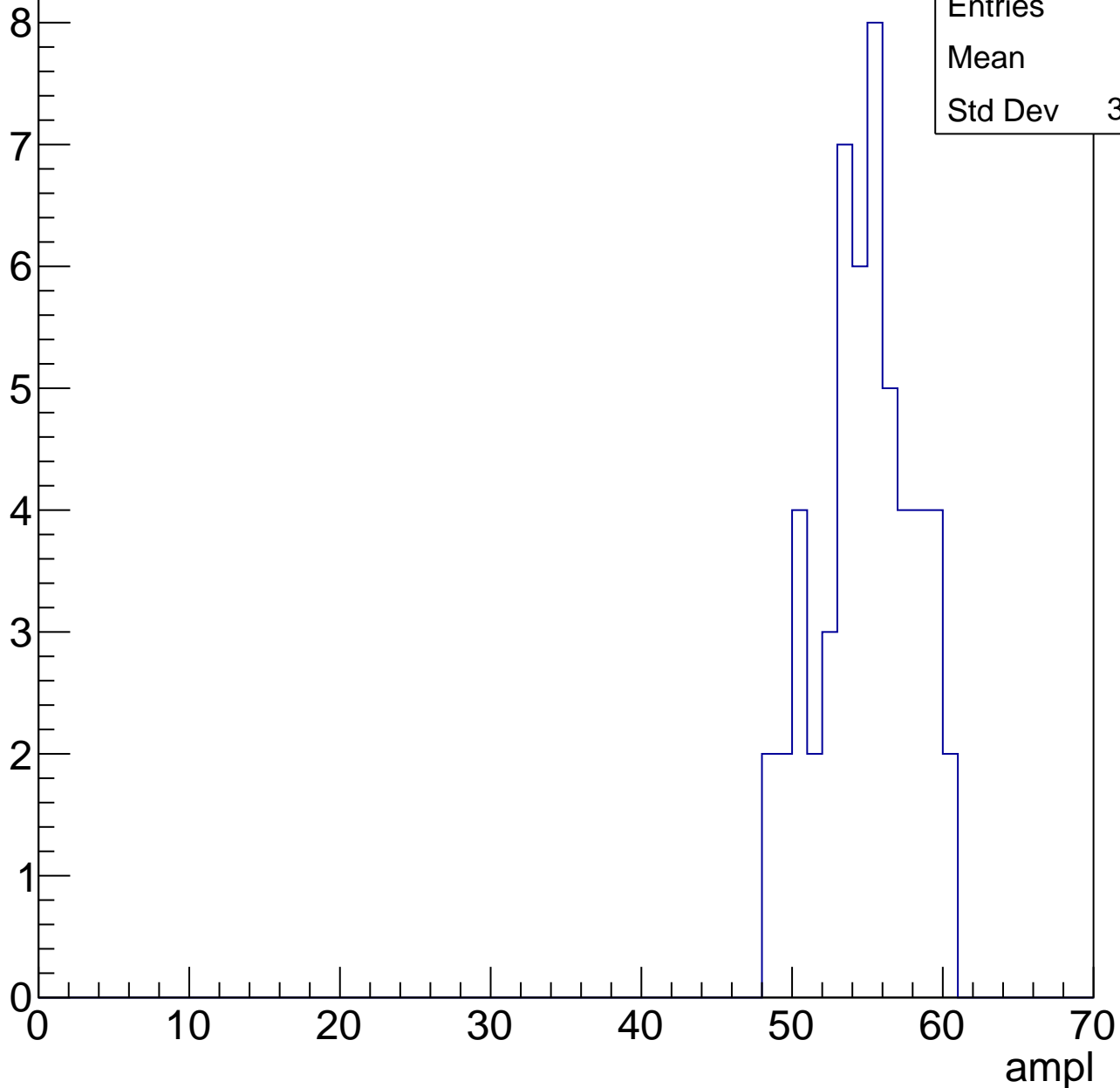


# B0L000S, U25-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	54.4
Std Dev	3.116

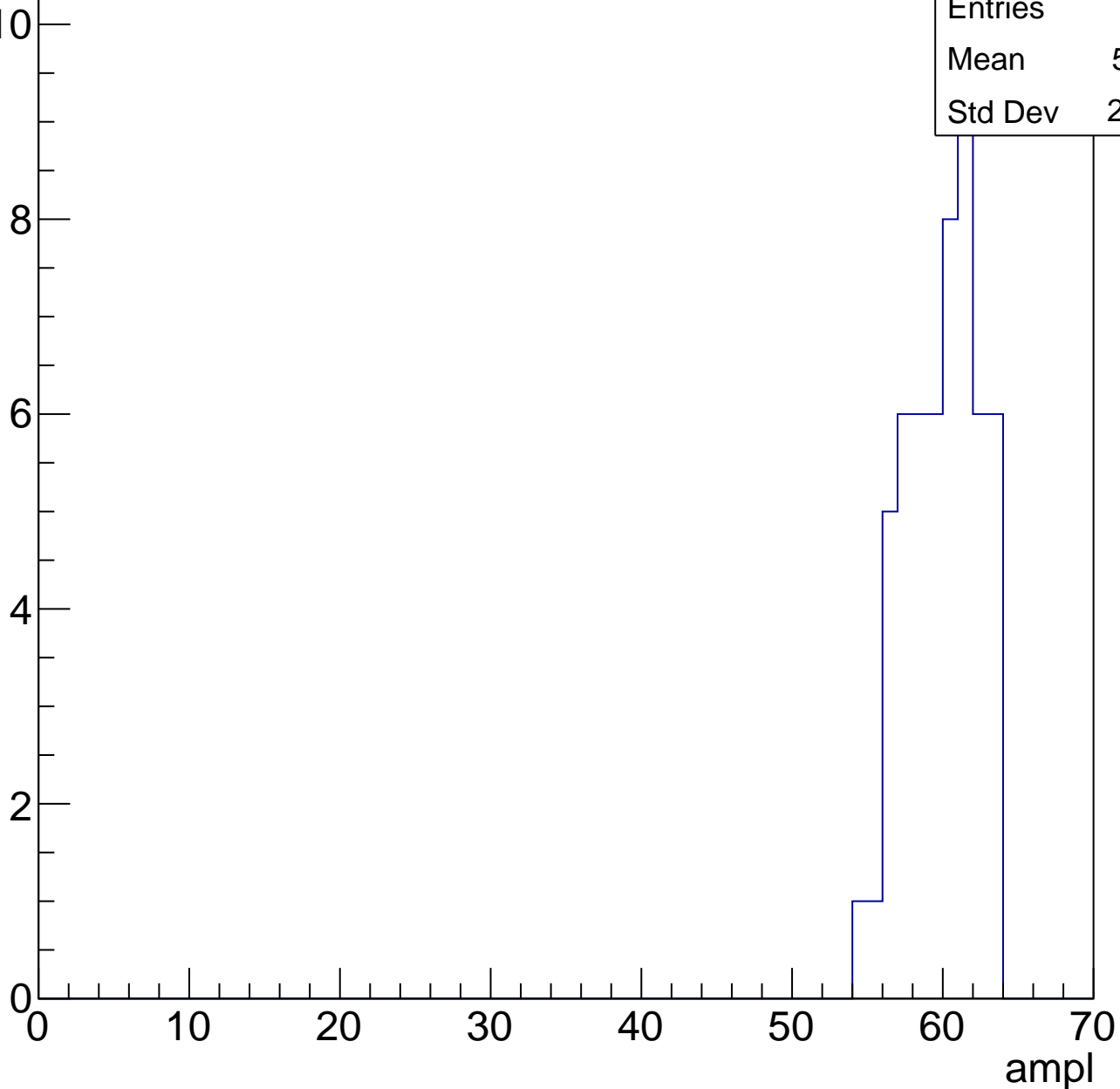


# B0L000S, U25-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

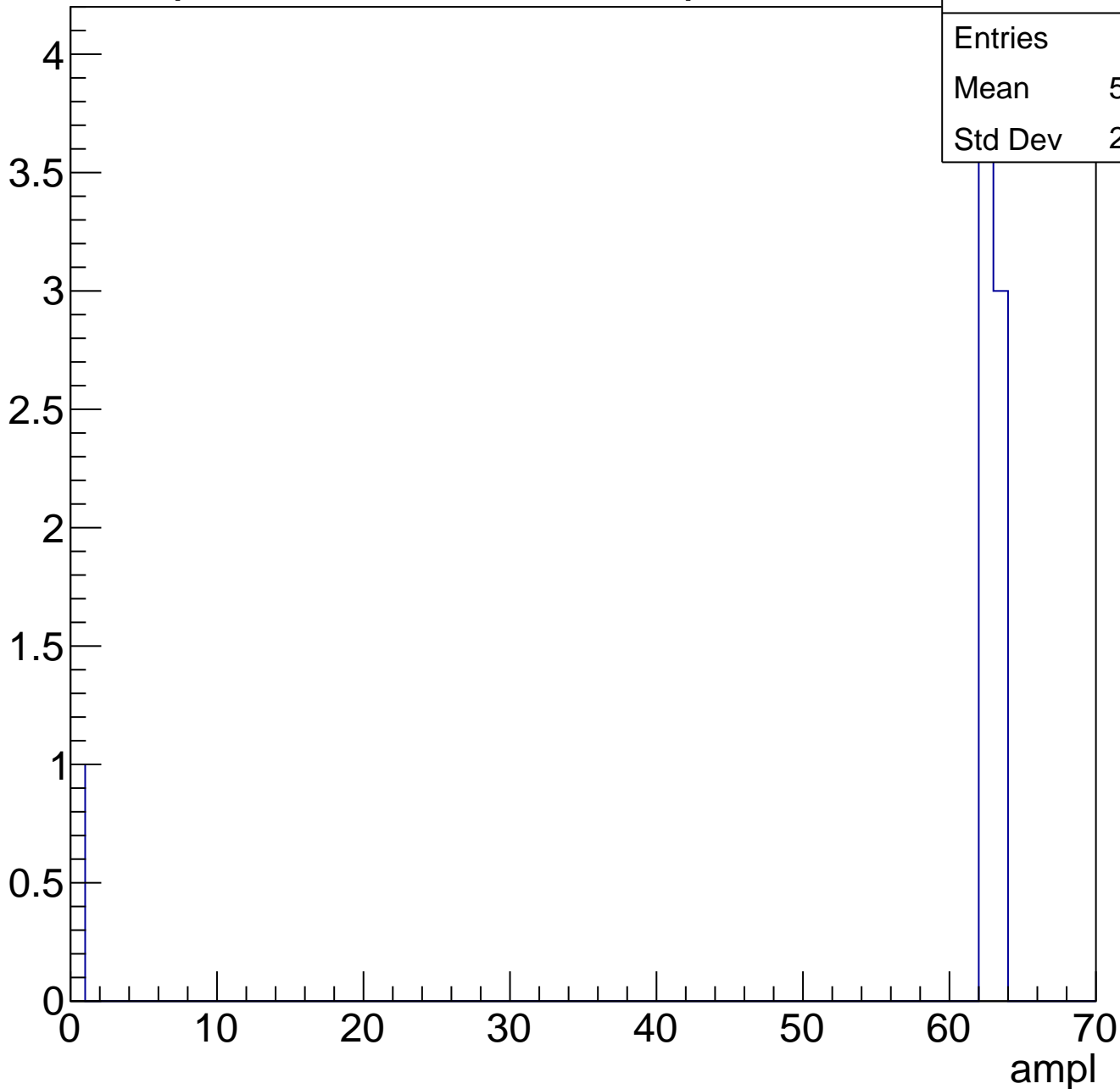
Entries	55
Mean	59.51
Std Dev	2.335



# B0L000S, U25-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch27, adc0

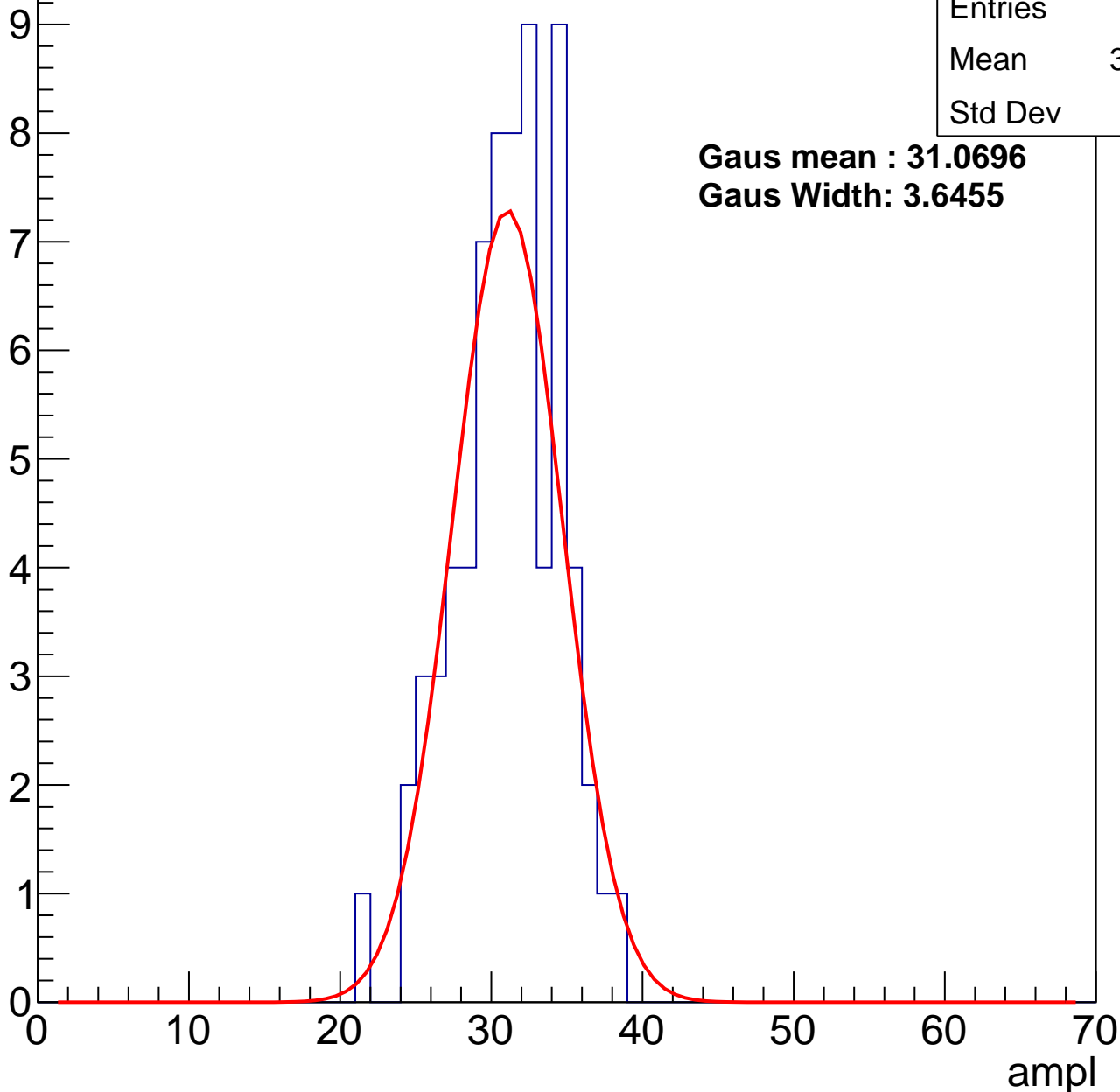
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.66
Std Dev	3.41

**Gaus mean : 31.0696**

**Gaus Width: 3.6455**



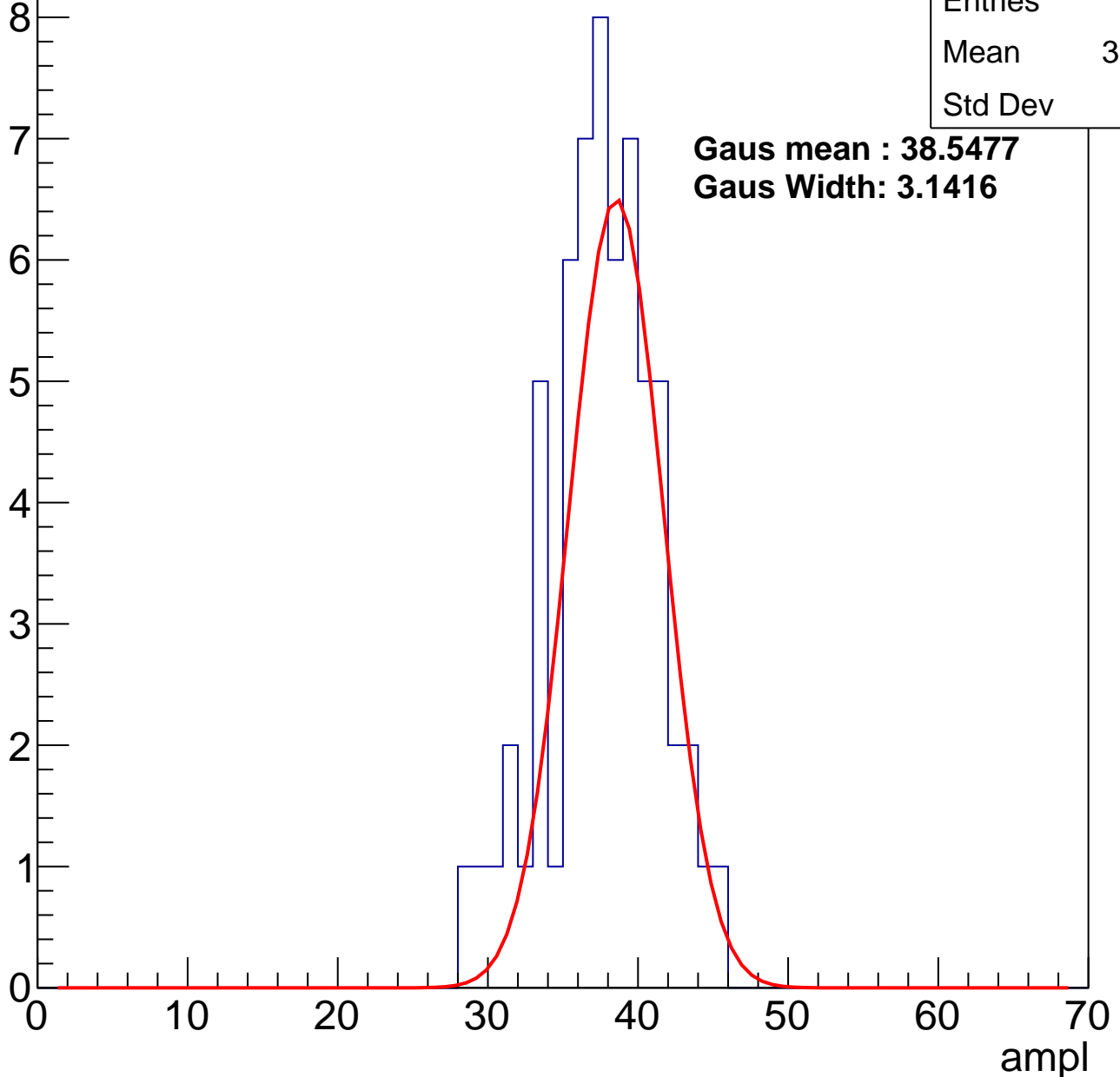
# B0L000S, U25-ch27, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.15
Std Dev	3.6

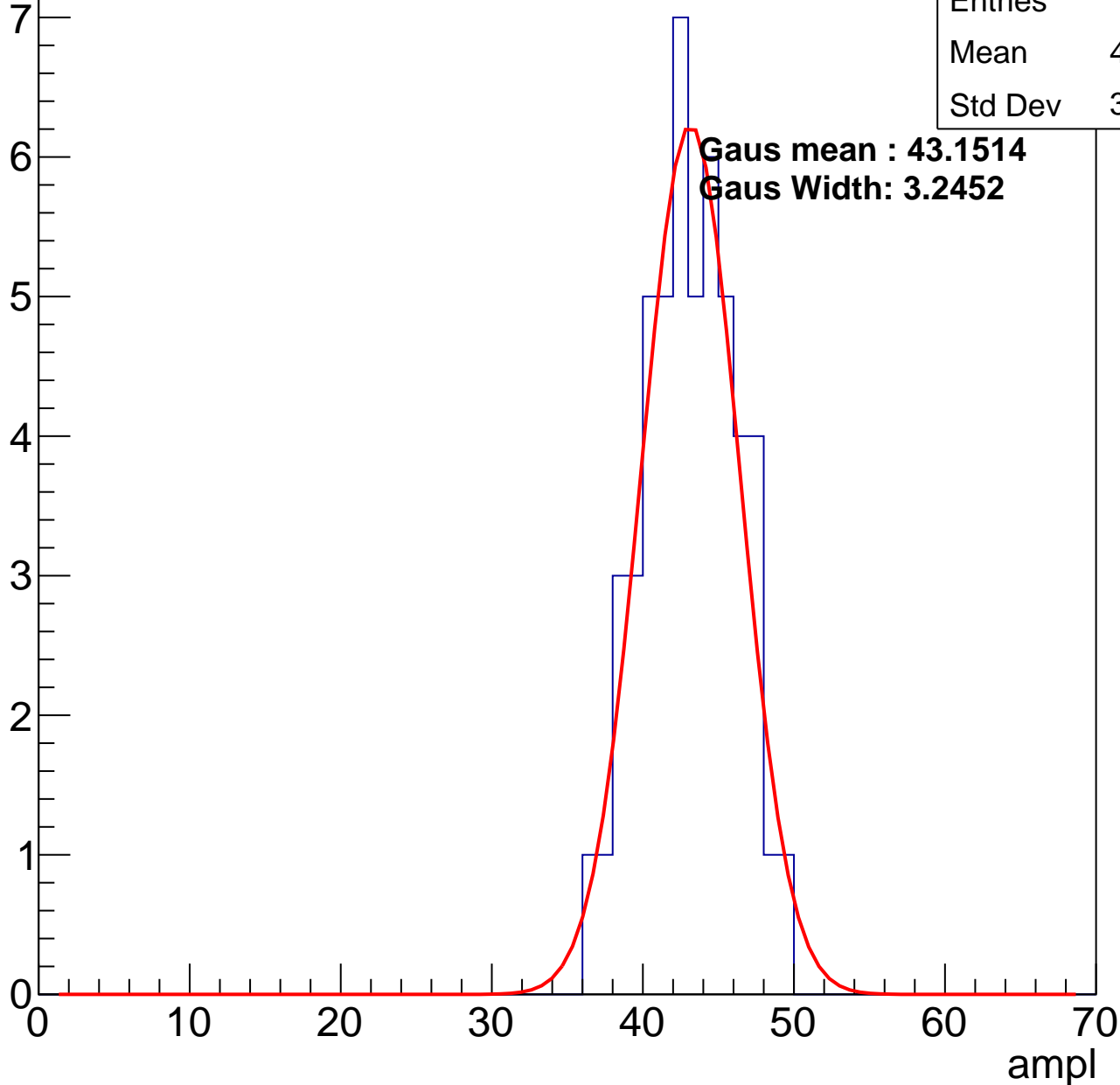
**Gaus mean : 38.5477**  
**Gaus Width: 3.1416**



# B0L000S, U25-ch27, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	49.87
Std Dev	3.789

Entry

10

8

6

4

2

0

0

10

20

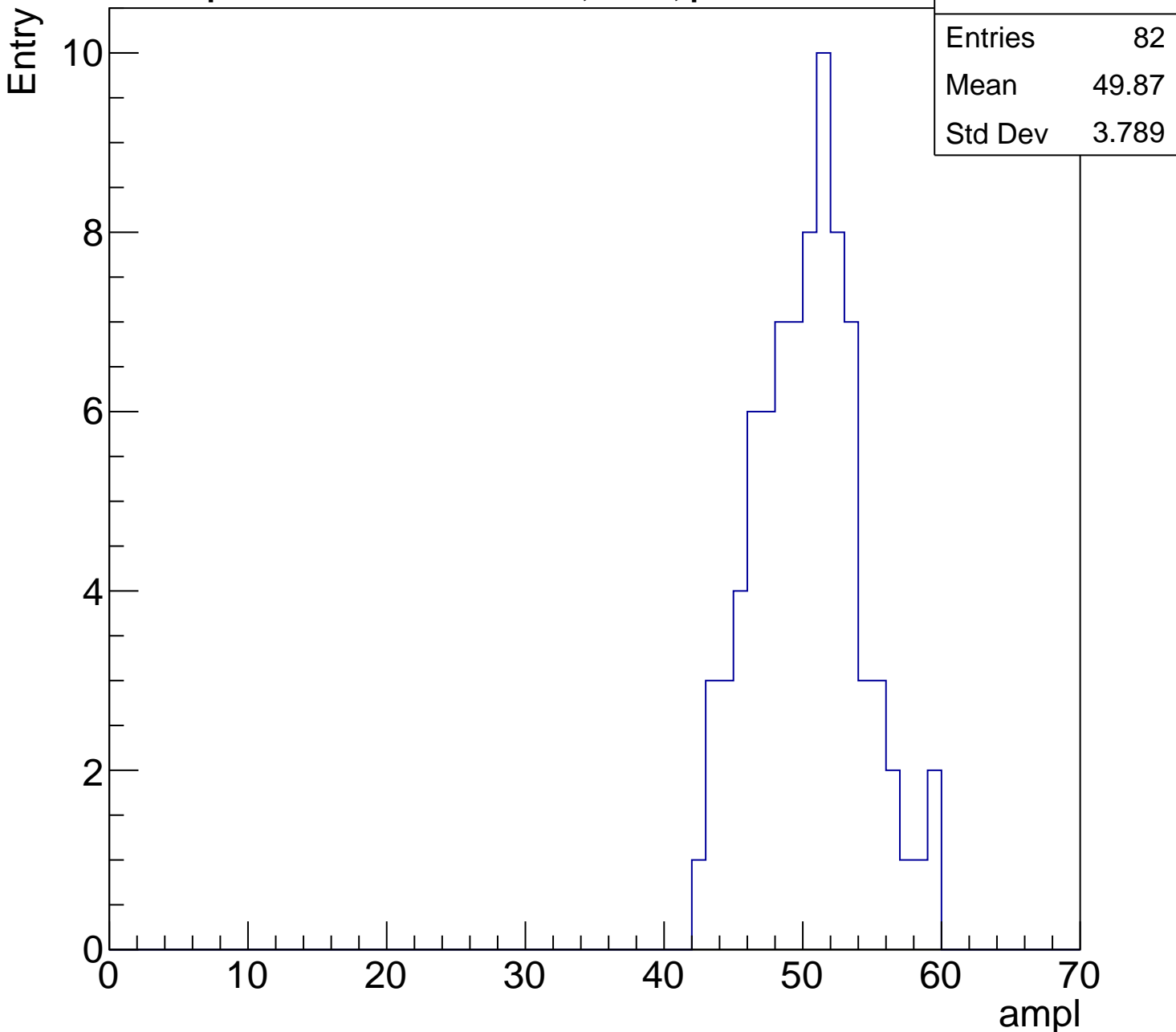
30

40

50

60

ampl

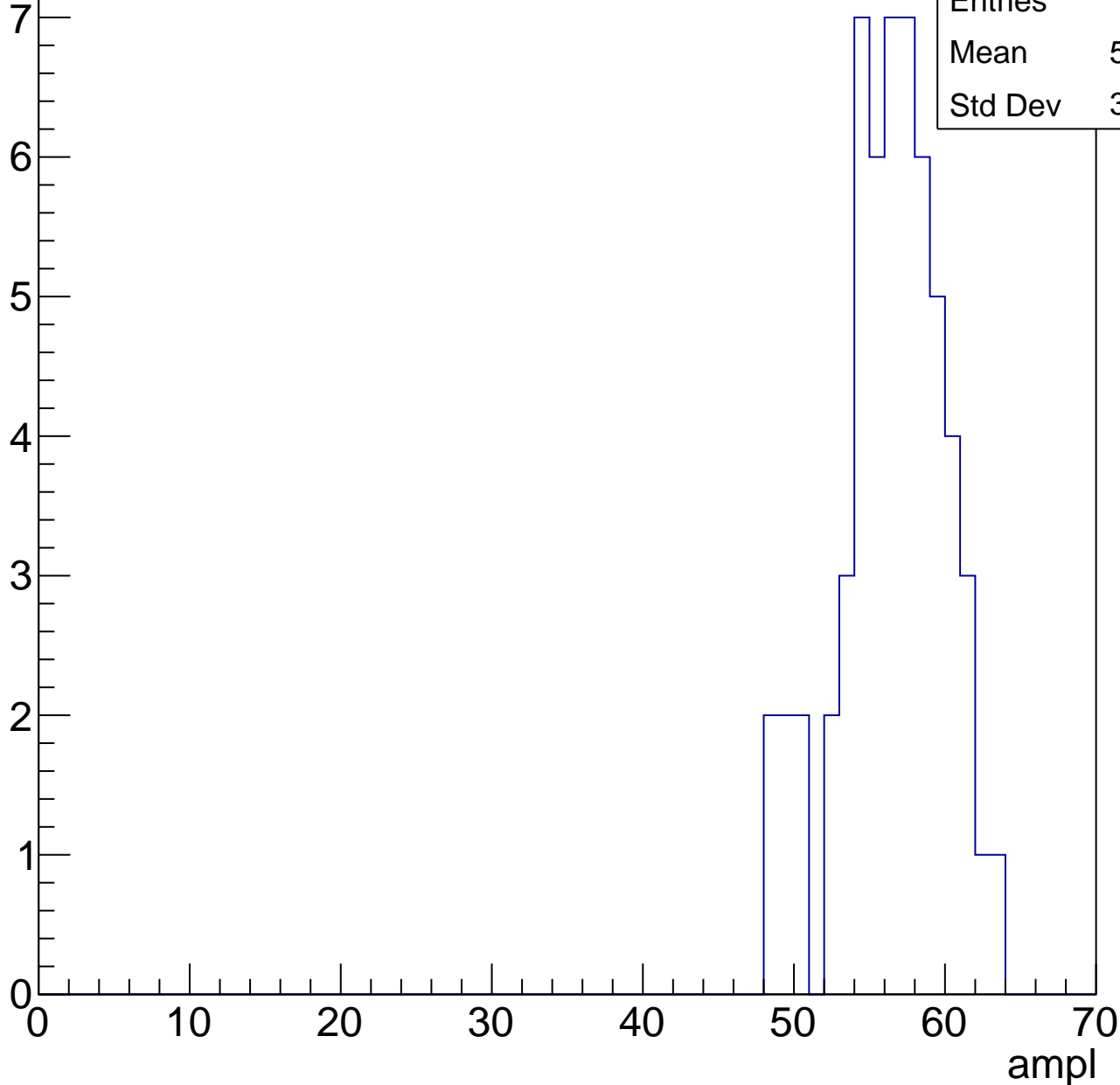


# B0L000S, U25-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.98
Std Dev	3.452

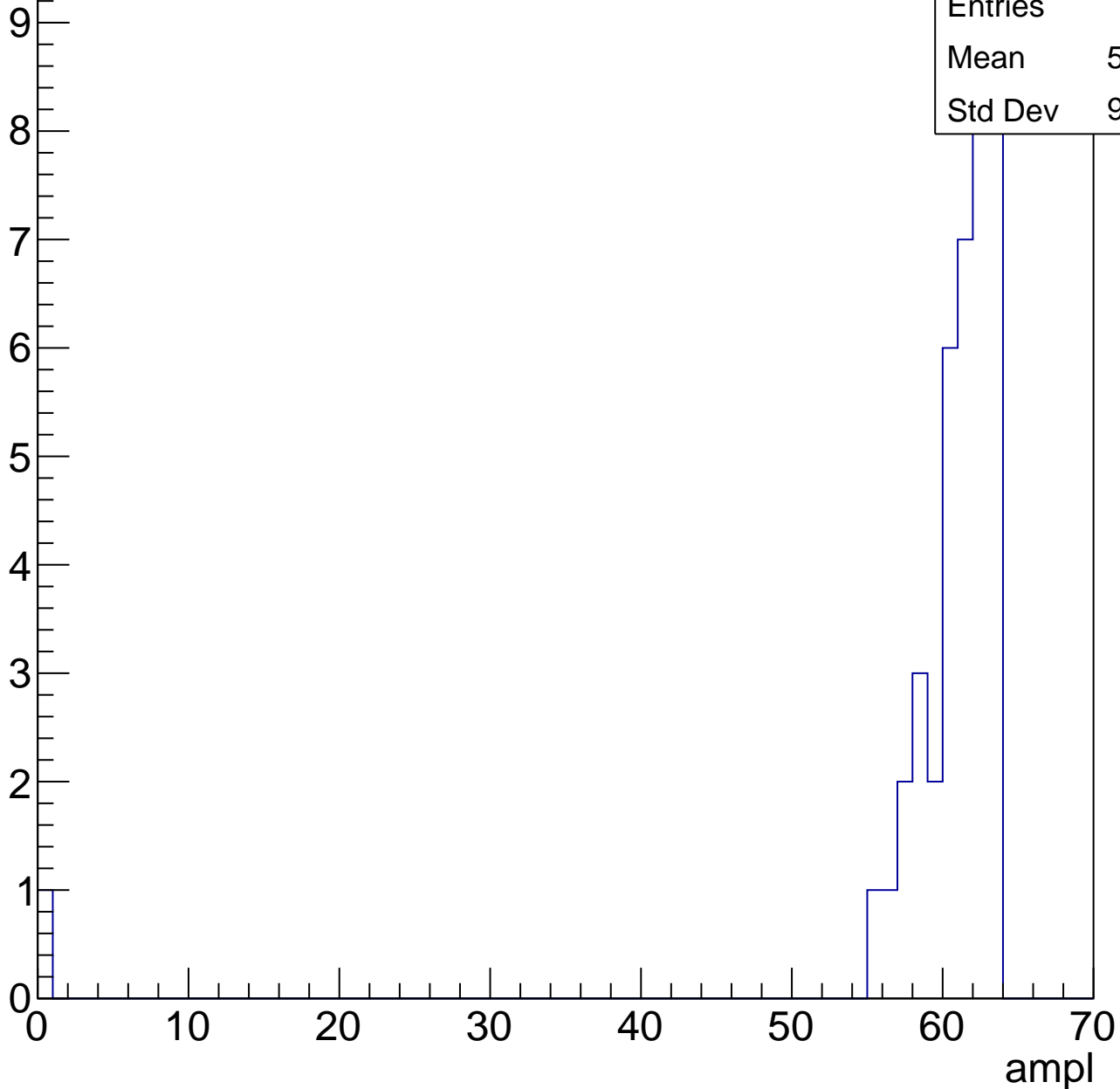


# B0L000S, U25-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

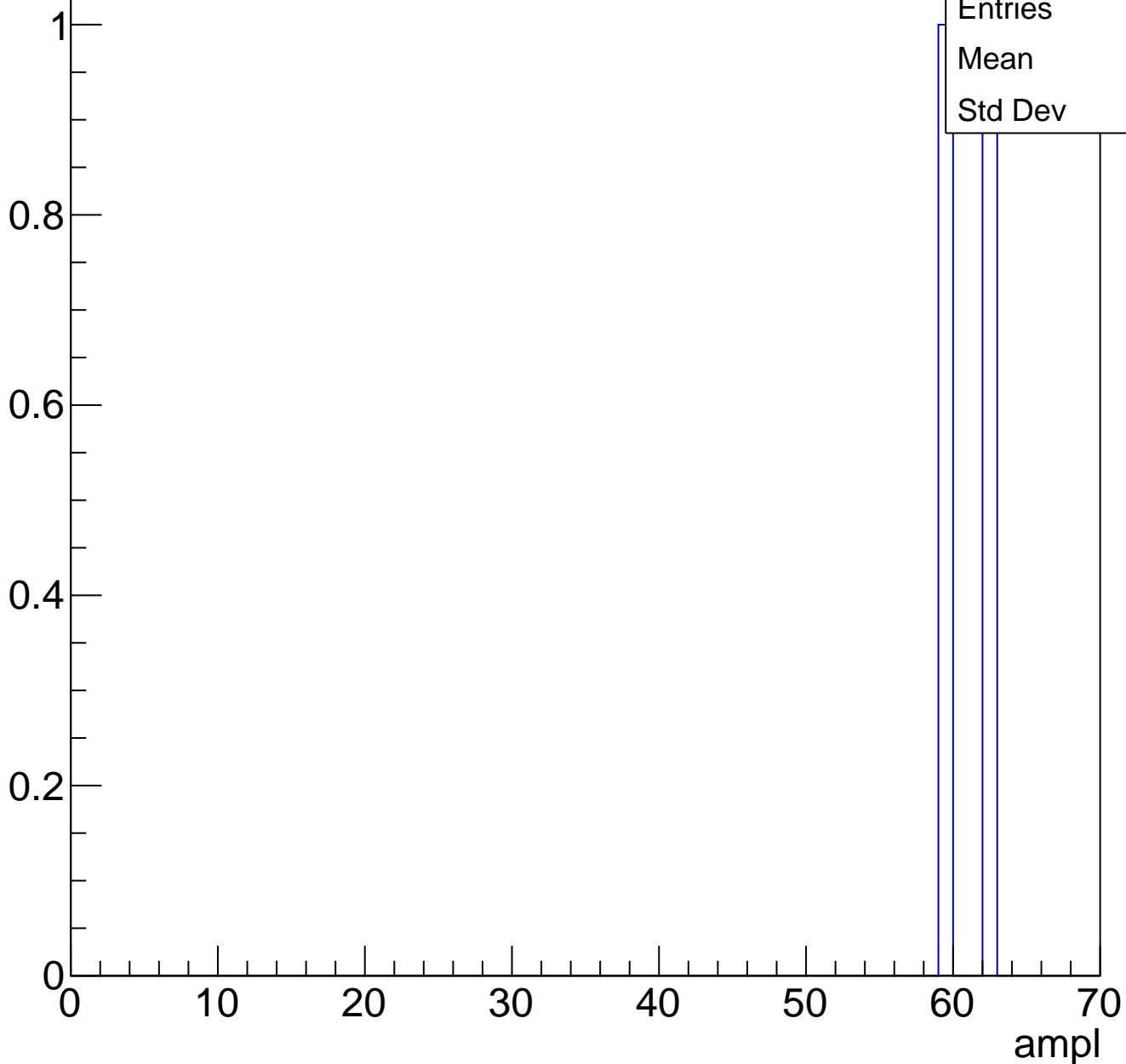
Entries	40
Mean	59.17
Std Dev	9.703



# B0L000S, U25-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch28, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	28.63
Std Dev	3.025

**Gaus mean : 29.2784**

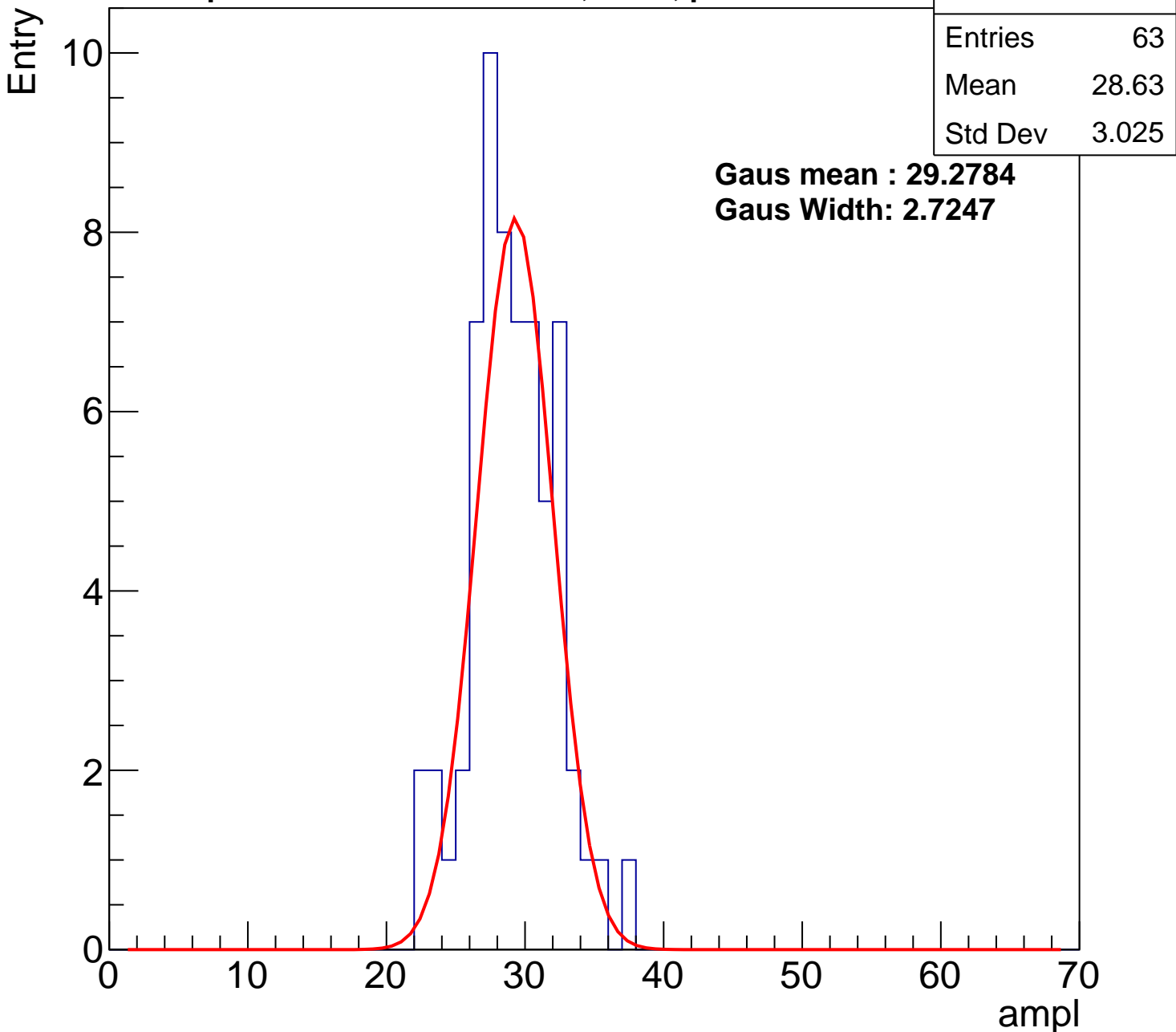
**Gaus Width: 2.7247**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch28, adc1

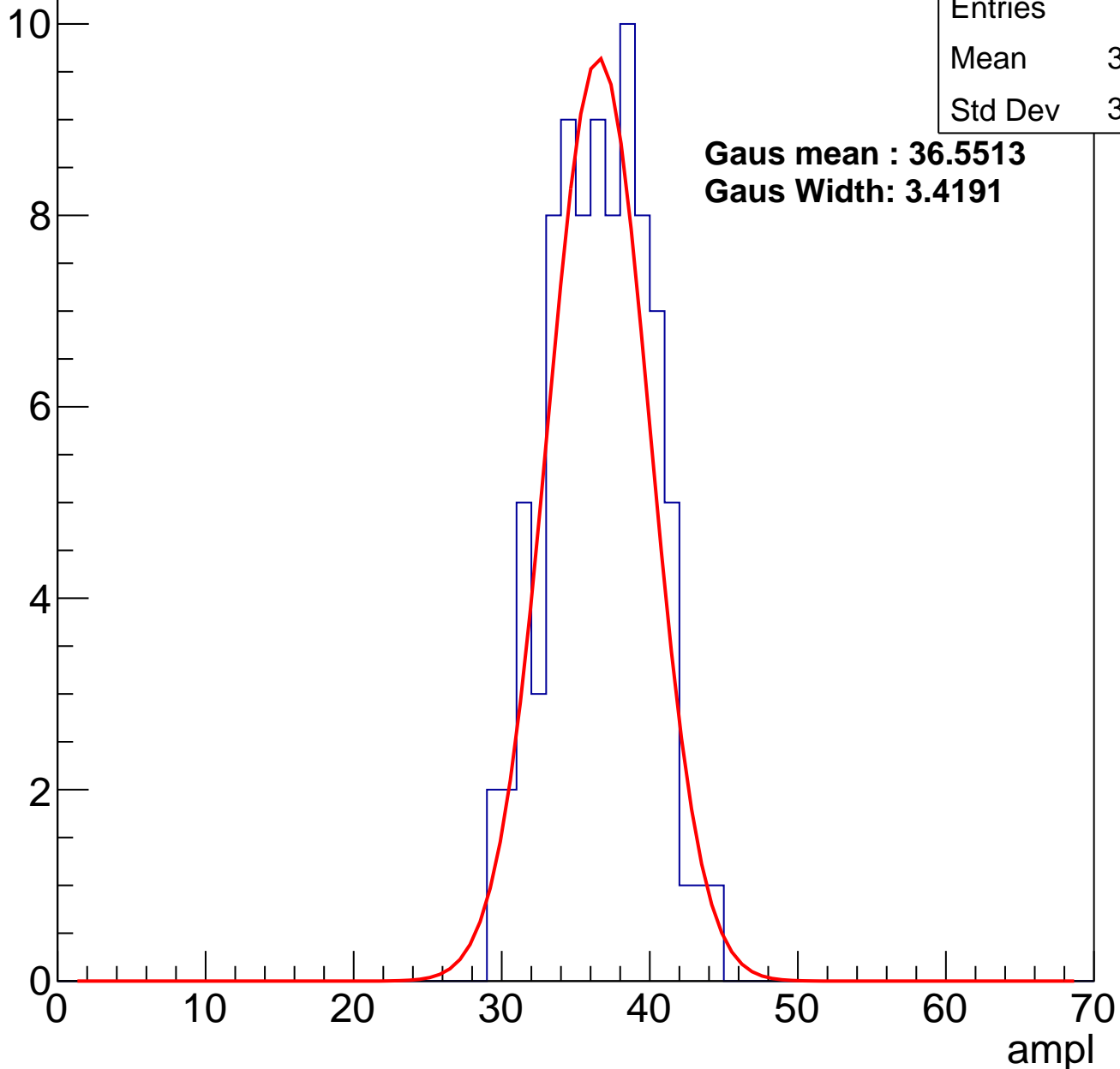
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	36.15
Std Dev	3.327

**Gaus mean : 36.5513**

**Gaus Width: 3.4191**

Entry



# B0L000S, U25-ch28, adc2

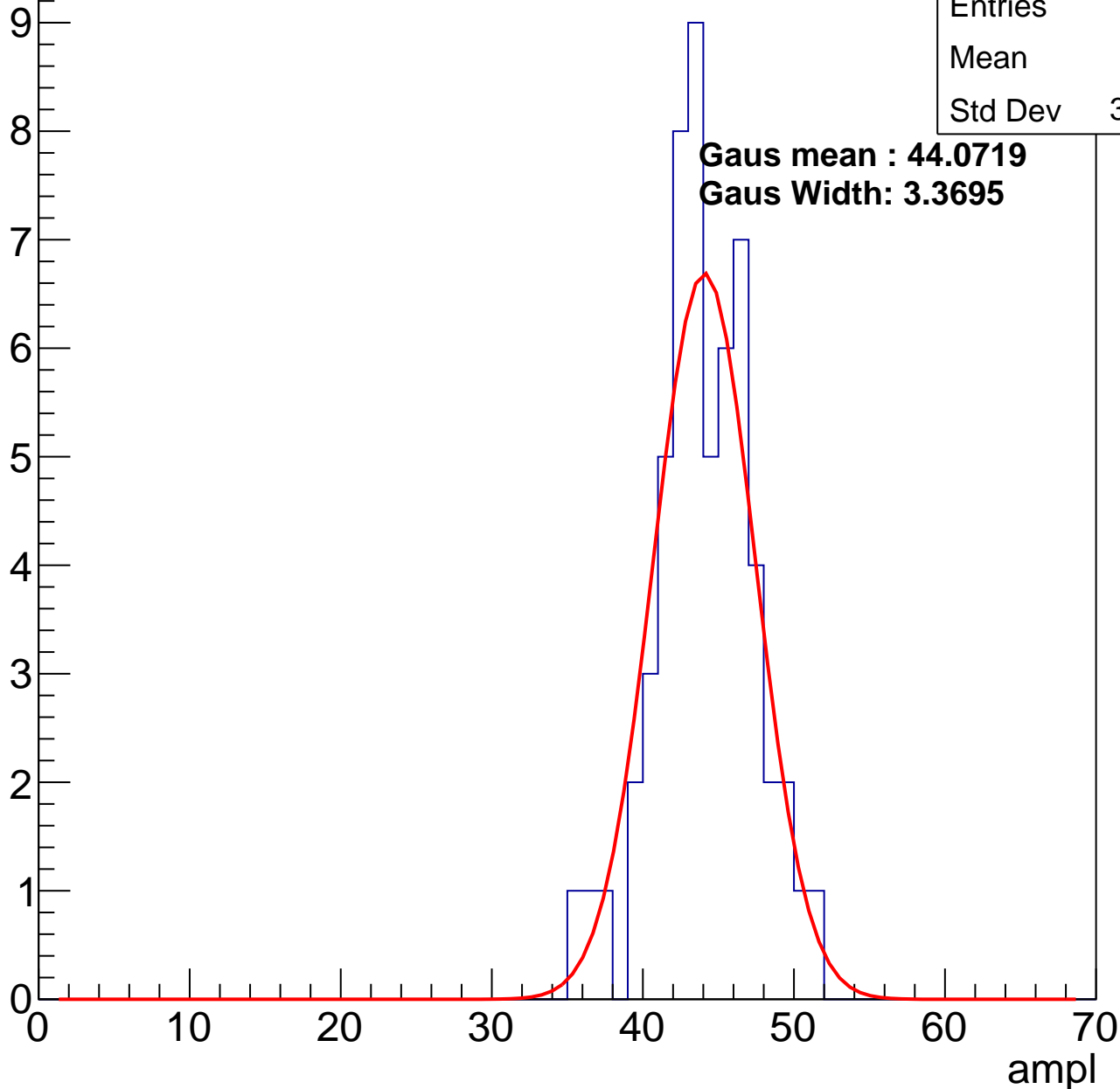
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	43.6
Std Dev	3.232

**Gaus mean : 44.0719**

**Gaus Width: 3.3695**

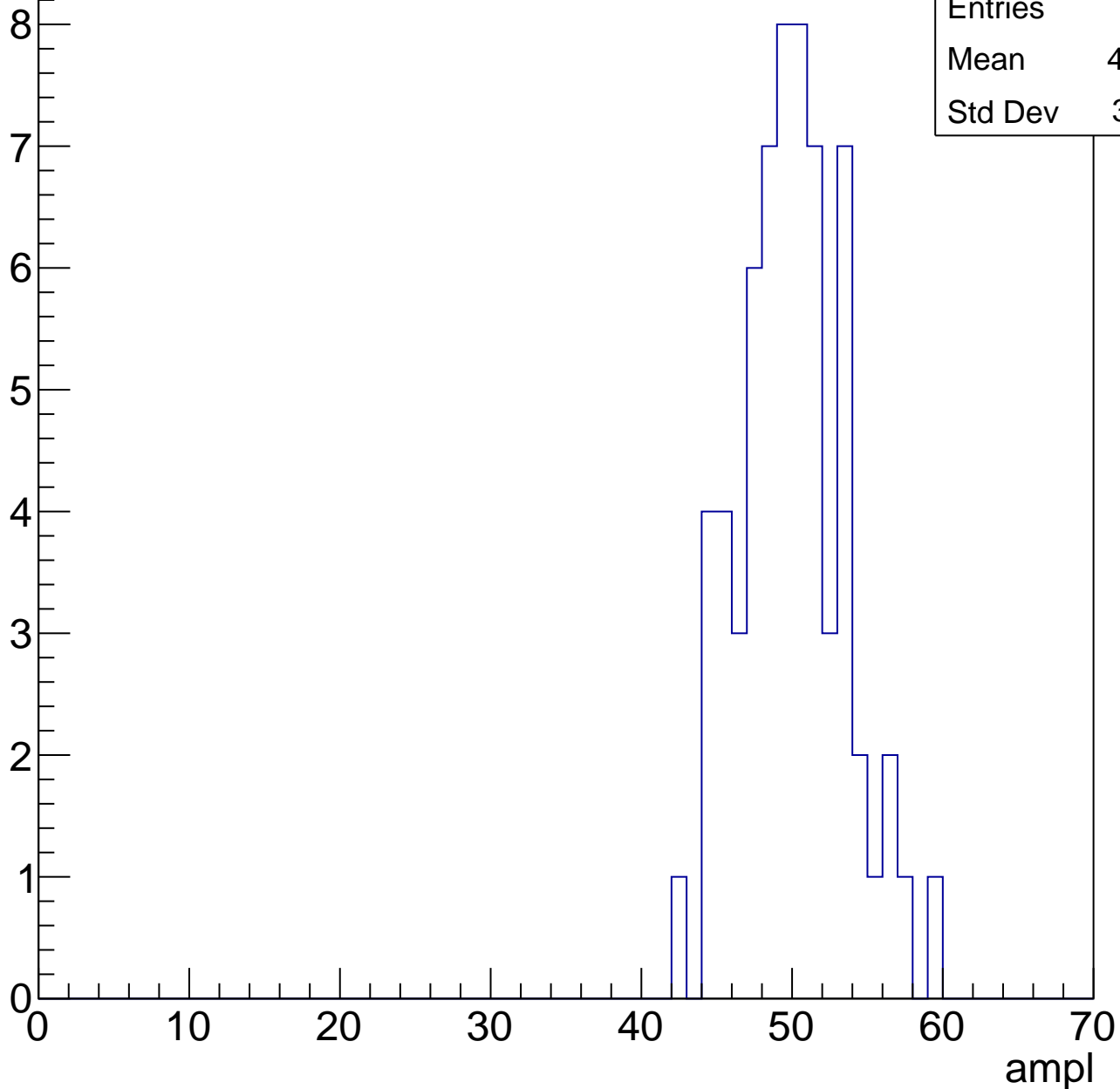


# B0L000S, U25-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	49.55
Std Dev	3.451

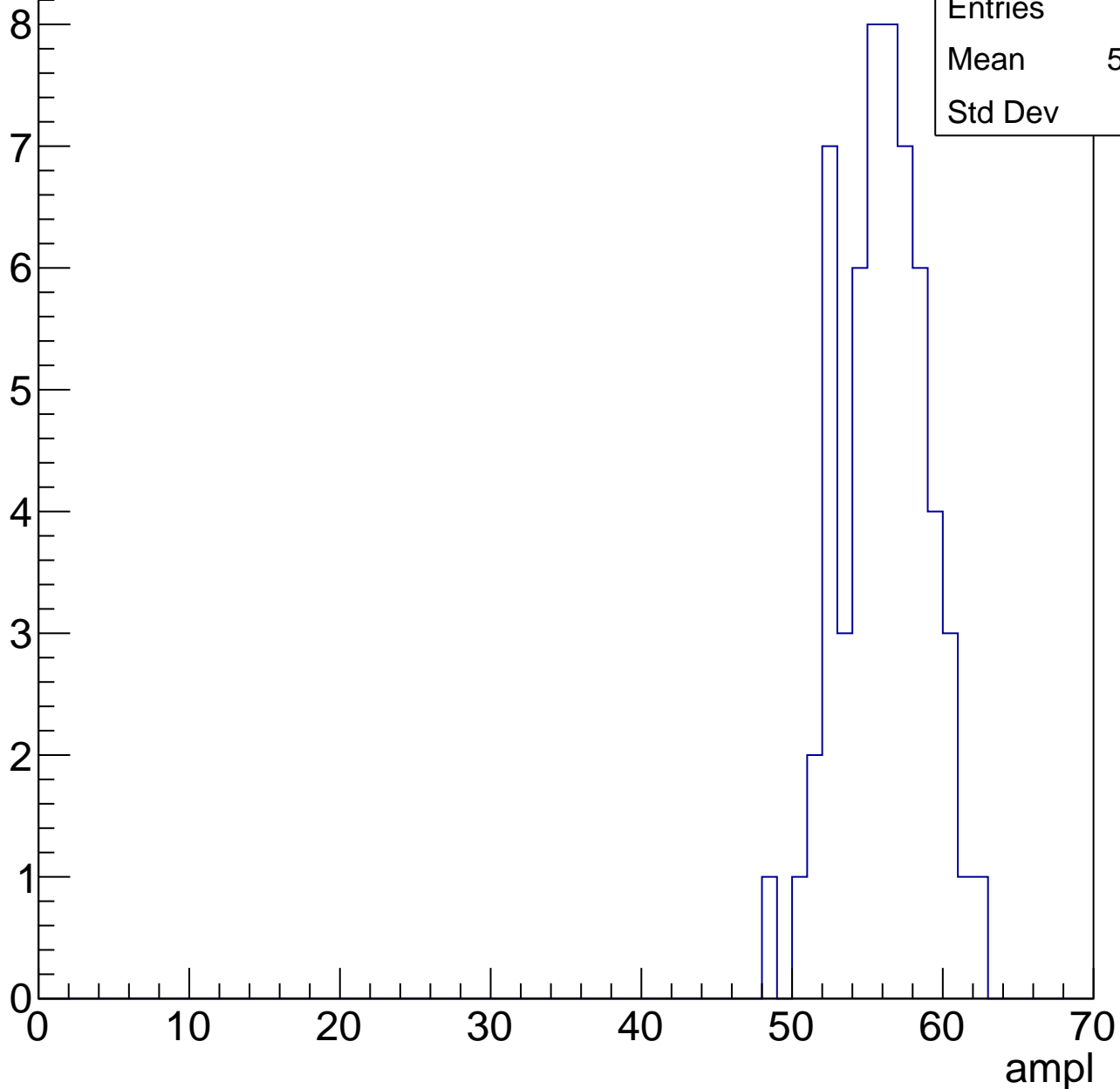


# B0L000S, U25-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.53
Std Dev	2.89

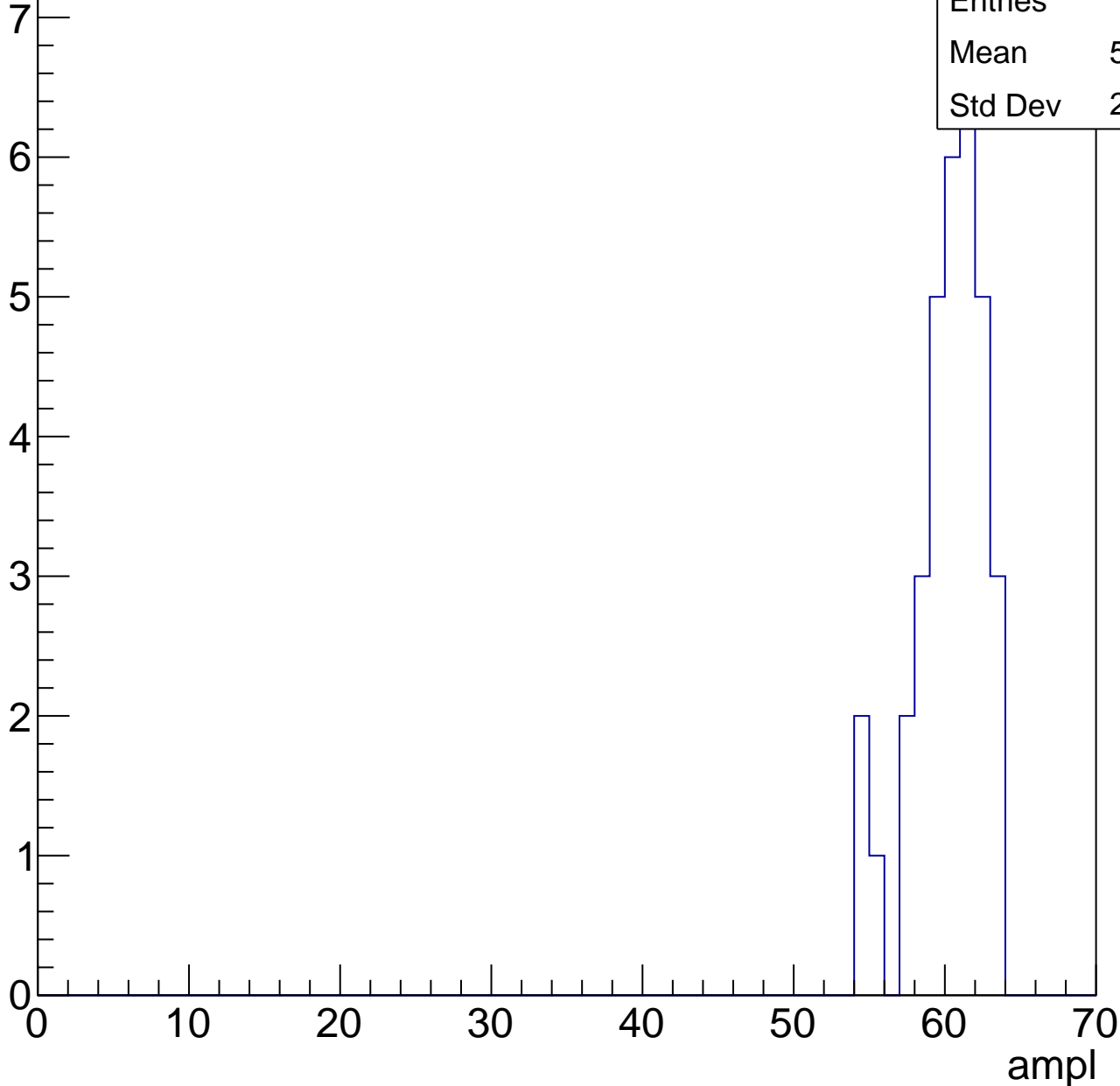


# B0L000S, U25-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.76
Std Dev	2.327

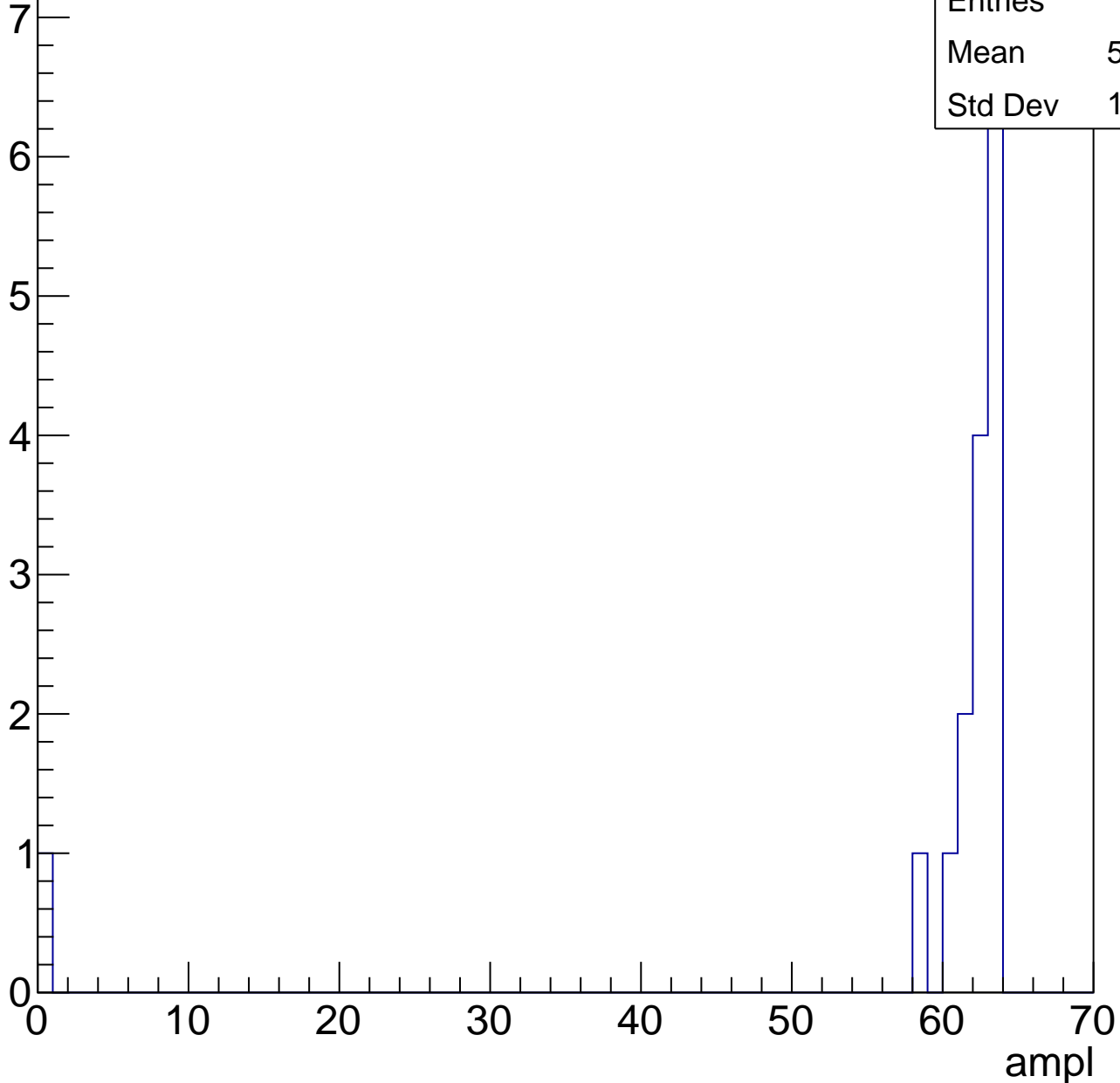


# B0L000S, U25-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	58.06
Std Dev	15.05





# B0L000S, U25-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch29, adc0

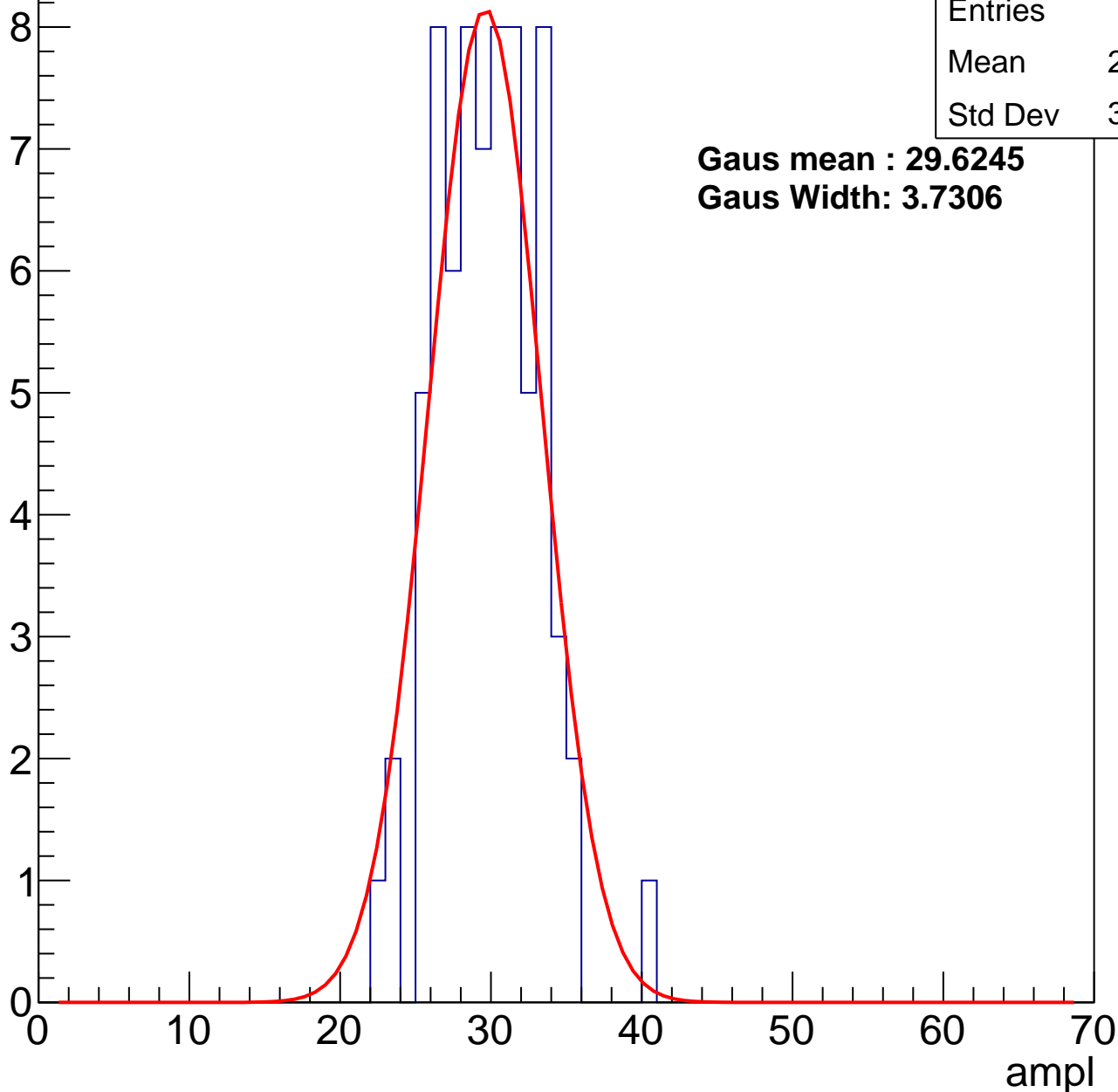
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	29.36
Std Dev	3.284

**Gaus mean : 29.6245**

**Gaus Width: 3.7306**



# B0L000S, U25-ch29, adc1

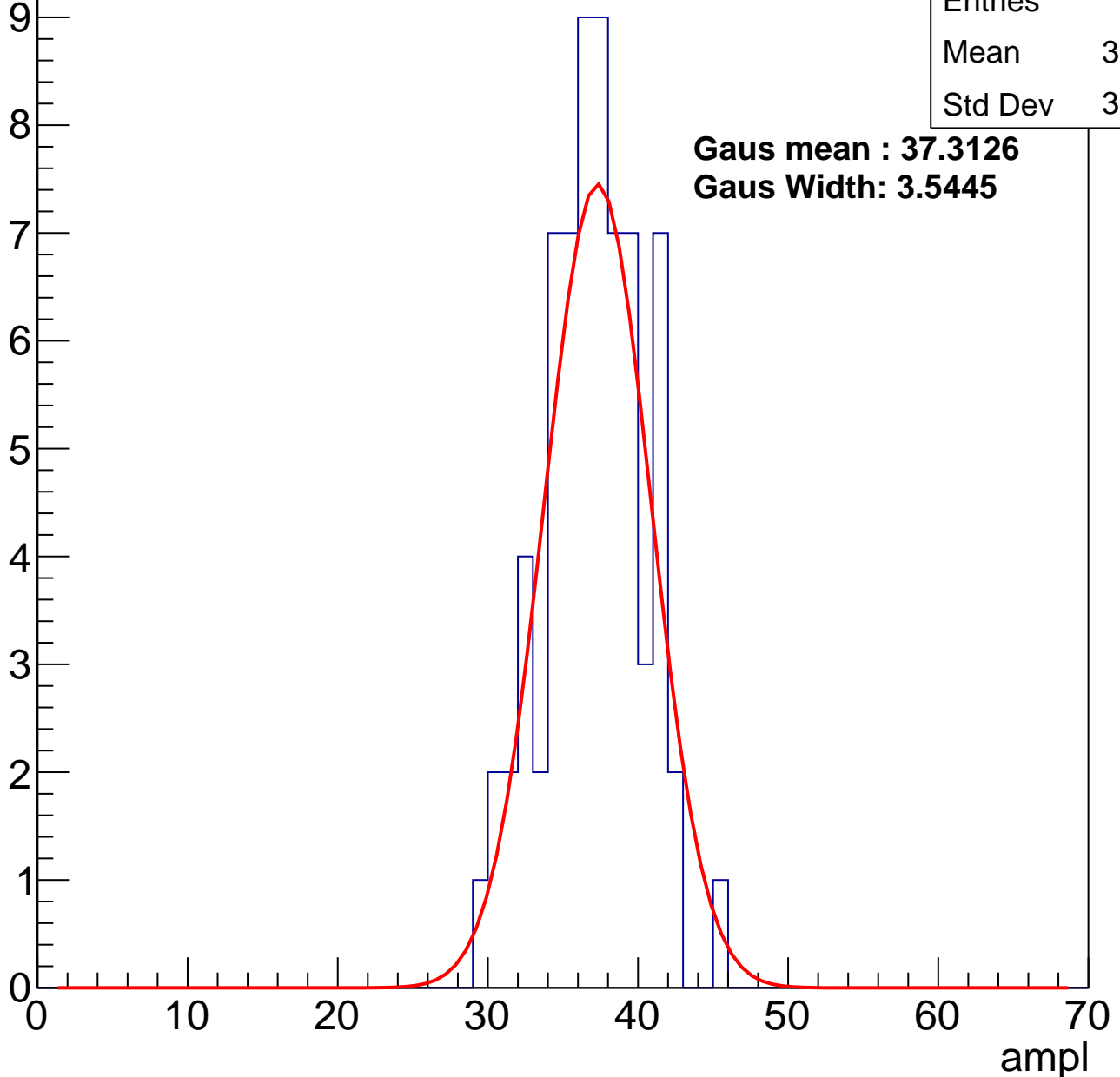
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.57
Std Dev	3.254

**Gaus mean : 37.3126**

**Gaus Width: 3.5445**



# B0L000S, U25-ch29, adc2

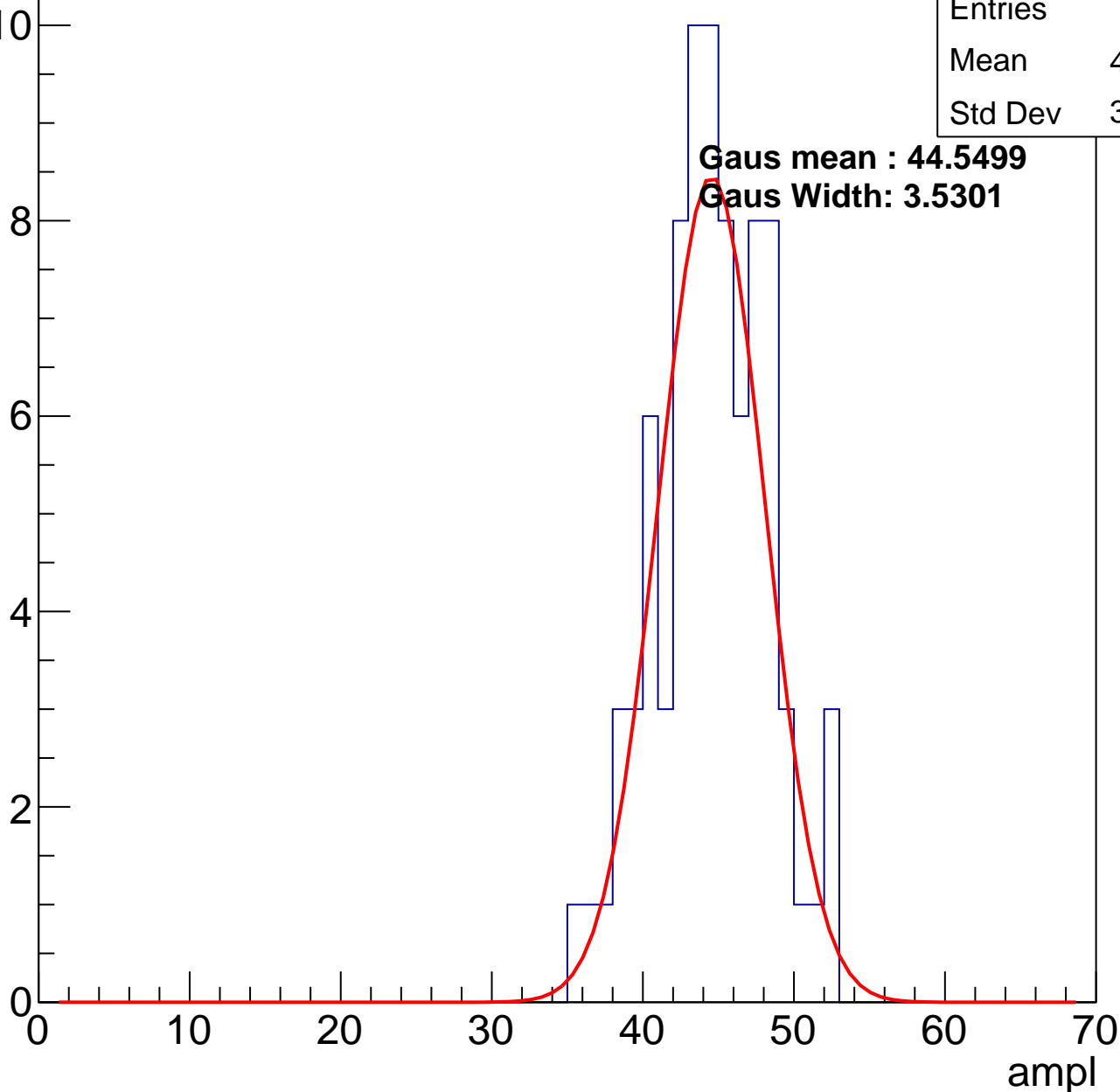
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	84
Mean	44.14
Std Dev	3.658

**Gaus mean : 44.5499**

**Gaus Width: 3.5301**

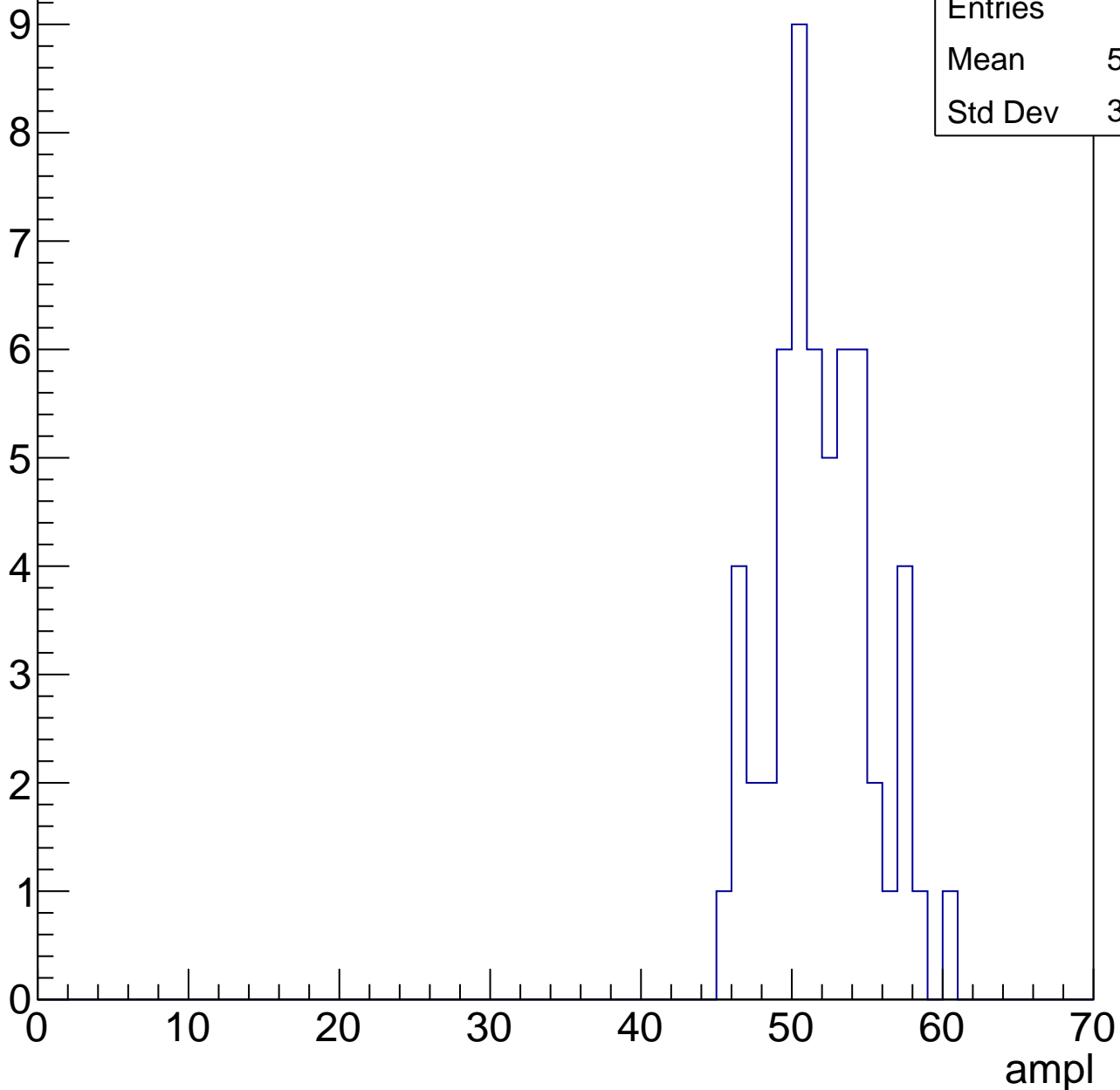


# B0L000S, U25-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

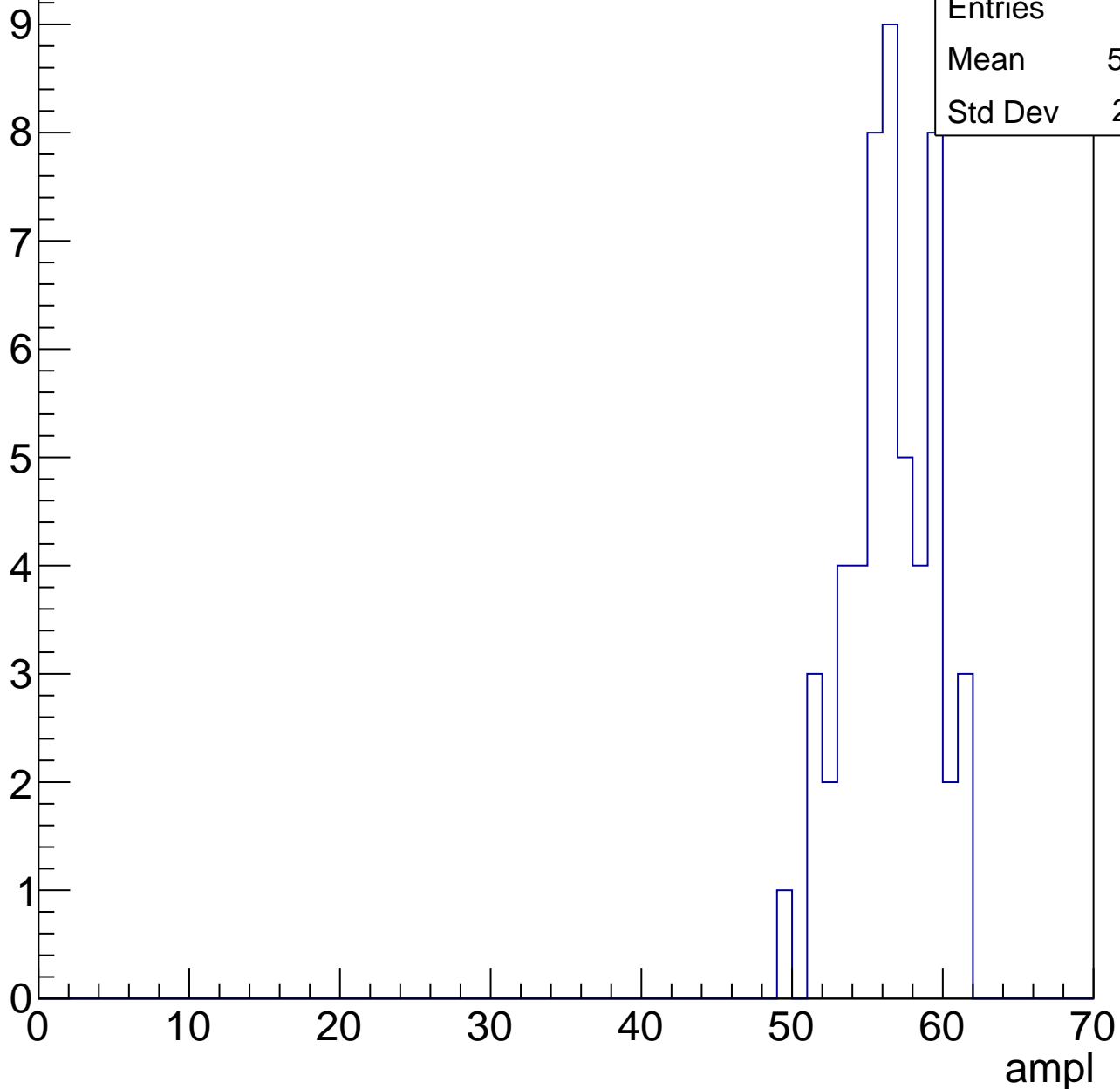
Entries	56
Mean	51.48
Std Dev	3.327



# B0L000S, U25-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	53
Mean	56.04
Std Dev	2.801

# B0L000S, U25-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries

34

Mean

58.88

Std Dev

10.4

ampl

0

10

20

30

40

50

60

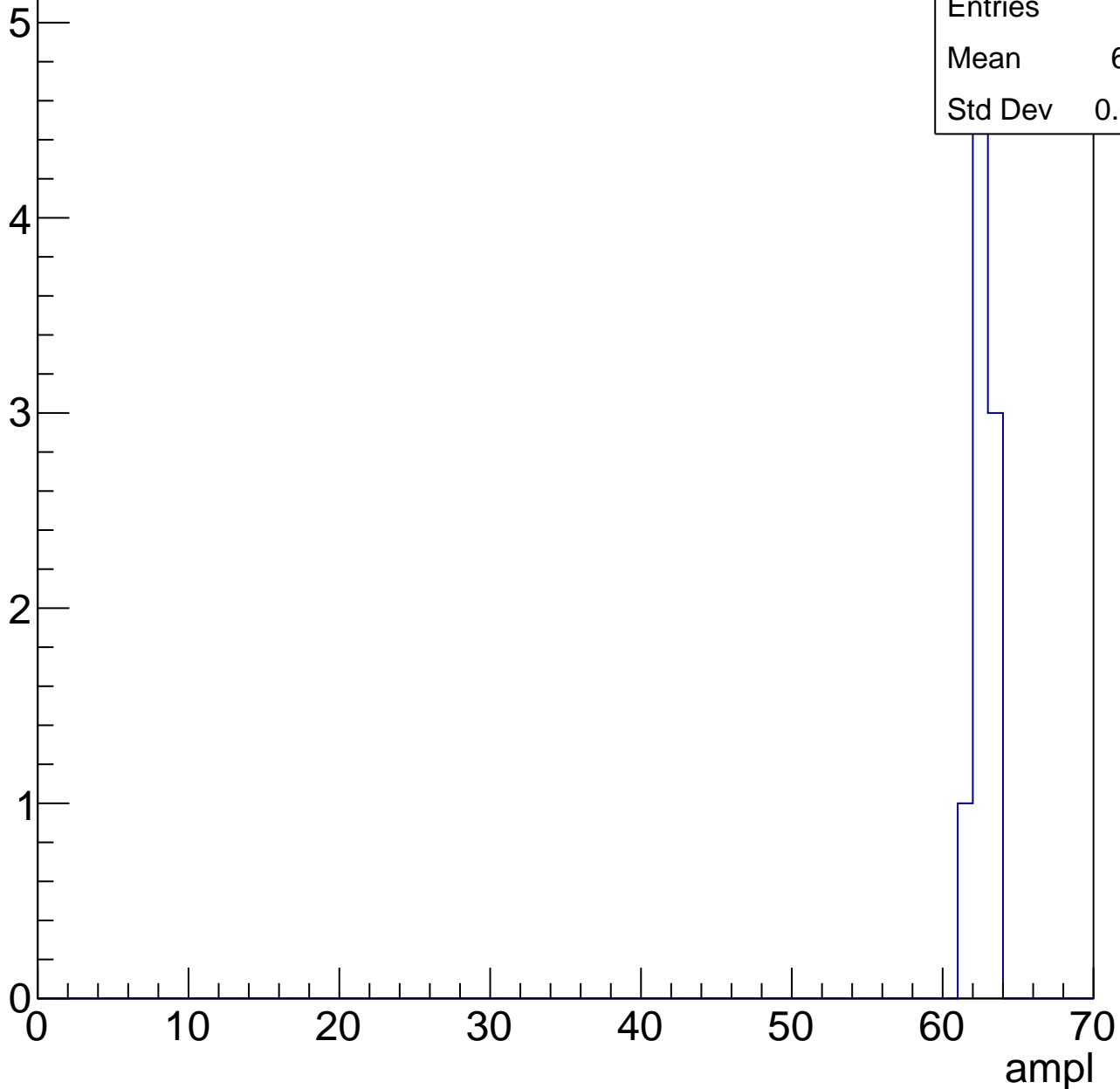
70

# B0L000S, U25-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.22
Std Dev	0.6285





# B0L000S, U25-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	79
Mean	31.27
Std Dev	3.344

**Gaus mean : 31.8556**

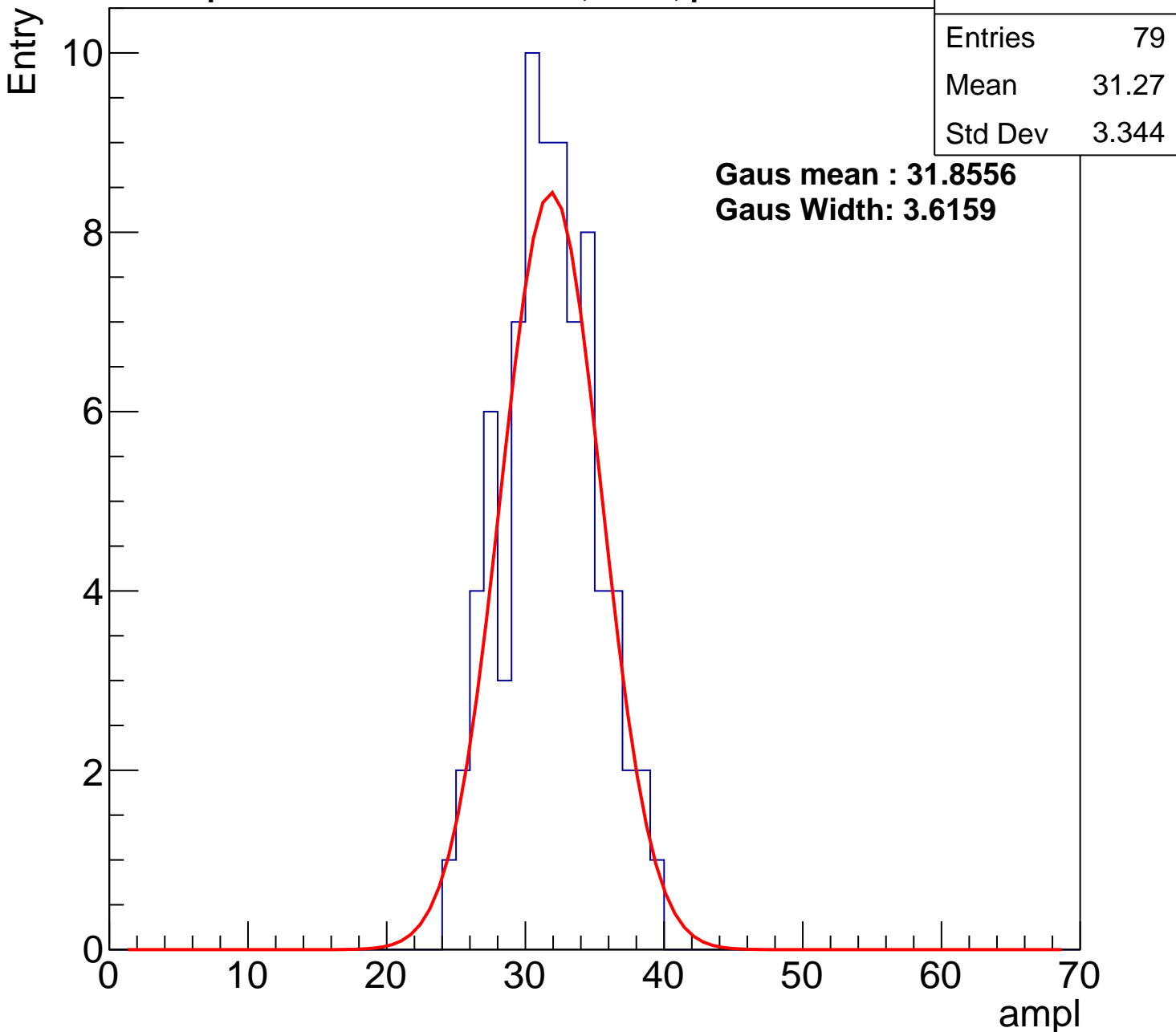
**Gaus Width: 3.6159**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch30, adc1

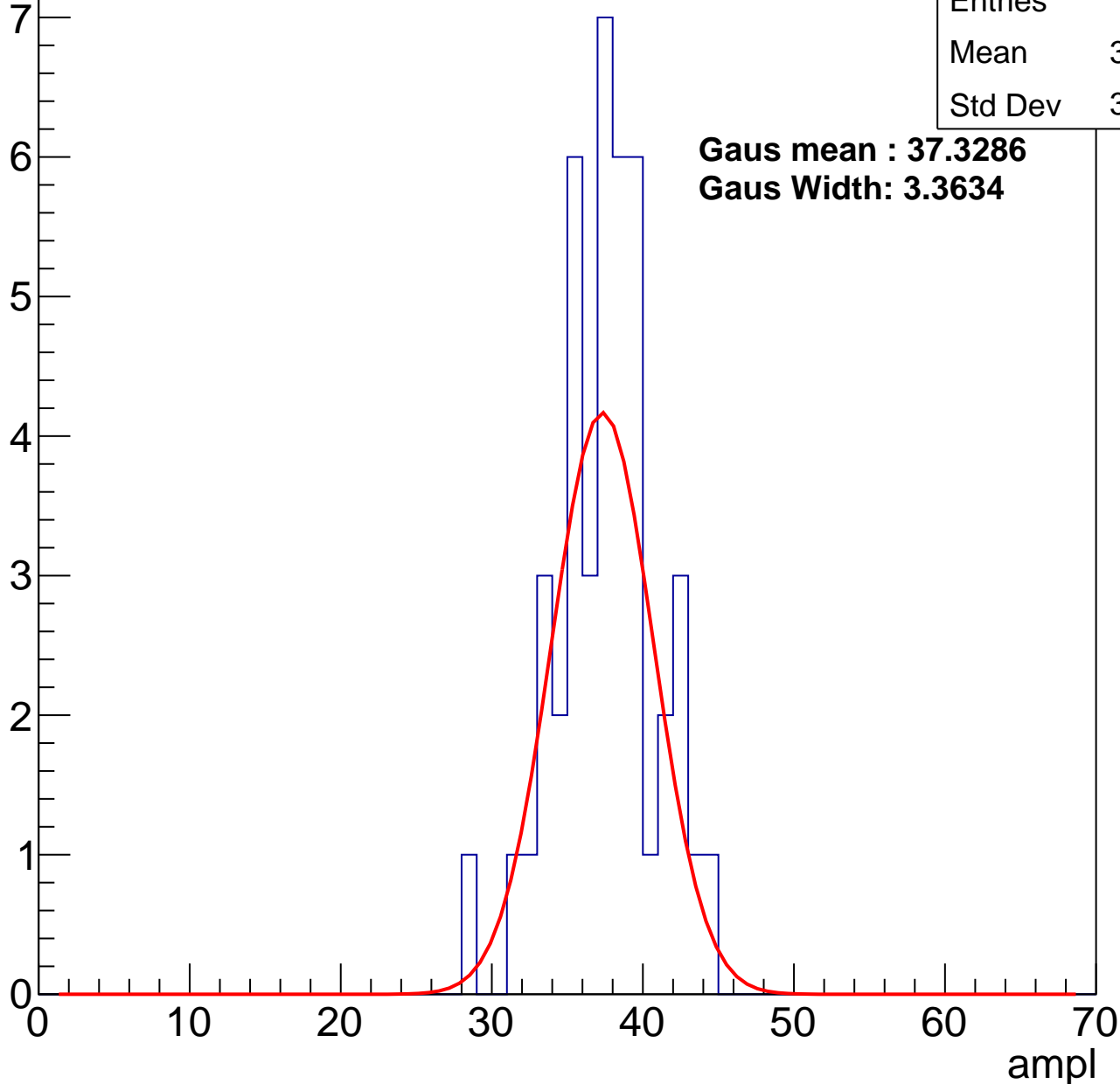
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	37.09
Std Dev	3.253

**Gaus mean : 37.3286**

**Gaus Width: 3.3634**



# B0L000S, U25-ch30, adc2

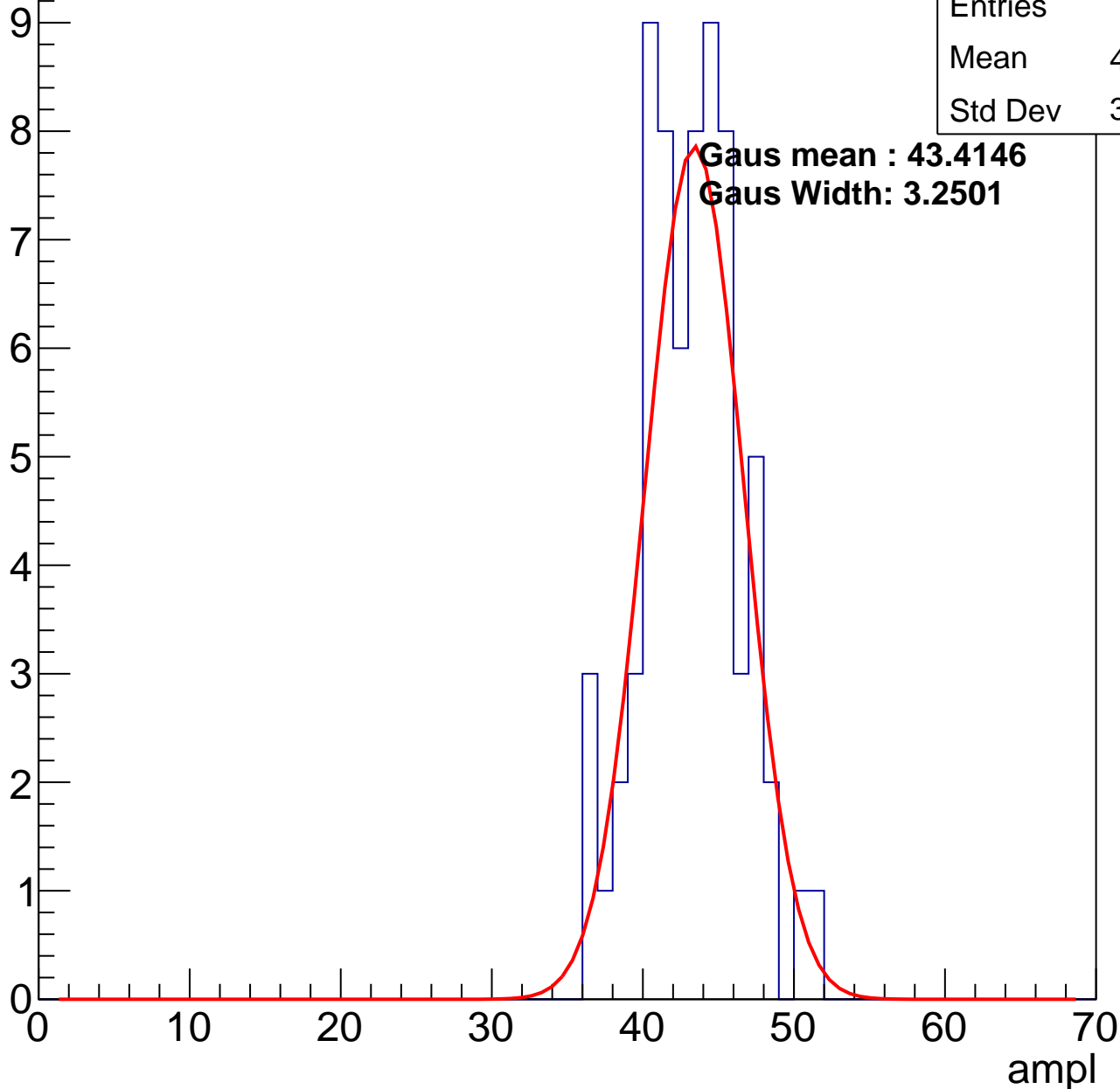
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	42.72
Std Dev	3.198

**Gaus mean : 43.4146**

**Gaus Width: 3.2501**



# B0L000S, U25-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

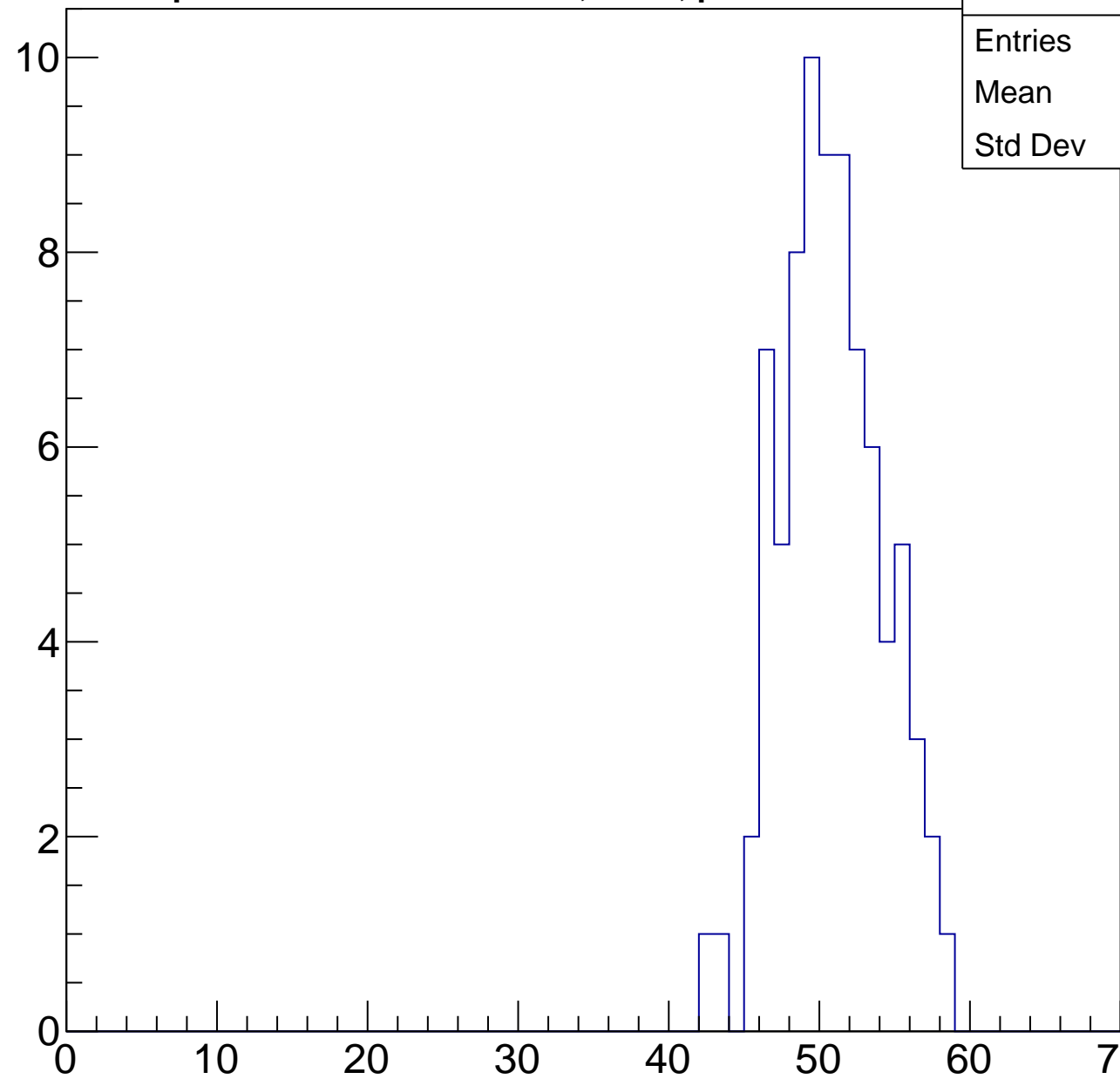
Entries	80
Mean	50.35
Std Dev	3.358

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

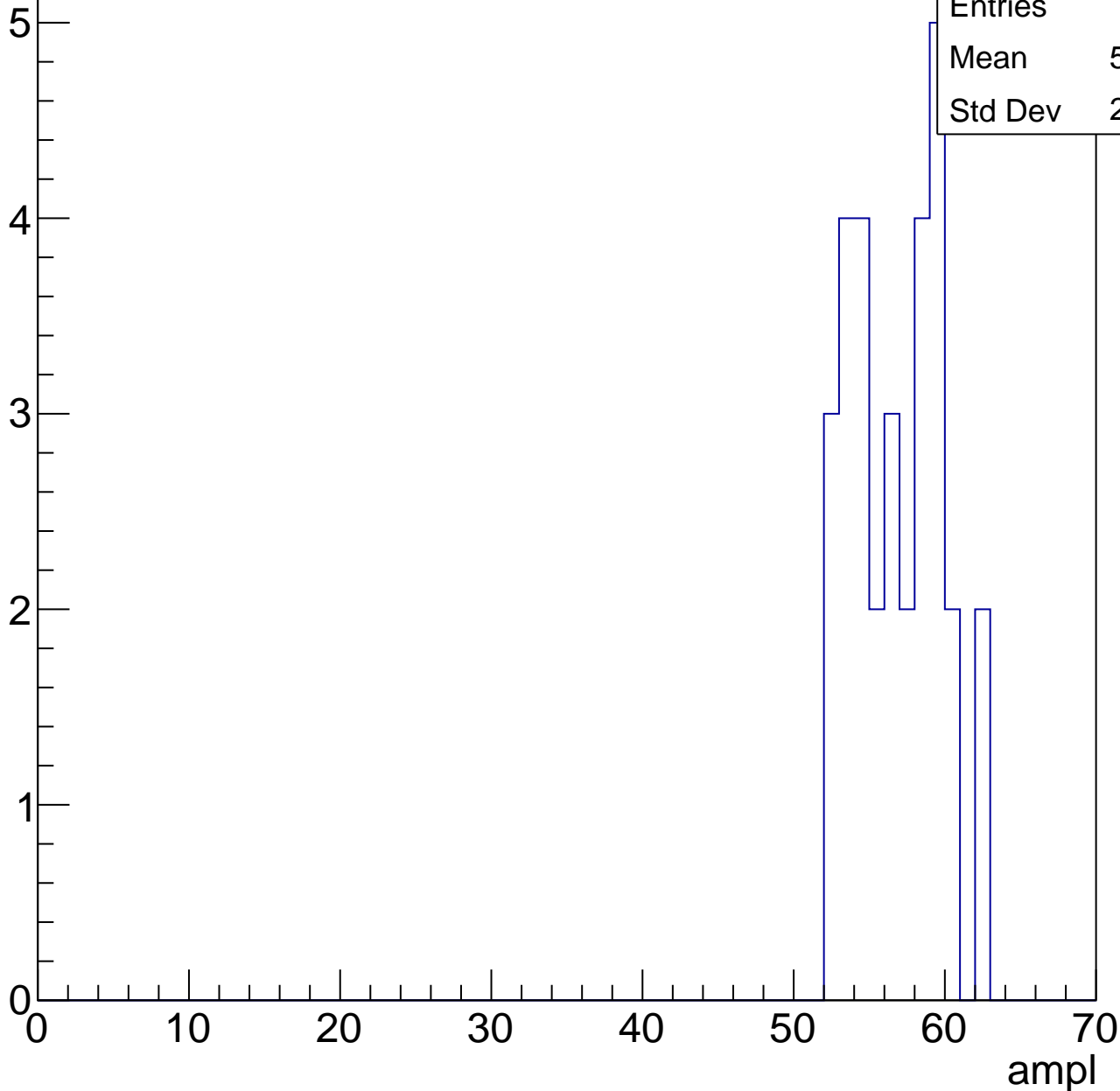


# B0L000S, U25-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	31
Mean	56.35
Std Dev	2.924

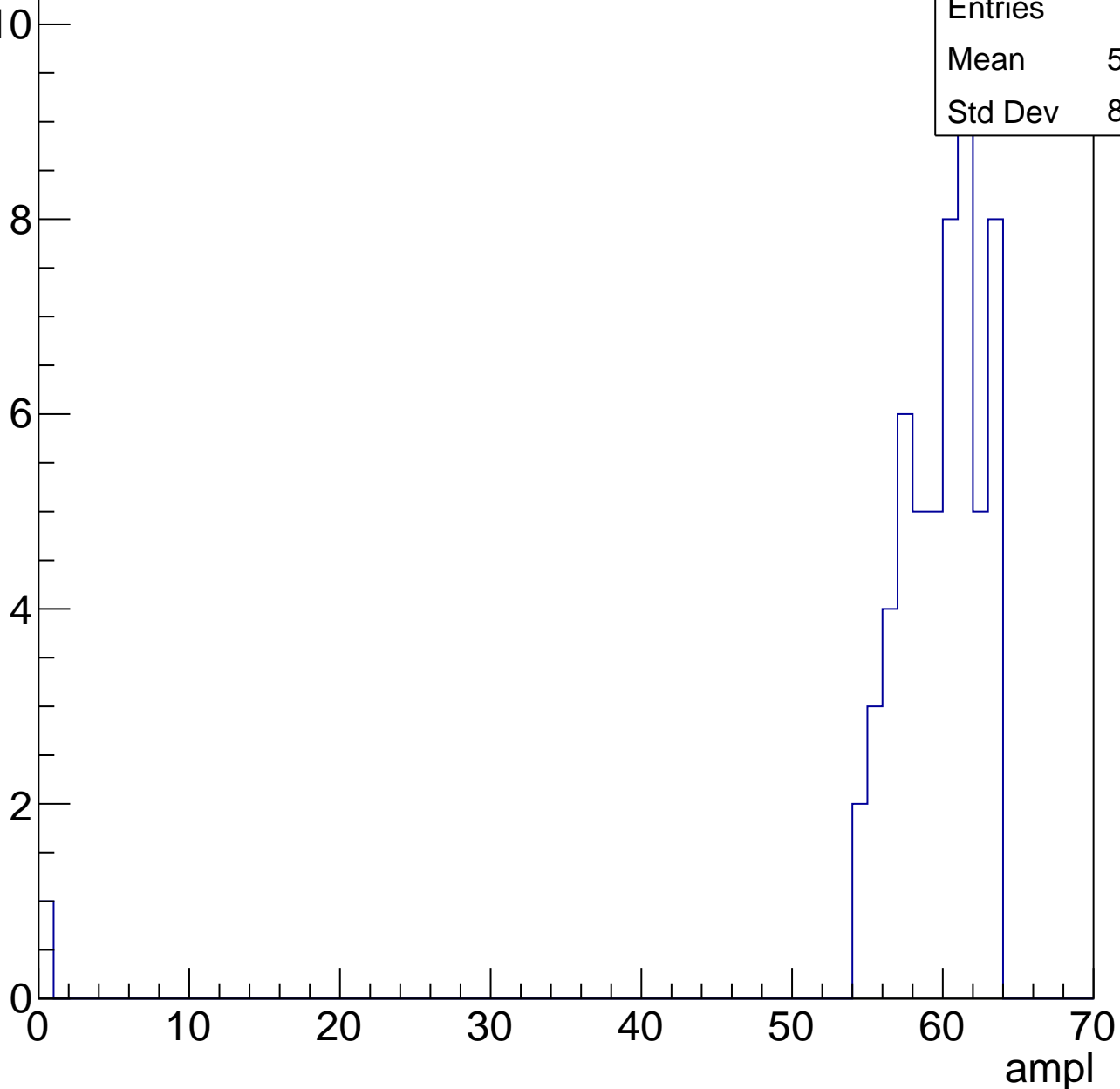


# B0L000S, U25-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

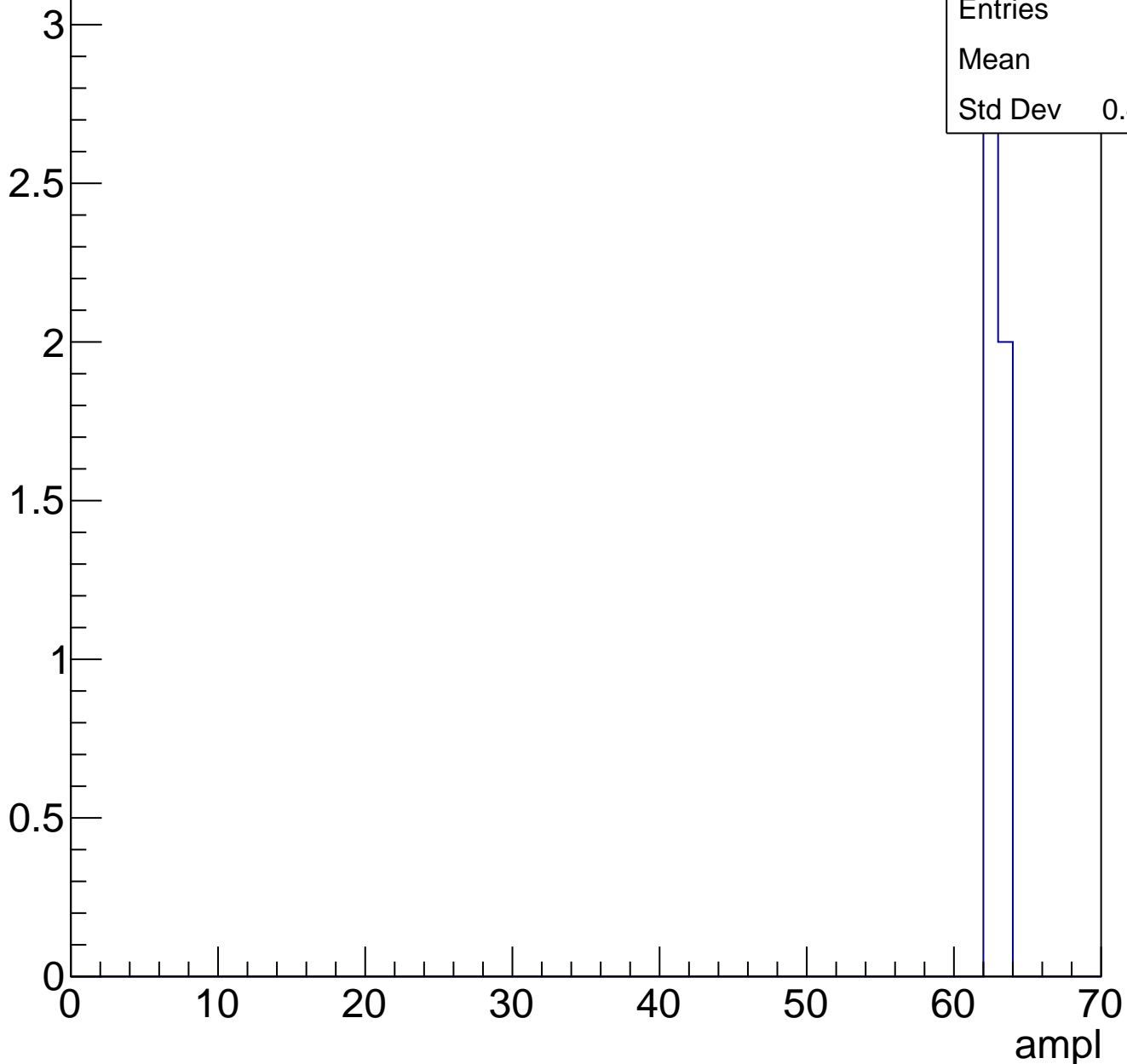
Entries	57
Mean	58.39
Std Dev	8.212



# B0L000S, U25-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch31, adc0

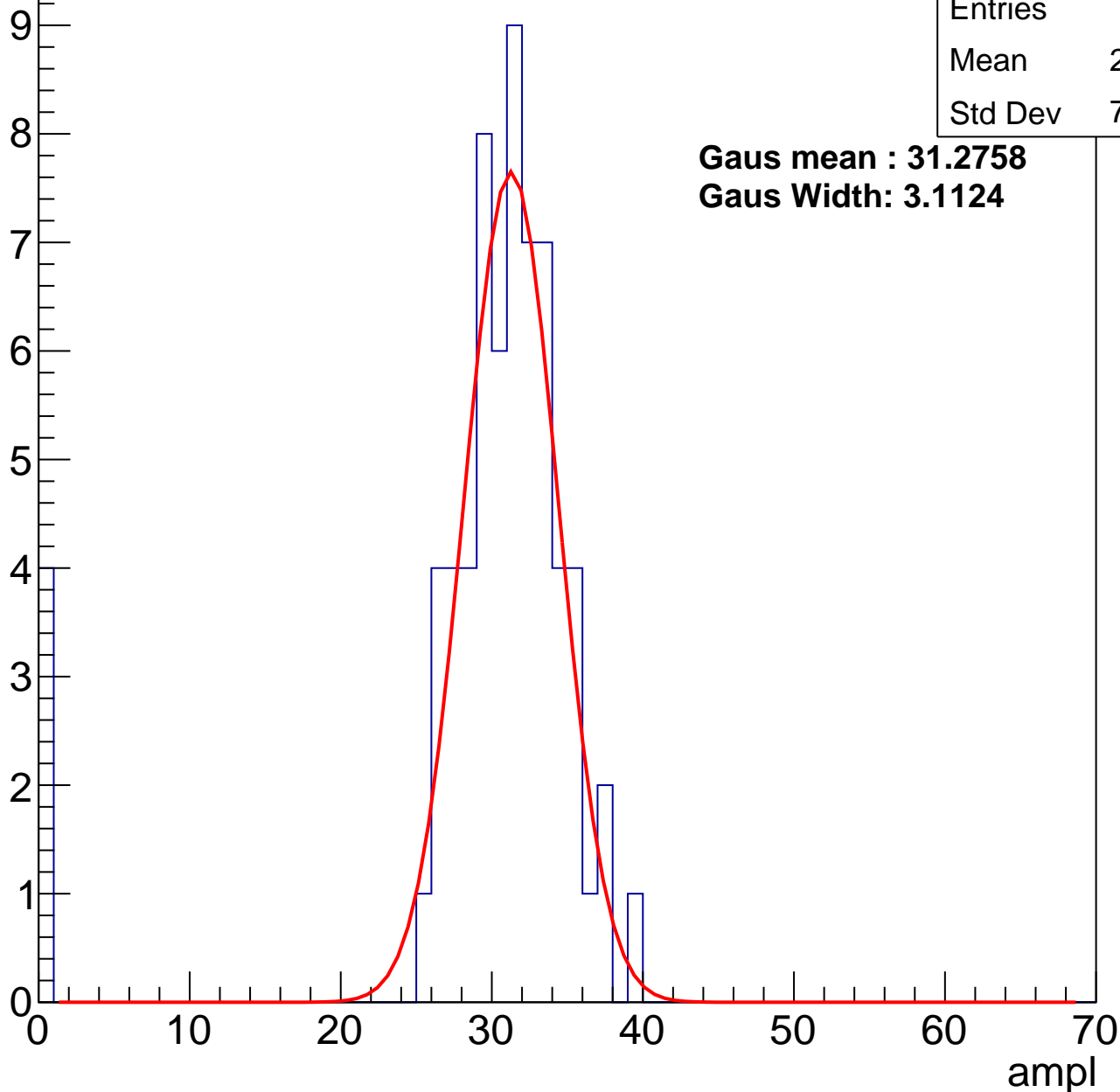
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.09
Std Dev	7.952

**Gaus mean : 31.2758**

**Gaus Width: 3.1124**



# B0L000S, U25-ch31, adc1

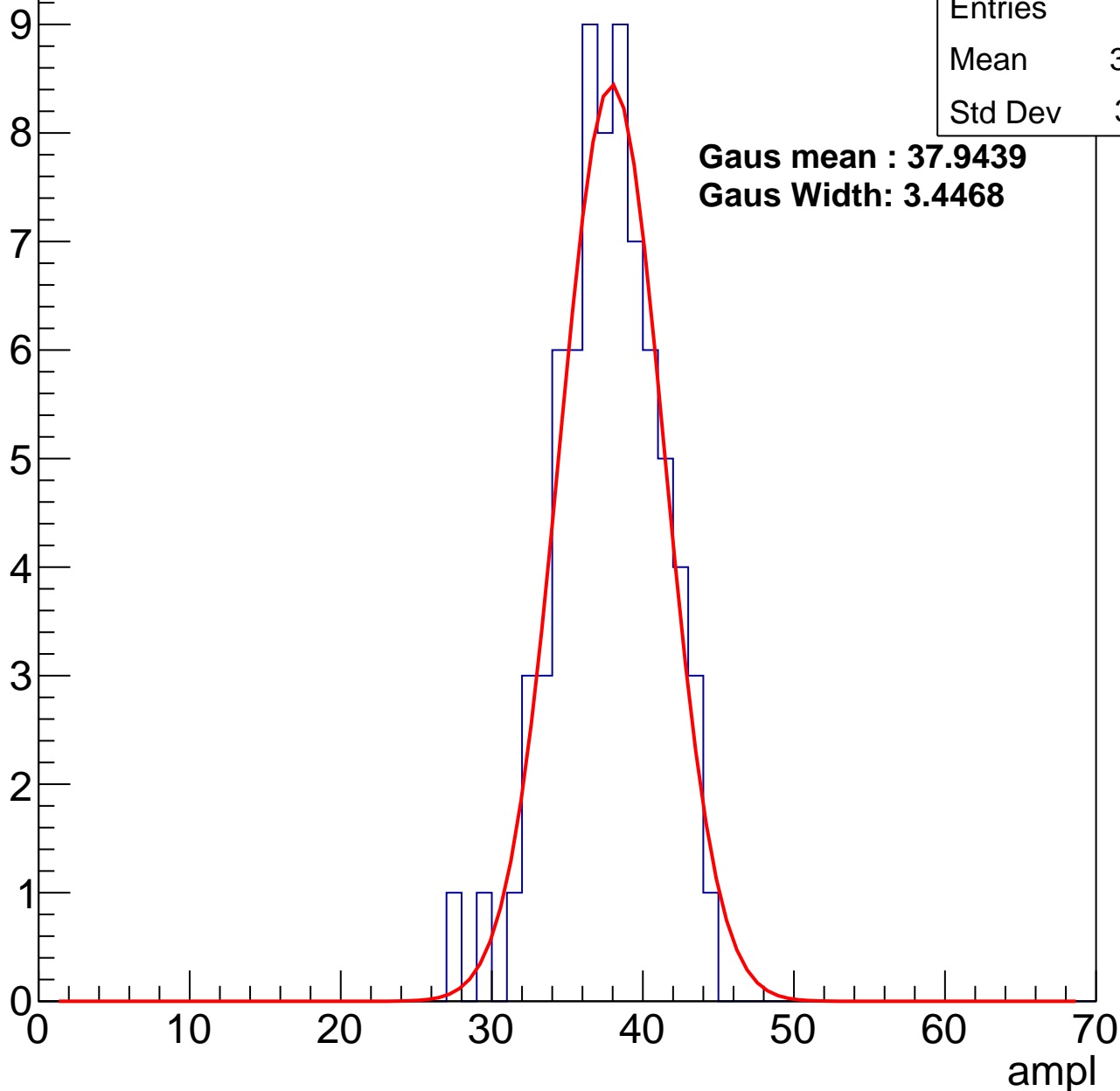
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	37.22
Std Dev	3.381

**Gaus mean : 37.9439**

**Gaus Width: 3.4468**



# B0L000S, U25-ch31, adc2

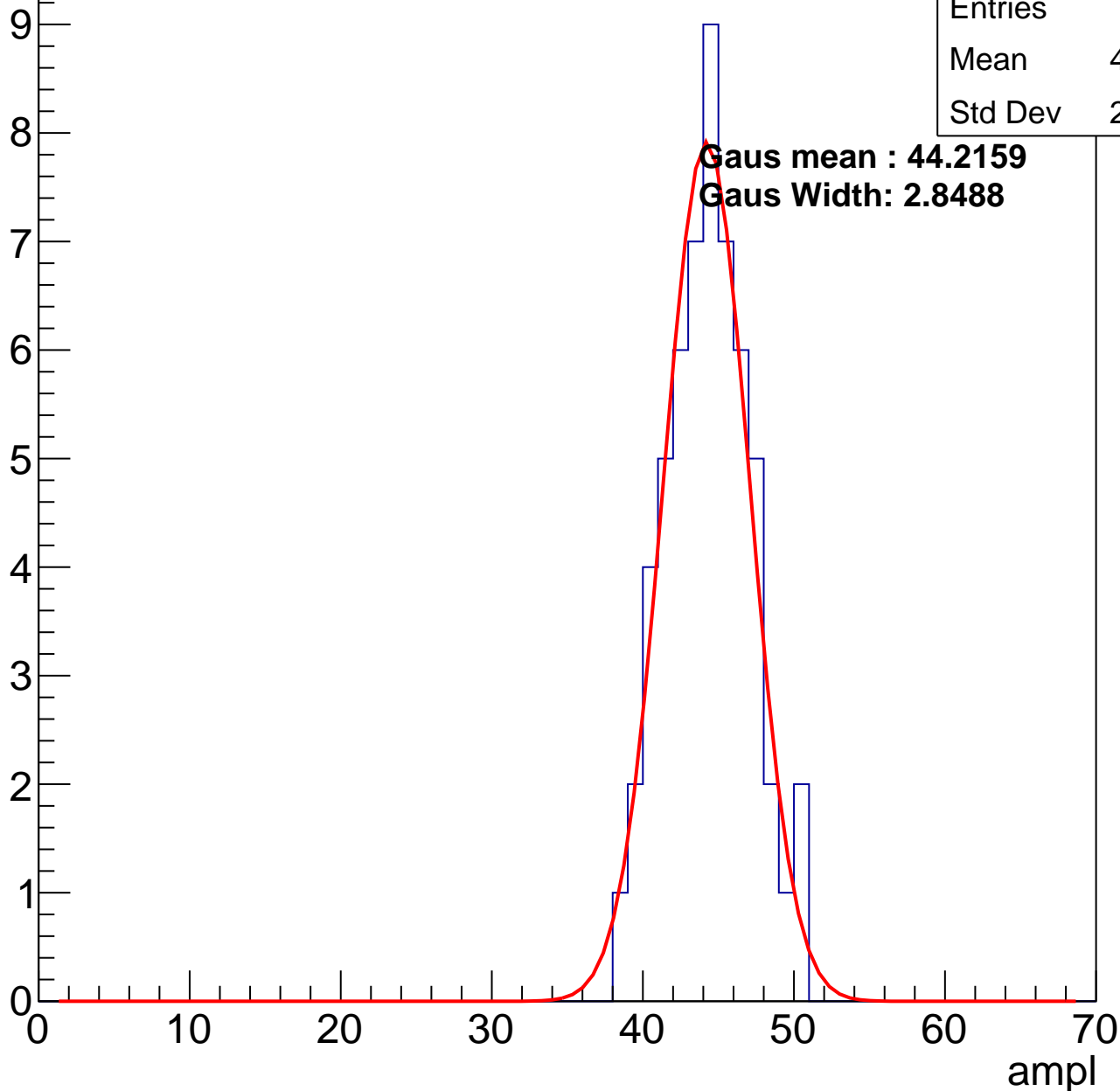
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	43.88
Std Dev	2.747

**Gaus mean : 44.2159**

**Gaus Width: 2.8488**



# B0L000S, U25-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	50.79
Std Dev	3.126

Entry

10

8

6

4

2

0

0

10

20

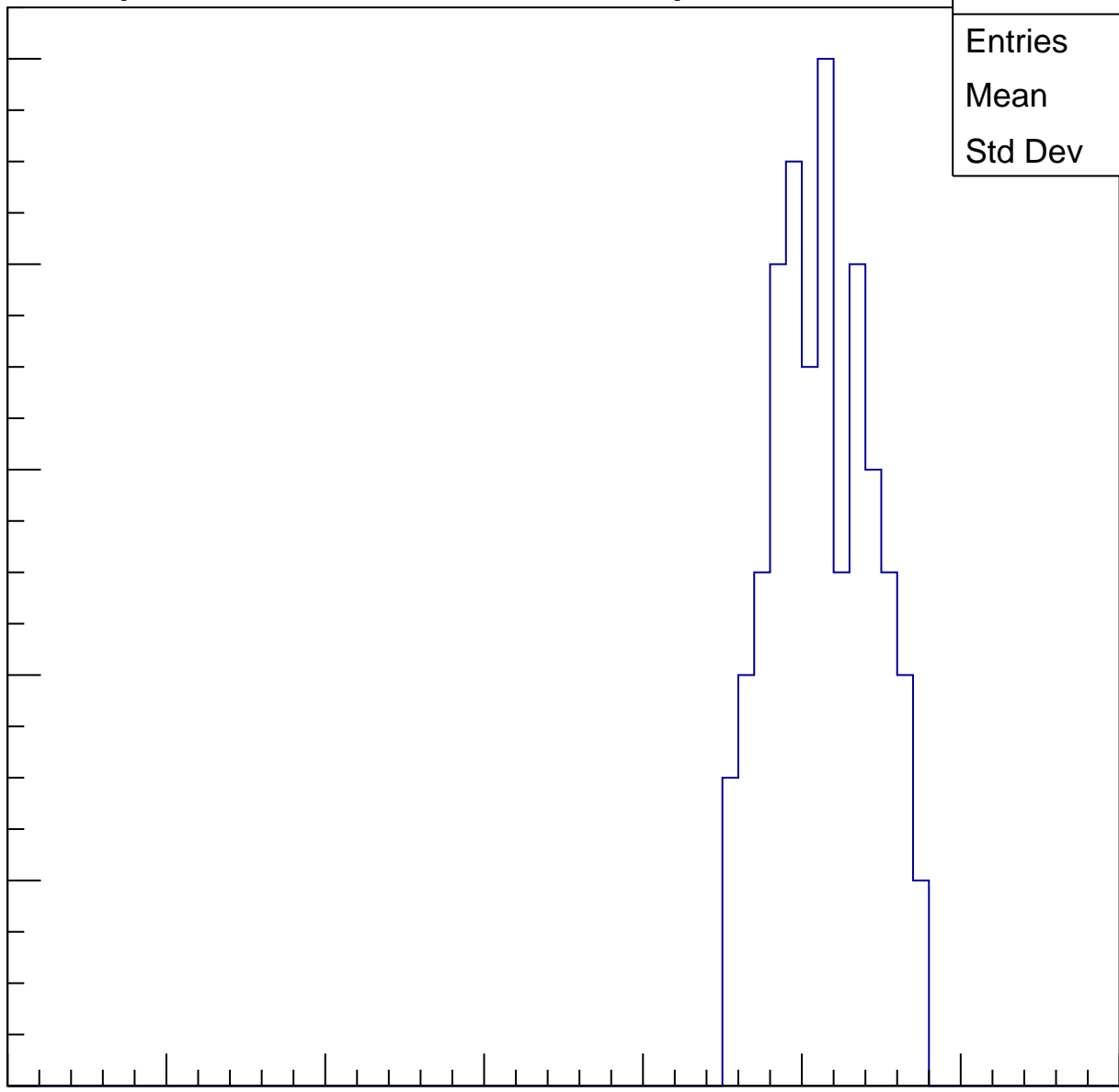
30

40

50

60

ampl

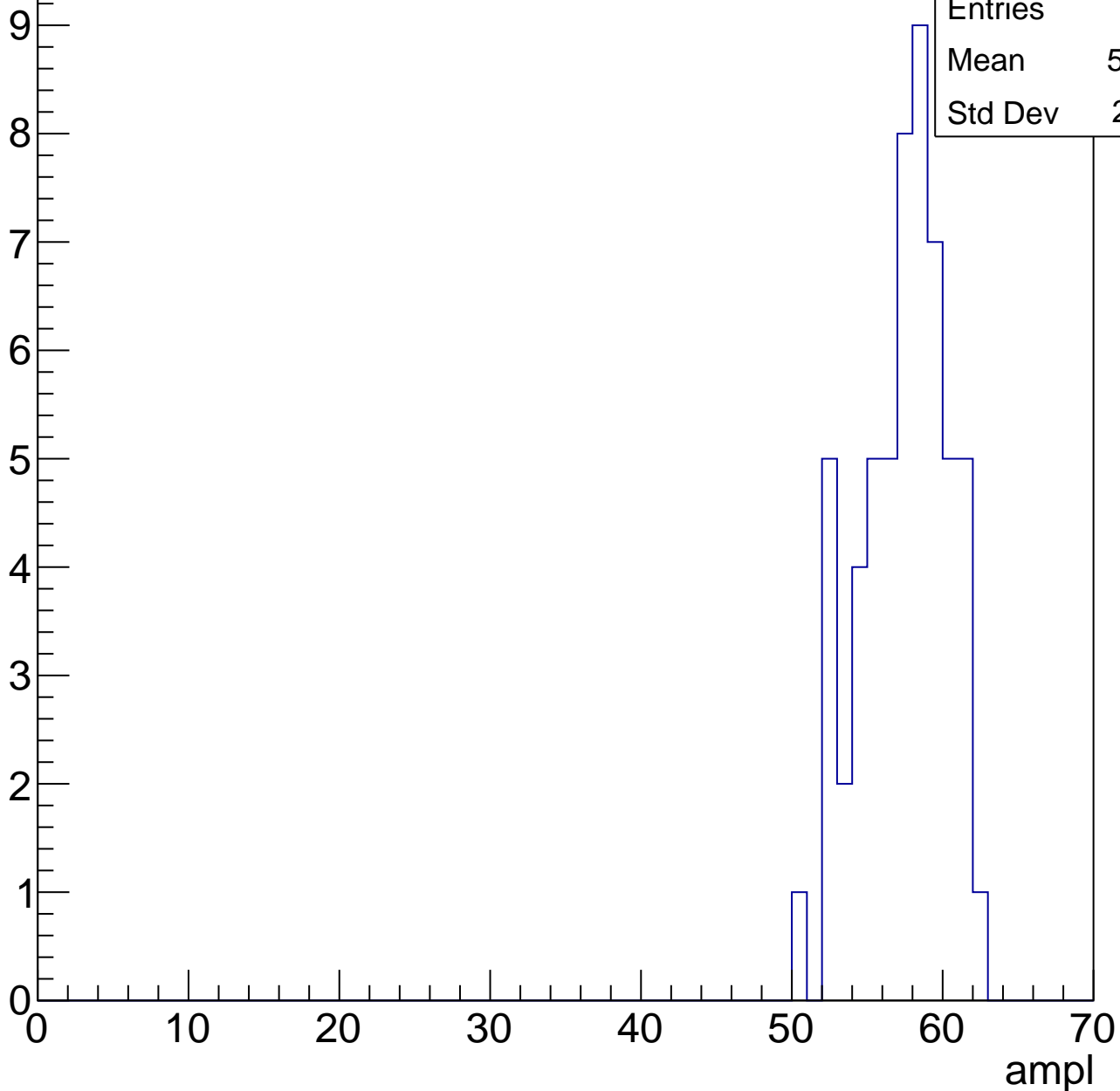


# B0L000S, U25-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	56.93
Std Dev	2.821

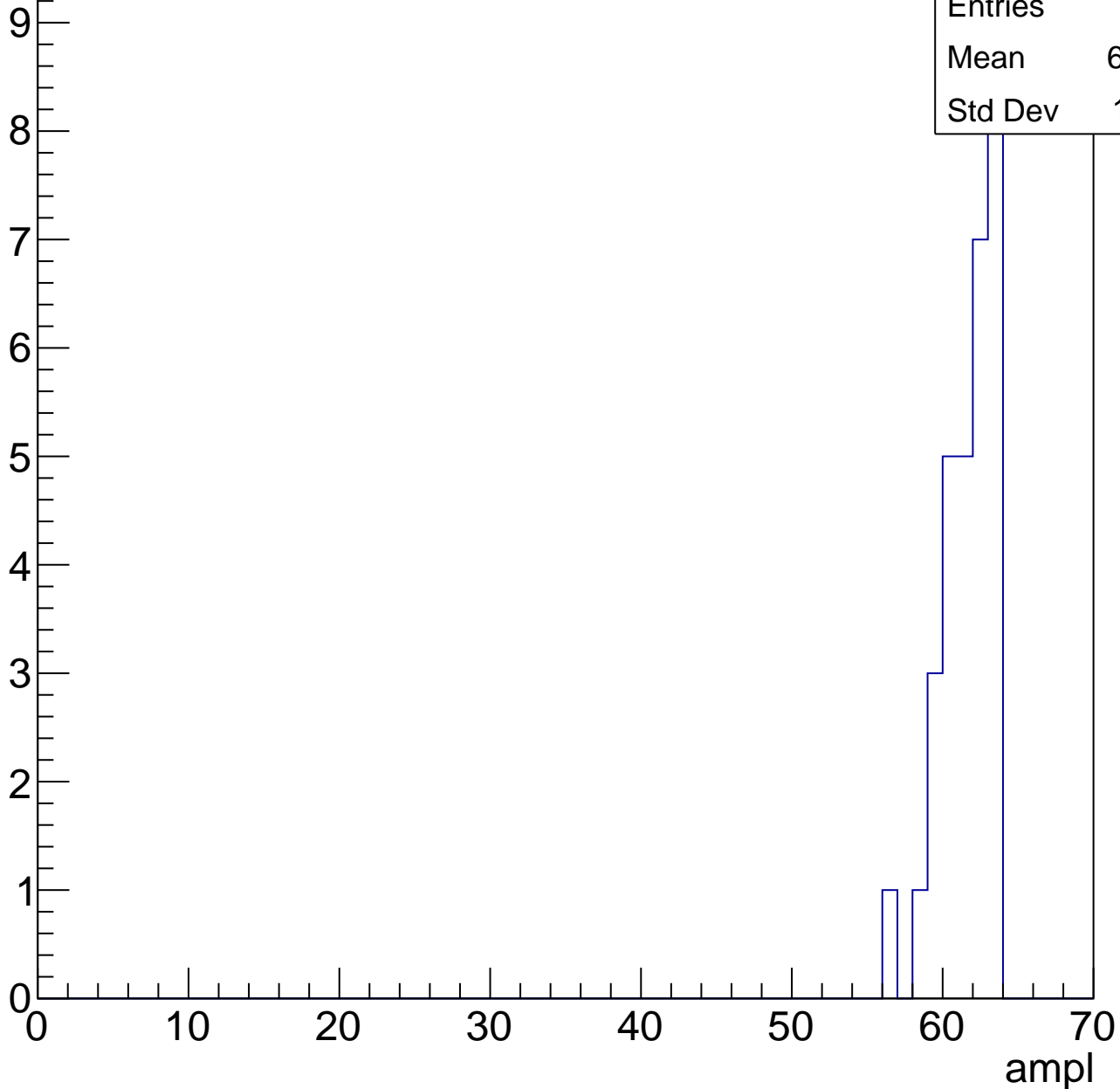


# B0L000S, U25-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

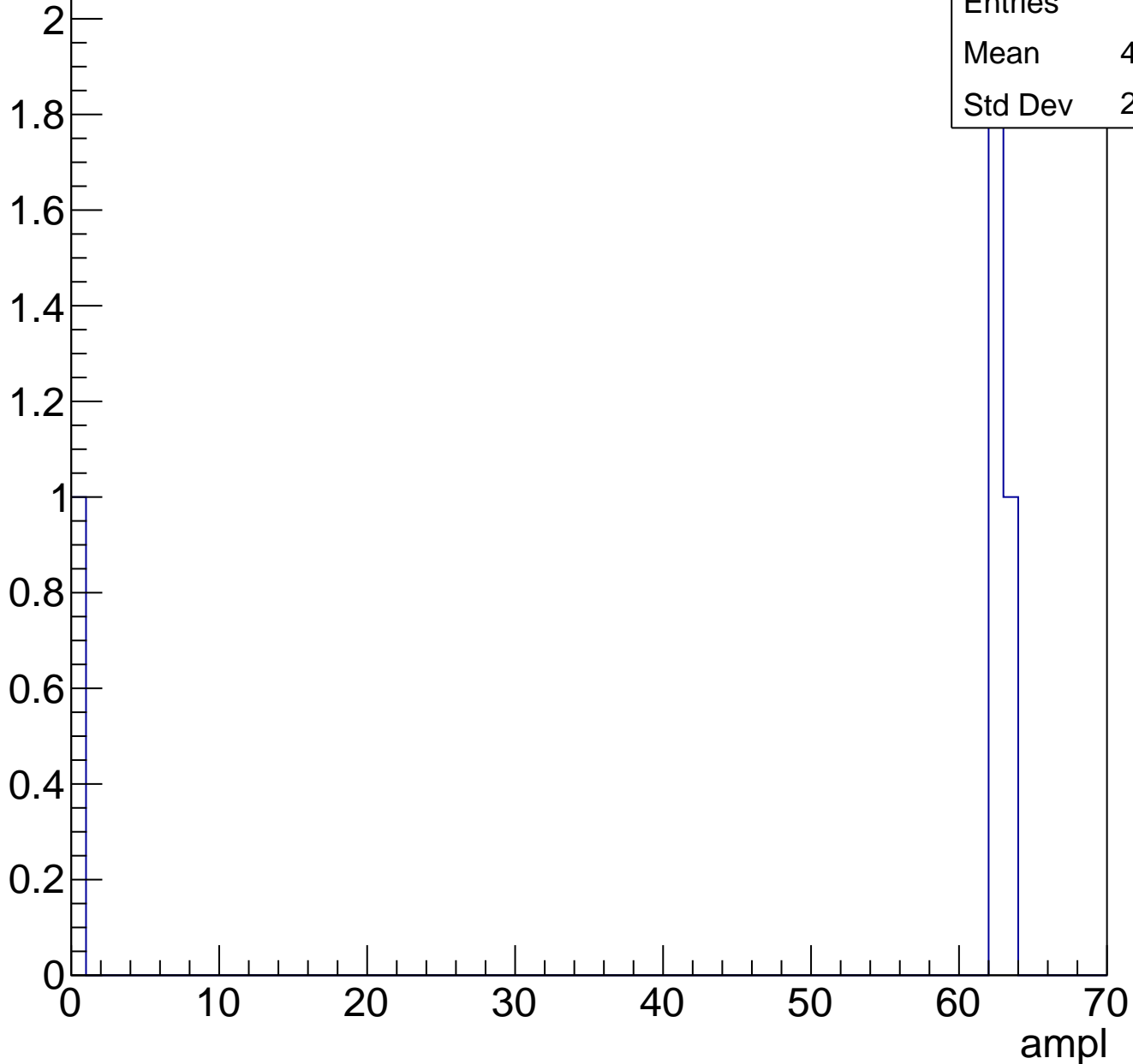
Entries	31
Mean	61.19
Std Dev	1.731



# B0L000S, U25-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch32, adc0

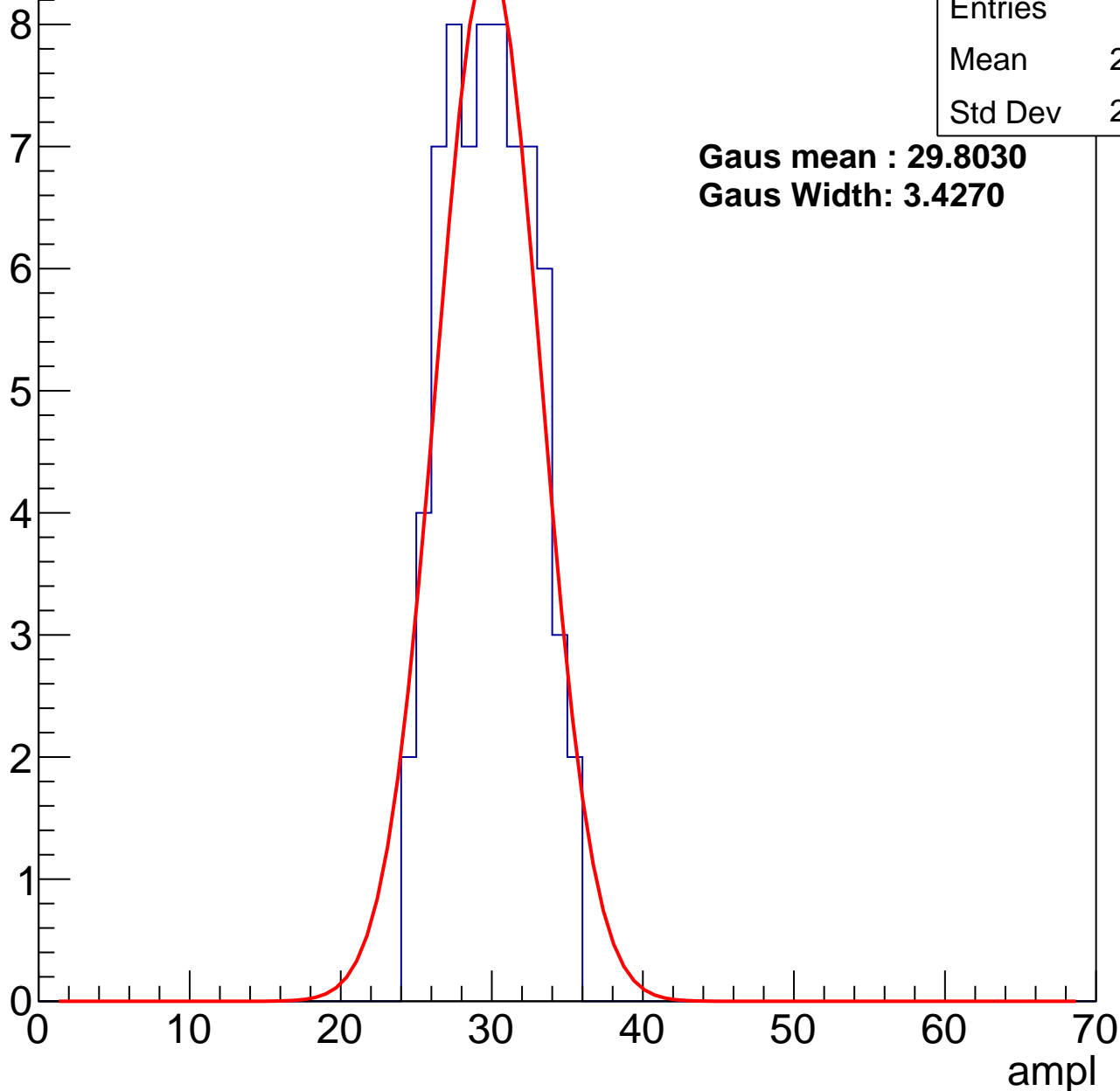
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.35
Std Dev	2.822

**Gaus mean : 29.8030**

**Gaus Width: 3.4270**



# B0L000S, U25-ch32, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	64
Mean	36.23
Std Dev	3.186

**Gaus mean : 36.7228**

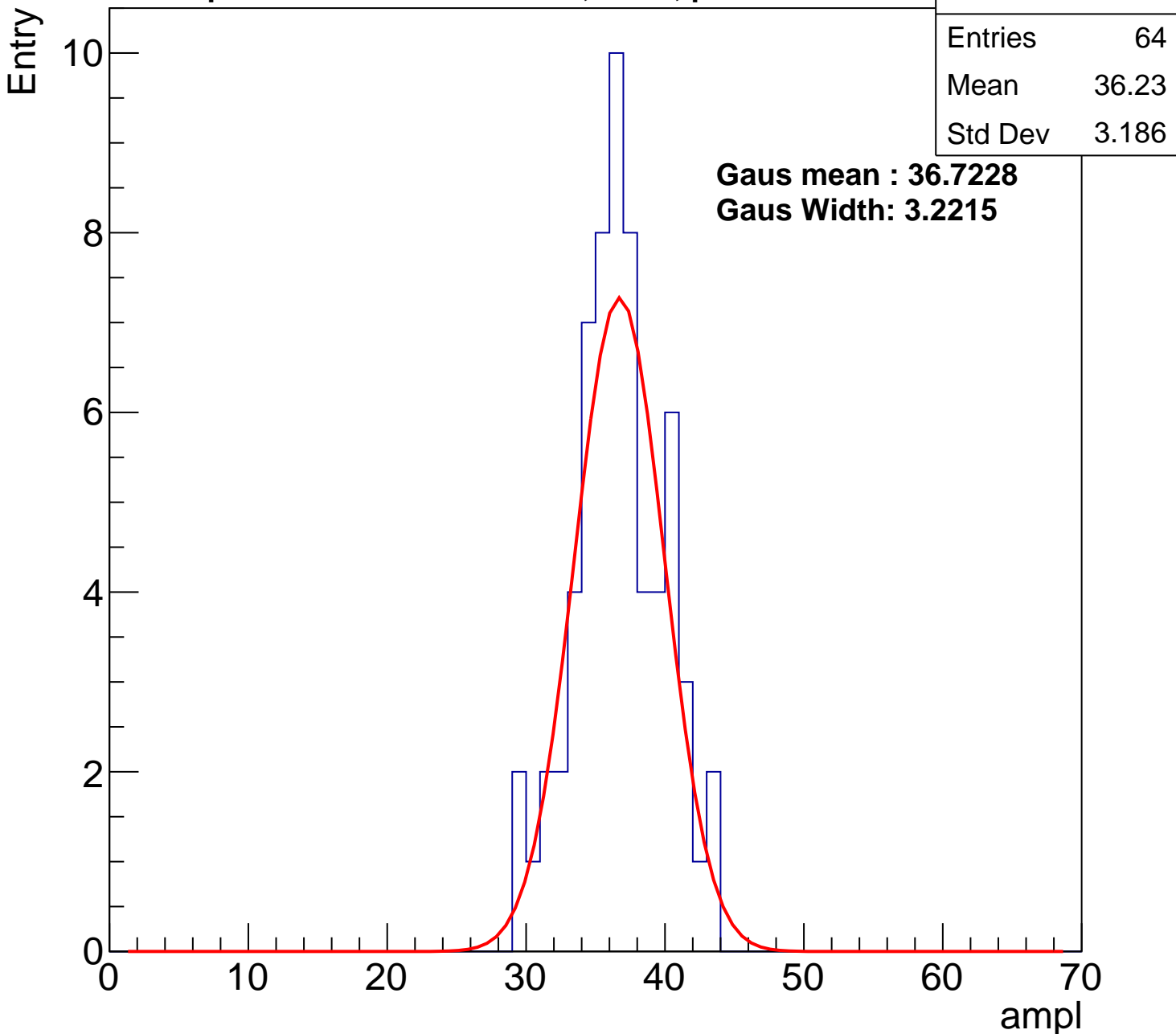
**Gaus Width: 3.2215**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch32, adc2

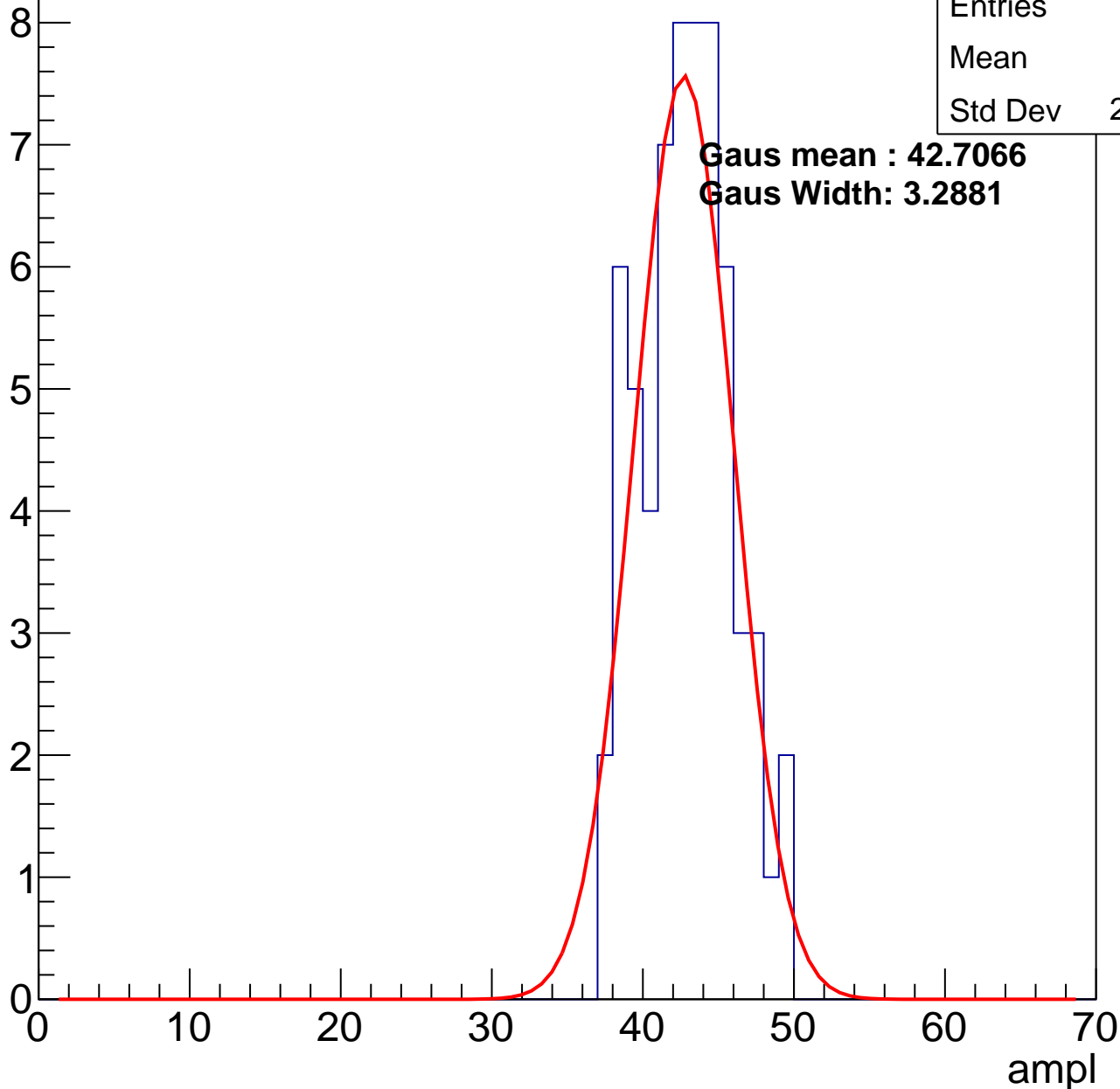
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.4
Std Dev	2.968

**Gaus mean : 42.7066**

**Gaus Width: 3.2881**

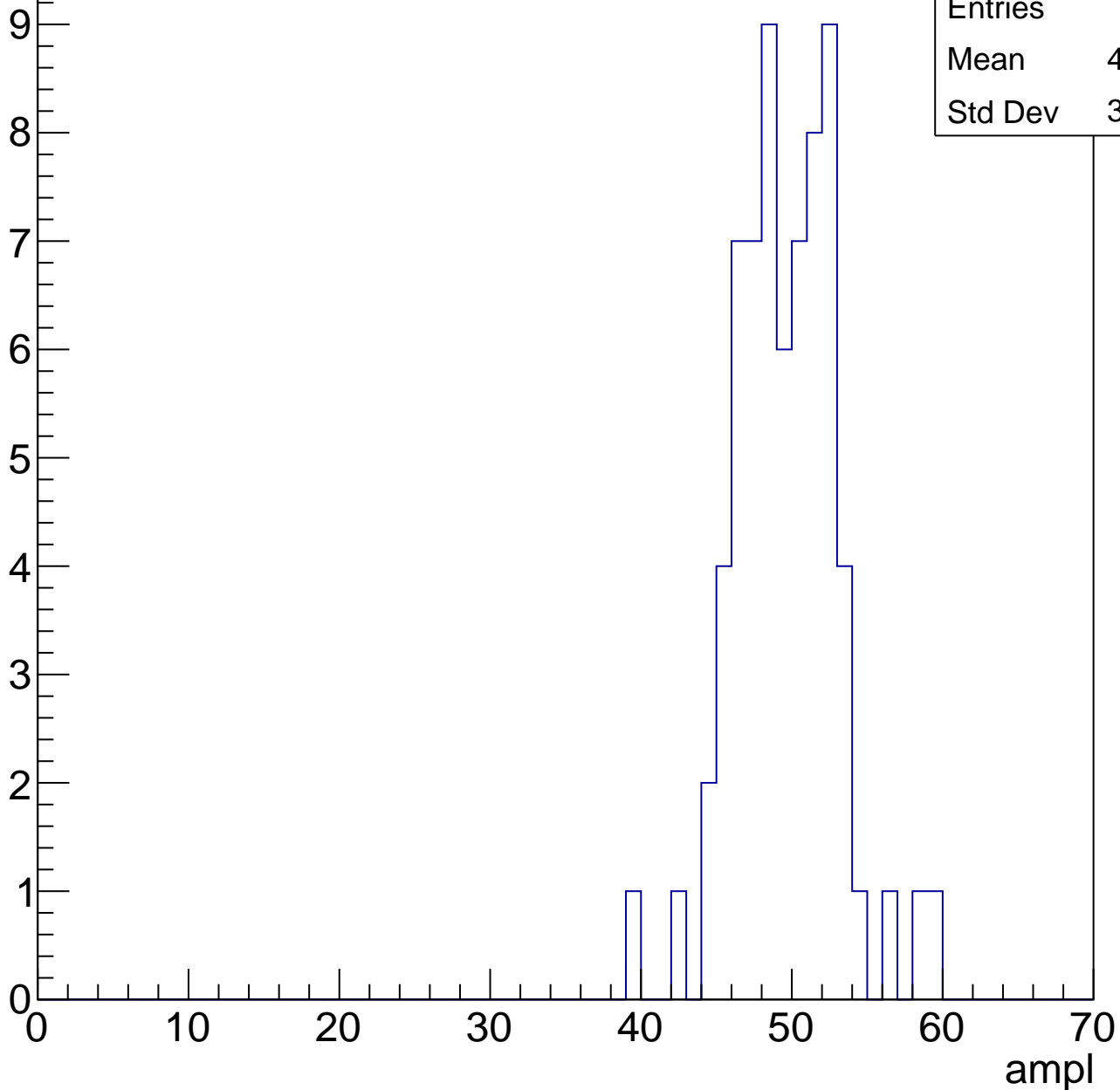


# B0L000S, U25-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	49.14
Std Dev	3.406



# B0L000S, U25-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

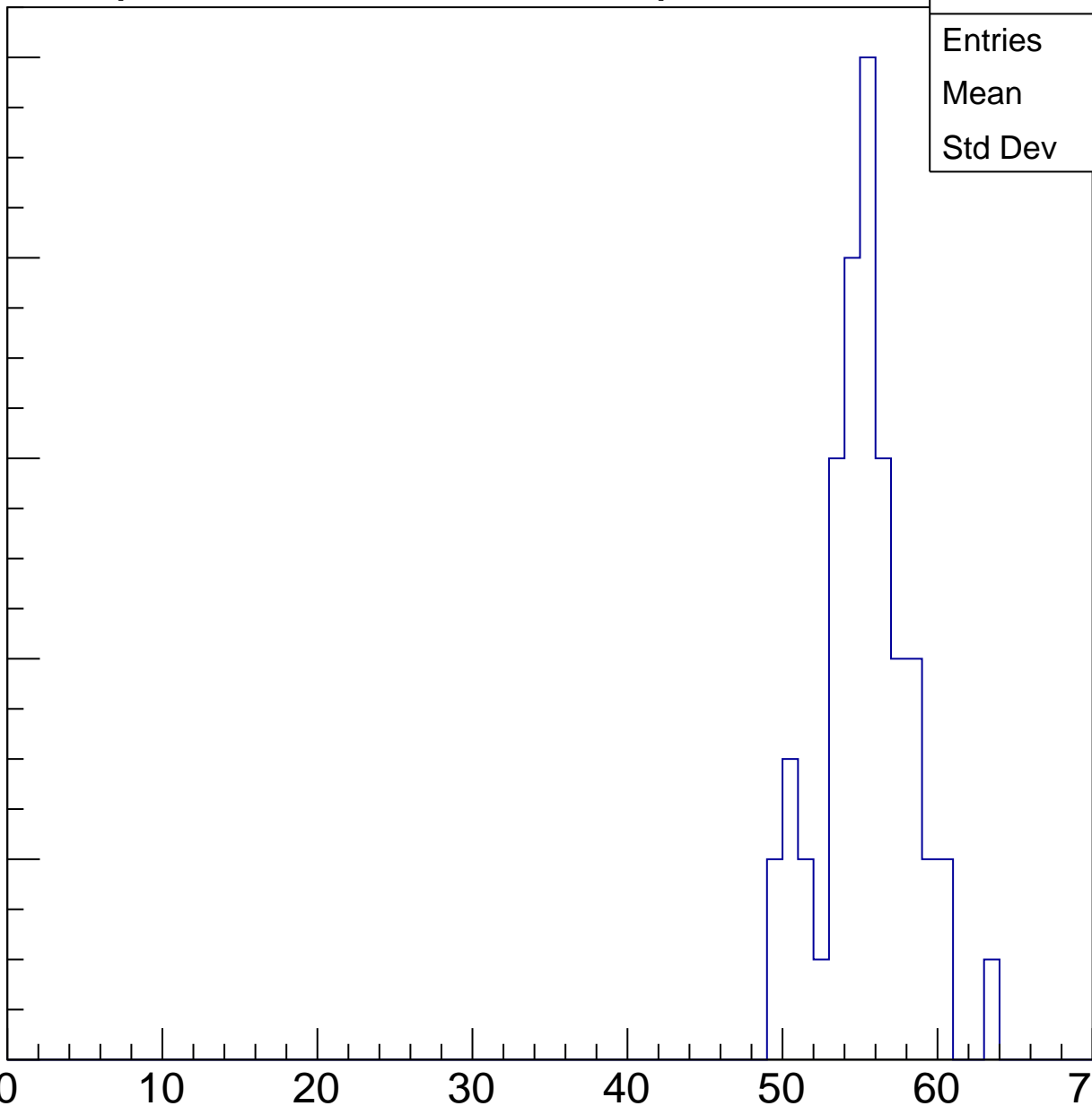
Entries	51
Mean	54.88
Std Dev	2.881

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

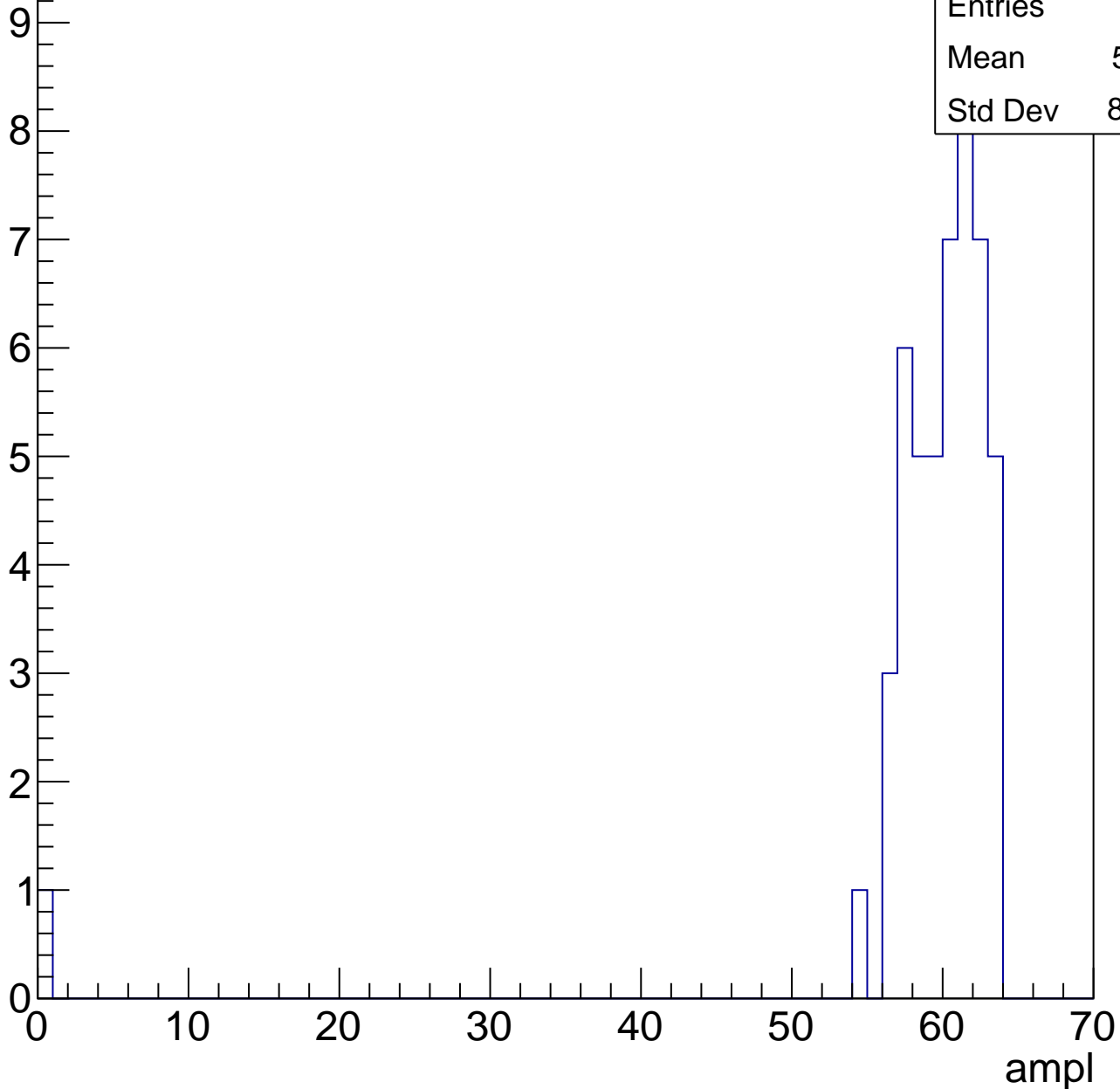


# B0L000S, U25-ch32, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

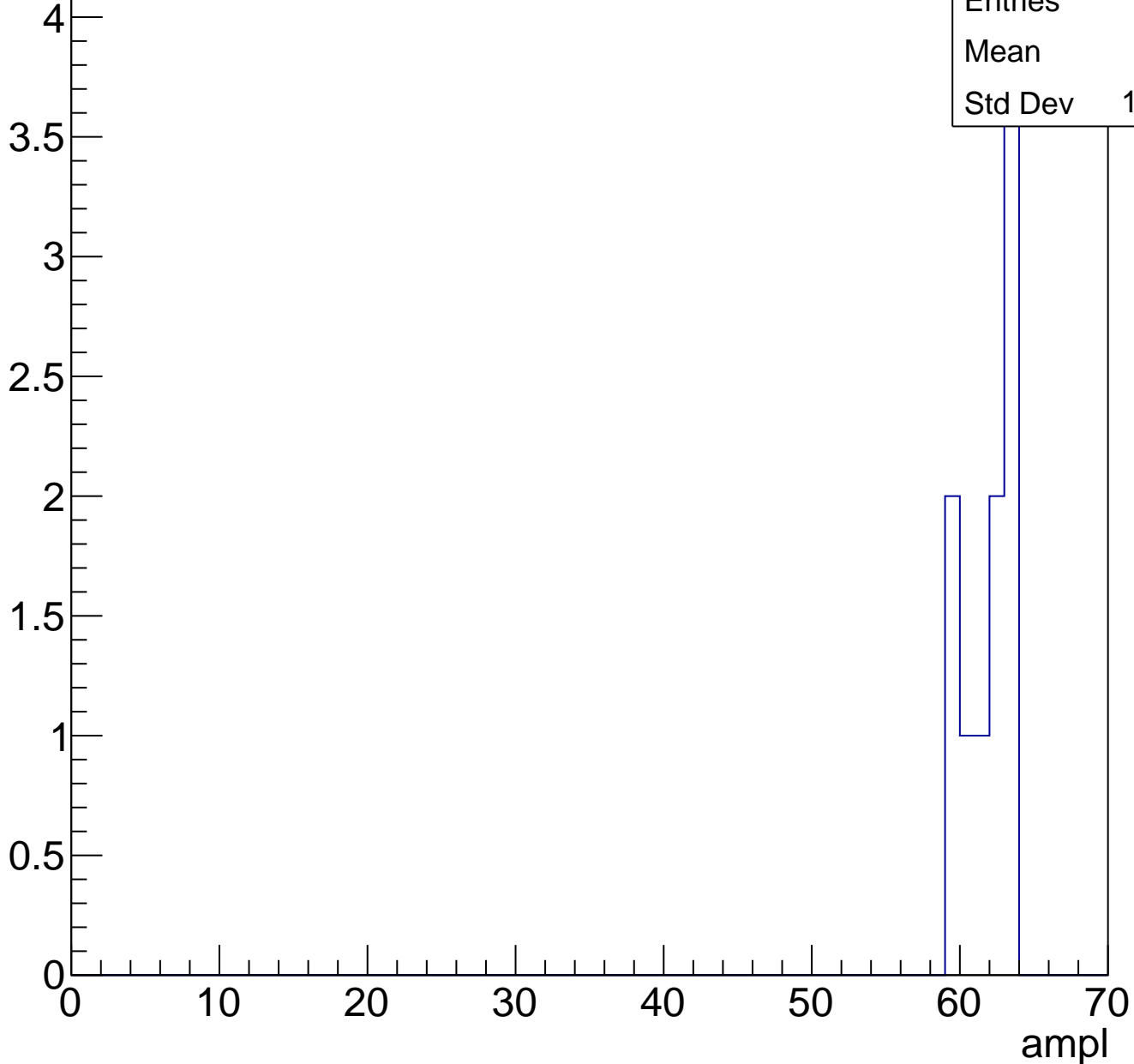
Entries	49
Mean	58.51
Std Dev	8.732



# B0L000S, U25-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch33, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	30.92
Std Dev	5.085

**Gaus mean : 31.7118**

**Gaus Width: 3.6218**

Entry

10  
8  
6  
4  
2  
0

ampl

0

10

20

30

40

50

60

70

# B0L000S, U25-ch33, adc1

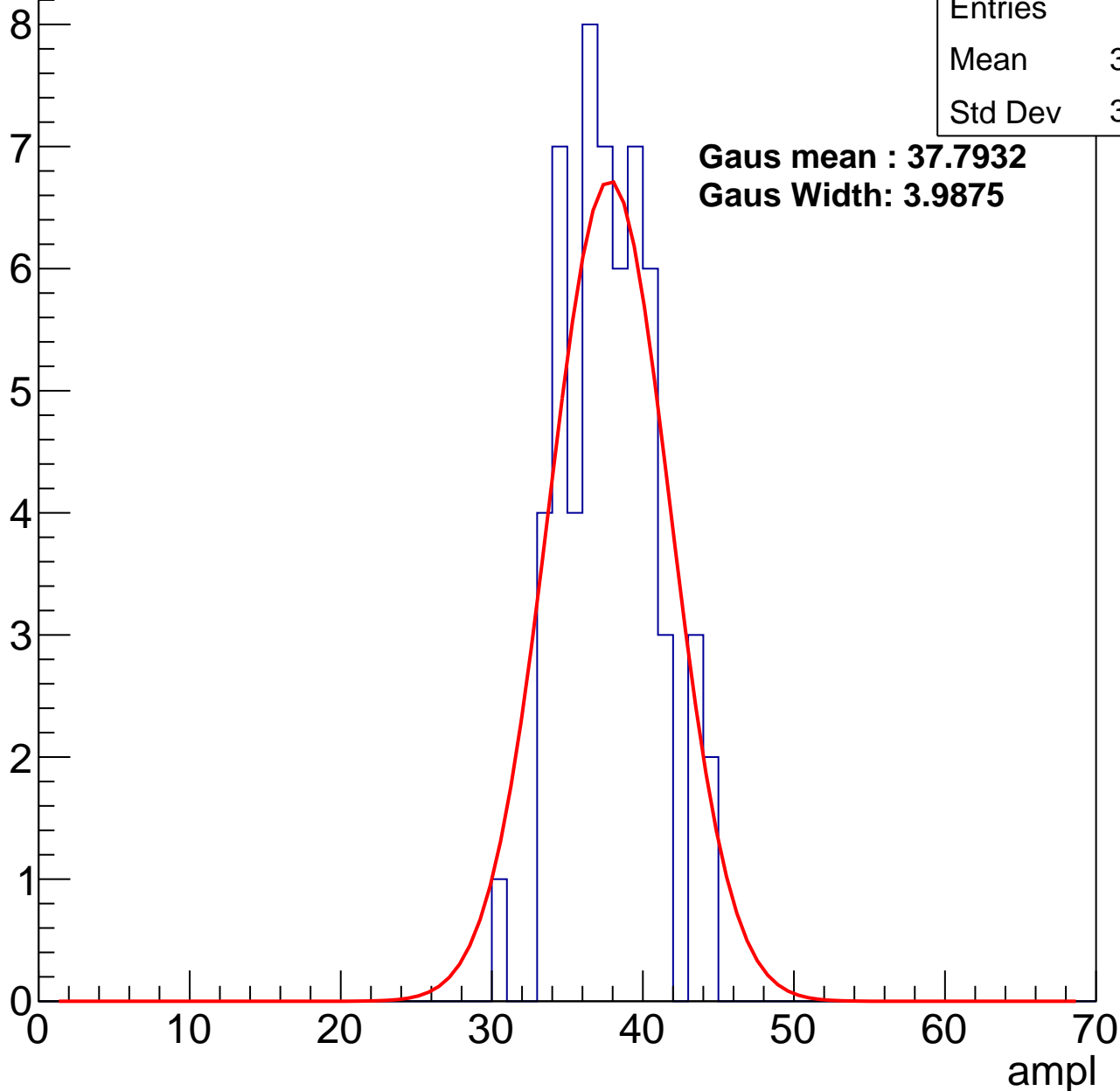
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	37.38
Std Dev	3.033

**Gaus mean : 37.7932**

**Gaus Width: 3.9875**



# B0L000S, U25-ch33, adc2

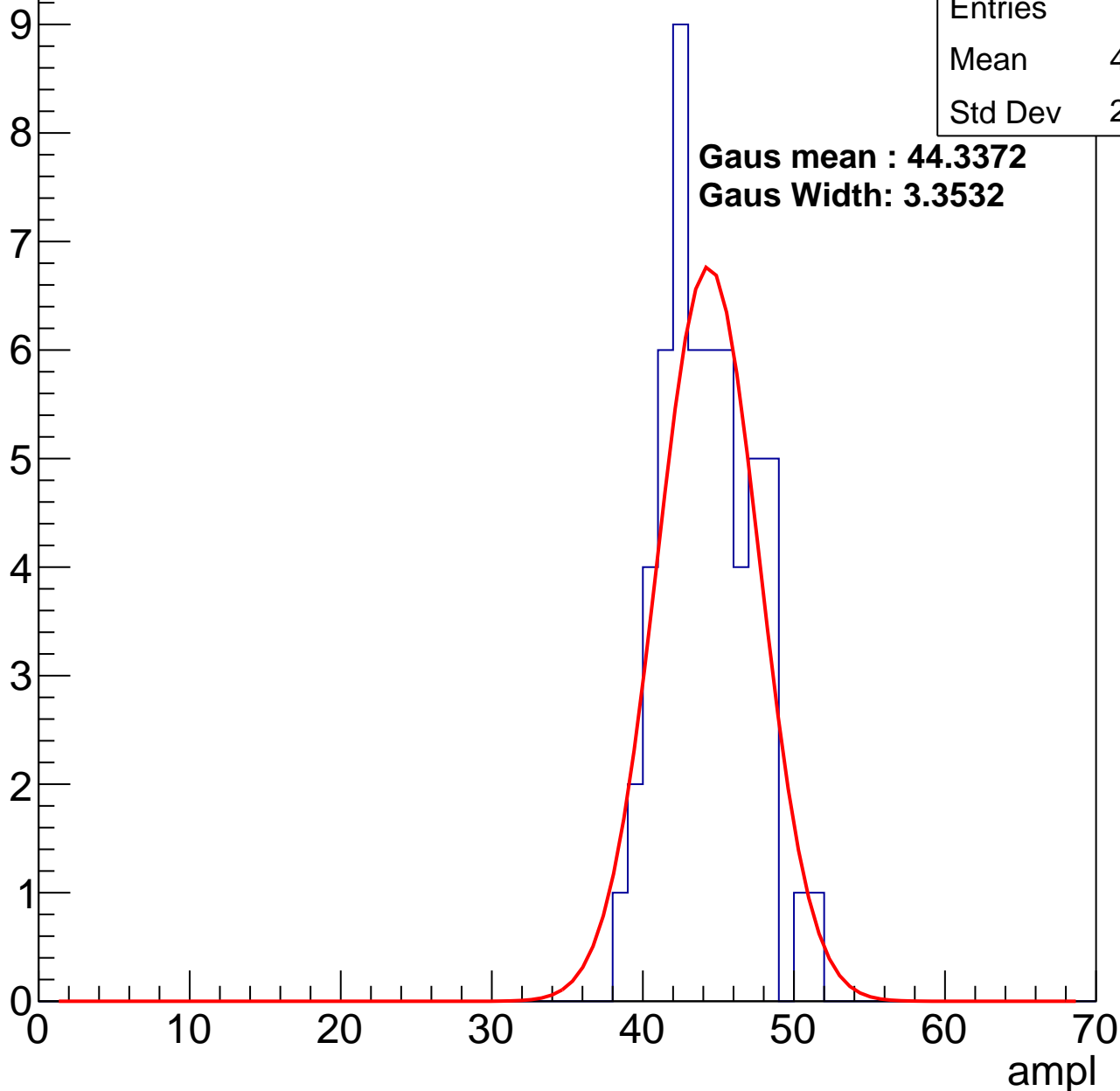
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	43.79
Std Dev	2.914

**Gaus mean : 44.3372**

**Gaus Width: 3.3532**



# B0L000S, U25-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

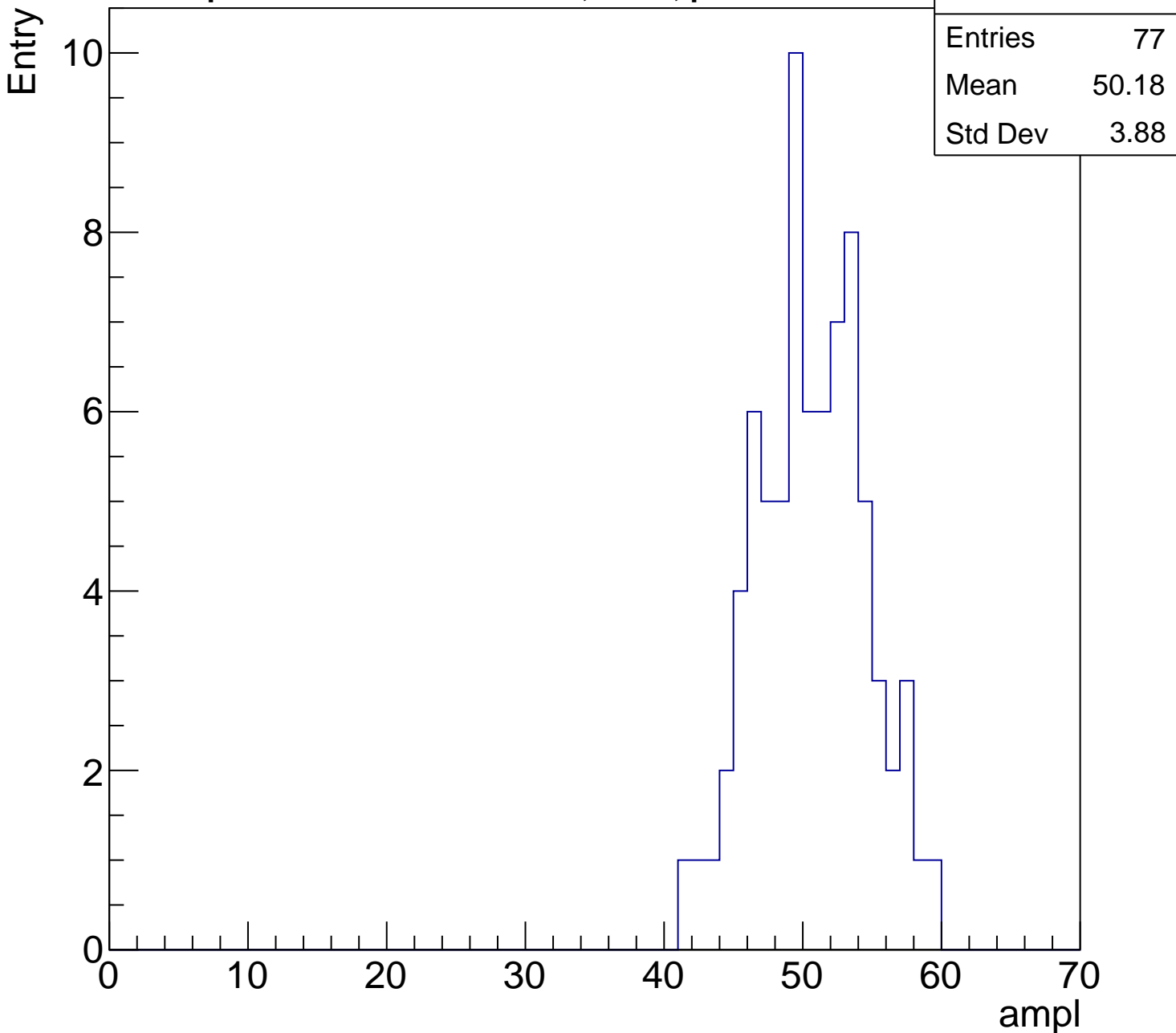
Entries	77
Mean	50.18
Std Dev	3.88

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

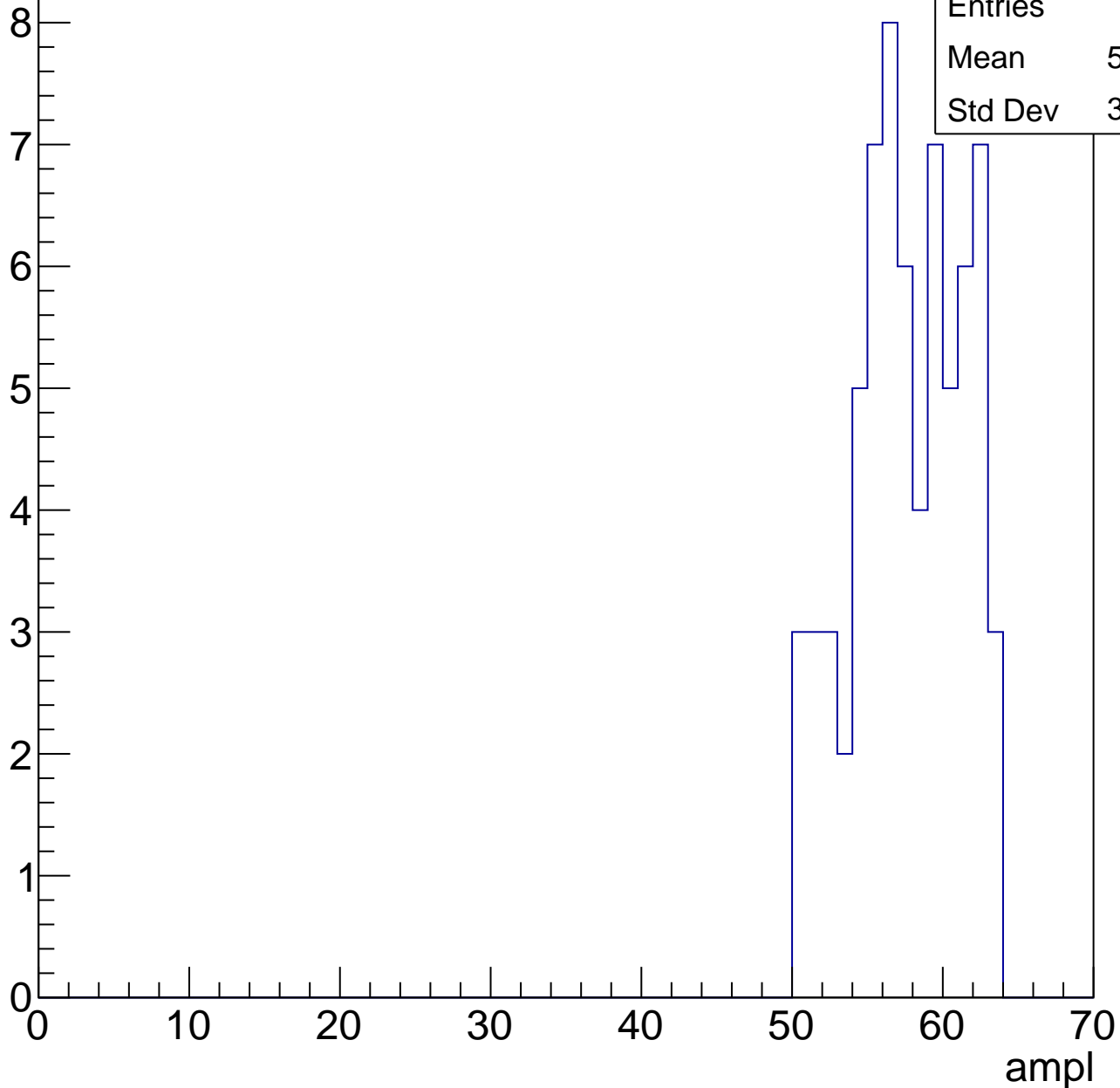
ampl



# B0L000S, U25-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



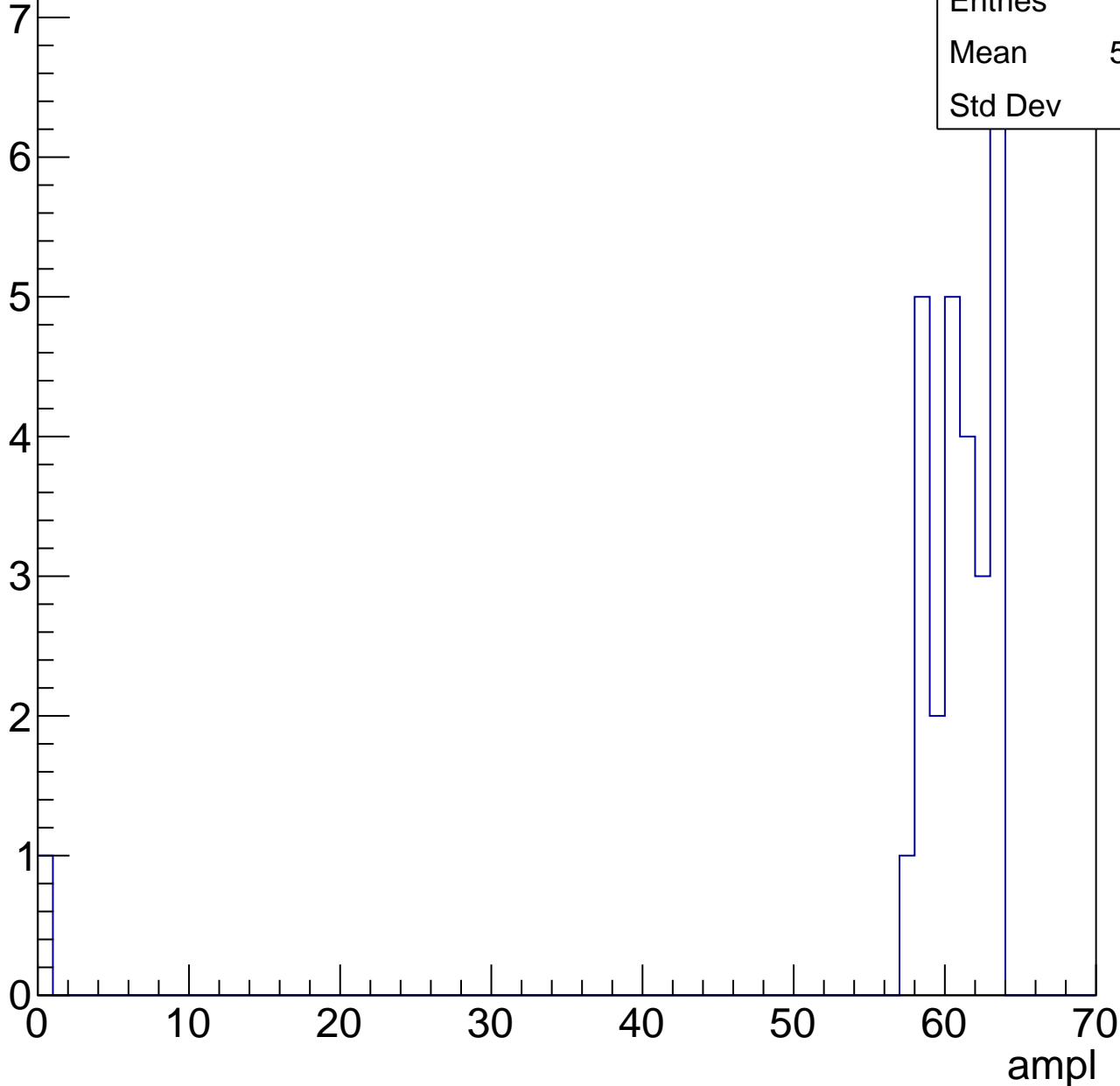
Entries	69
Mean	57.16
Std Dev	3.606

# B0L000S, U25-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	28
Mean	58.43
Std Dev	11.4



# B0L000S, U25-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U25-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch34, adc0

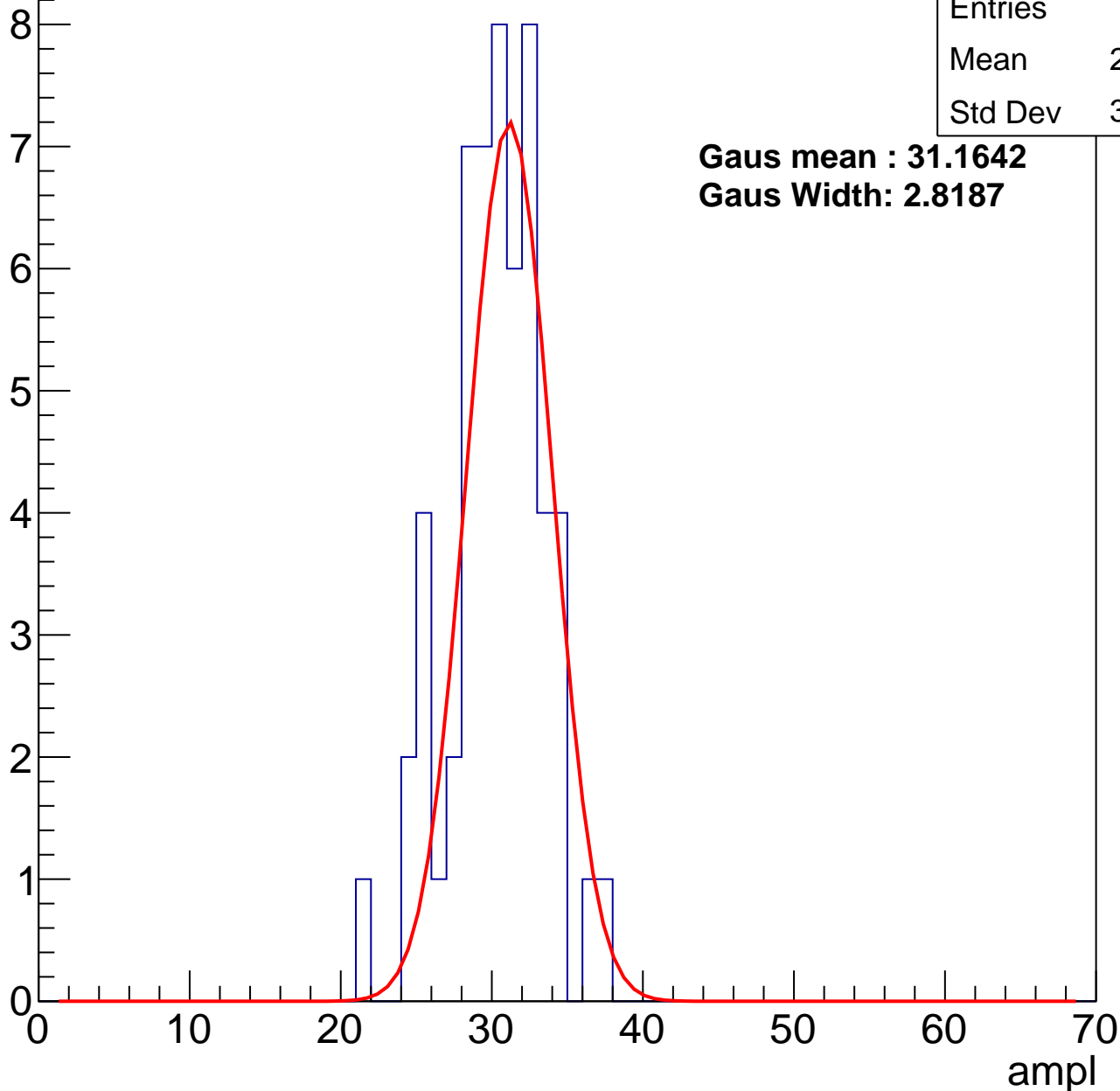
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	29.84
Std Dev	3.115

**Gaus mean : 31.1642**

**Gaus Width: 2.8187**



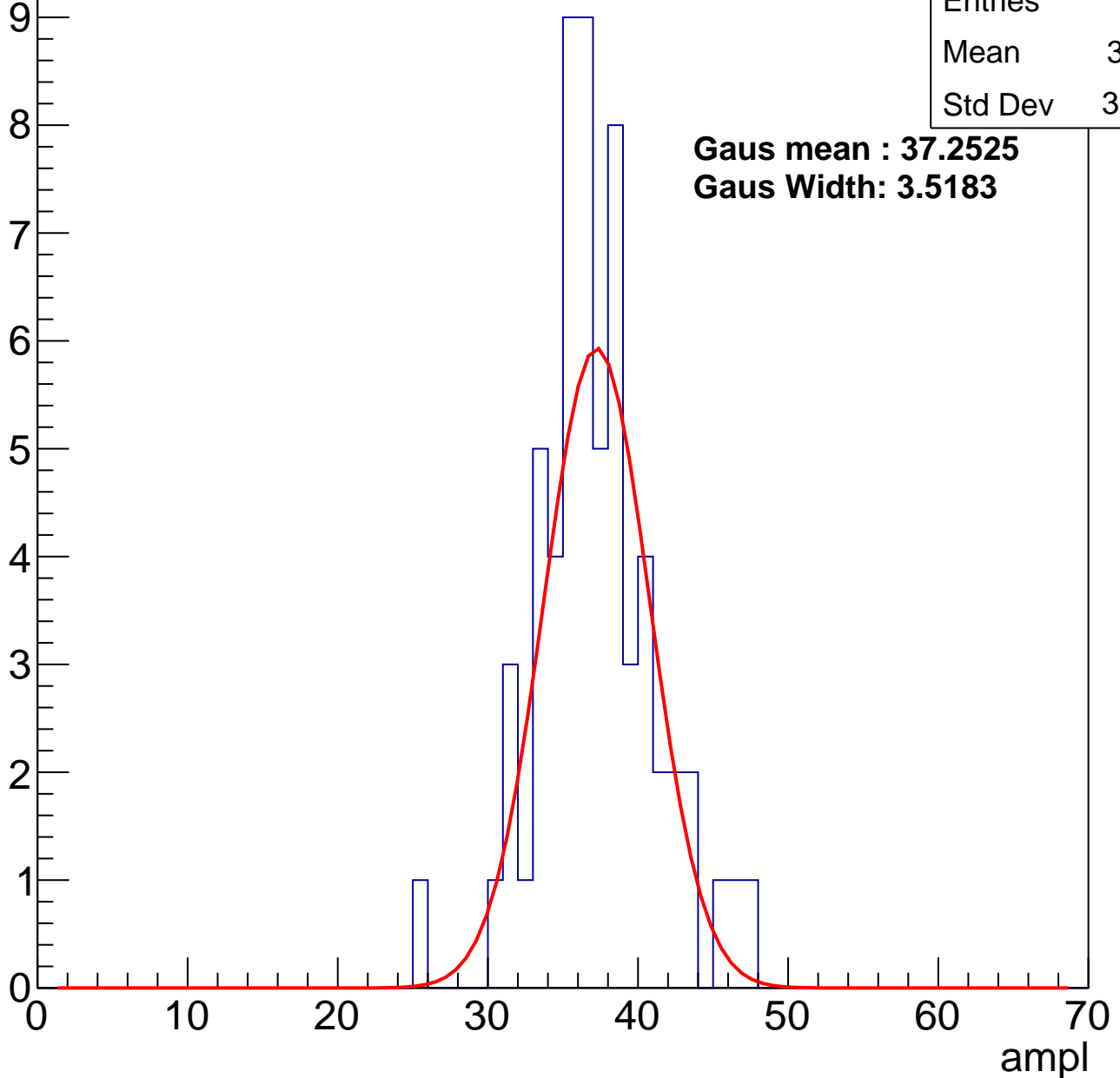
# B0L000S, U25-ch34, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	36.71
Std Dev	3.887

**Gaus mean : 37.2525**  
**Gaus Width: 3.5183**



# B0L000S, U25-ch34, adc2

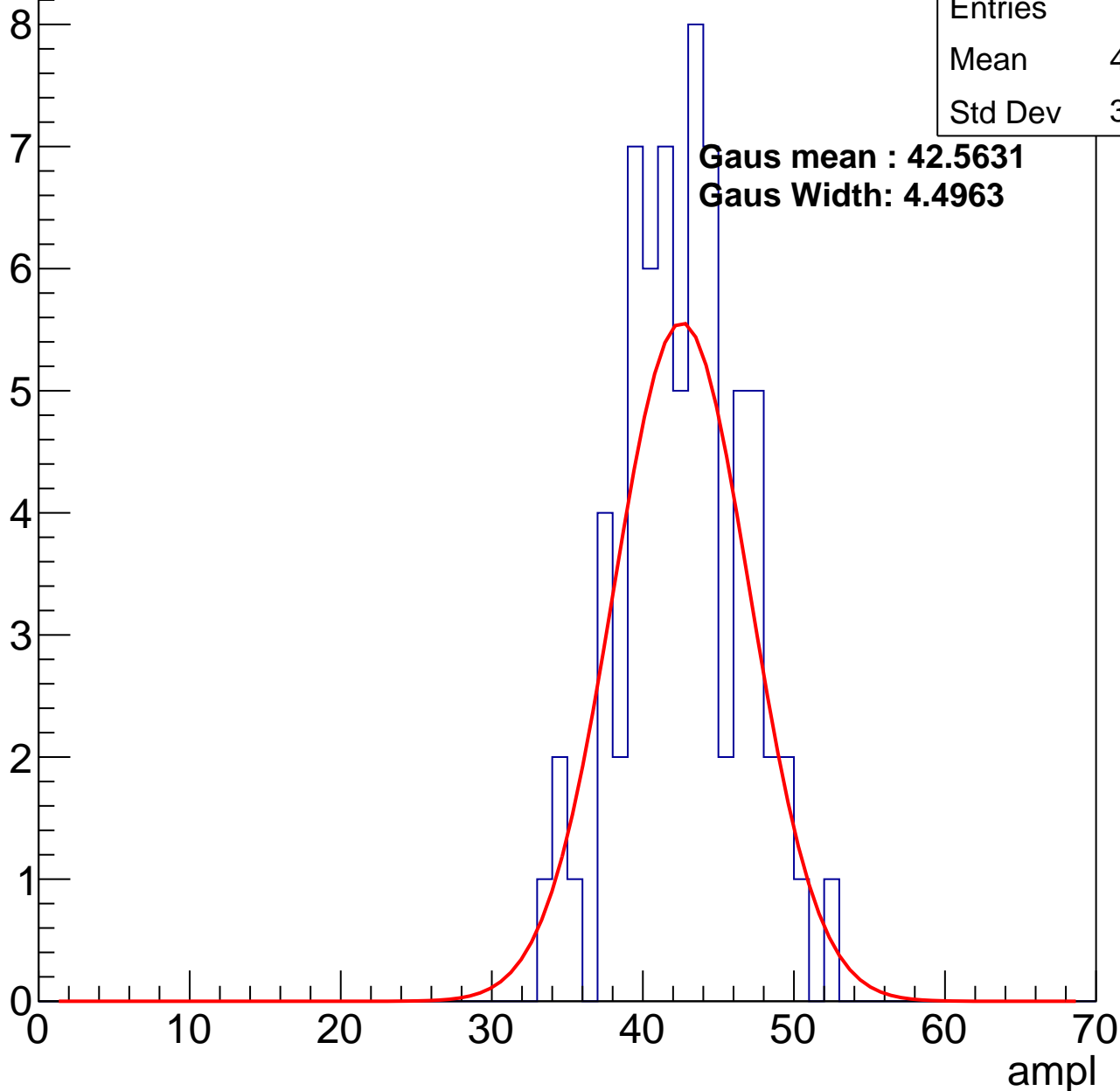
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	42.25
Std Dev	3.972

**Gaus mean : 42.5631**

**Gaus Width: 4.4963**

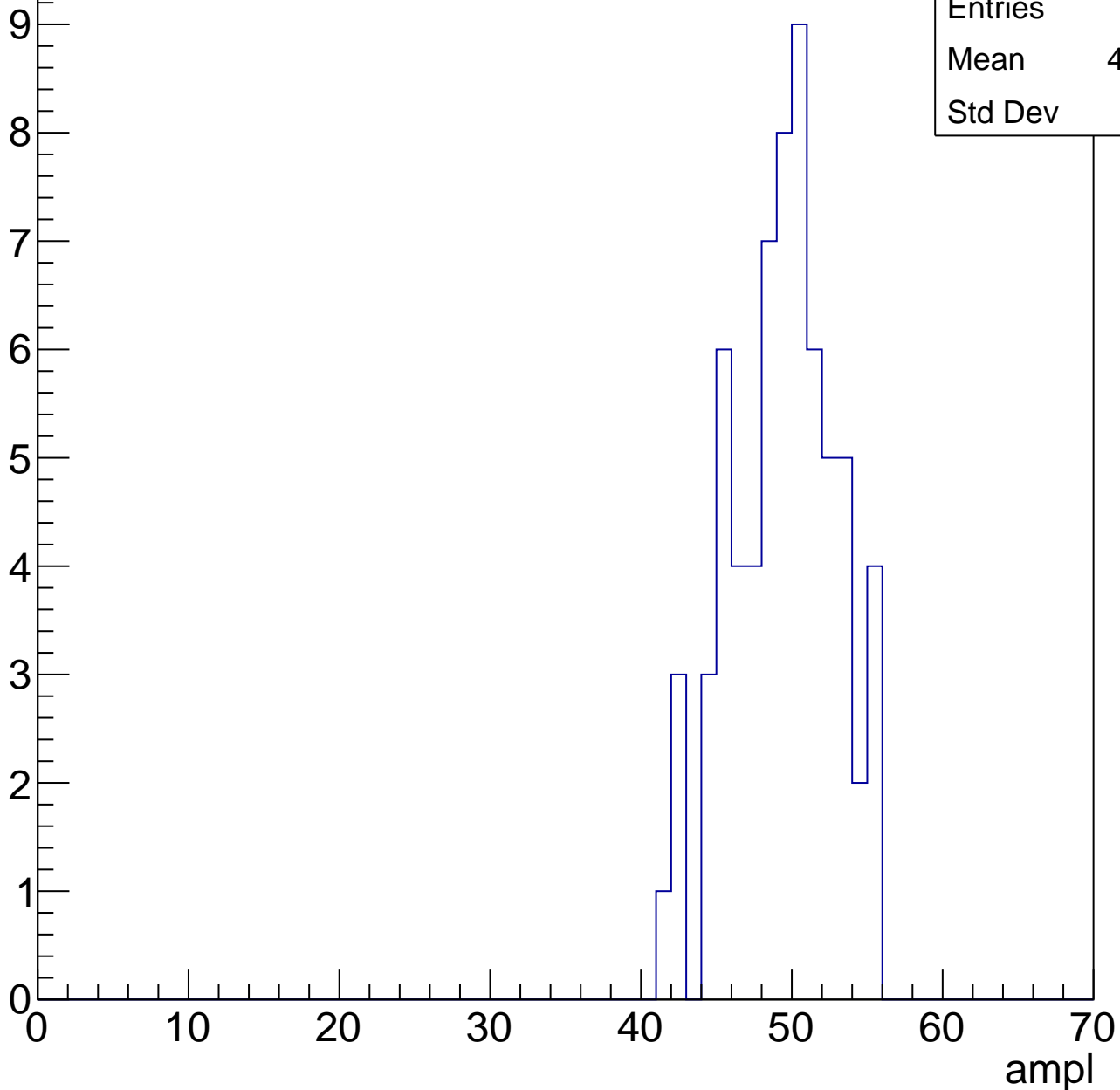


# B0L000S, U25-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	48.93
Std Dev	3.44

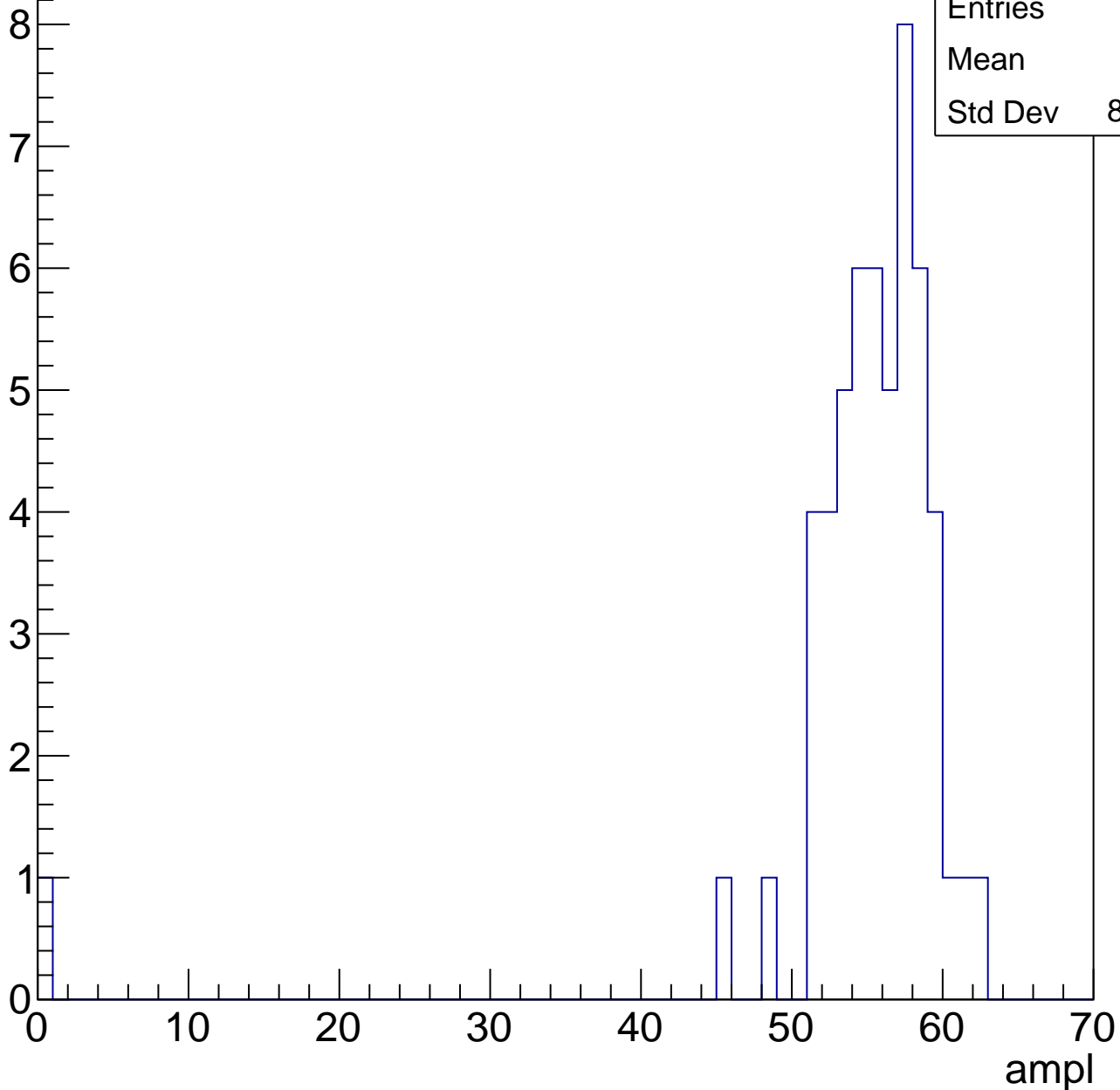


# B0L000S, U25-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

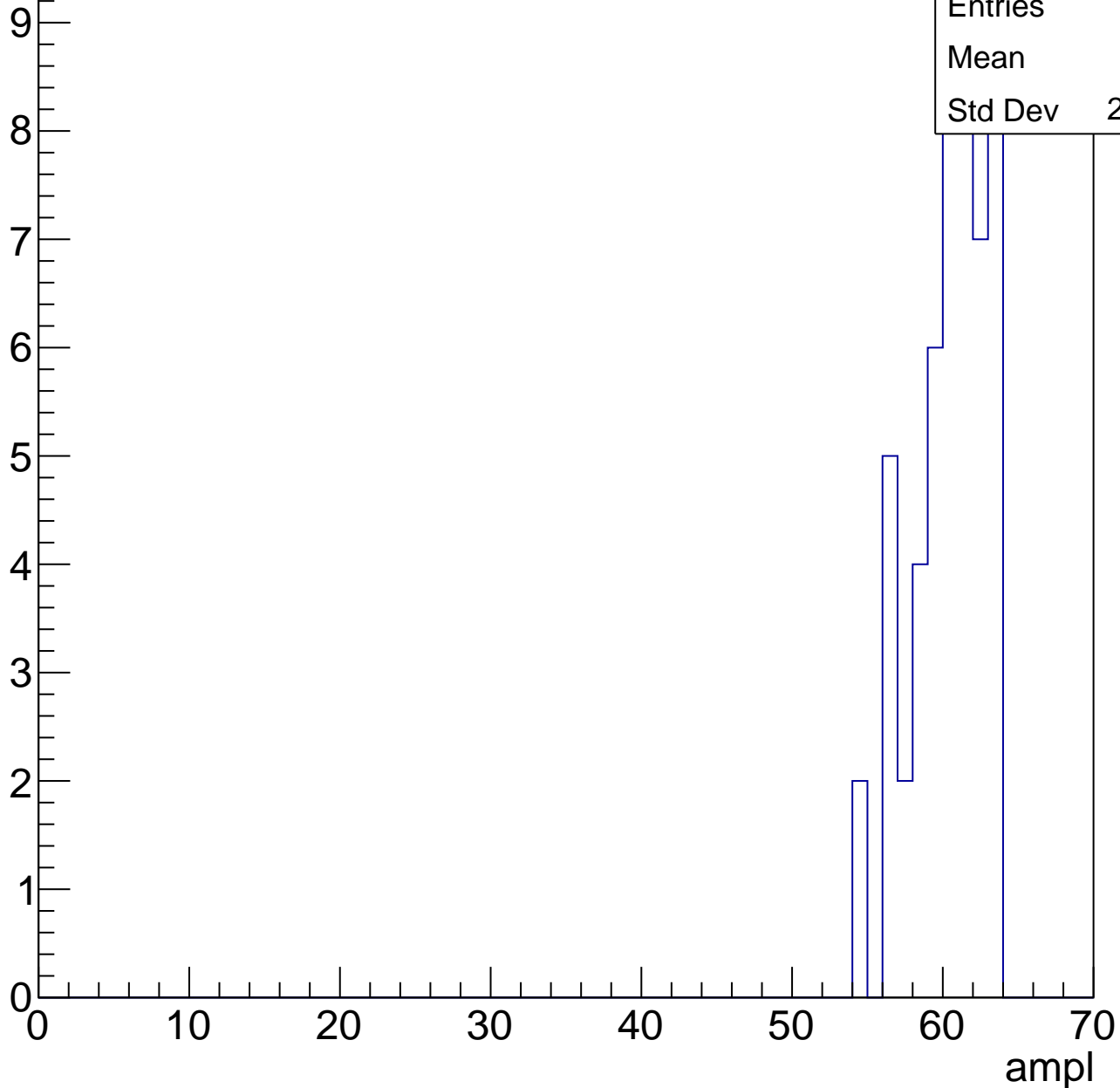
Entries	54
Mean	54.2
Std Dev	8.086



# B0L000S, U25-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch35, adc0

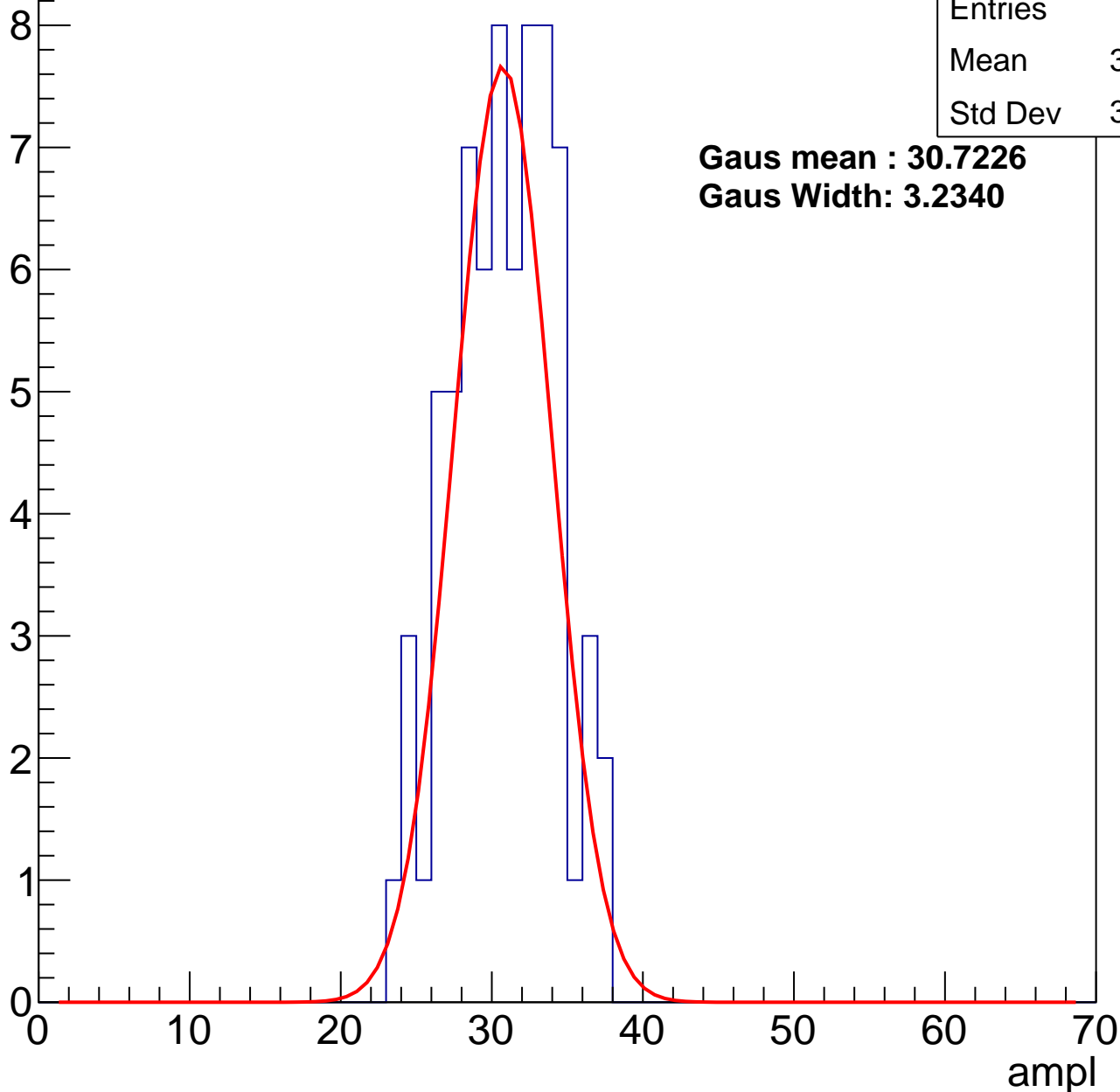
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	30.37
Std Dev	3.324

**Gaus mean : 30.7226**

**Gaus Width: 3.2340**



# B0L000S, U25-ch35, adc1

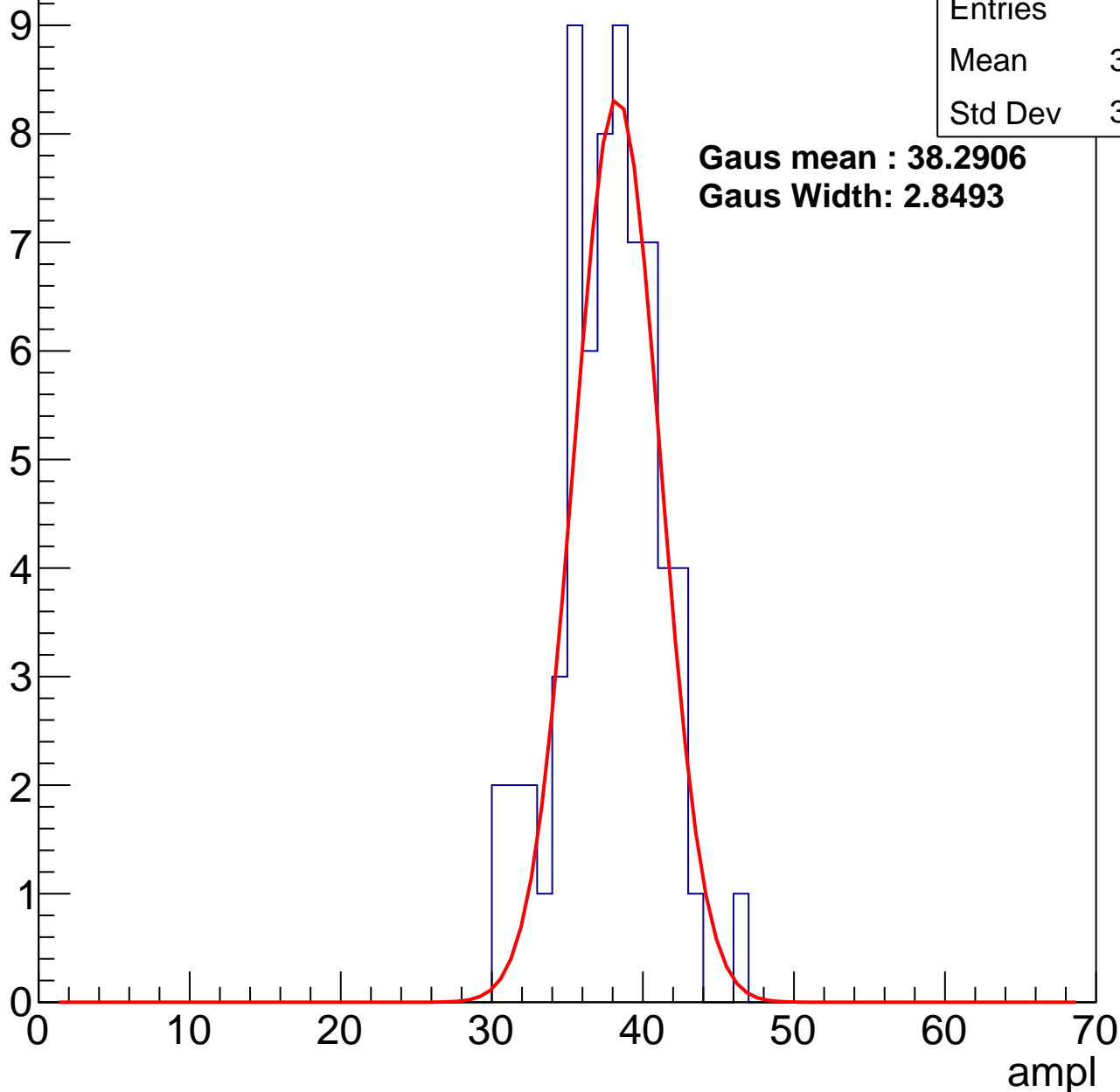
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	37.33
Std Dev	3.207

**Gaus mean : 38.2906**

**Gaus Width: 2.8493**



# B0L000S, U25-ch35, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	43.95
Std Dev	3.179

**Gaus mean : 44.8850**

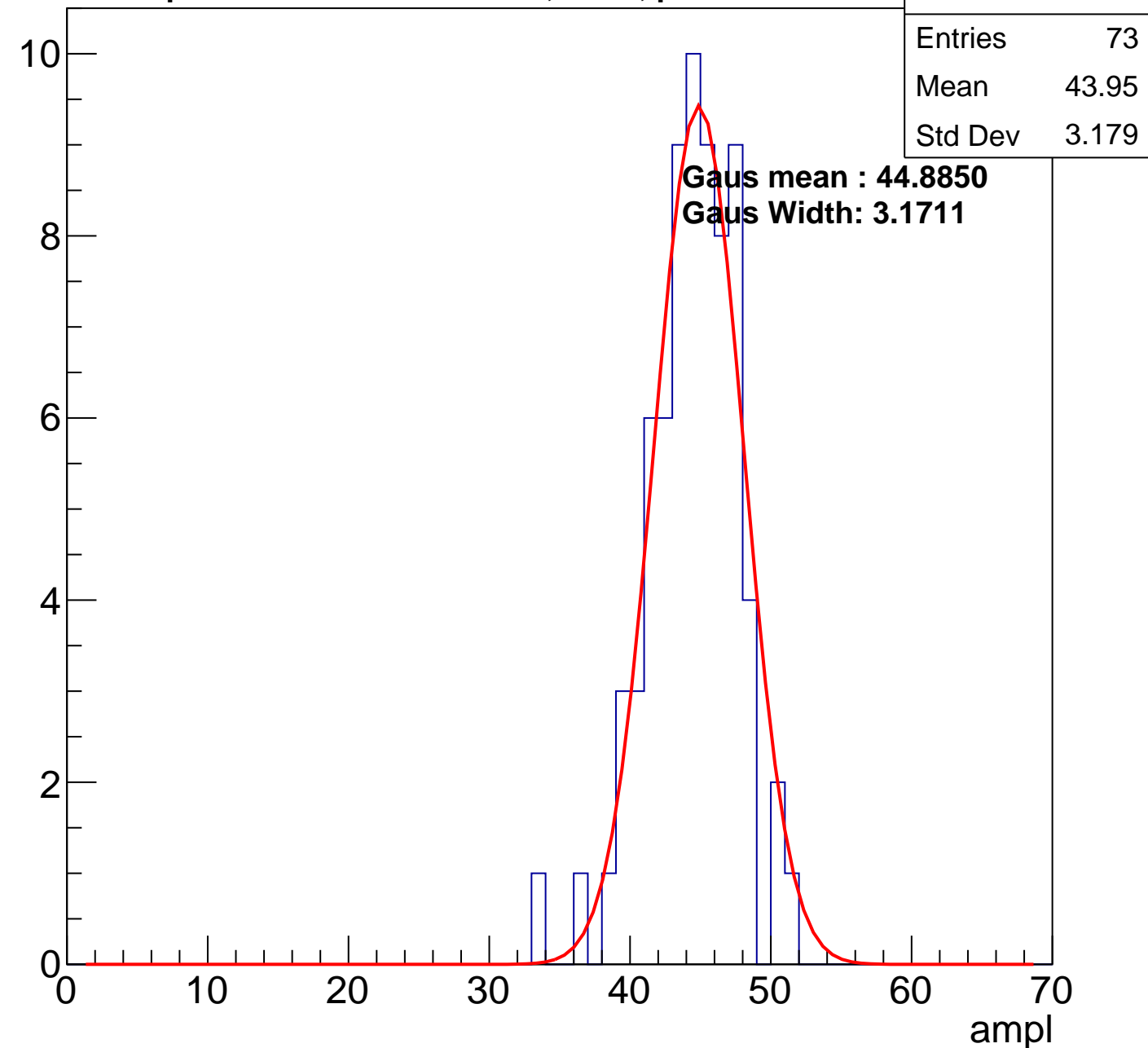
**Gaus Width: 3.1711**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

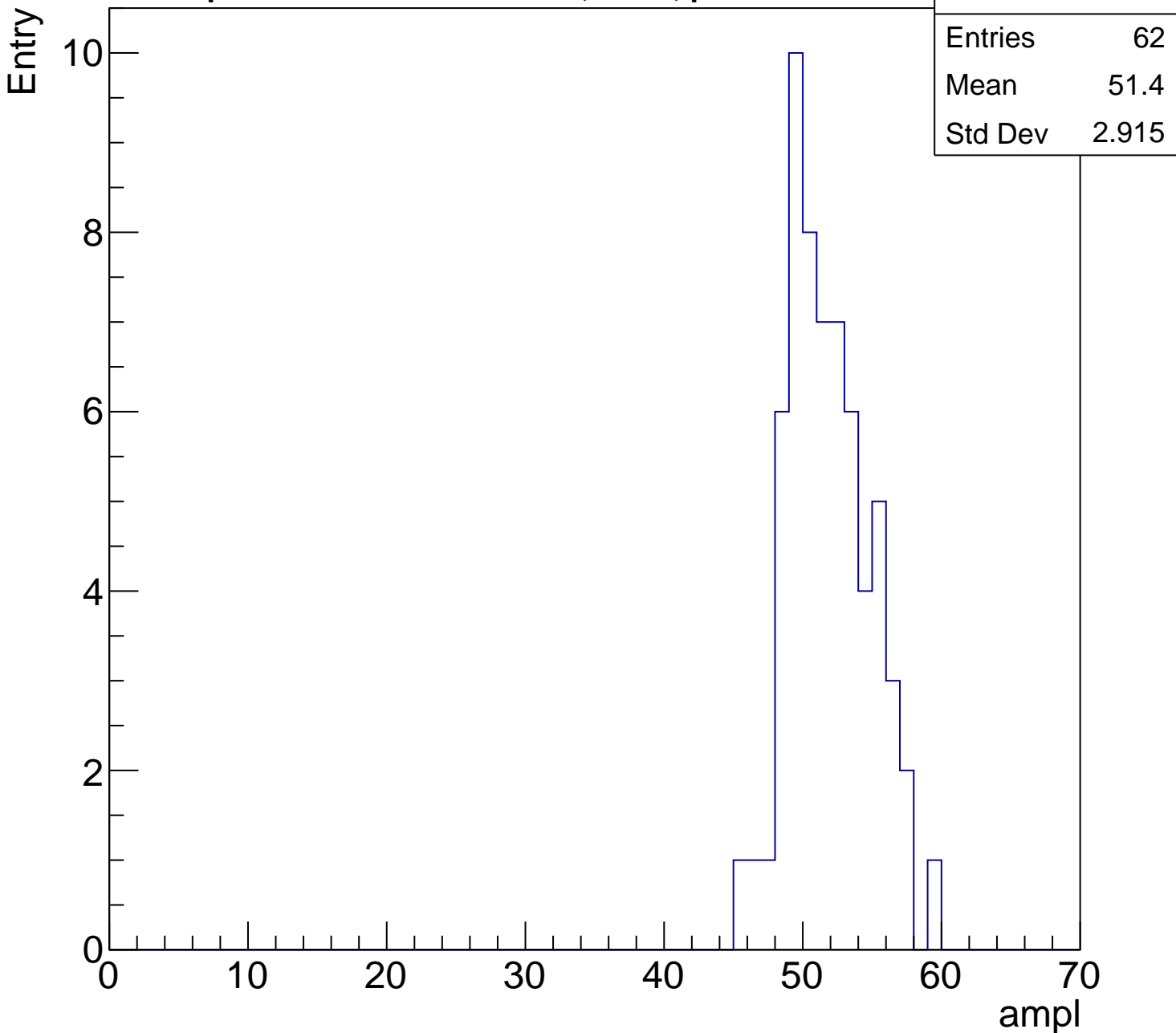
Entries	62
Mean	51.4
Std Dev	2.915

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

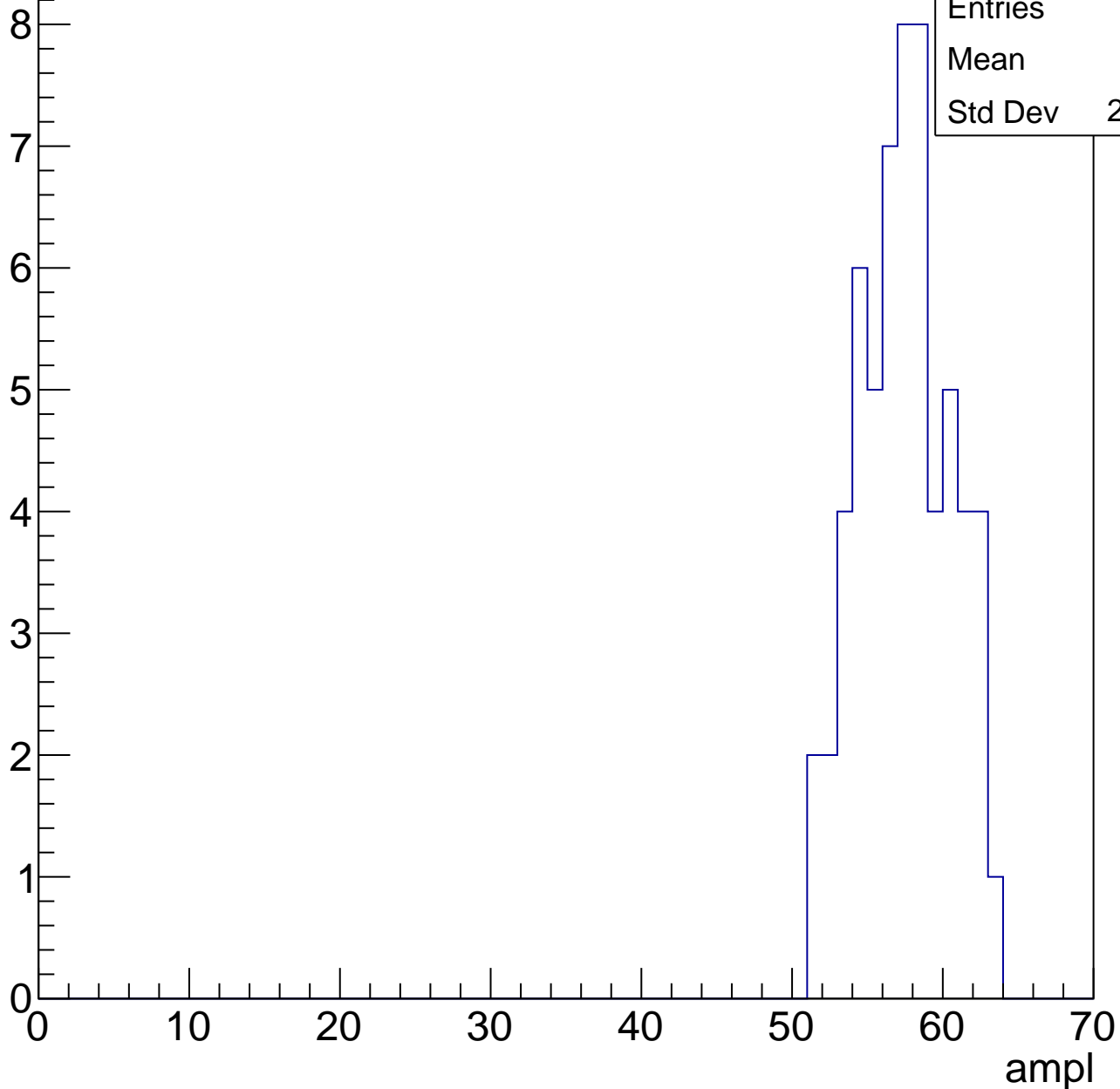


# B0L000S, U25-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57
Std Dev	2.989

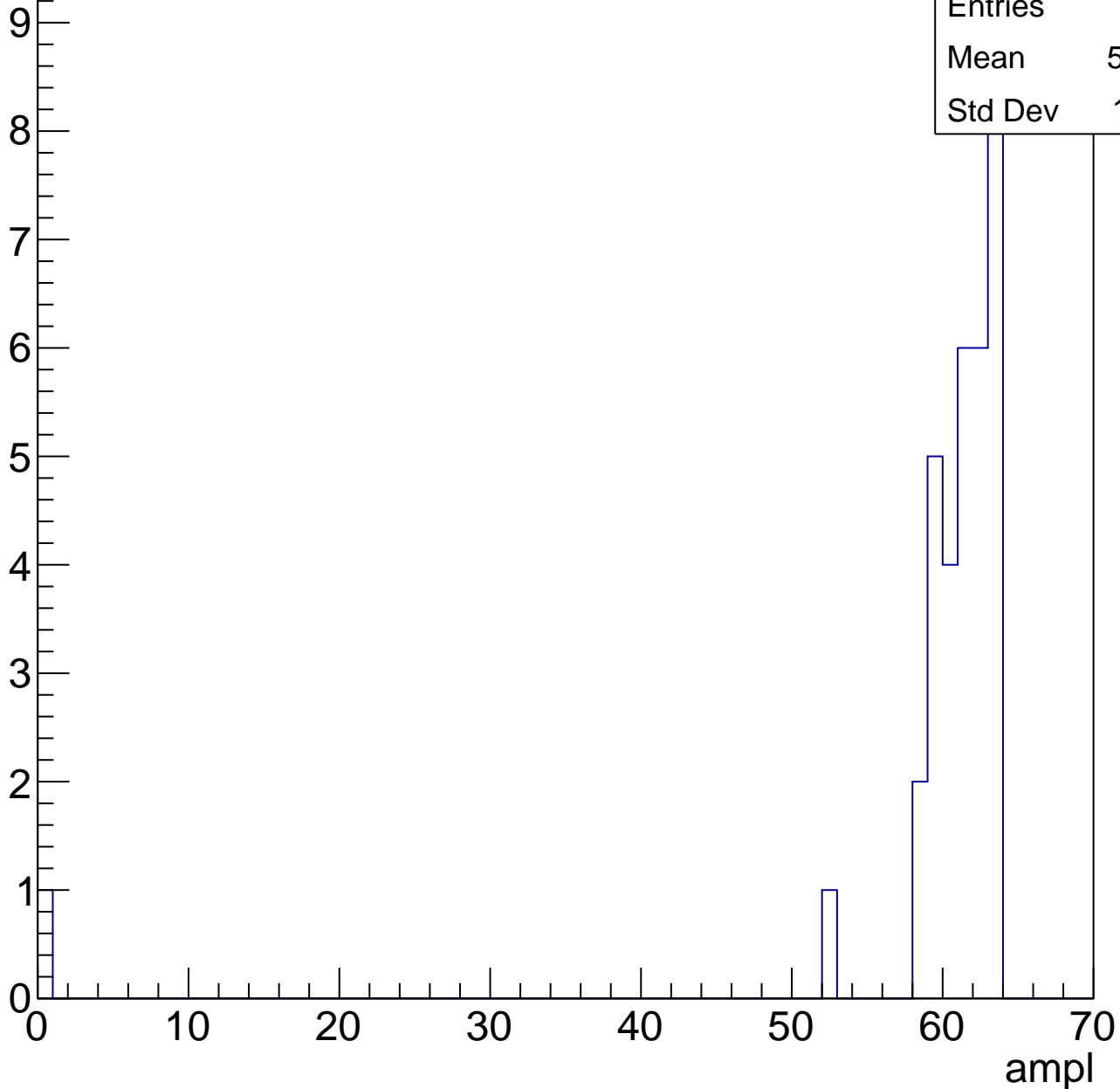


# B0L000S, U25-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.06
Std Dev	10.51



# B0L000S, U25-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

# B0L000S, U25-ch36, adc0

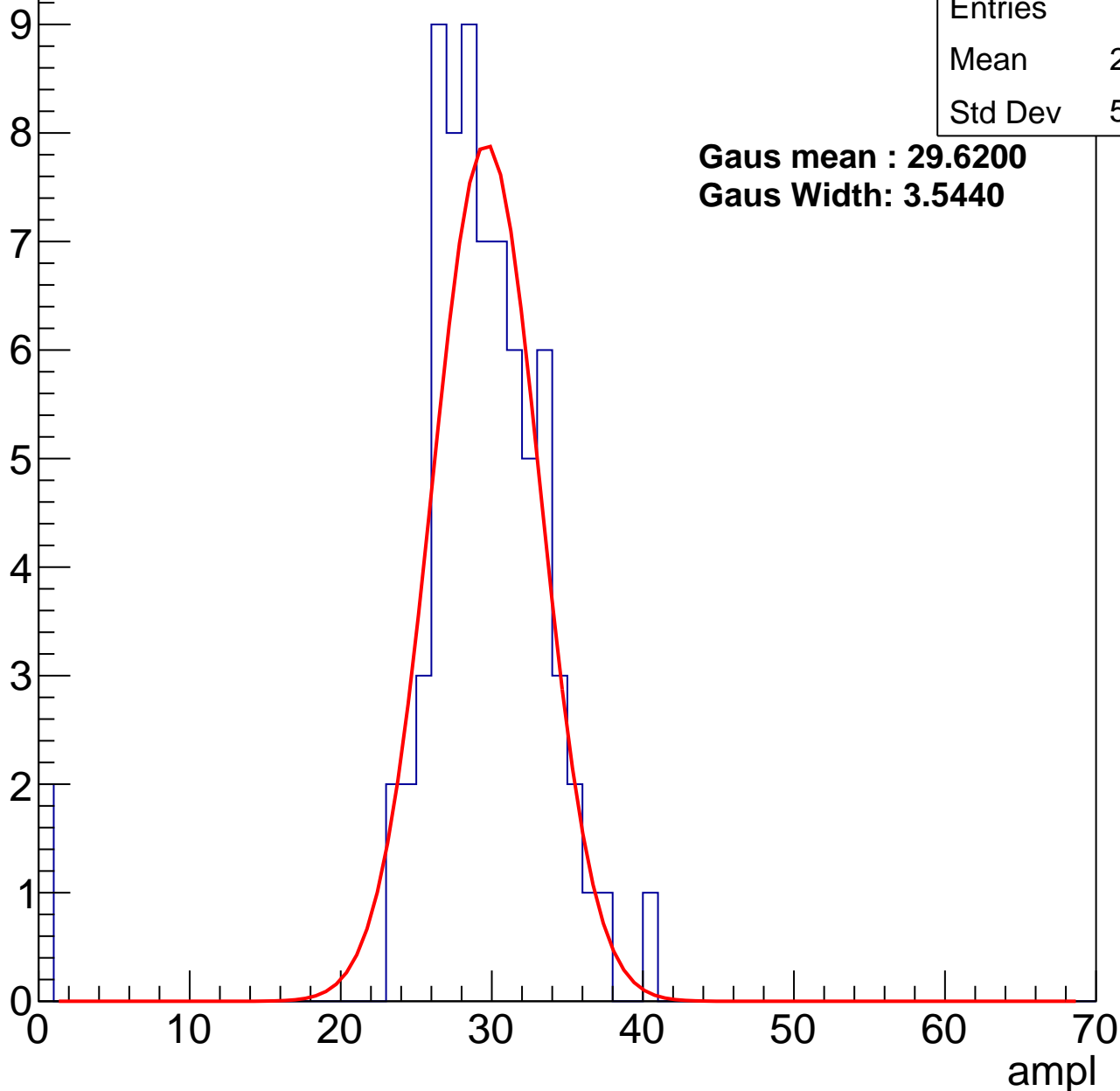
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.55
Std Dev	5.827

**Gaus mean : 29.6200**

**Gaus Width: 3.5440**



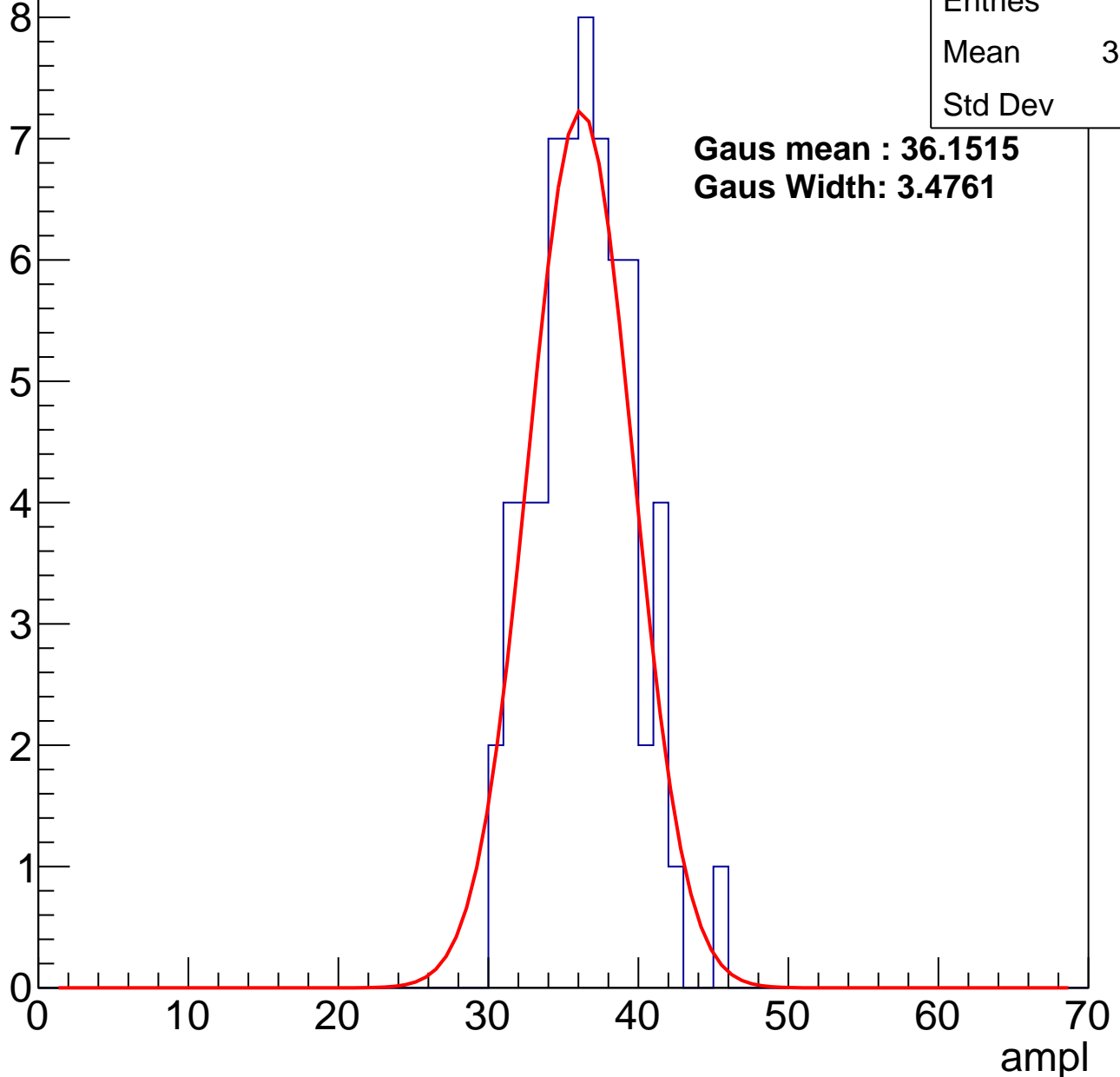
# B0L000S, U25-ch36, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	35.98
Std Dev	3.19

**Gaus mean : 36.1515**  
**Gaus Width: 3.4761**

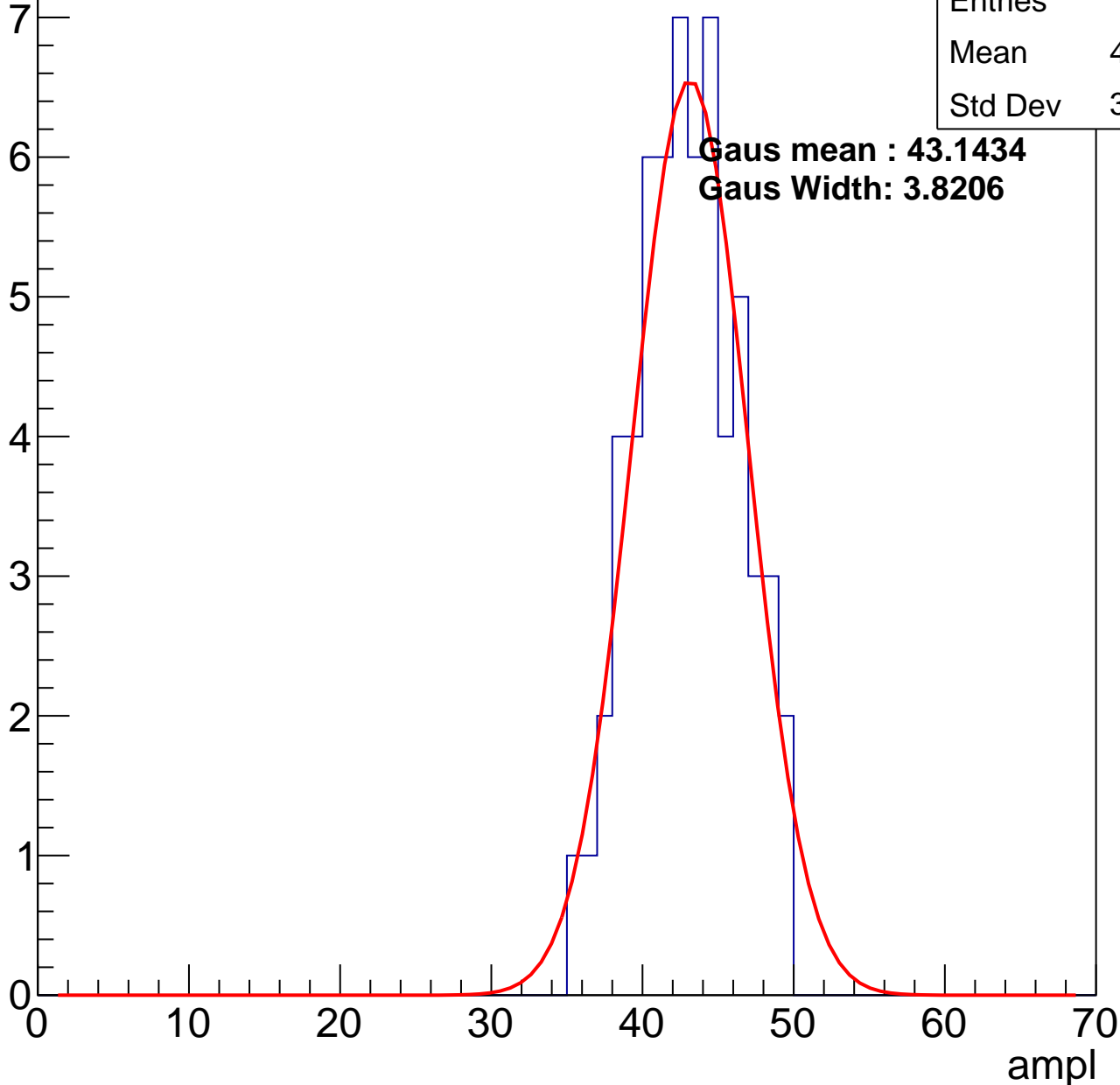


# B0L000S, U25-ch36, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	42.49
Std Dev	3.342

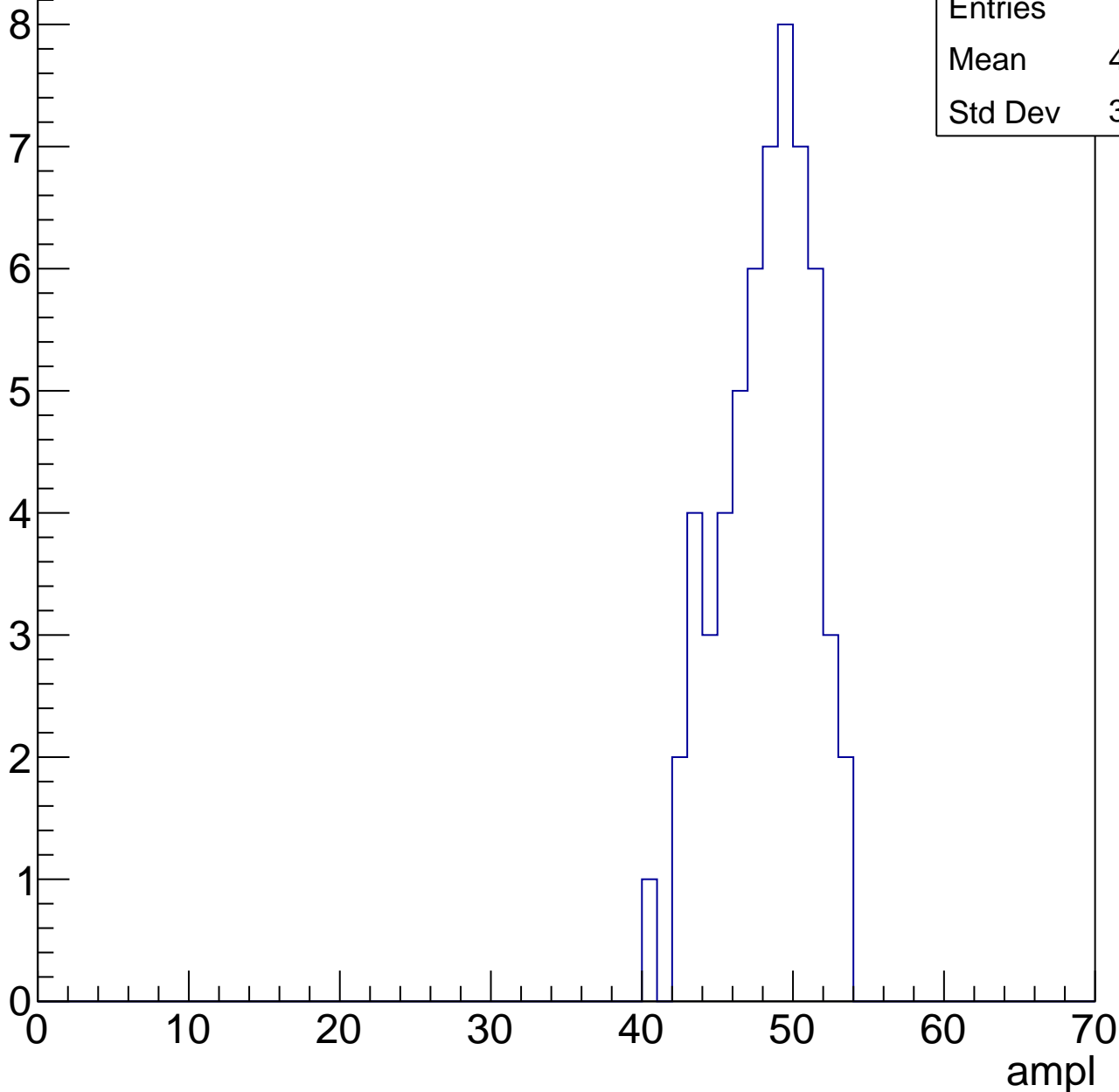


# B0L000S, U25-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	47.69
Std Dev	3.018

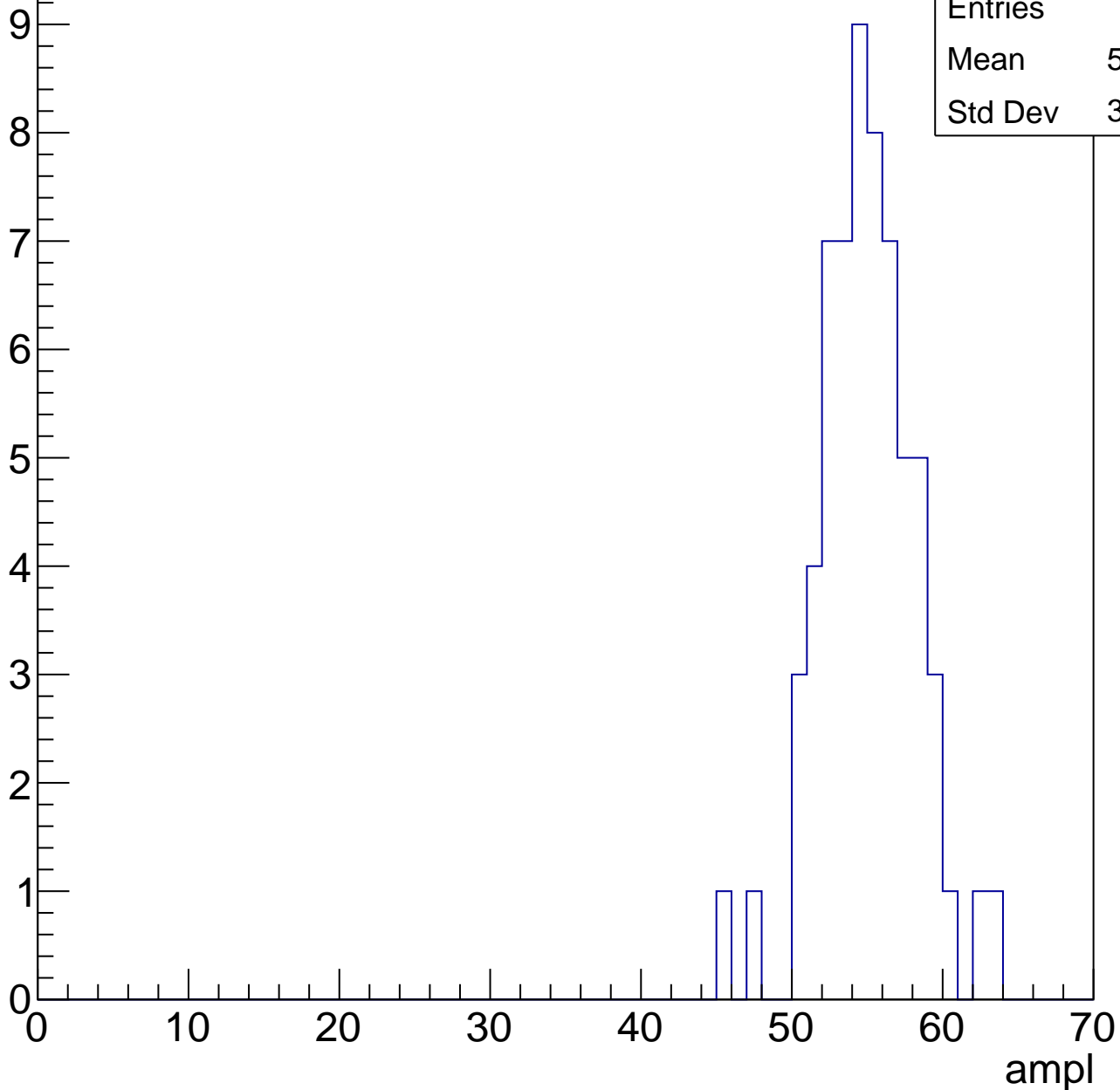


# B0L000S, U25-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	54.54
Std Dev	3.206



# B0L000S, U25-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	59.61
Std Dev	2.469

Entry

10

8

6

4

2

0

0

10

20

30

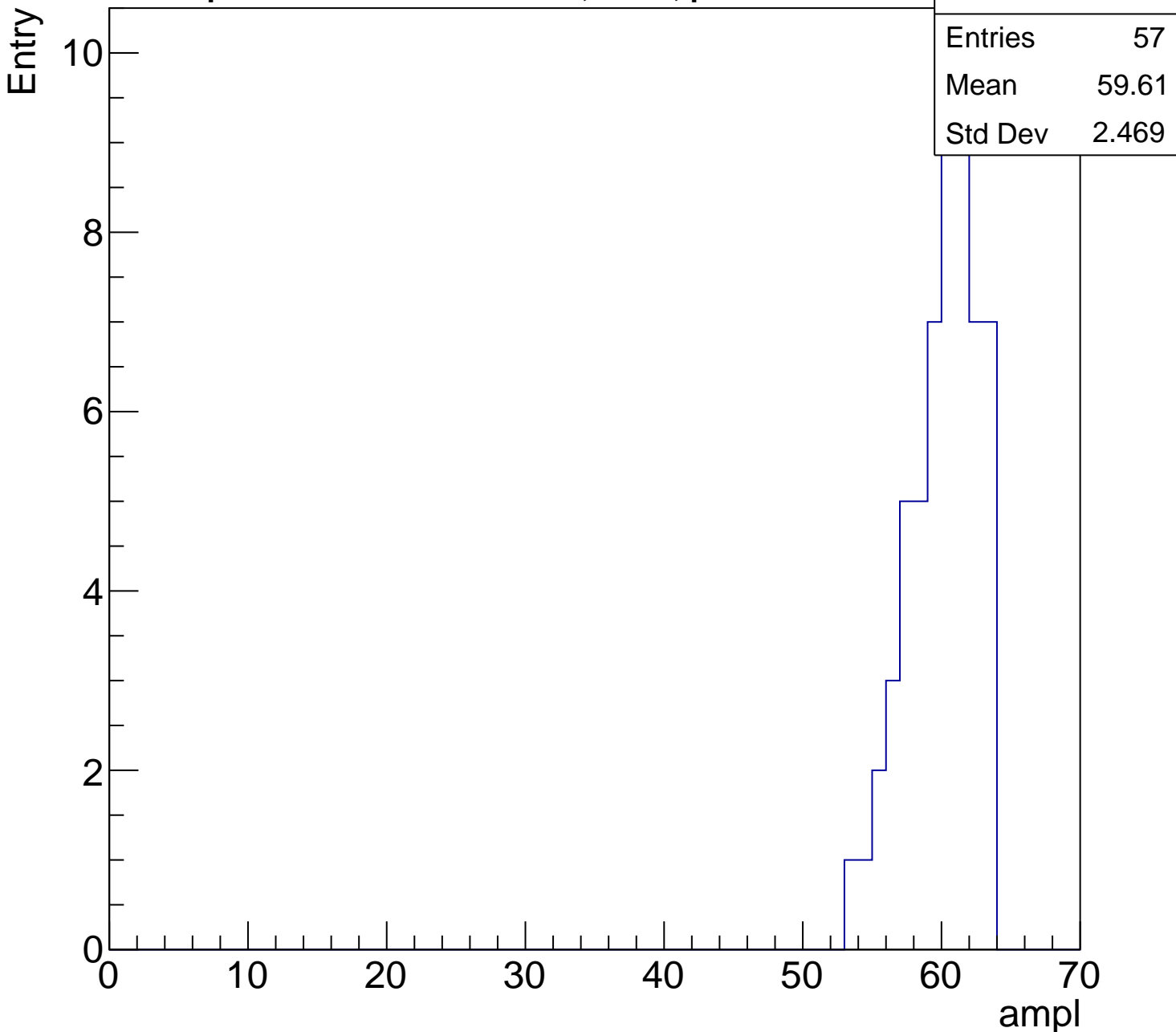
40

50

60

ampl

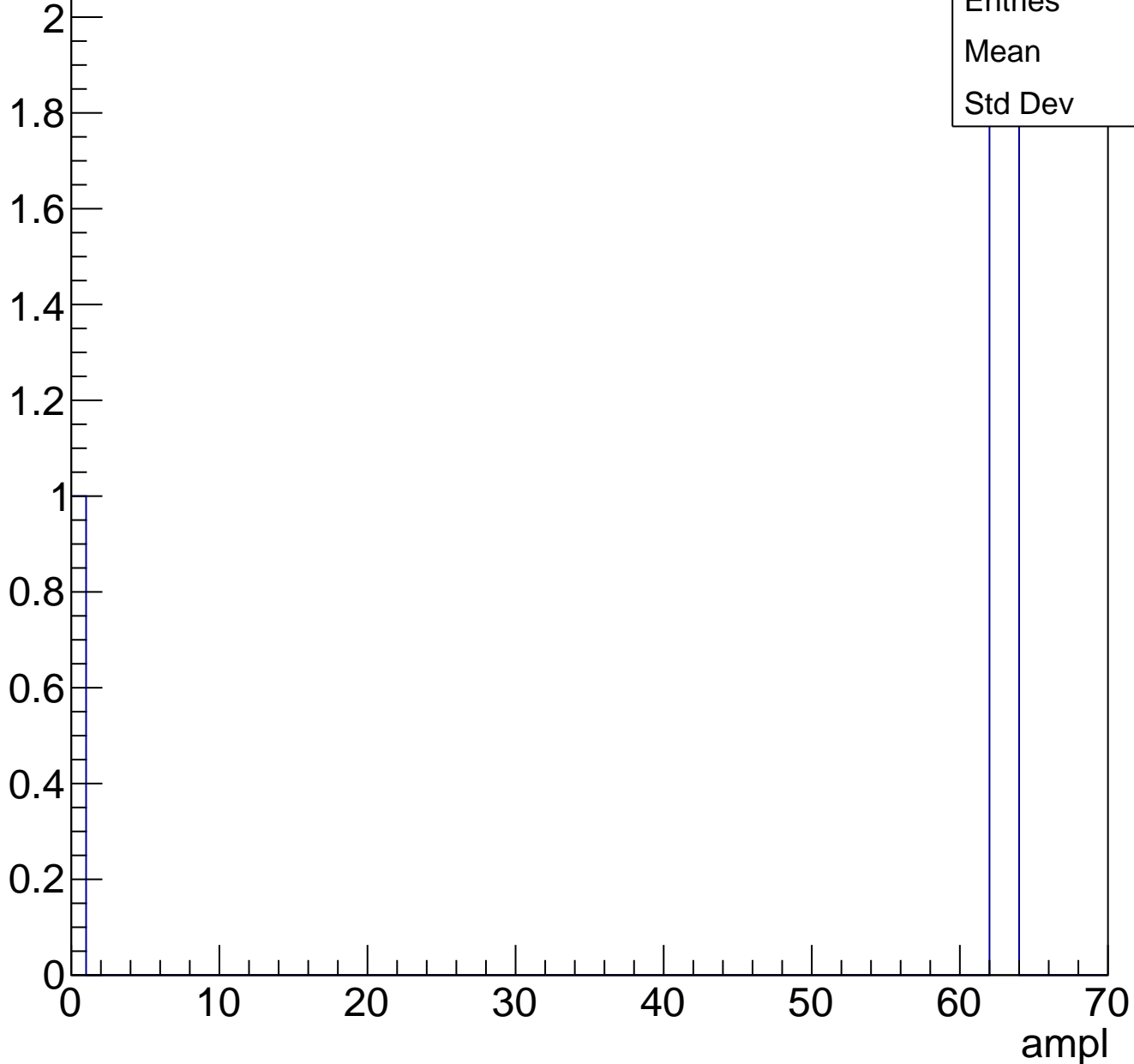
70



# B0L000S, U25-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U25-ch37, adc0

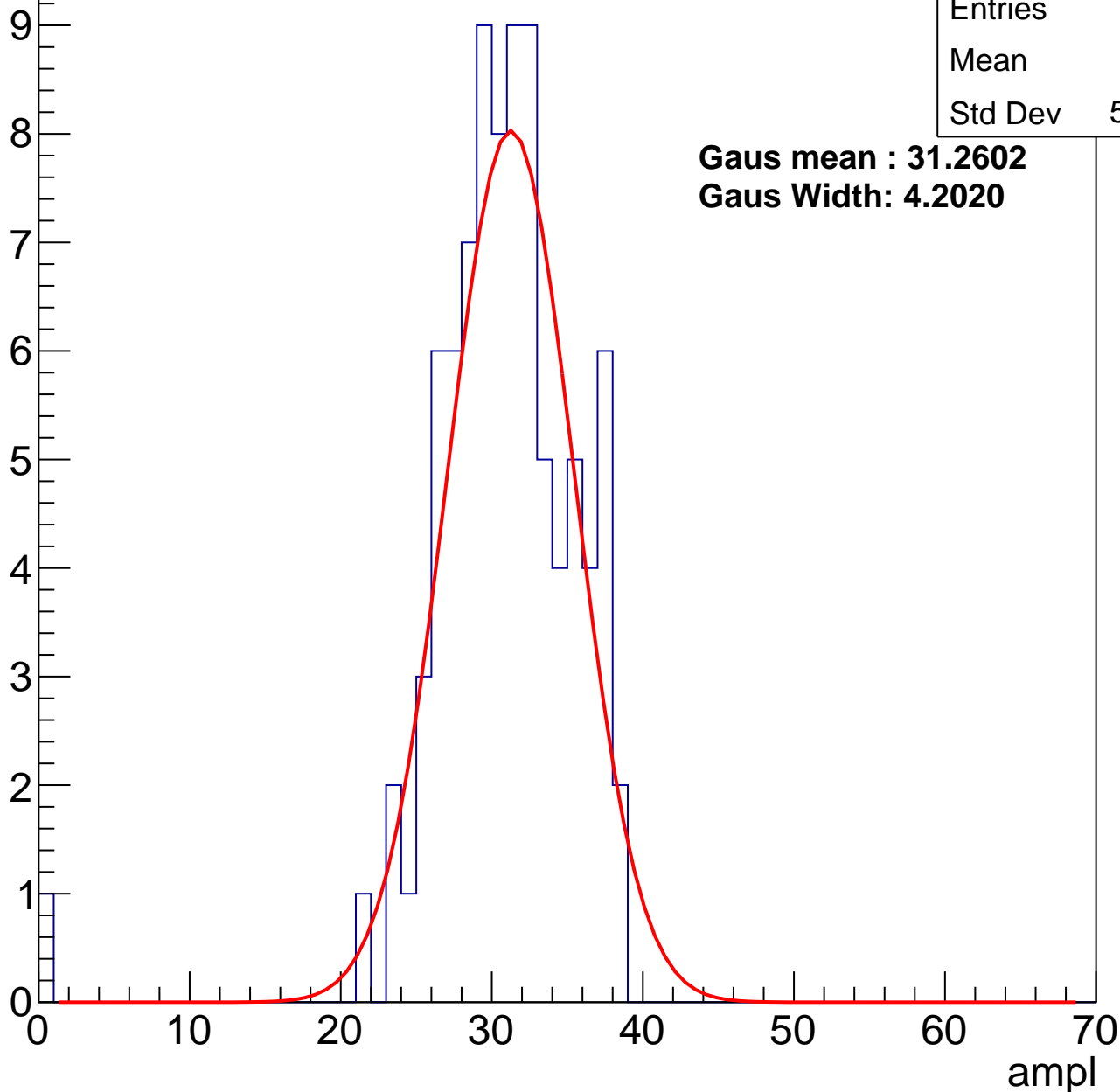
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	88
Mean	30.3
Std Dev	5.016

**Gaus mean : 31.2602**

**Gaus Width: 4.2020**



# B0L000S, U25-ch37, adc1

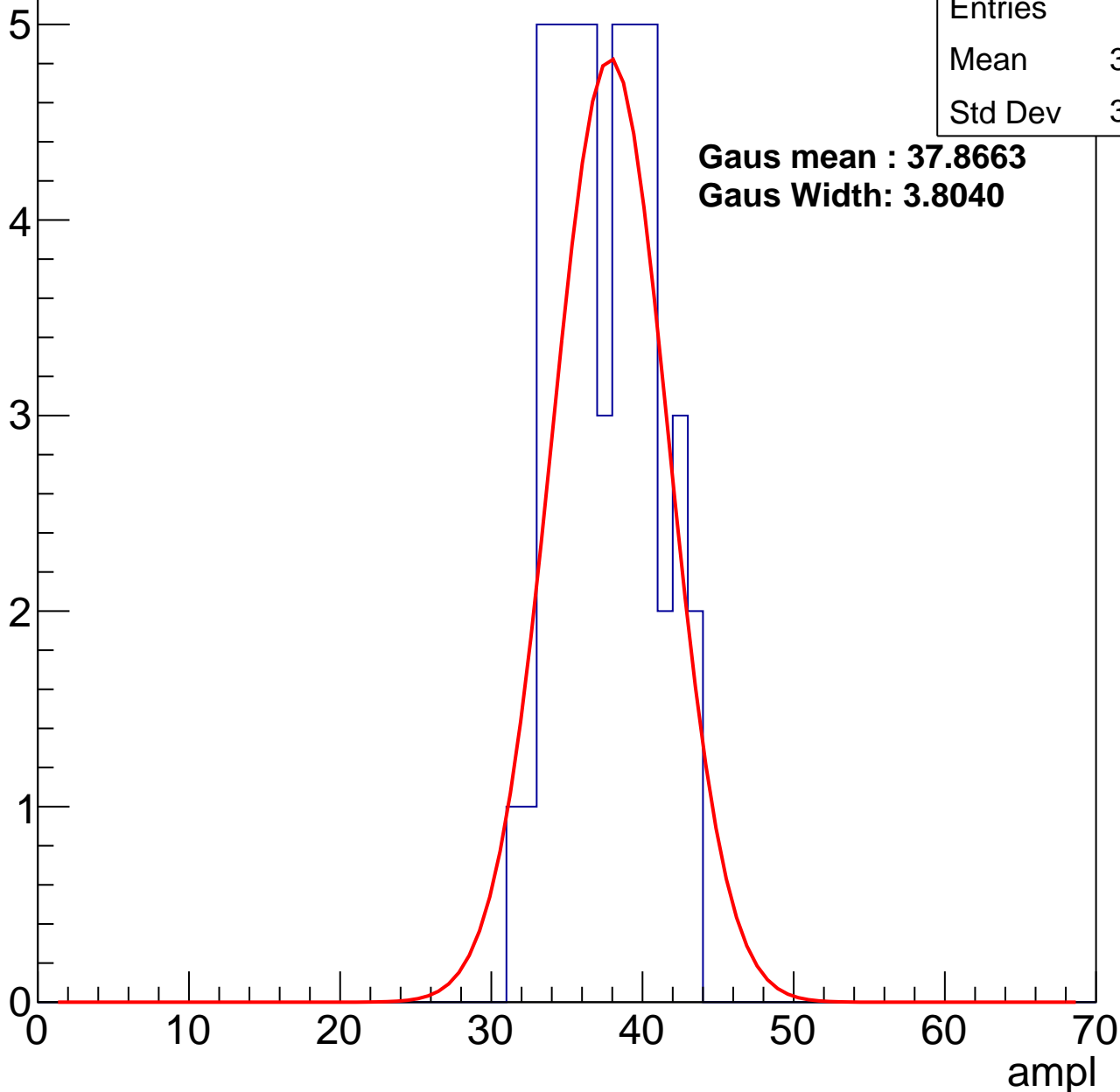
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	37.09
Std Dev	3.127

**Gaus mean : 37.8663**

**Gaus Width: 3.8040**



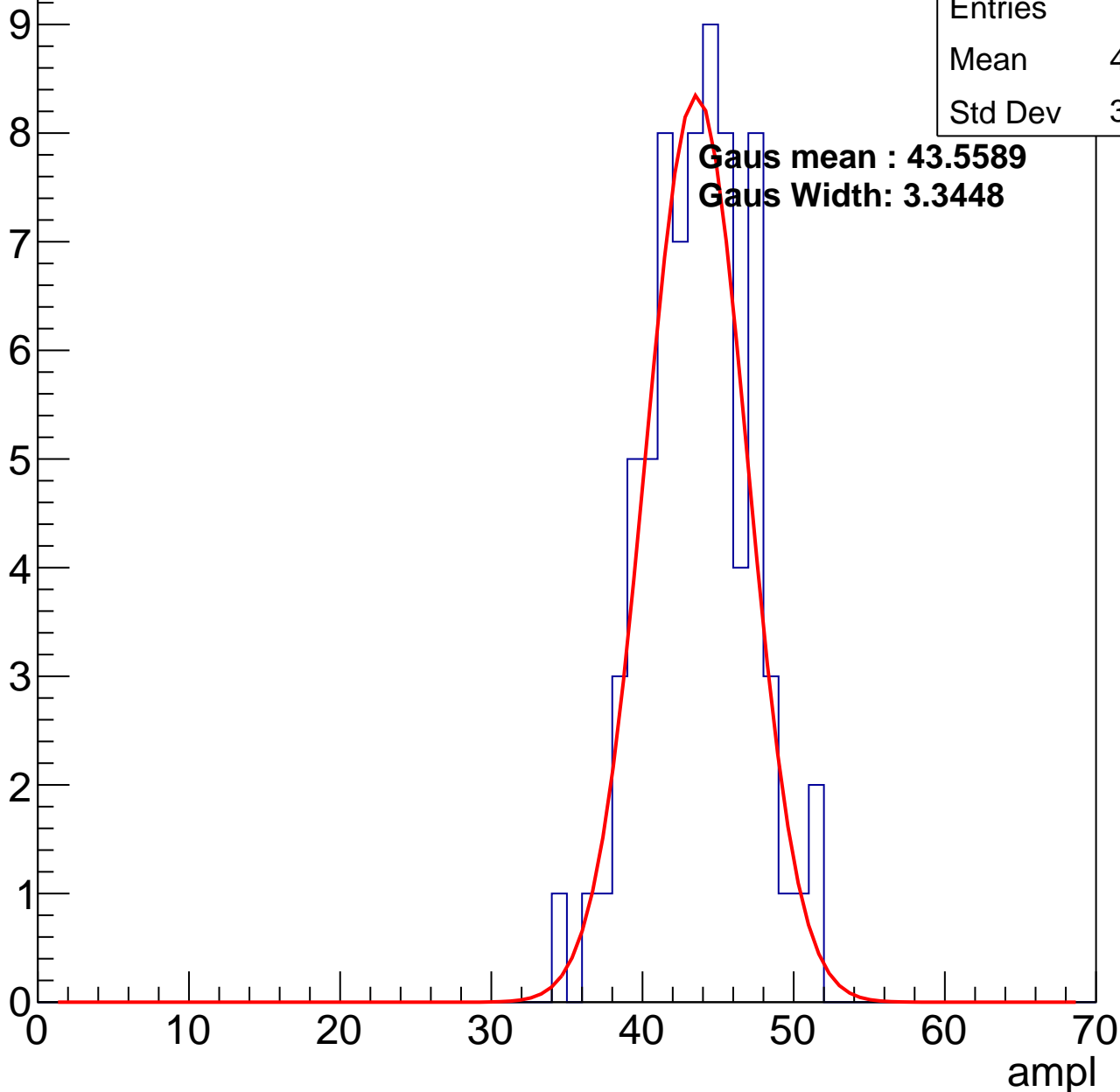
# B0L000S, U25-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	43.24
Std Dev	3.452

**Gaus mean : 43.5589**  
**Gaus Width: 3.3448**

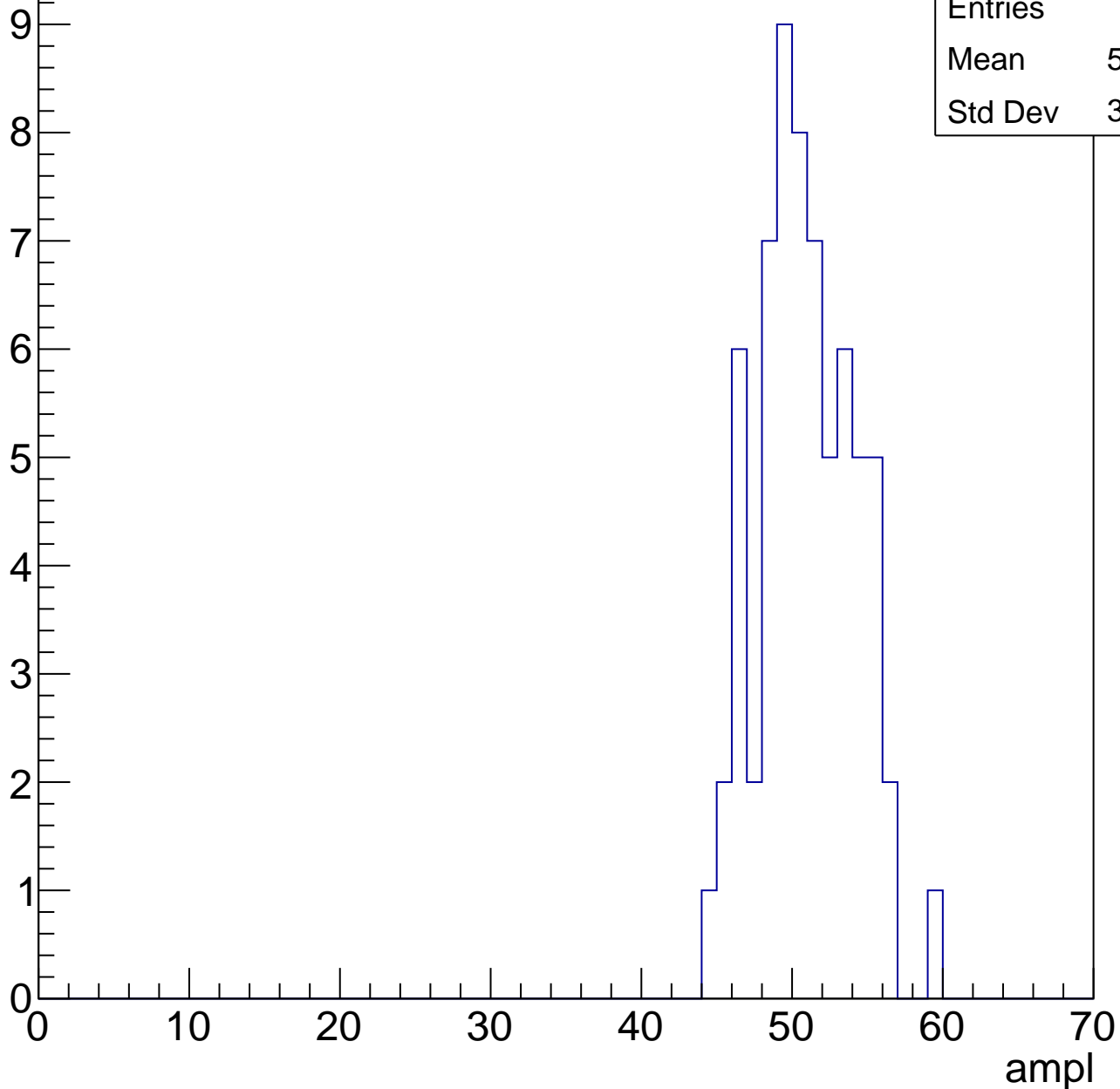


# B0L000S, U25-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	50.48
Std Dev	3.163

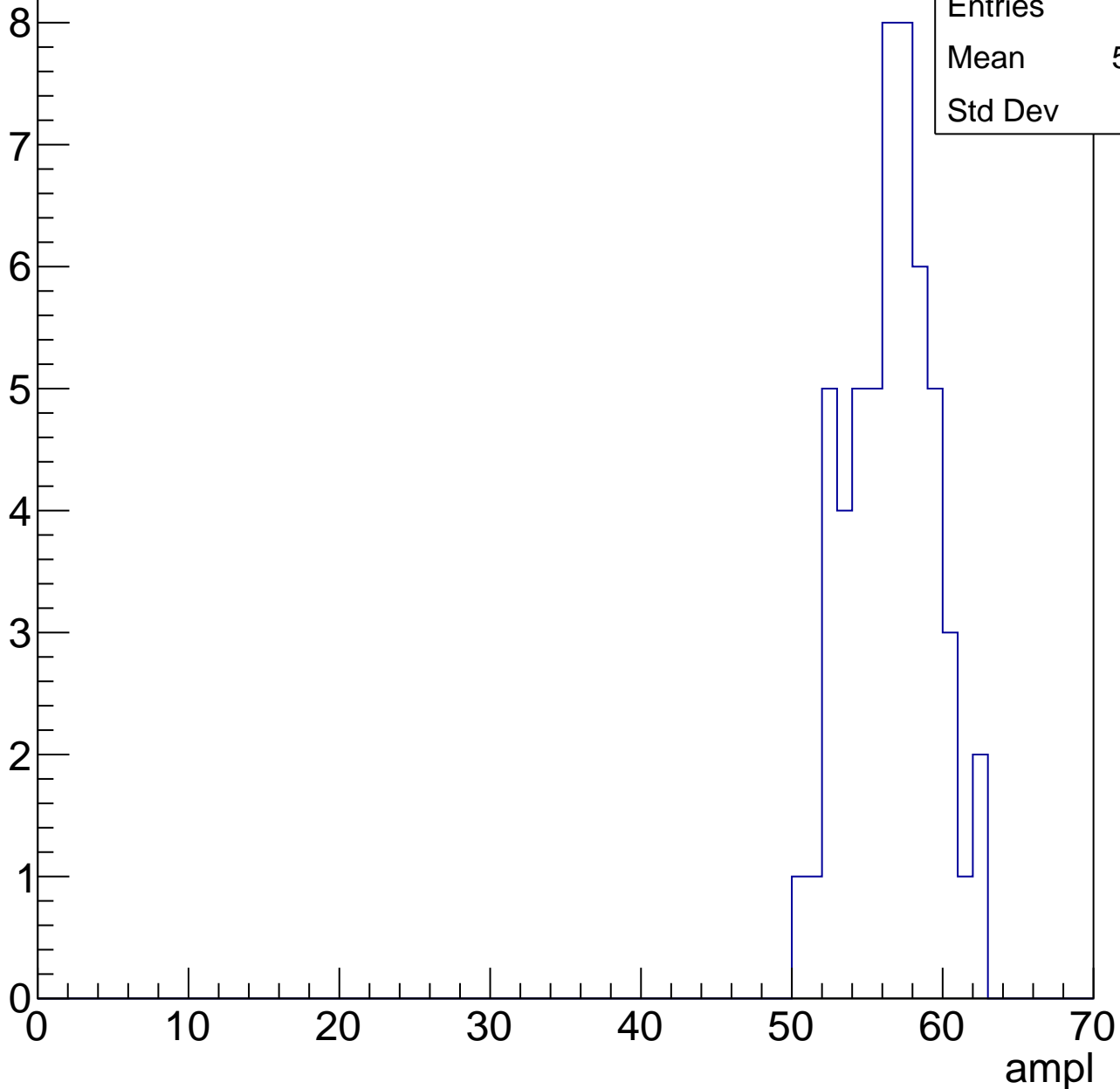


# B0L000S, U25-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

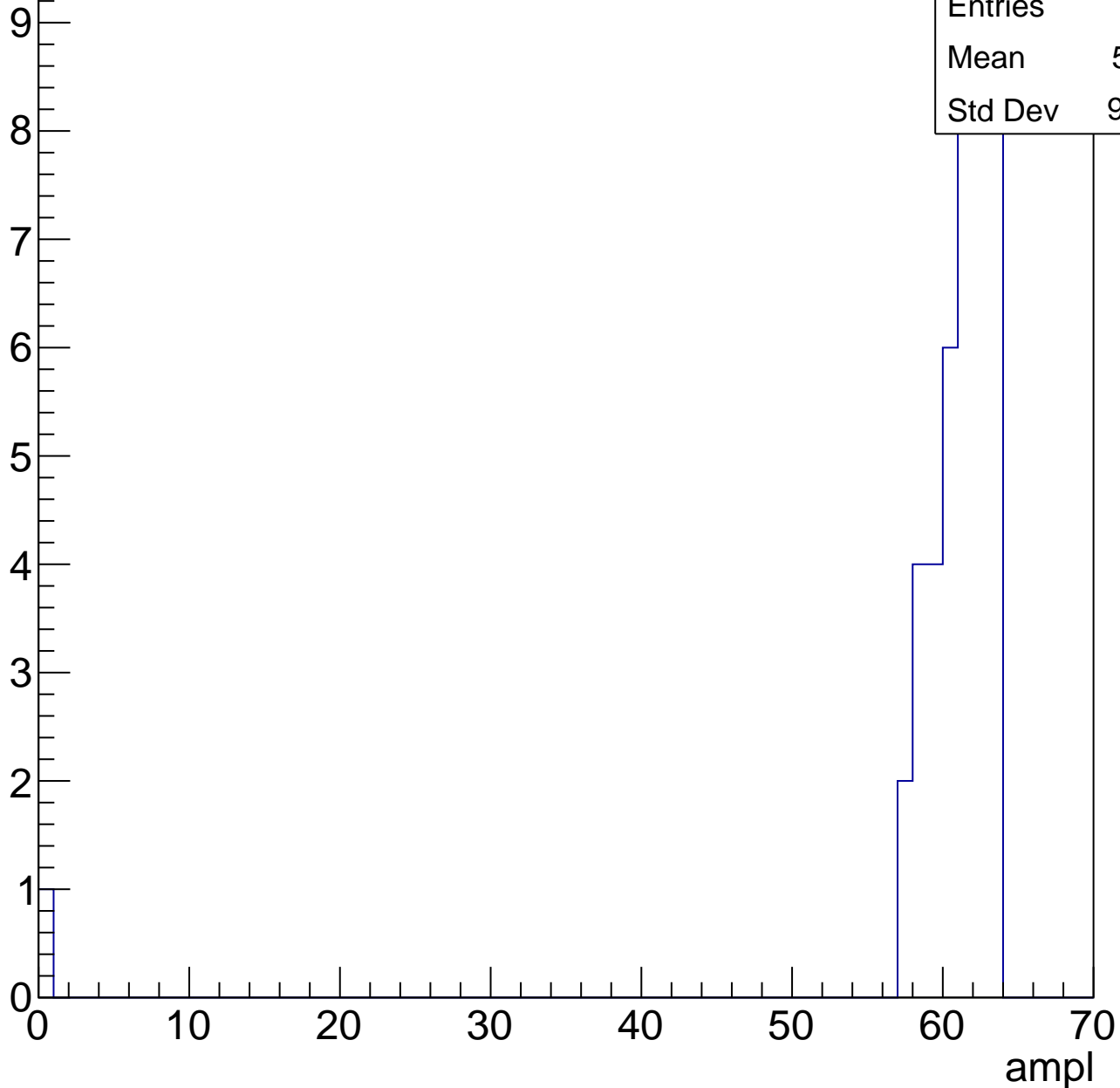
Entries	54
Mean	56.11
Std Dev	2.8



# B0L000S, U25-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch38, adc0

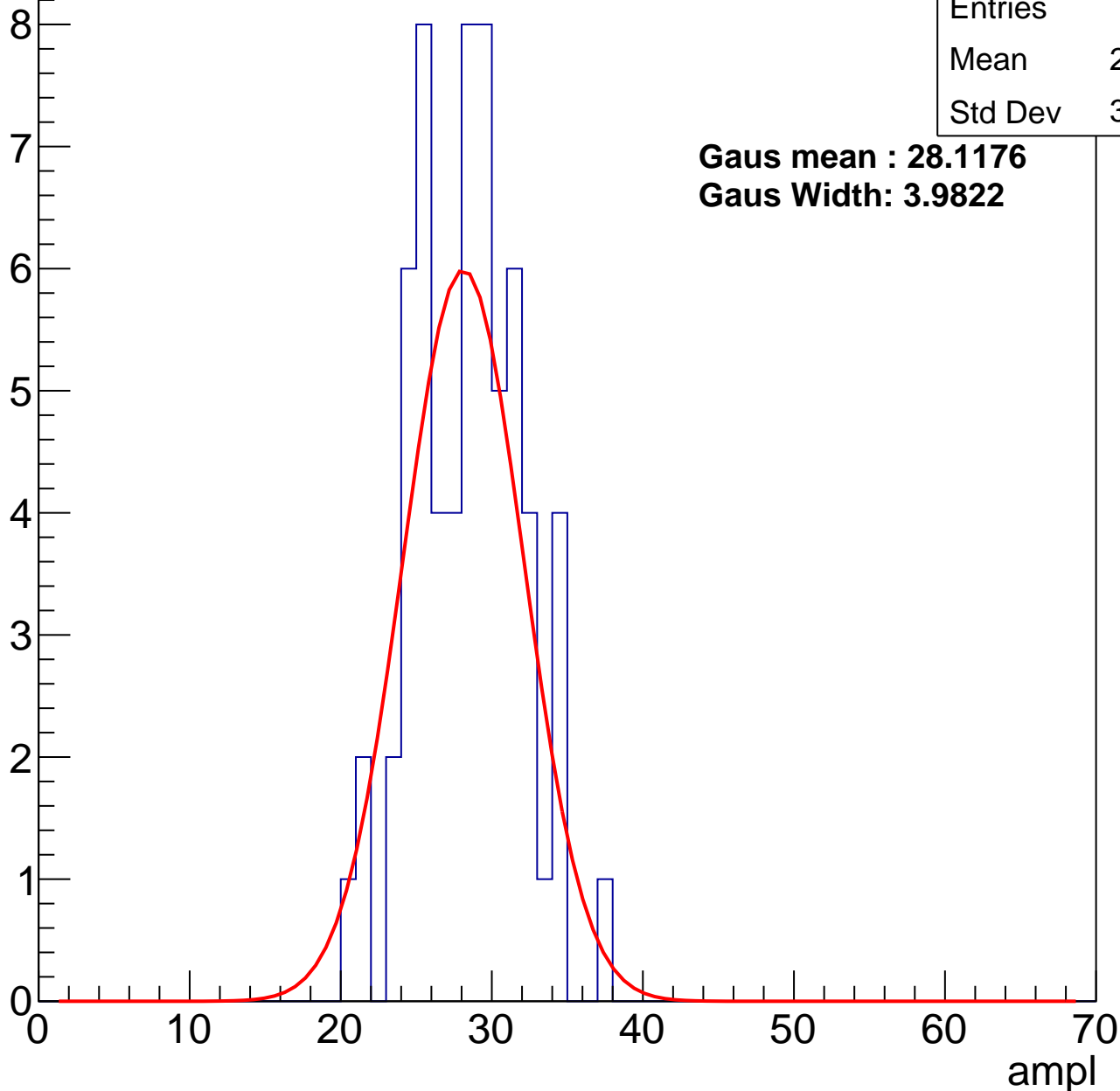
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	27.97
Std Dev	3.527

**Gaus mean : 28.1176**

**Gaus Width: 3.9822**



# B0L000S, U25-ch38, adc1

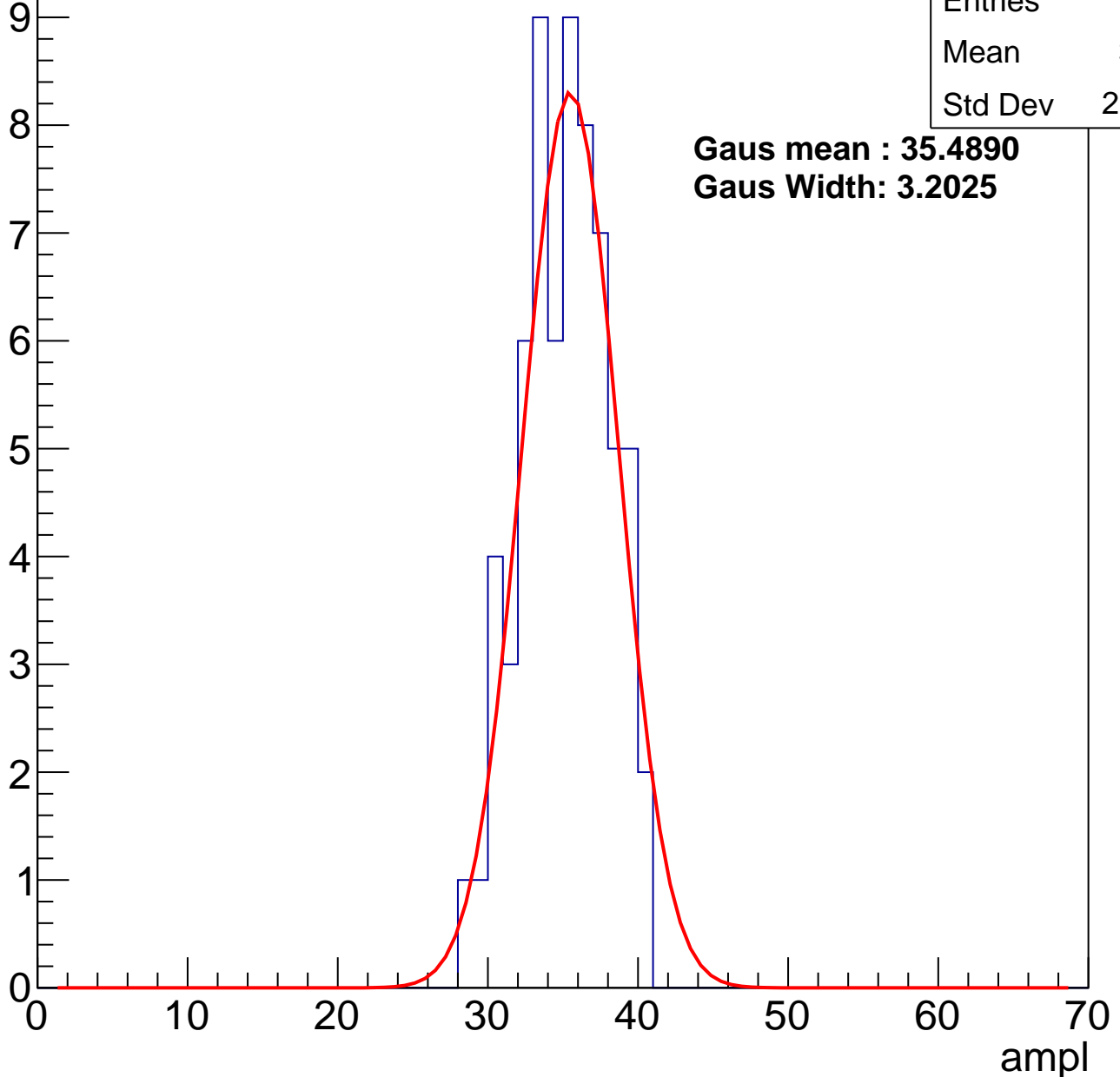
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	34.7
Std Dev	2.844

**Gaus mean : 35.4890**

**Gaus Width: 3.2025**



# B0L000S, U25-ch38, adc2

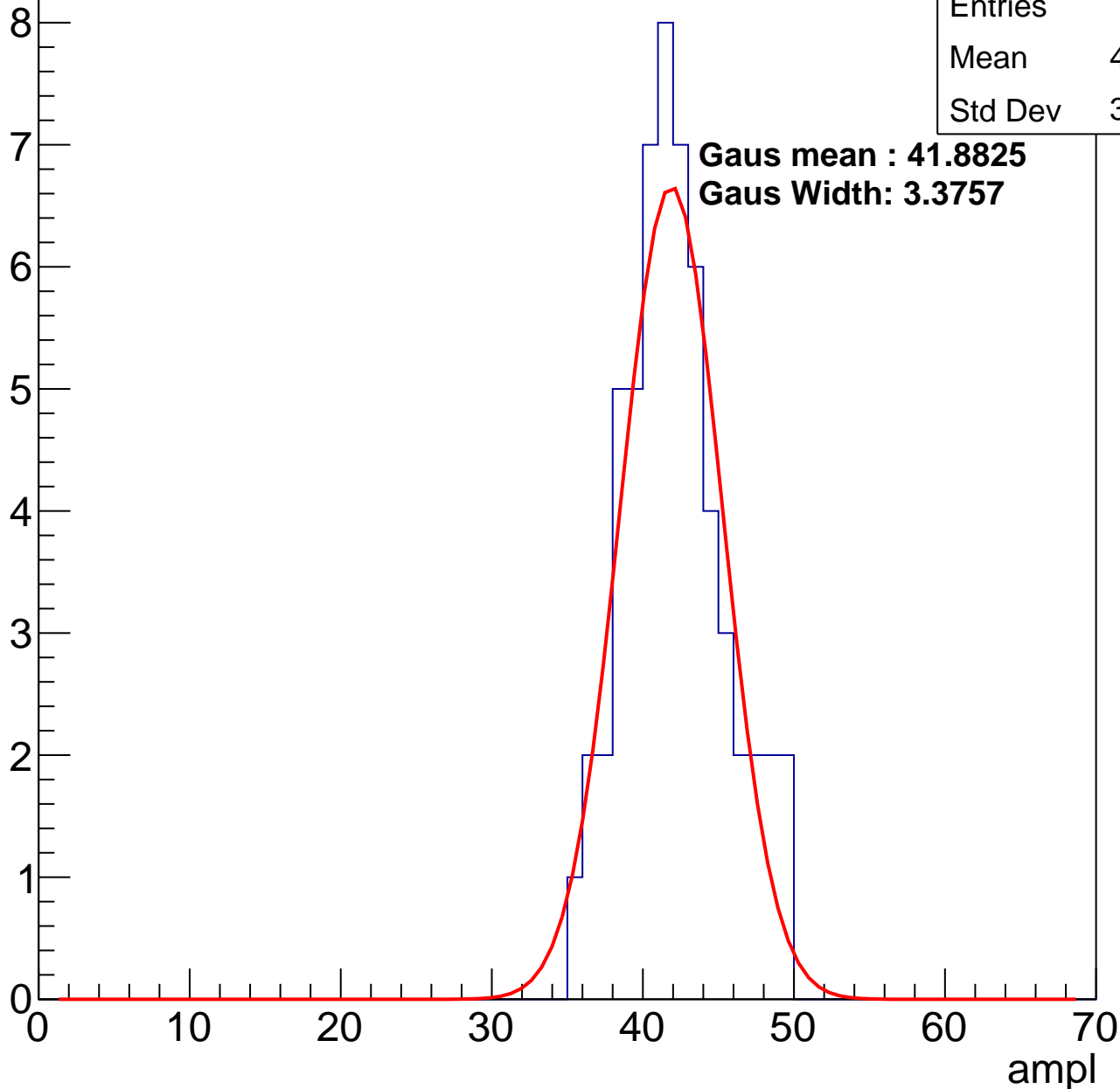
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	41.67
Std Dev	3.287

**Gaus mean : 41.8825**

**Gaus Width: 3.3757**

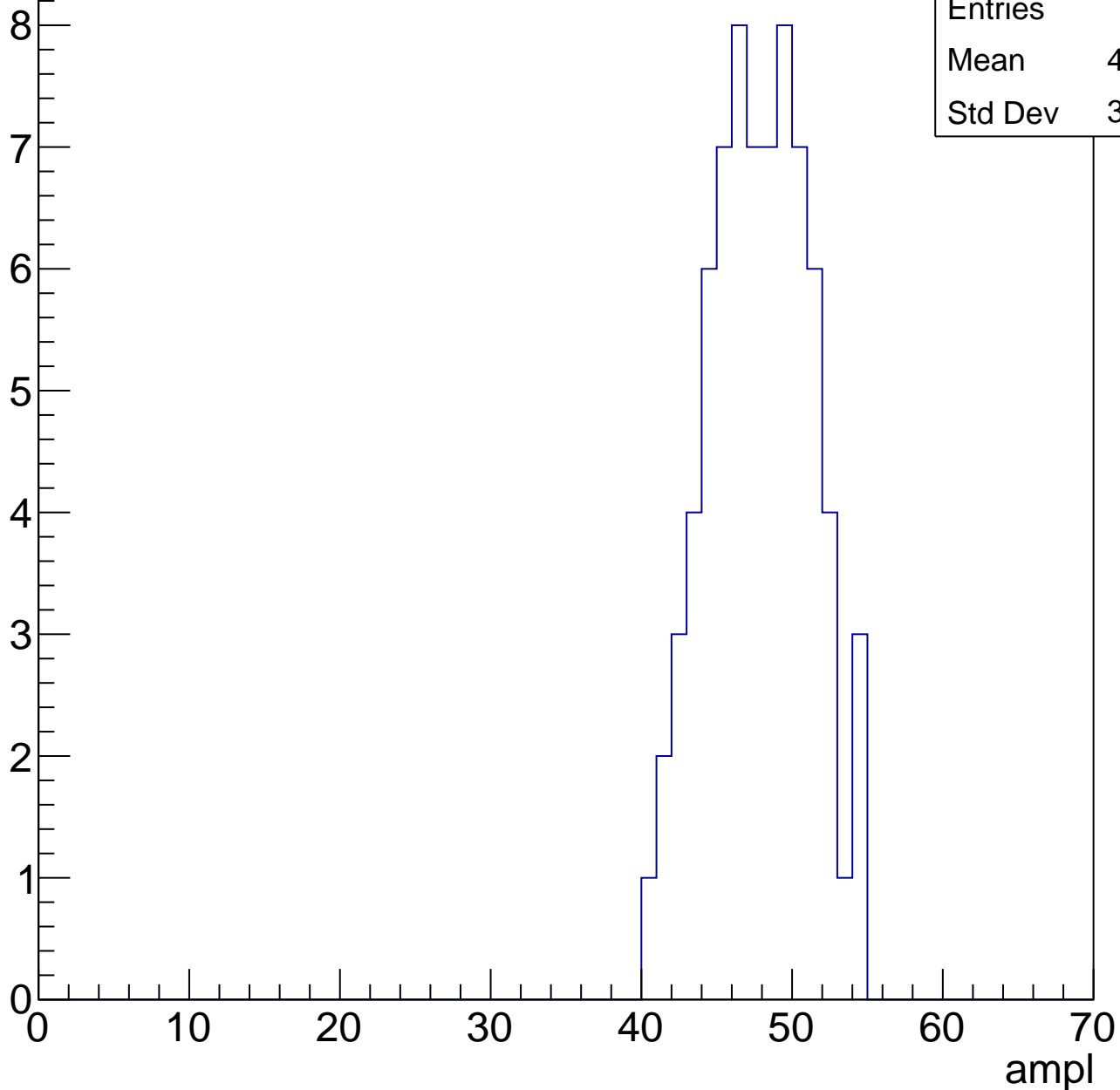


# B0L000S, U25-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	47.34
Std Dev	3.334

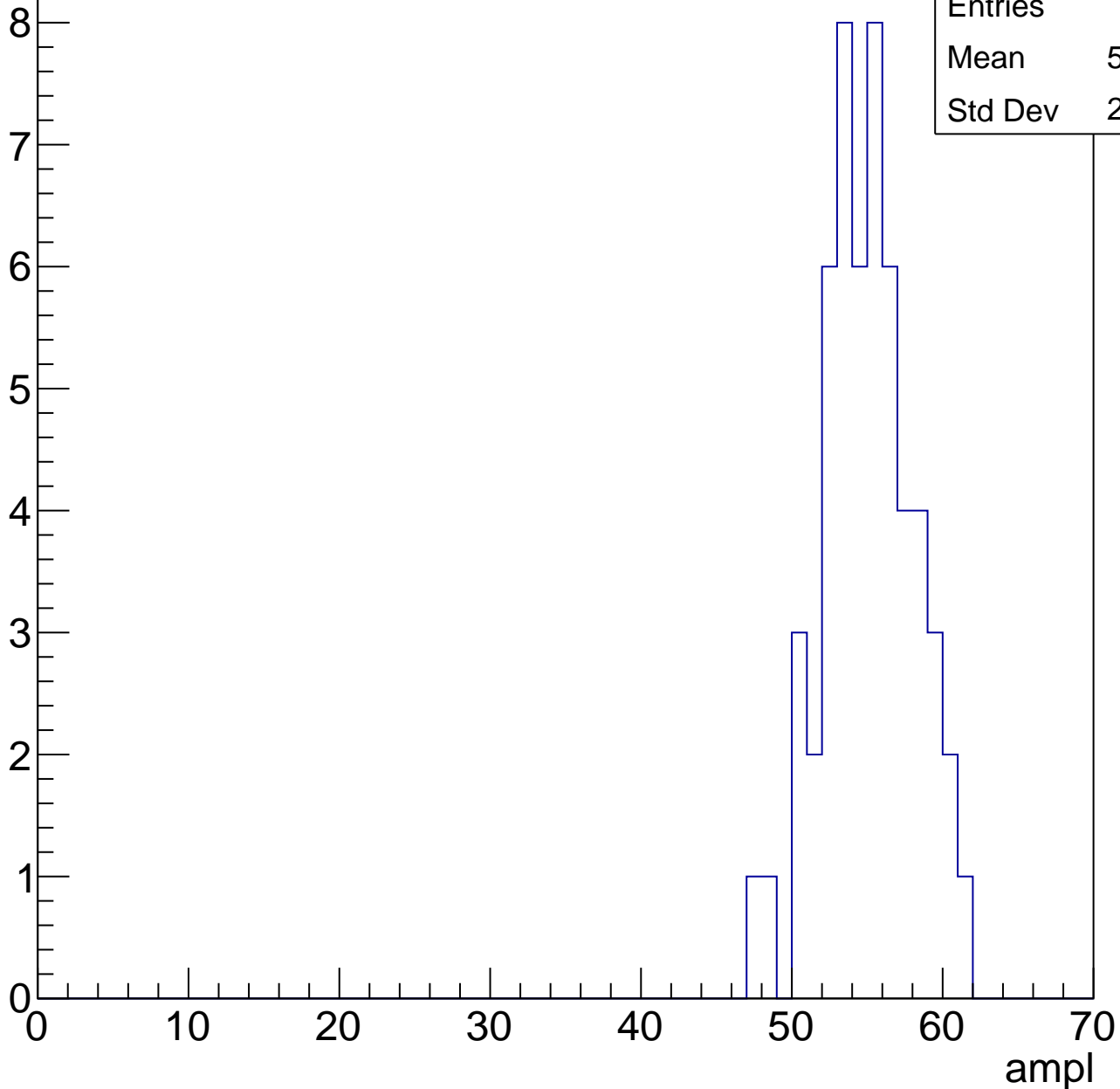


# B0L000S, U25-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	54.56
Std Dev	2.996

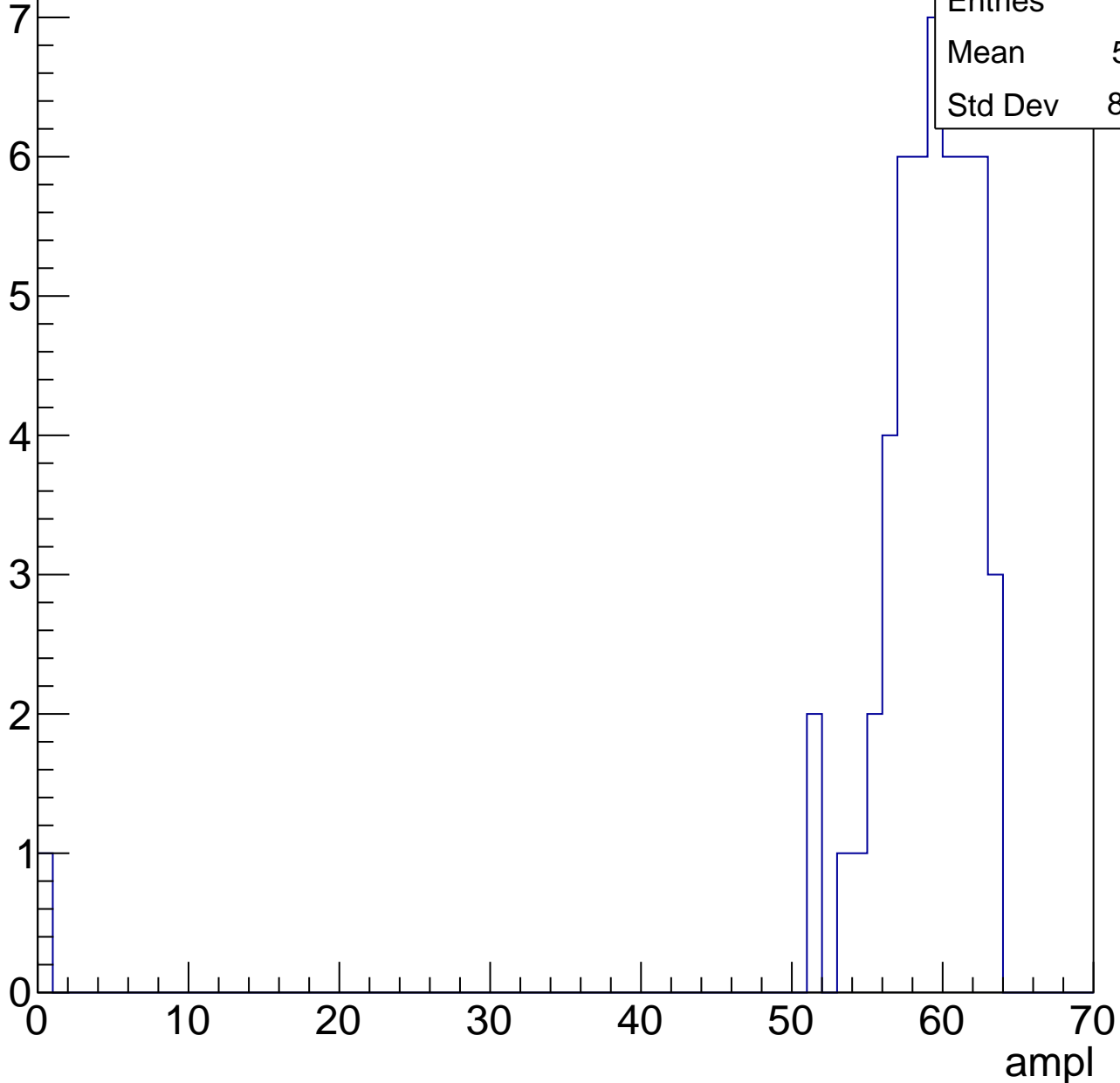


# B0L000S, U25-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	57.51
Std Dev	8.617

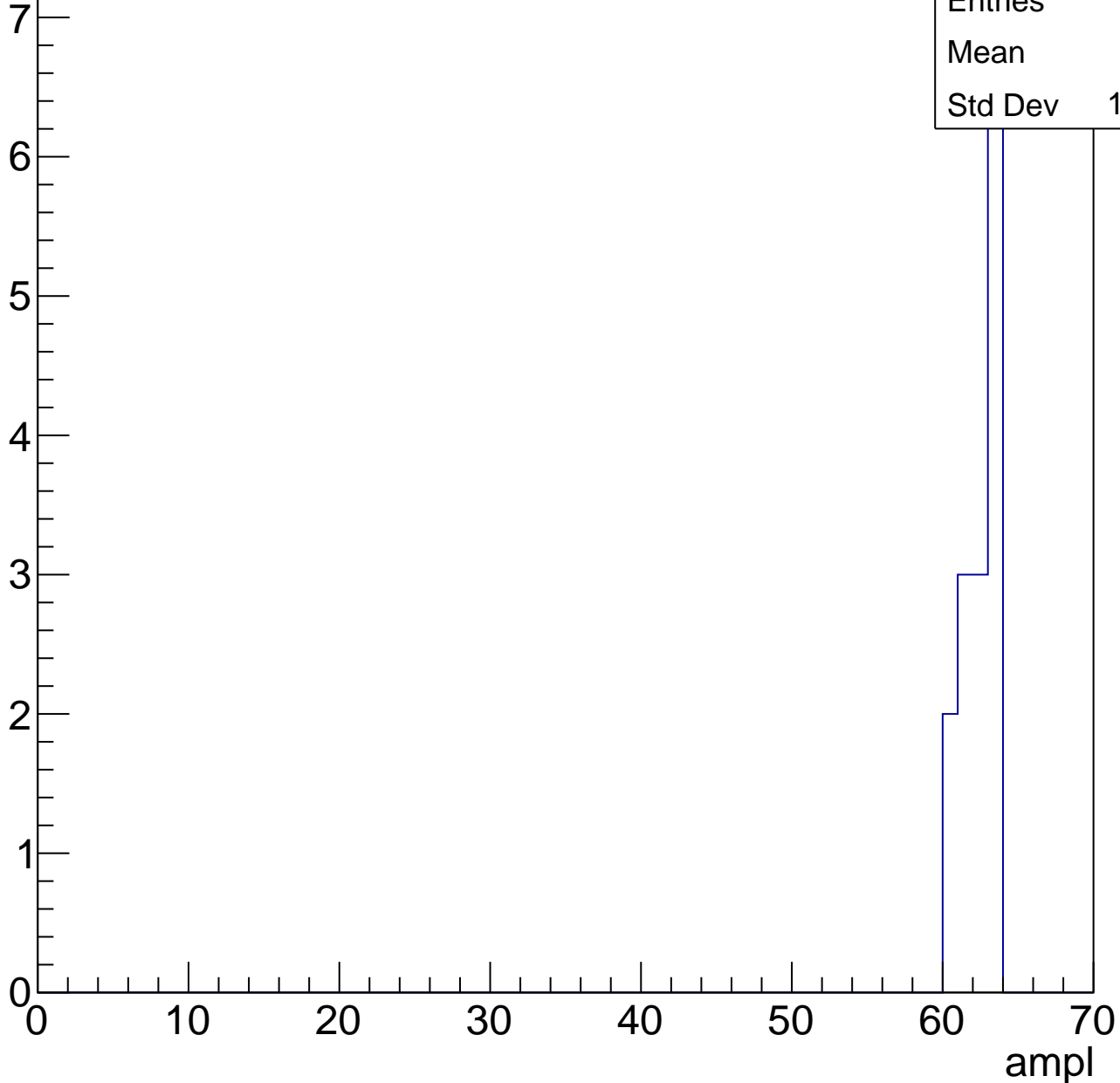


# B0L000S, U25-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	15
Mean	62
Std Dev	1.095





# B0L000S, U25-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch39, adc0

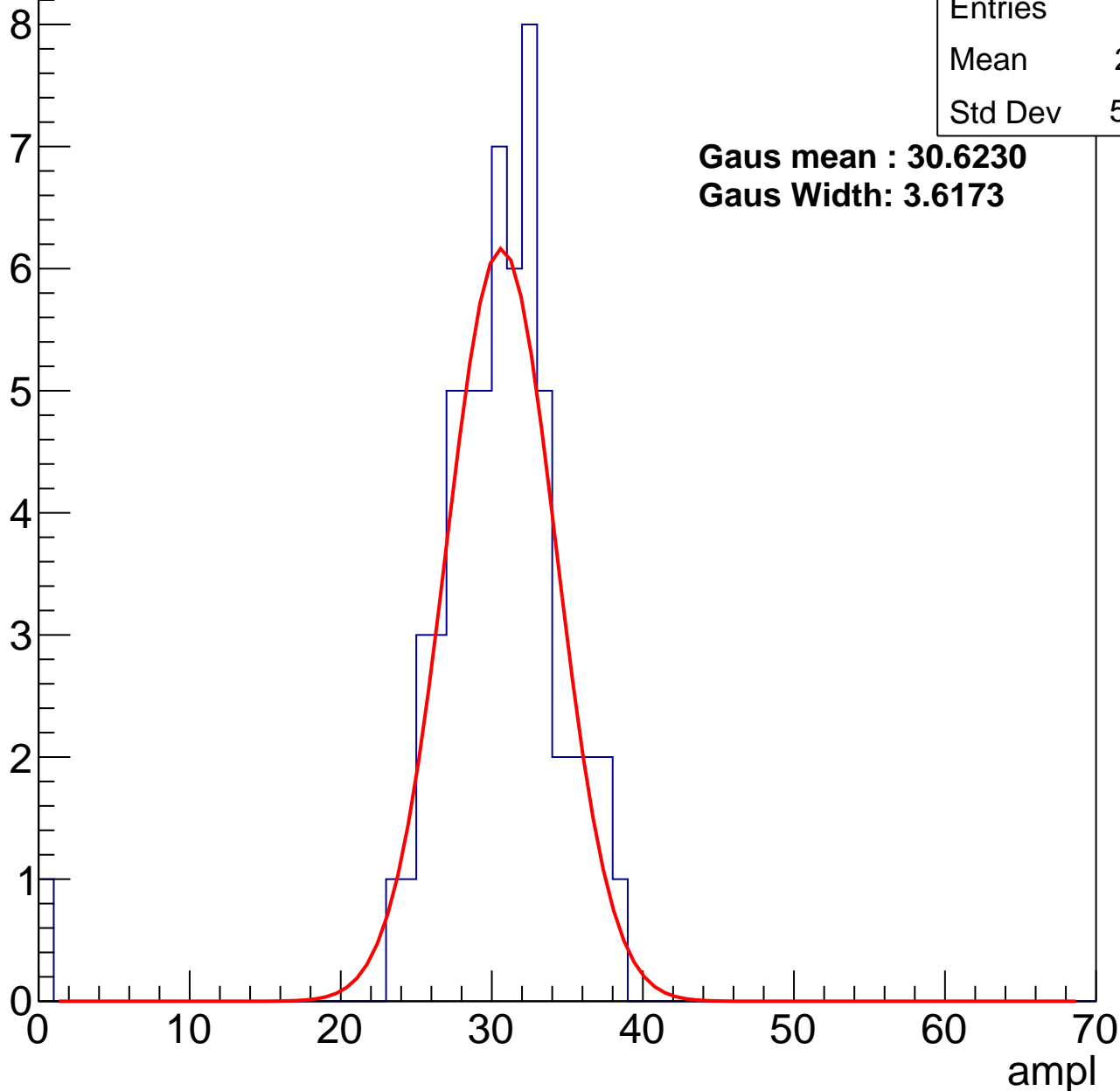
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.81
Std Dev	5.167

**Gaus mean : 30.6230**

**Gaus Width: 3.6173**



# B0L000S, U25-ch39, adc1

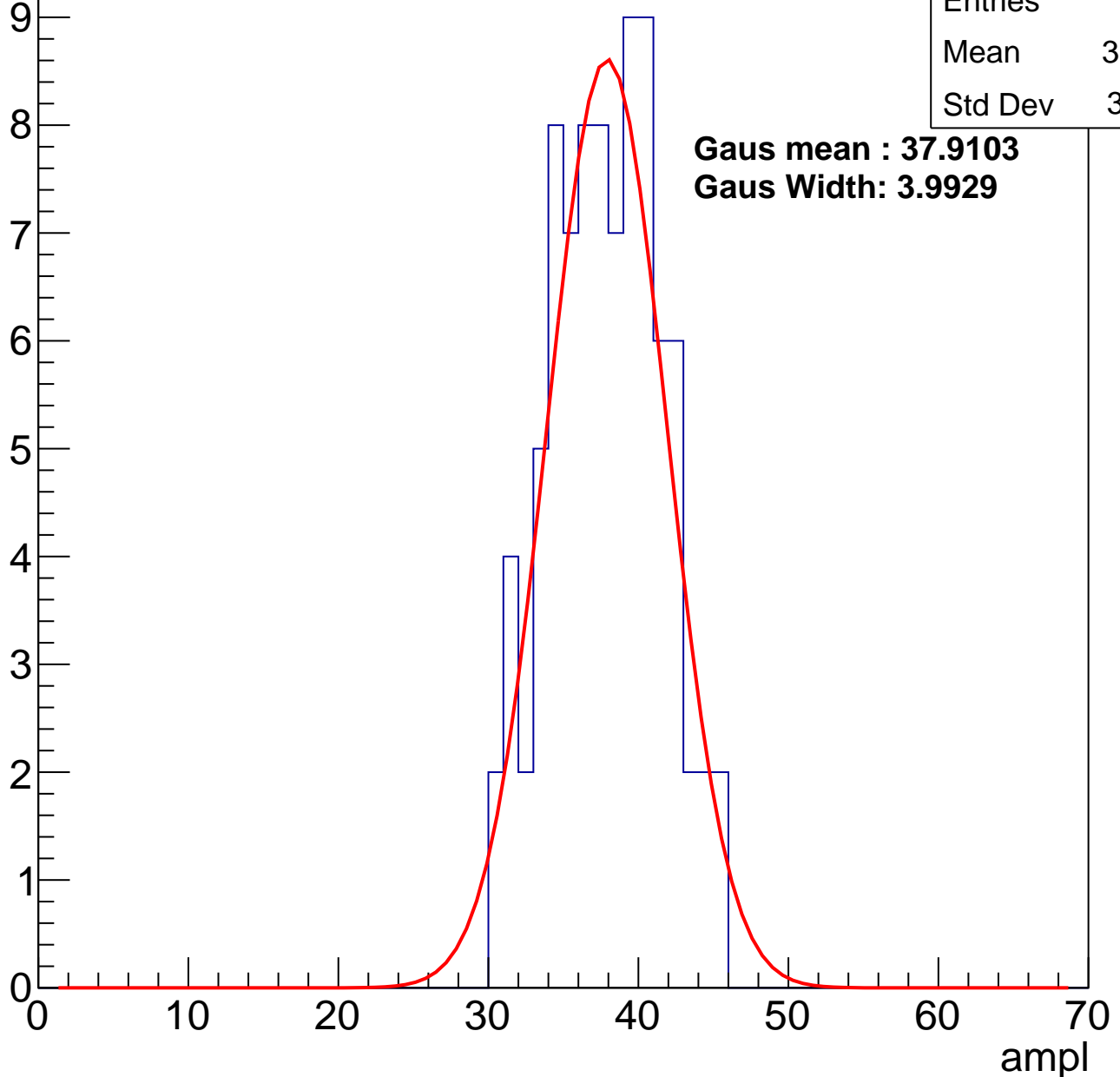
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	87
Mean	37.39
Std Dev	3.611

**Gaus mean : 37.9103**

**Gaus Width: 3.9929**



# B0L000S, U25-ch39, adc2

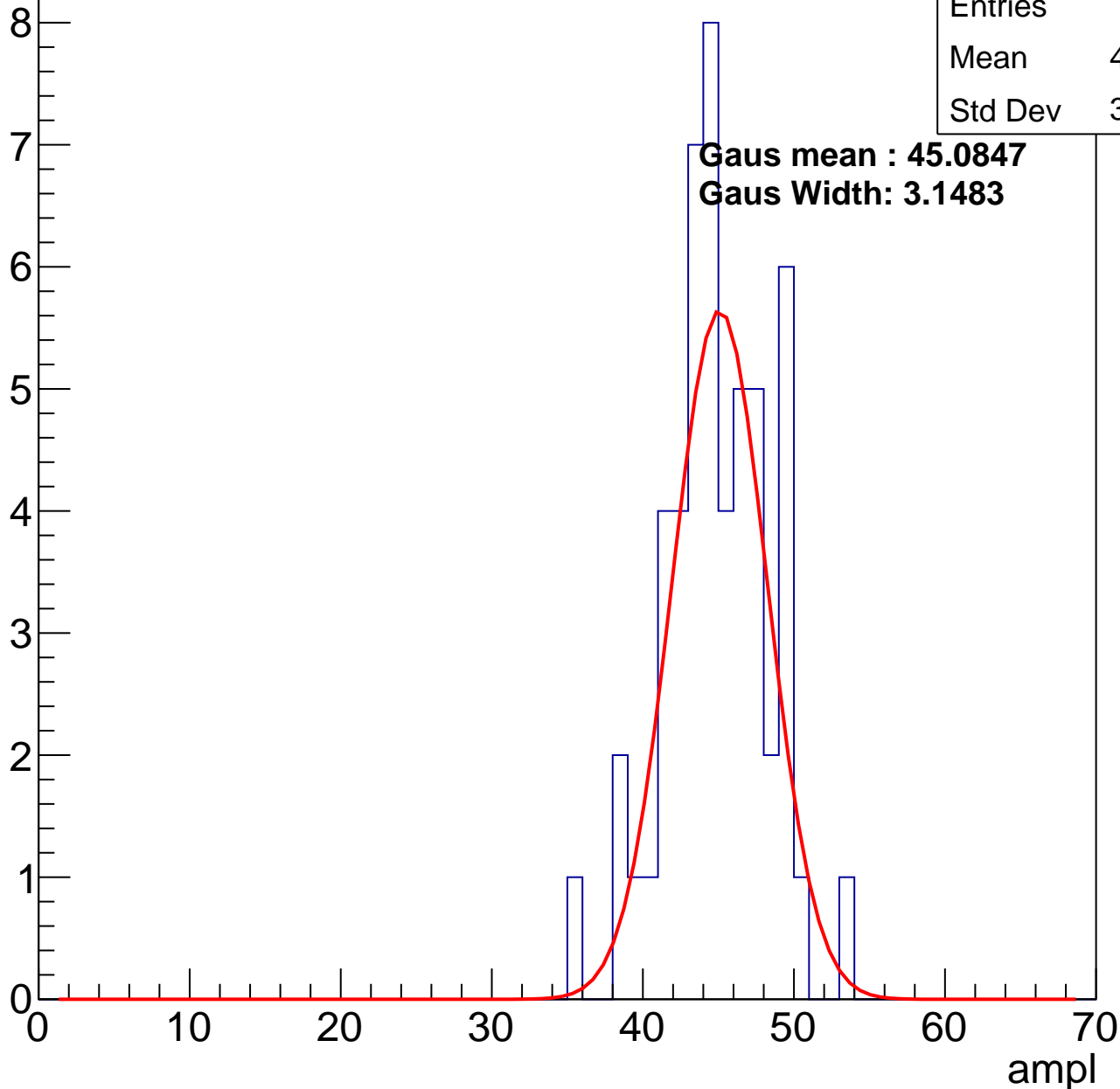
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.48
Std Dev	3.422

**Gaus mean : 45.0847**

**Gaus Width: 3.1483**

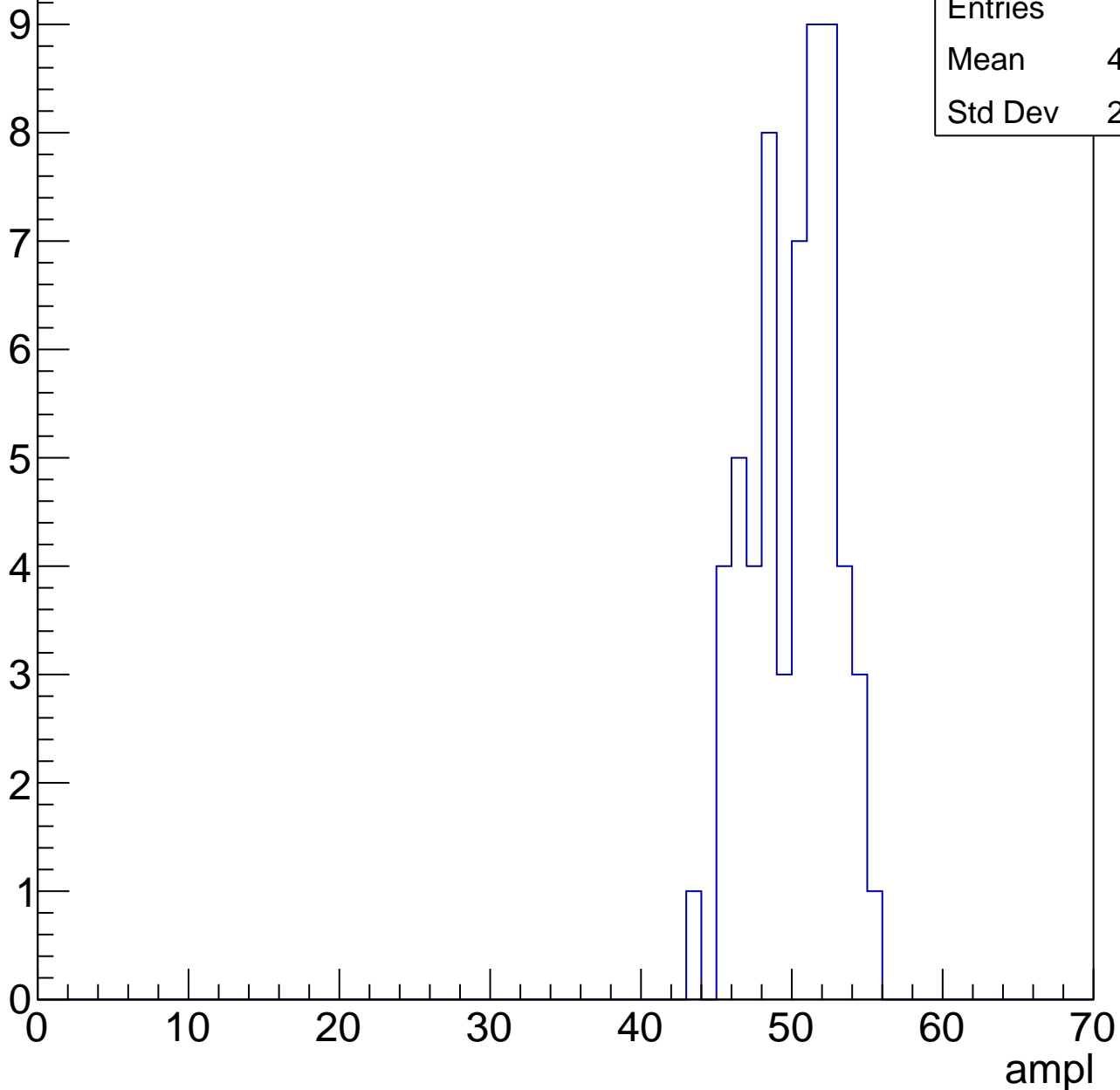


# B0L000S, U25-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	49.62
Std Dev	2.772

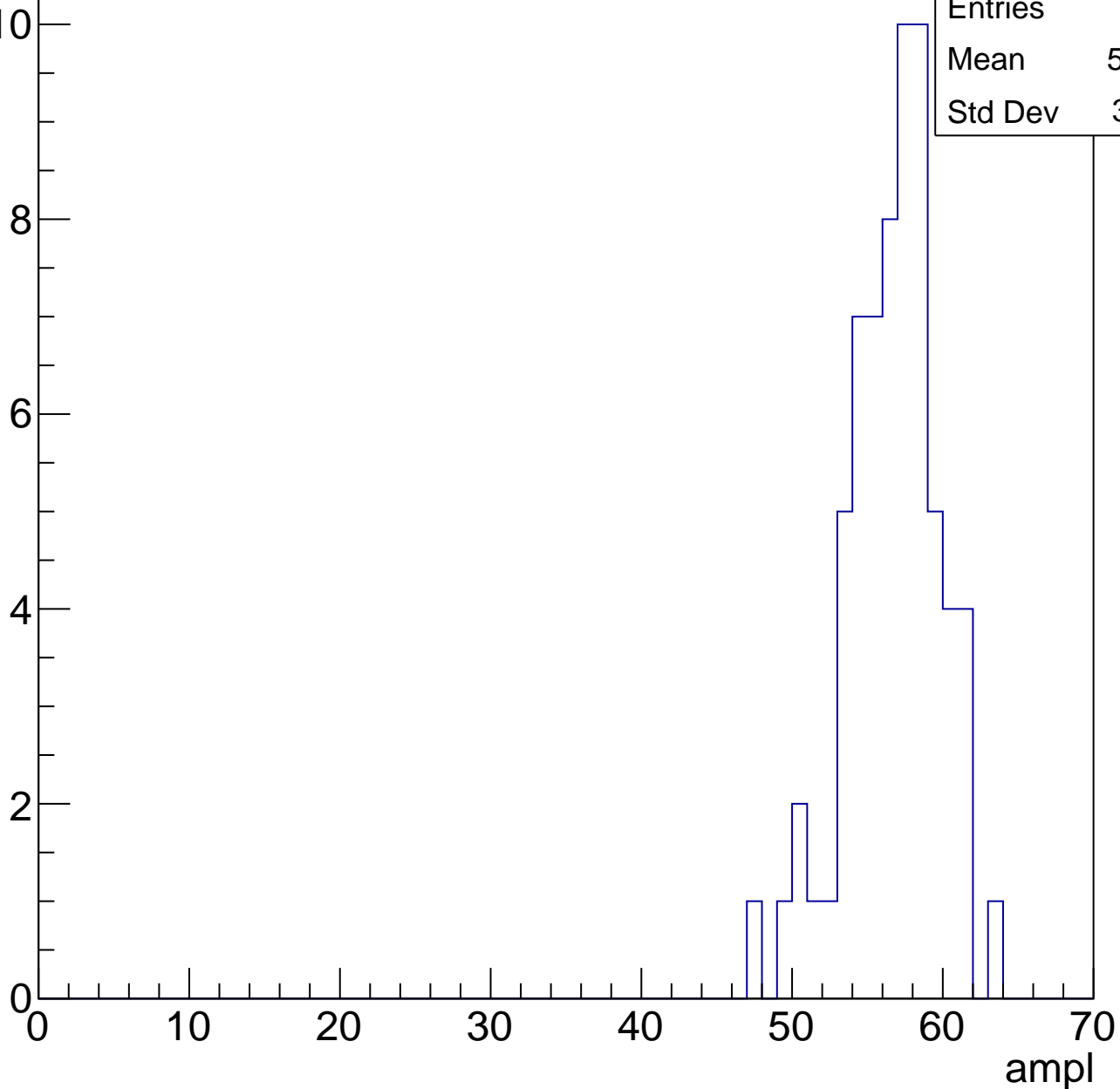


# B0L000S, U25-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	56.22
Std Dev	3.061

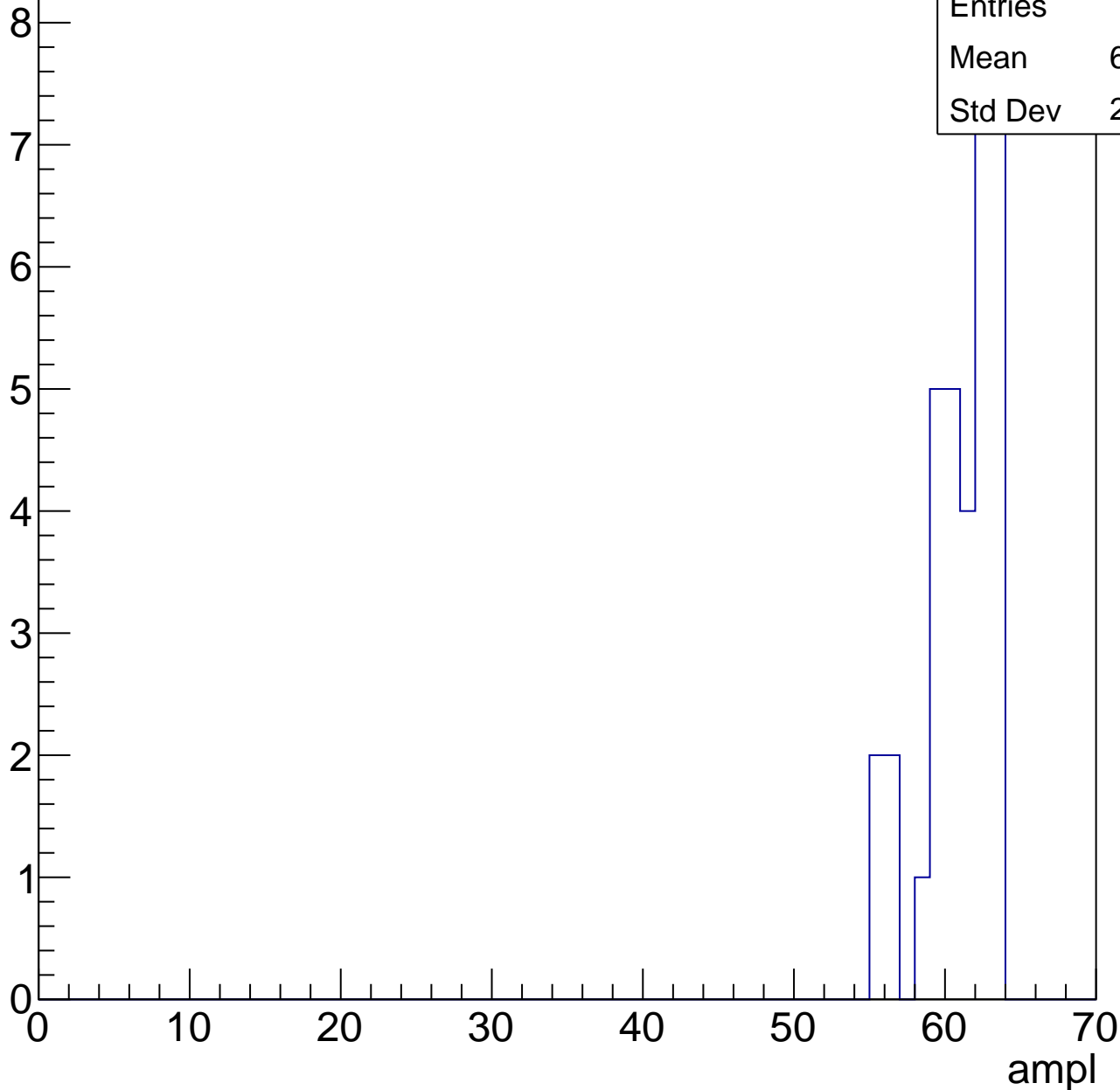


# B0L000S, U25-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	60.54
Std Dev	2.322



# B0L000S, U25-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

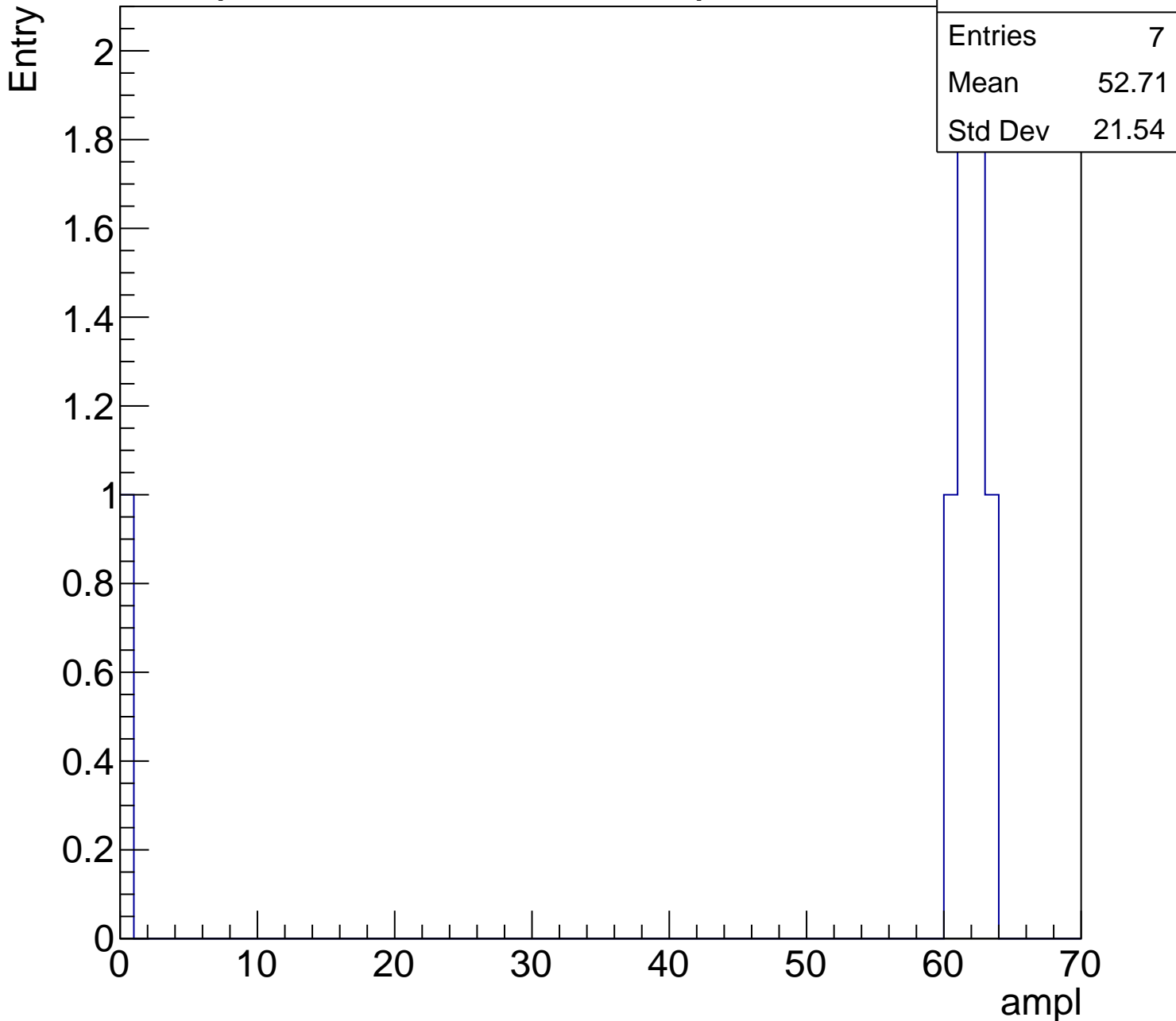
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.71
Std Dev	21.54

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch40, adc0

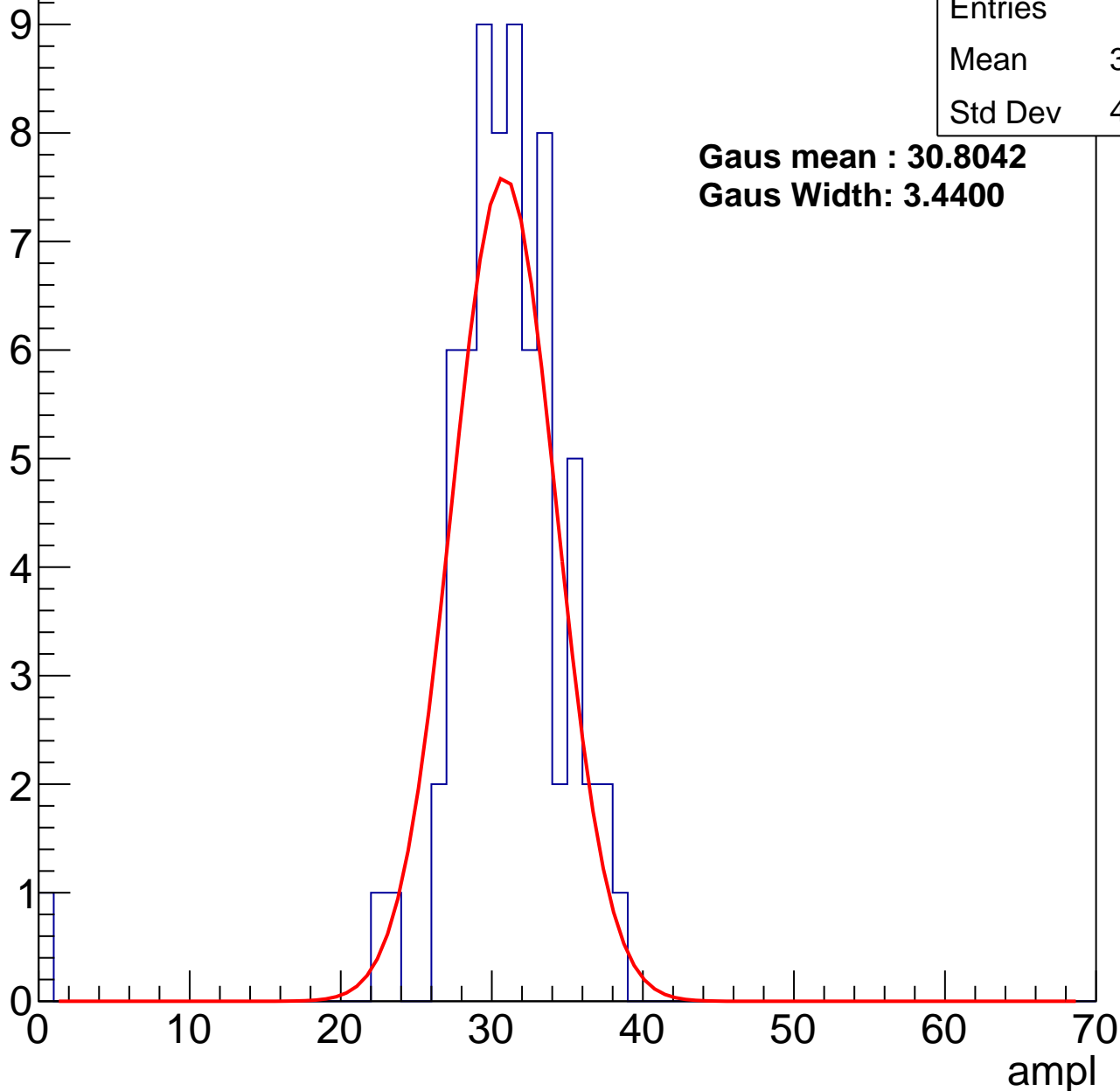
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.29
Std Dev	4.849

**Gaus mean : 30.8042**

**Gaus Width: 3.4400**



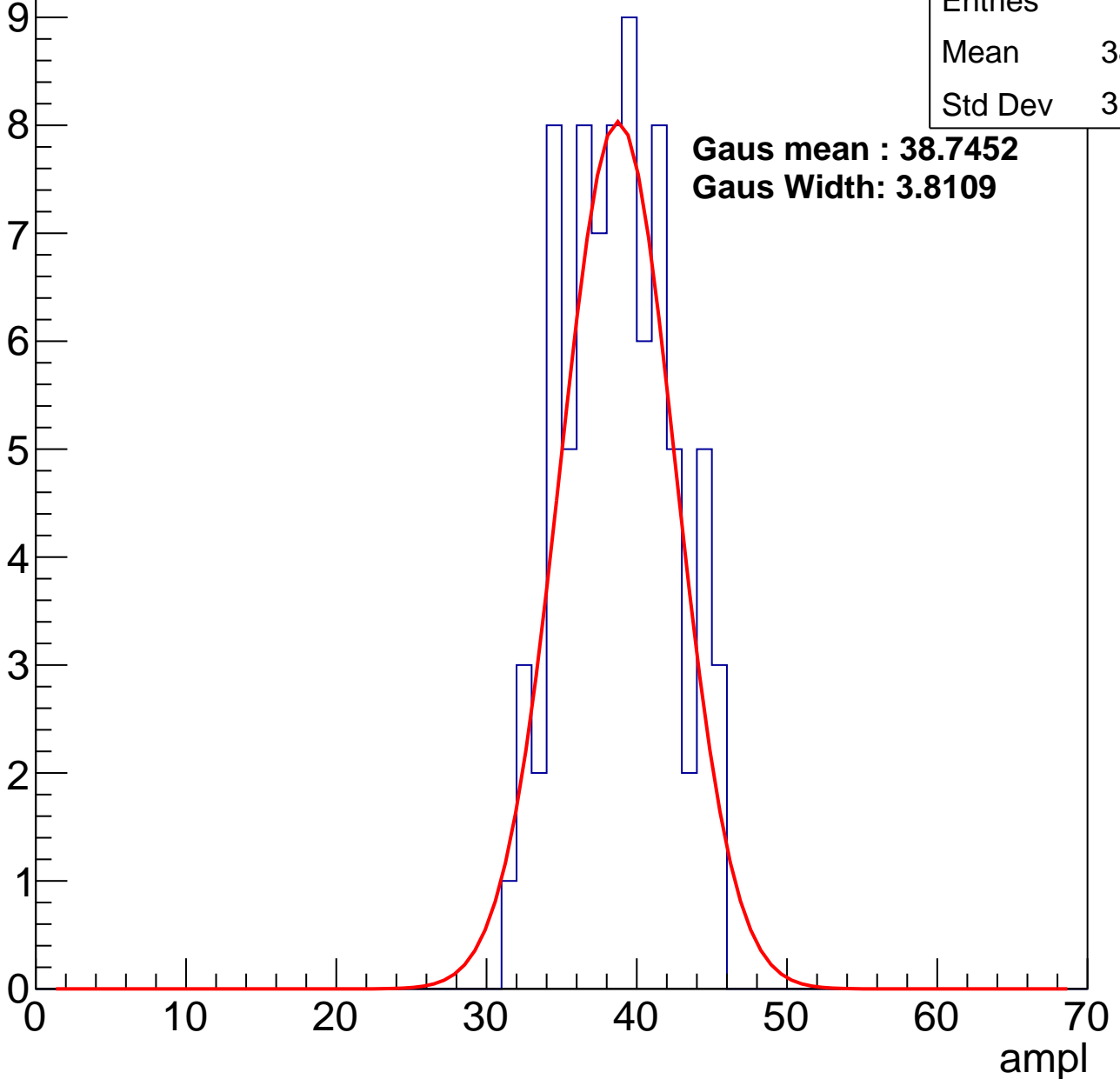
# B0L000S, U25-ch40, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	38.26
Std Dev	3.492

**Gaus mean : 38.7452**  
**Gaus Width: 3.8109**



# B0L000S, U25-ch40, adc2

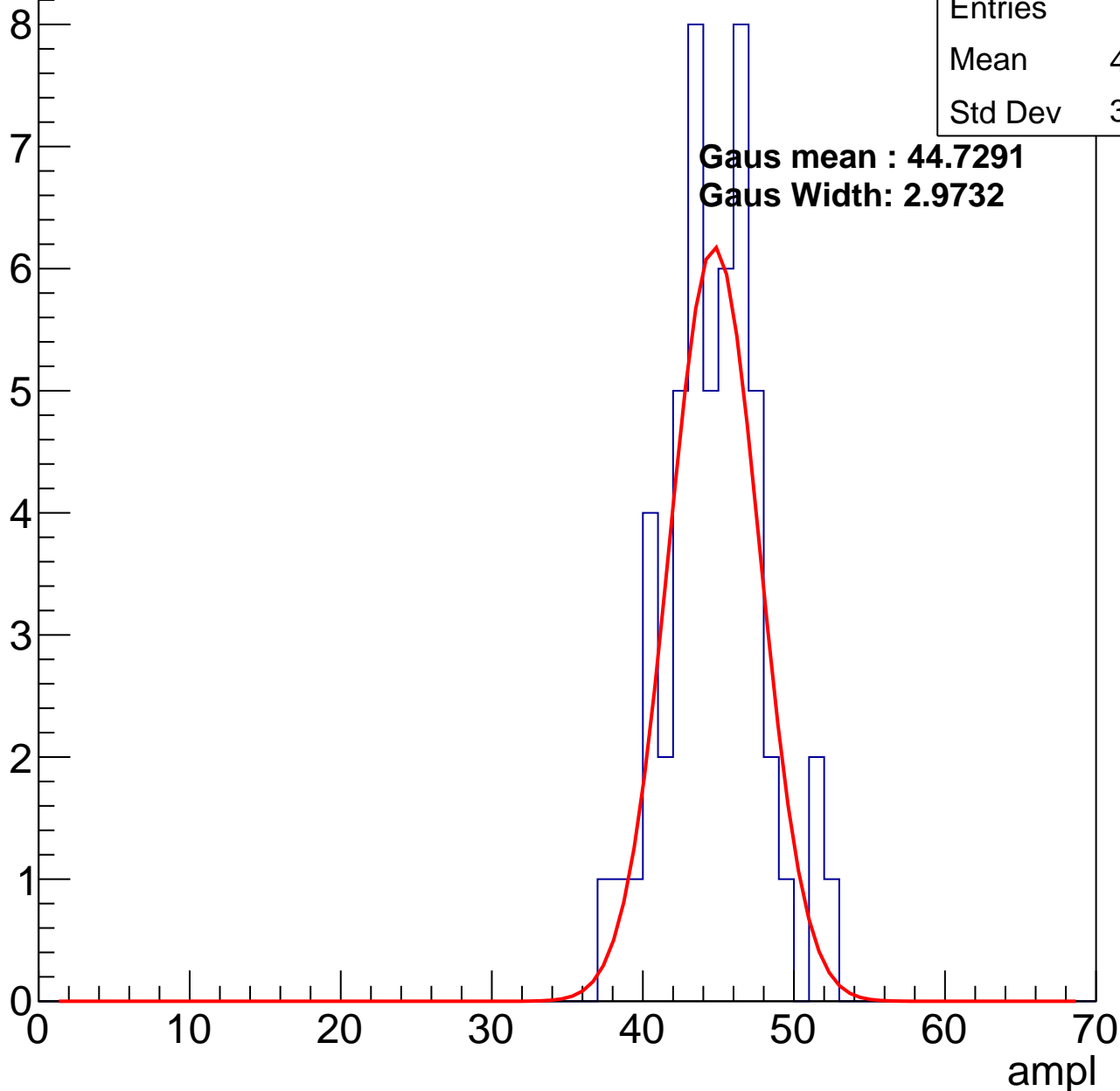
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	44.27
Std Dev	3.157

**Gaus mean : 44.7291**

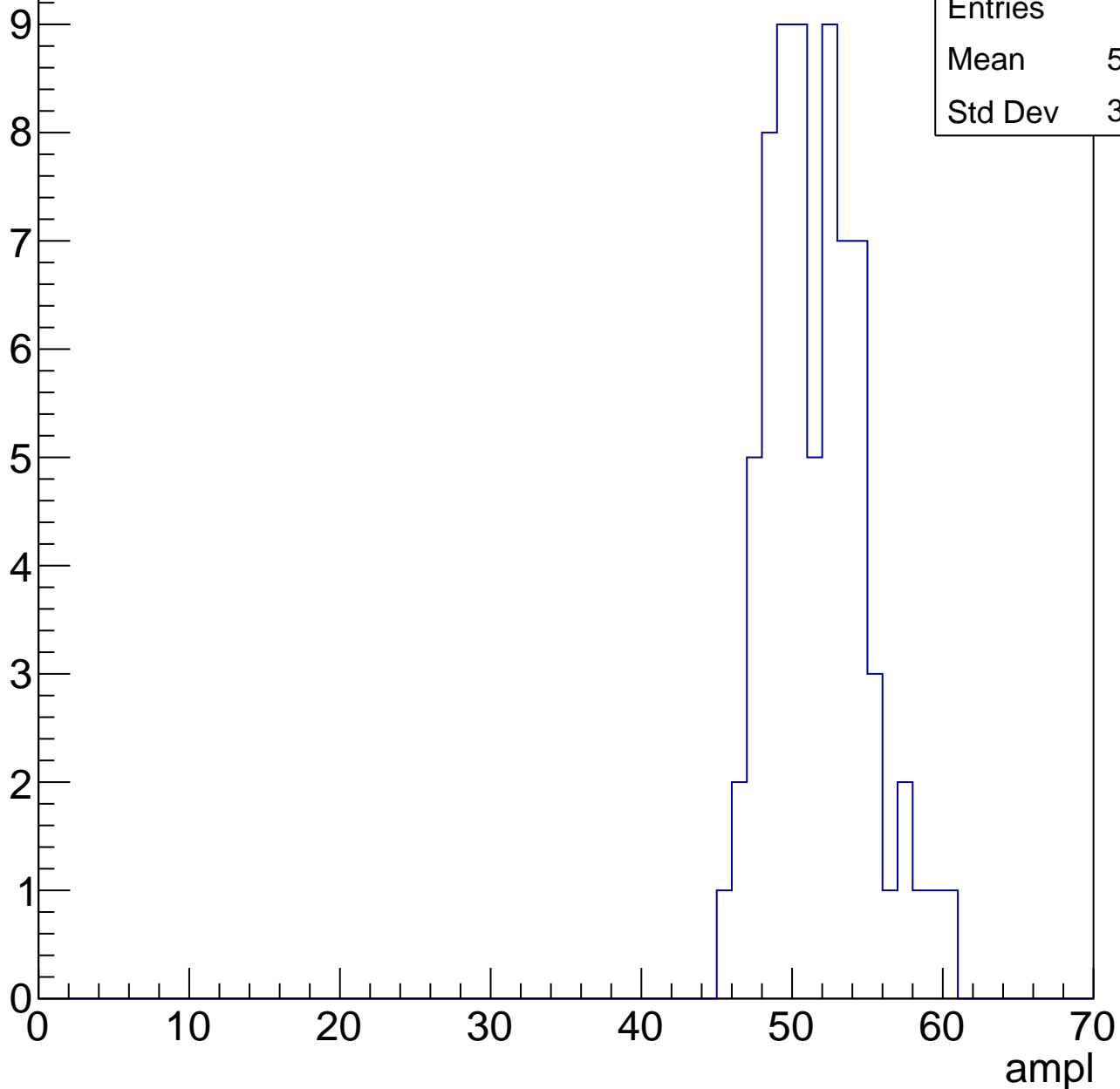
**Gaus Width: 2.9732**



# B0L000S, U25-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



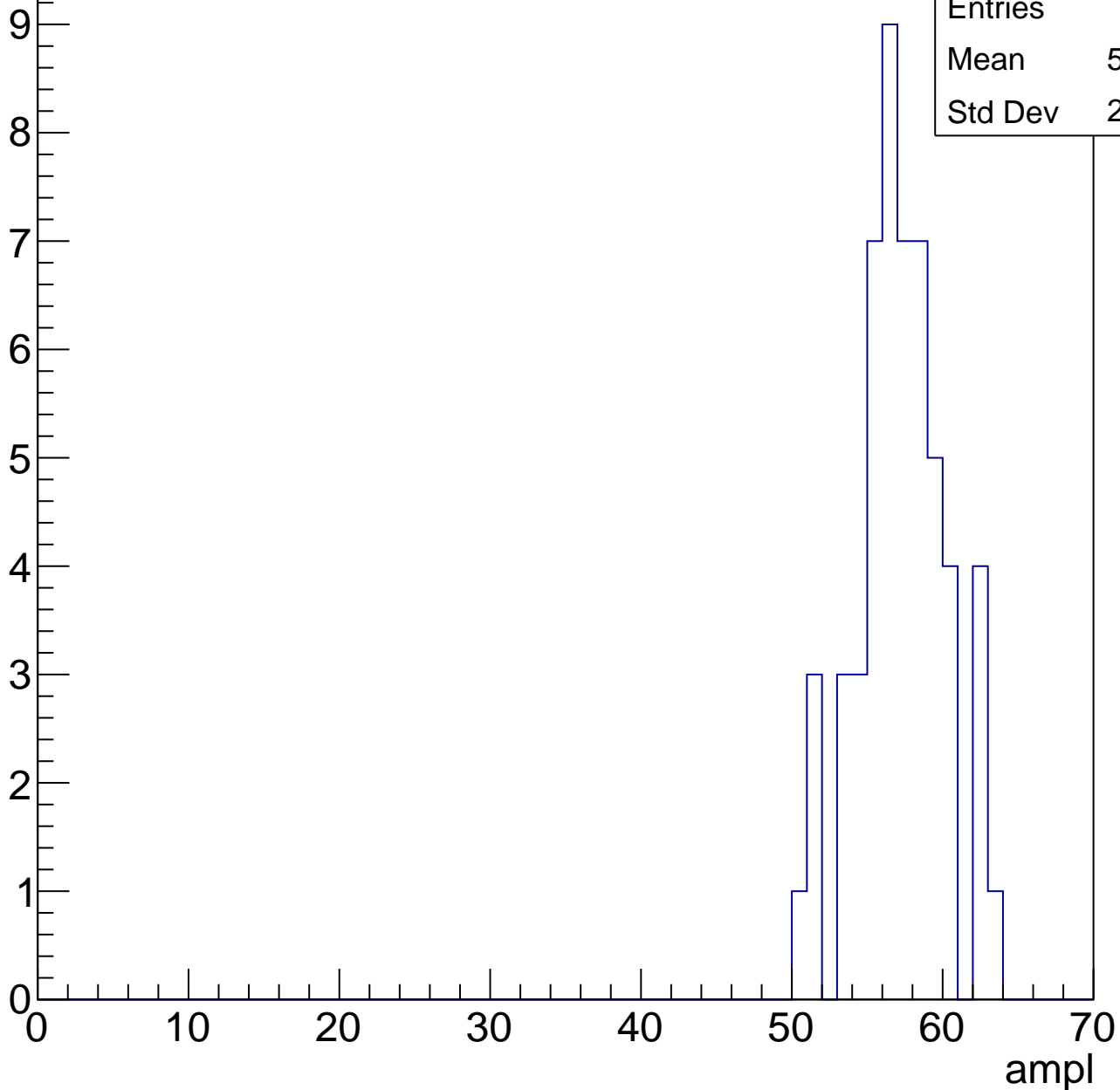
Entries	71
Mean	51.14
Std Dev	3.186

# B0L000S, U25-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	56.74
Std Dev	2.932

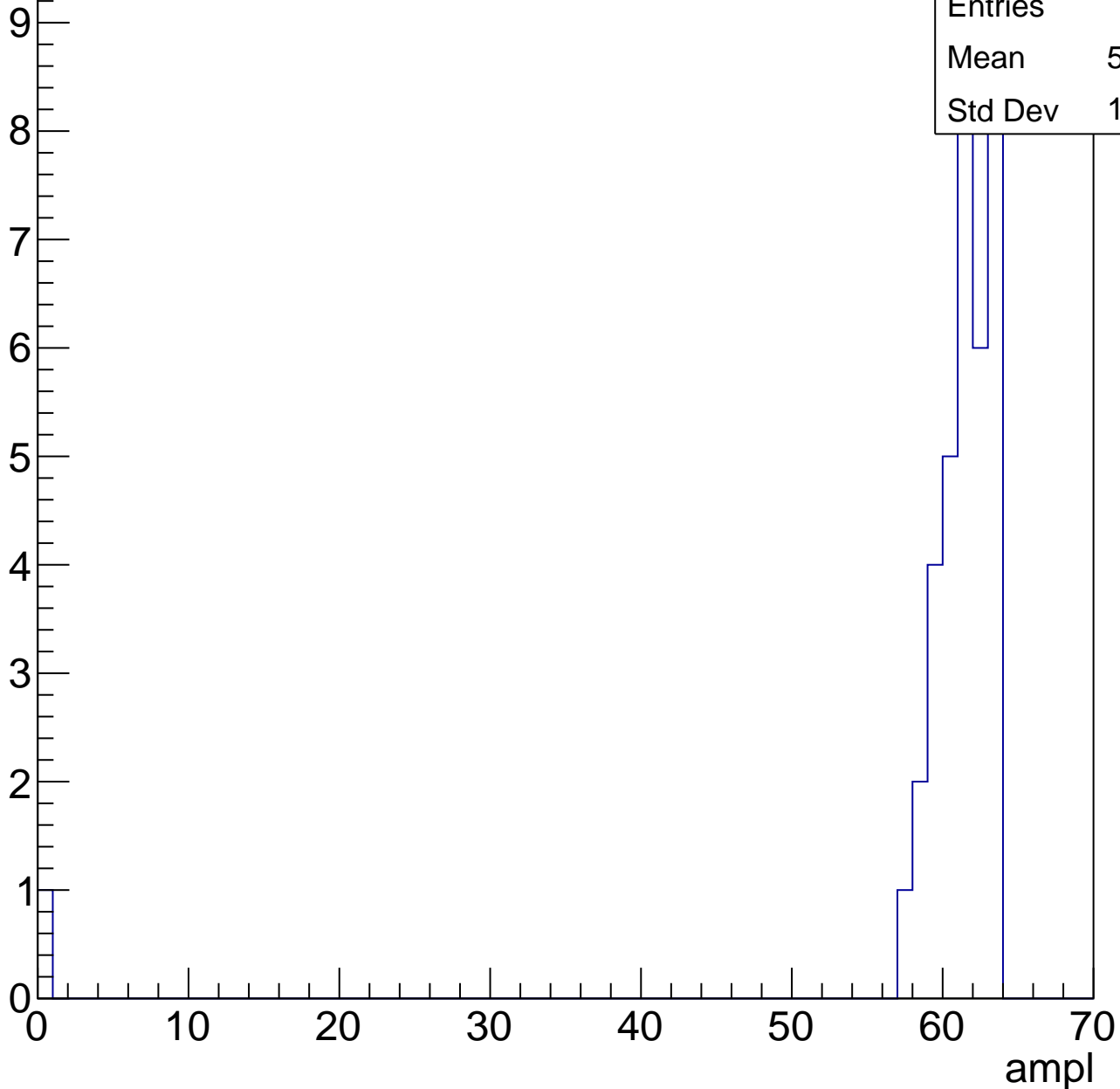


# B0L000S, U25-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	59.28
Std Dev	10.15



# B0L000S, U25-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch41, adc0

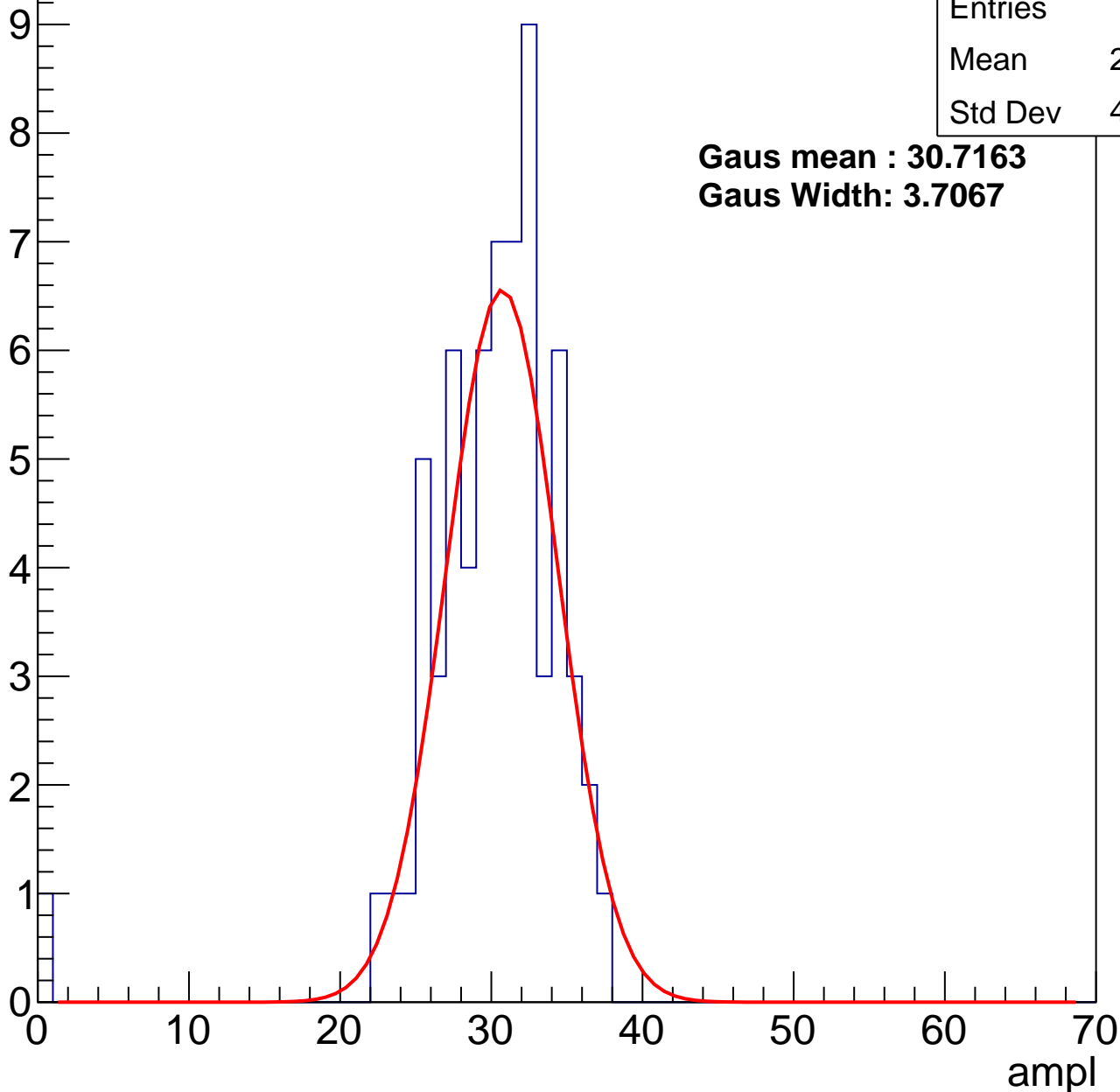
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.58
Std Dev	4.997

**Gaus mean : 30.7163**

**Gaus Width: 3.7067**



# B0L000S, U25-ch41, adc1

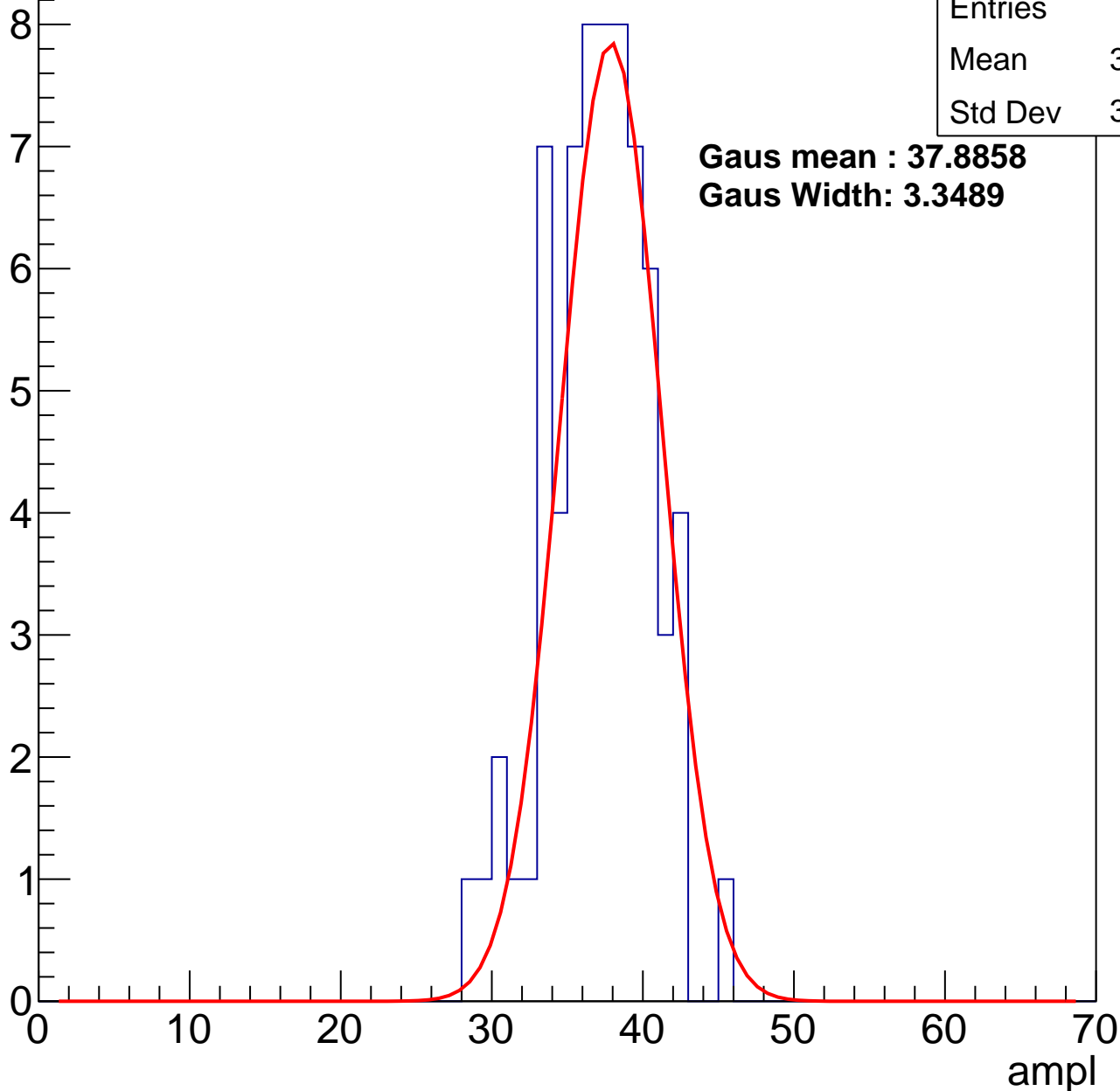
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.65
Std Dev	3.366

**Gaus mean : 37.8858**

**Gaus Width: 3.3489**



# B0L000S, U25-ch41, adc2

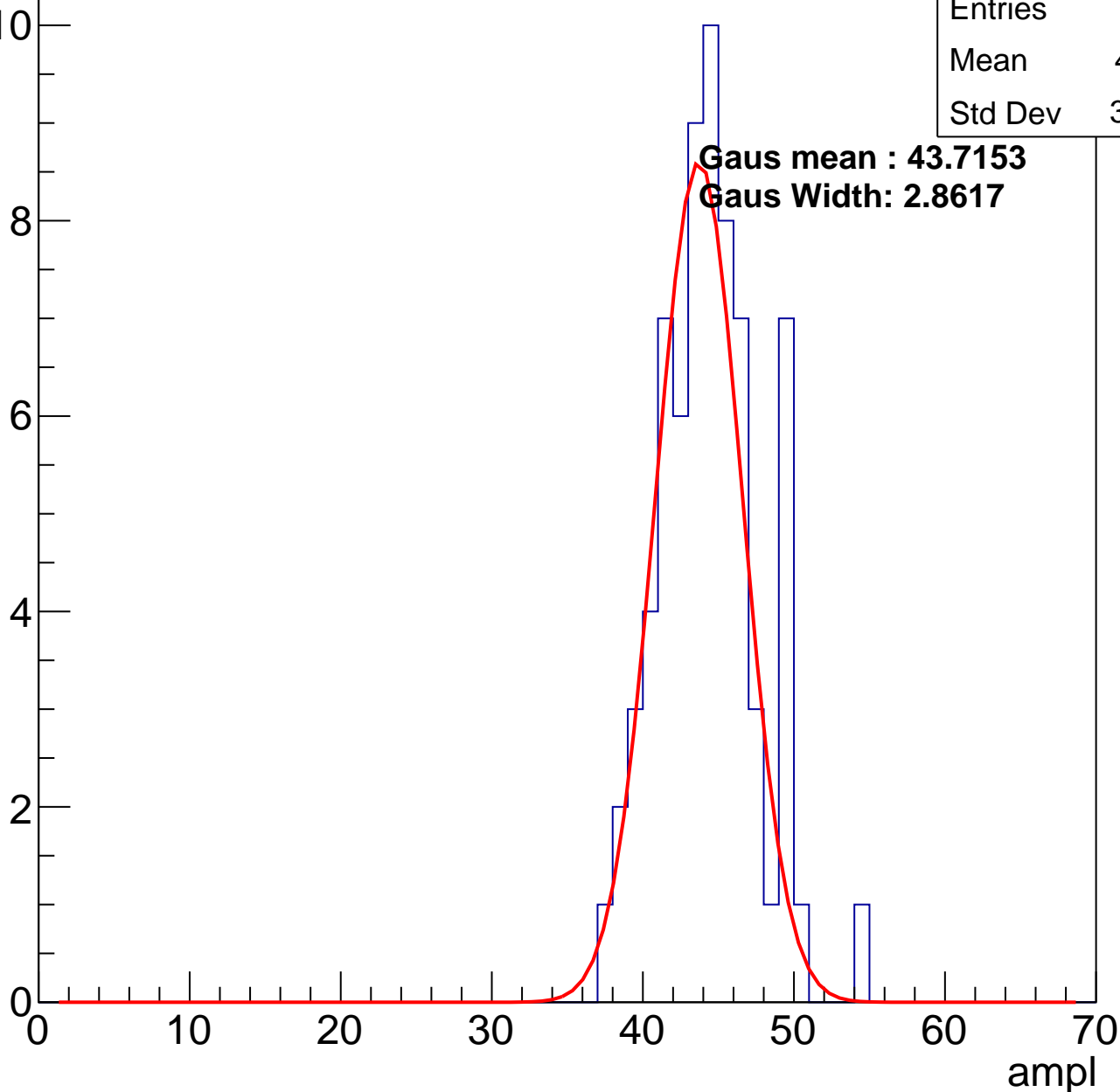
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	43.91
Std Dev	3.263

**Gaus mean : 43.7153**

**Gaus Width: 2.8617**

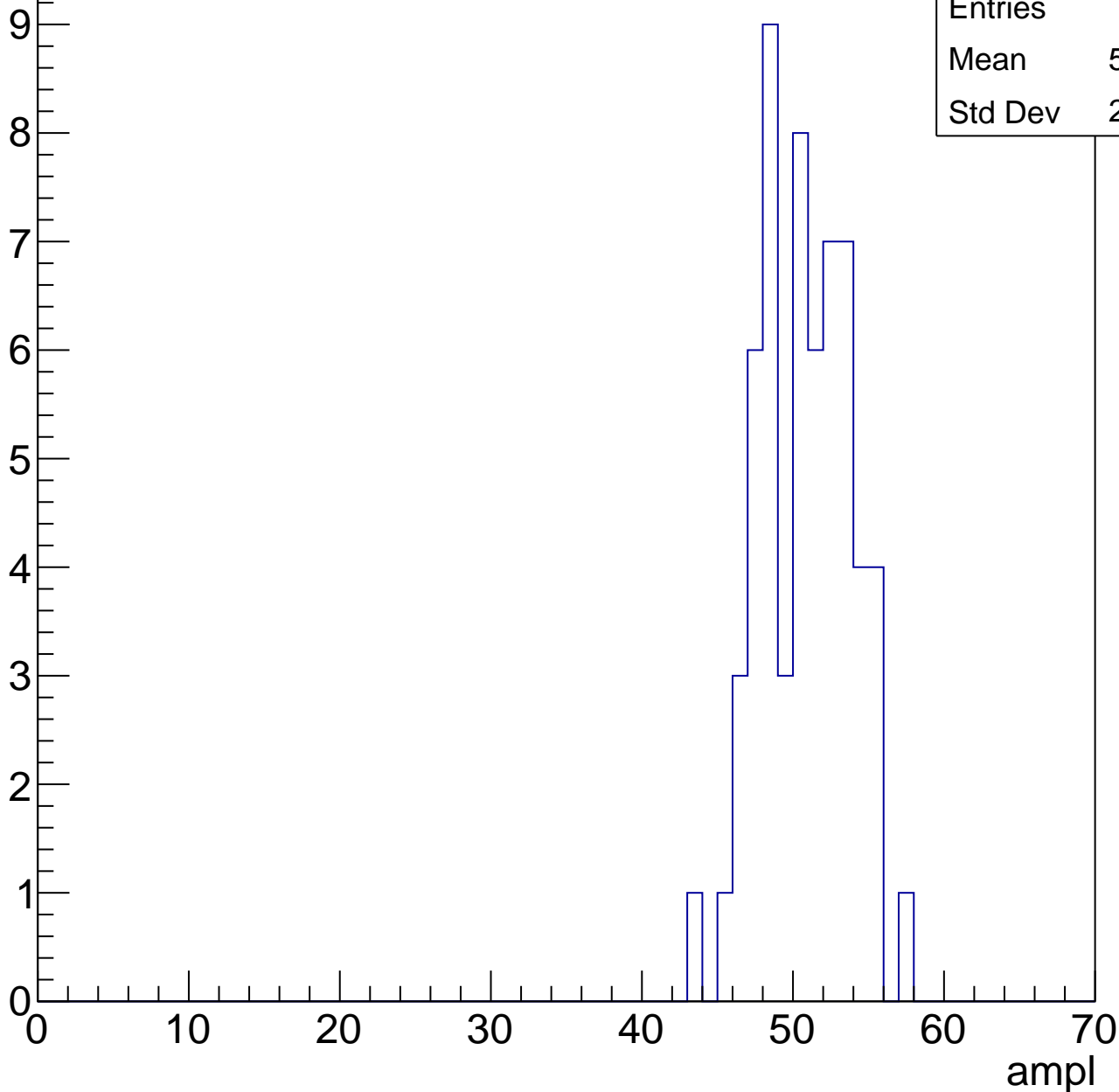


# B0L000S, U25-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	50.35
Std Dev	2.937

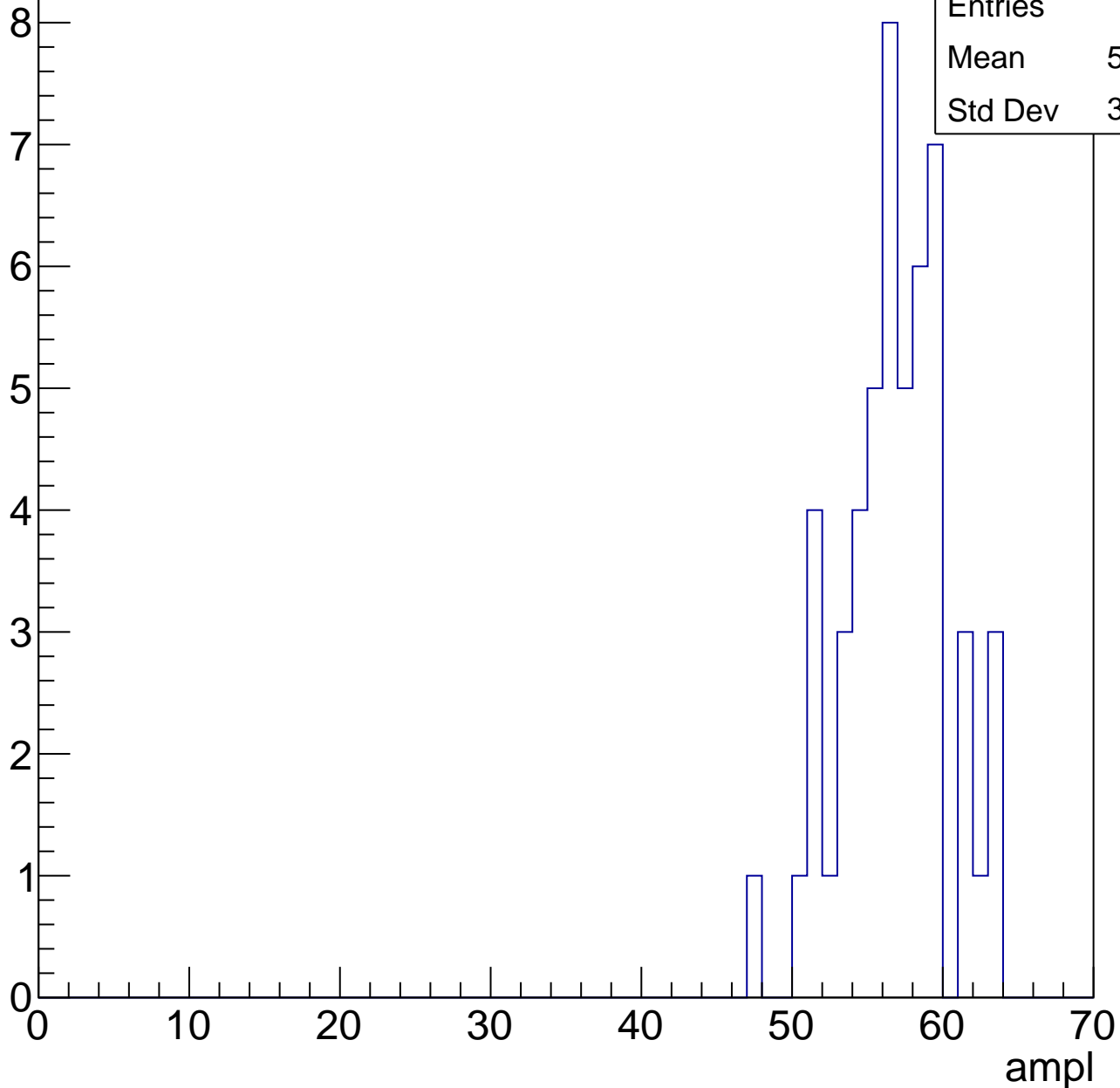


# B0L000S, U25-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

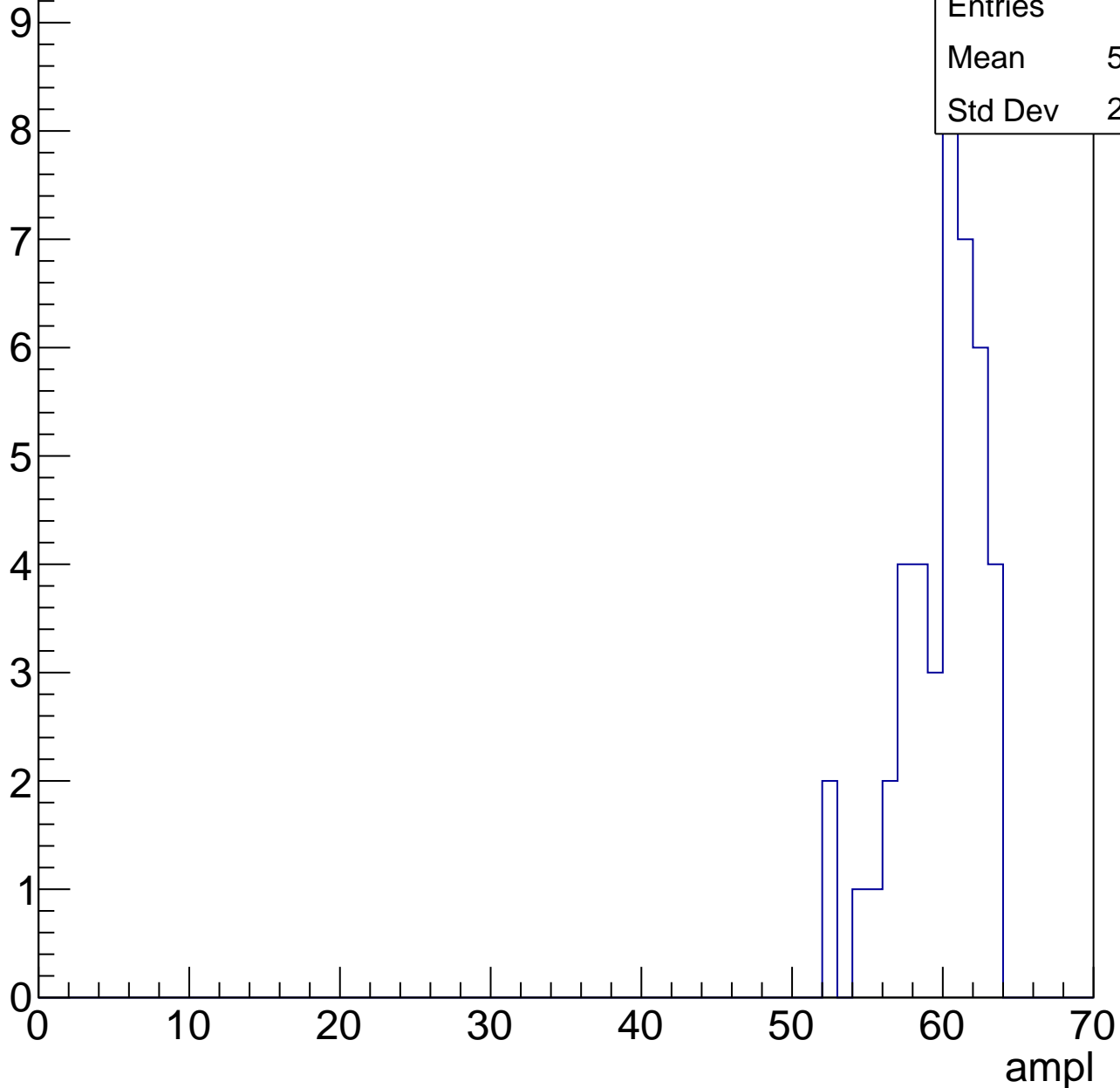
Entries	52
Mean	56.37
Std Dev	3.464



# B0L000S, U25-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

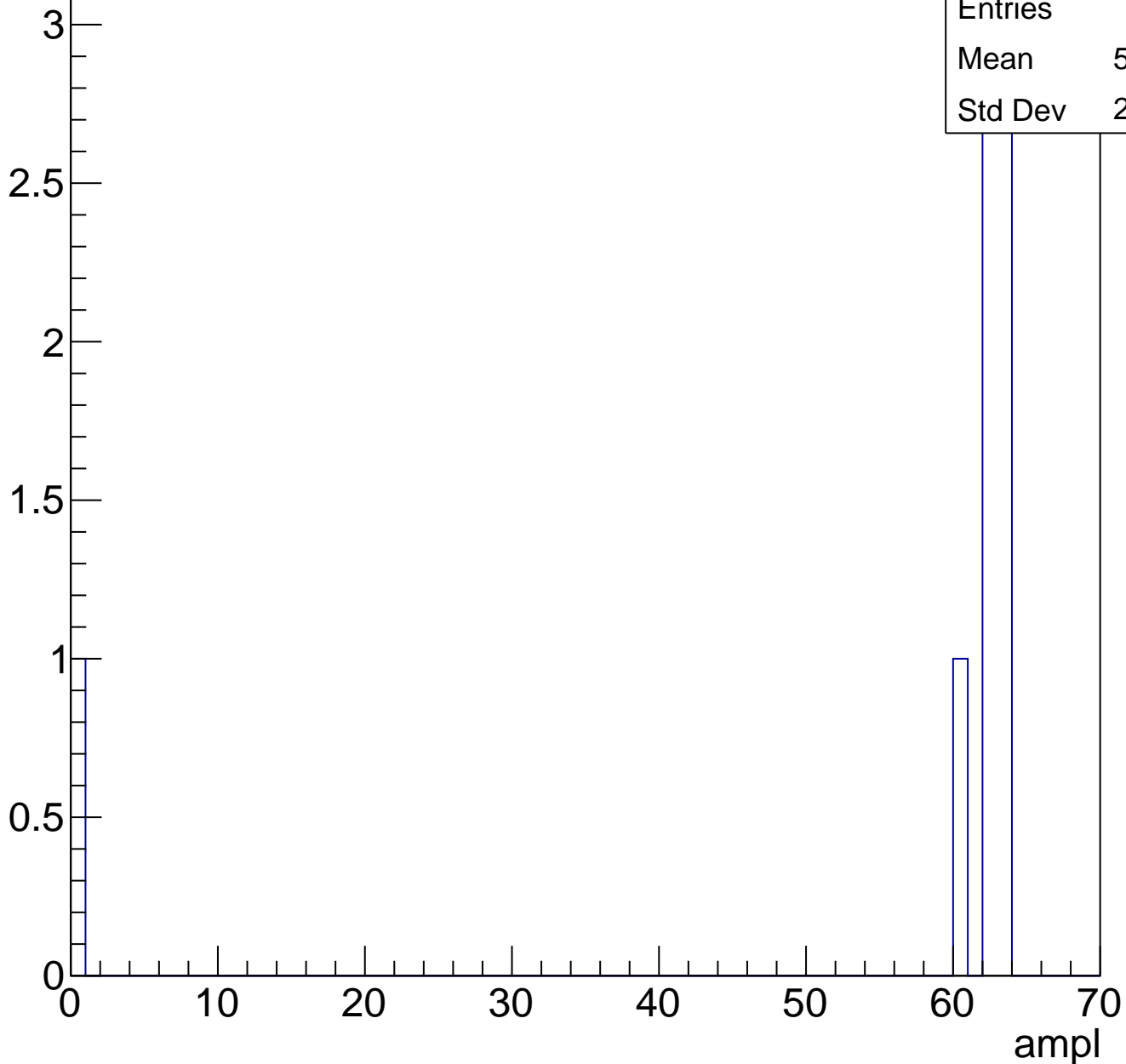
Entry



# B0L000S, U25-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch42, adc0

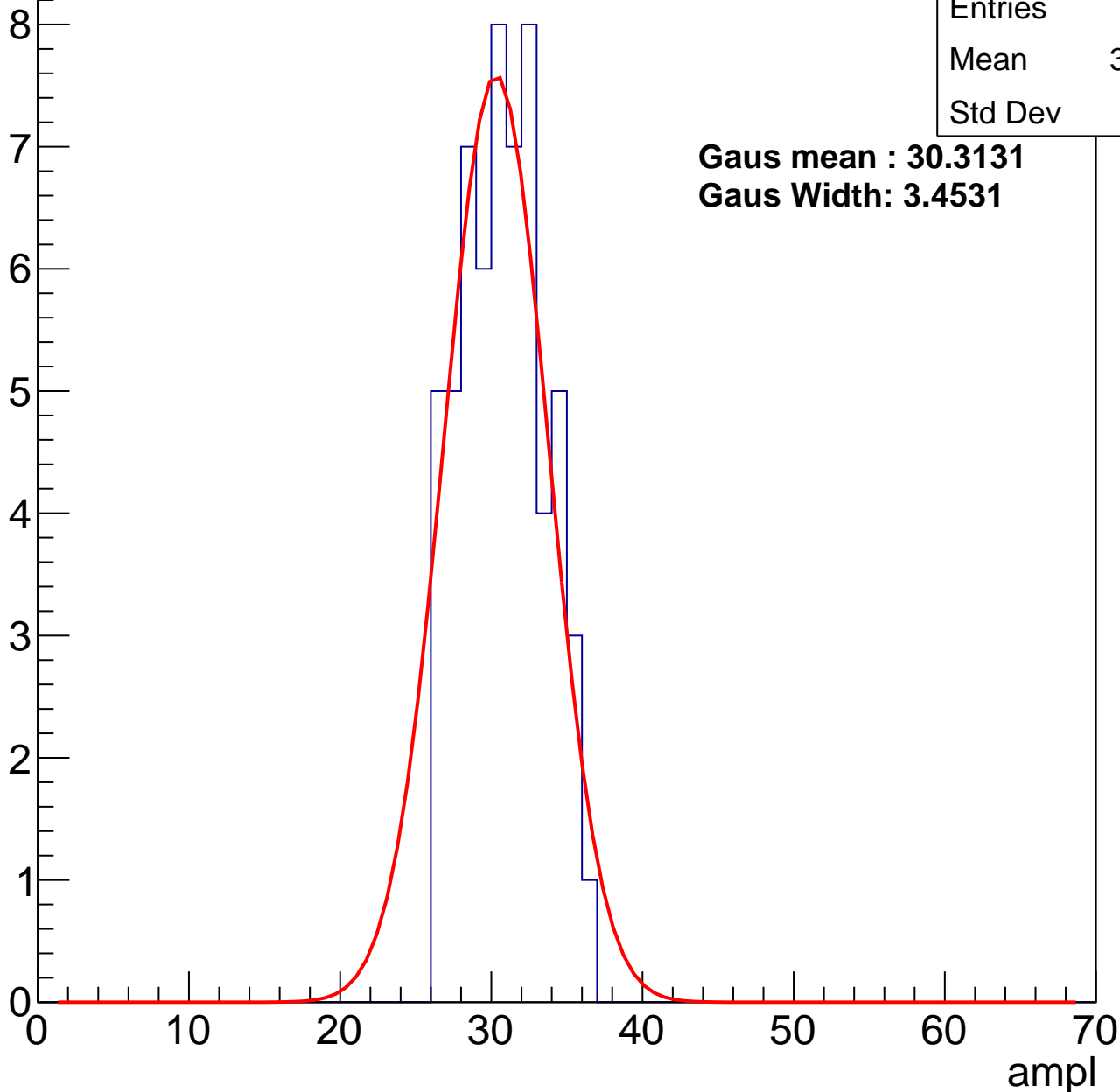
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.36
Std Dev	2.66

**Gaus mean : 30.3131**

**Gaus Width: 3.4531**



# B0L000S, U25-ch42, adc1

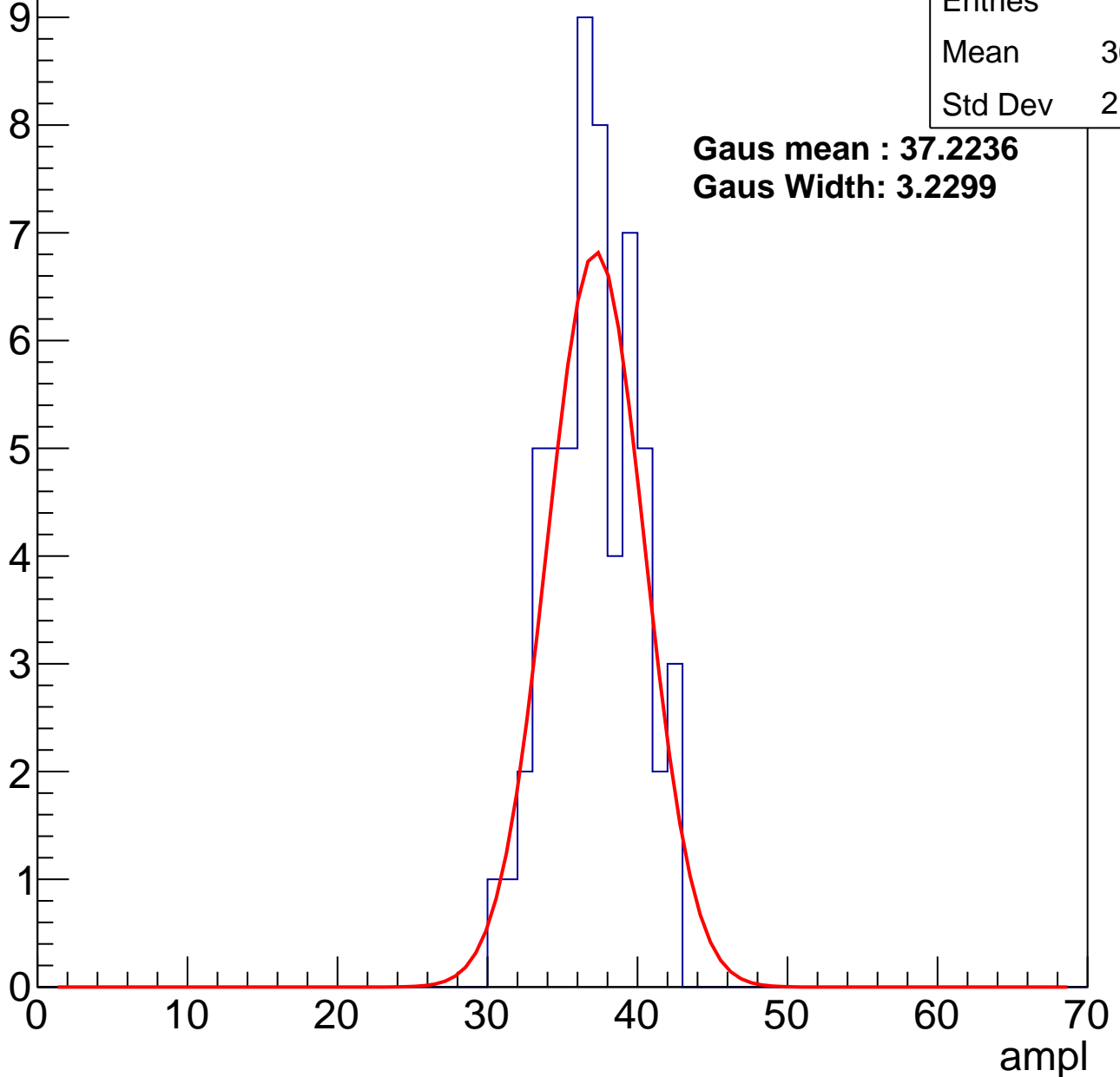
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	36.63
Std Dev	2.857

**Gaus mean : 37.2236**

**Gaus Width: 3.2299**



# B0L000S, U25-ch42, adc2

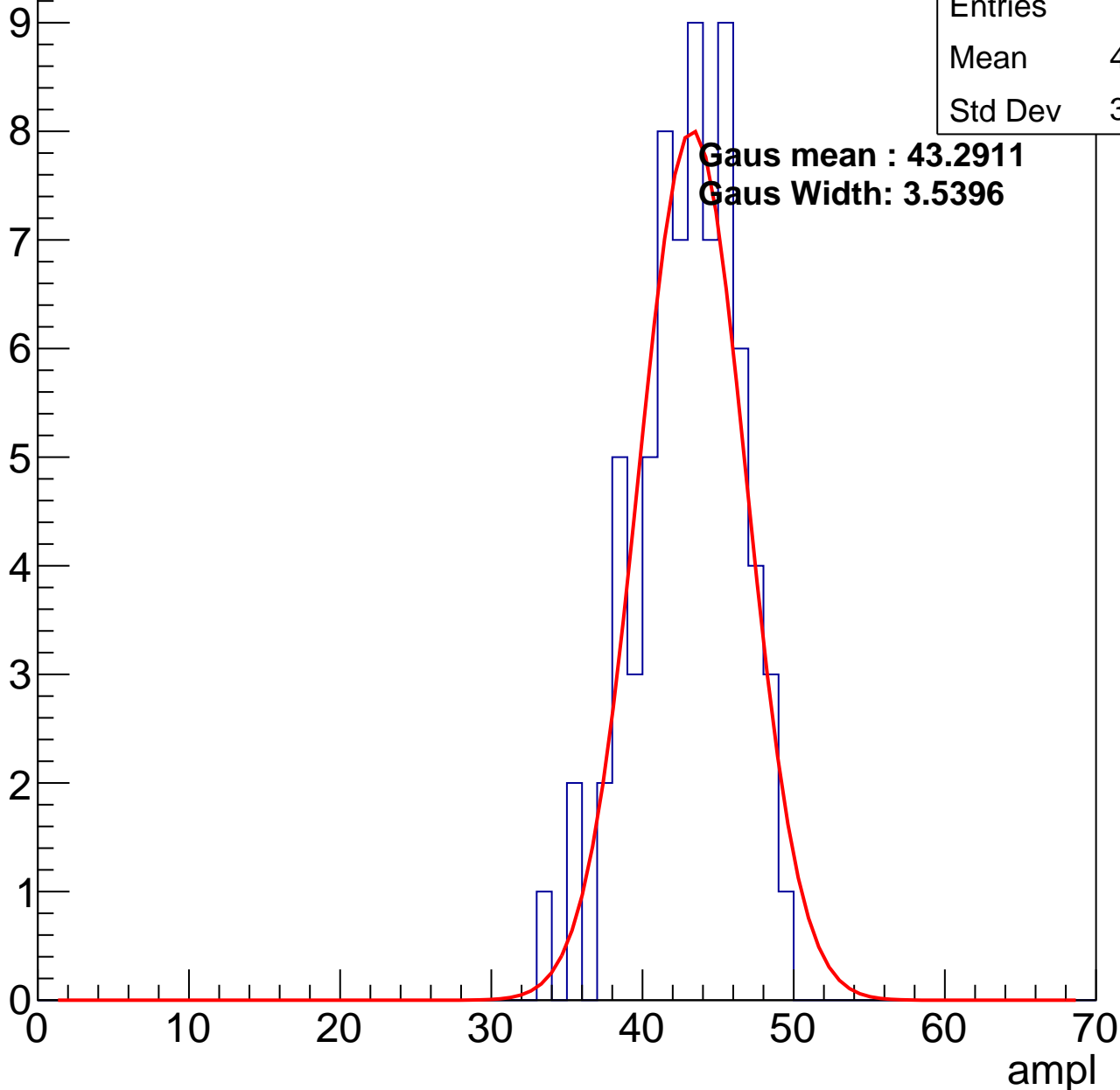
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	42.54
Std Dev	3.362

**Gaus mean : 43.2911**

**Gaus Width: 3.5396**

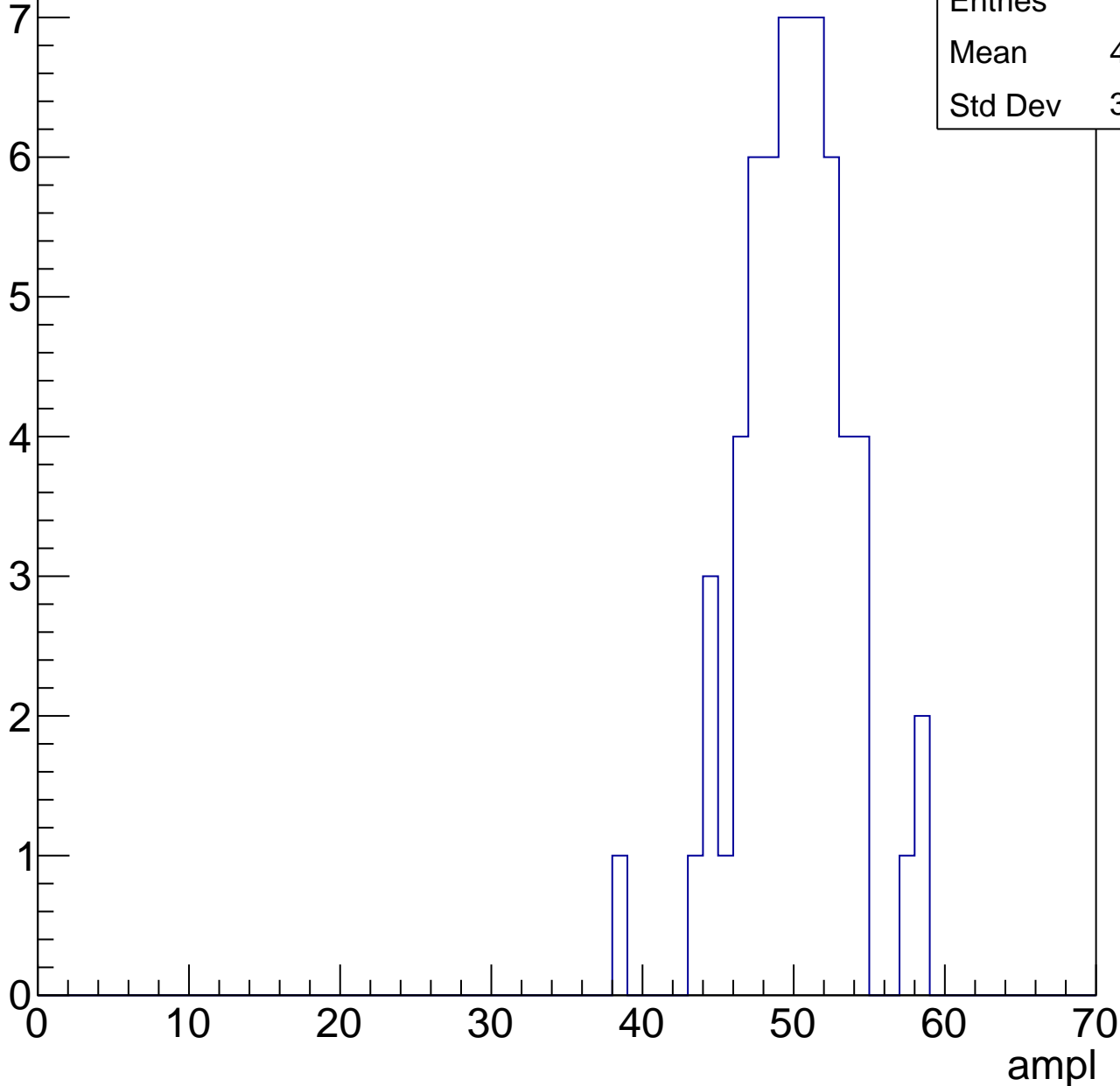


# B0L000S, U25-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

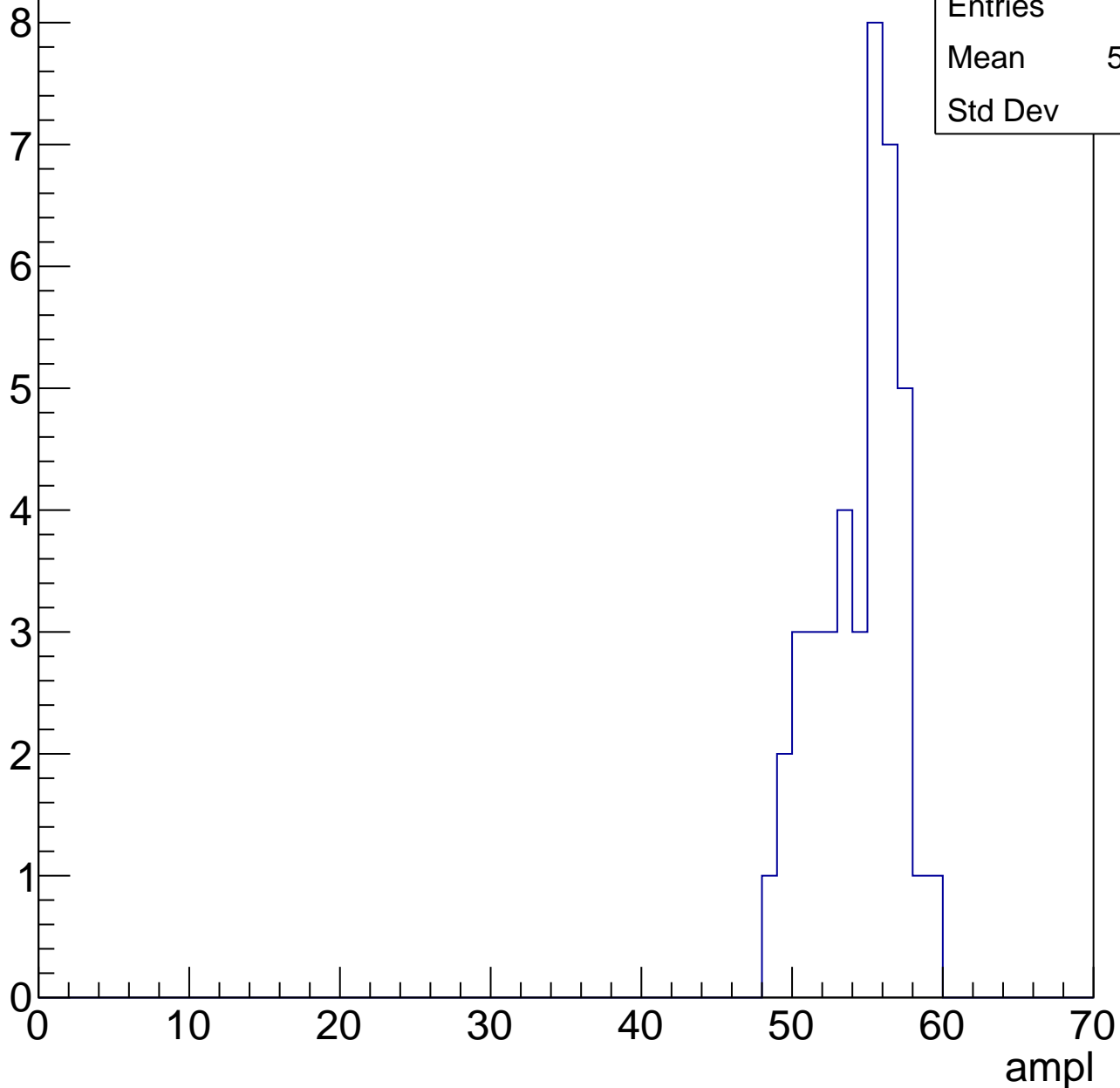
Entries	60
Mean	49.58
Std Dev	3.588



# B0L000S, U25-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



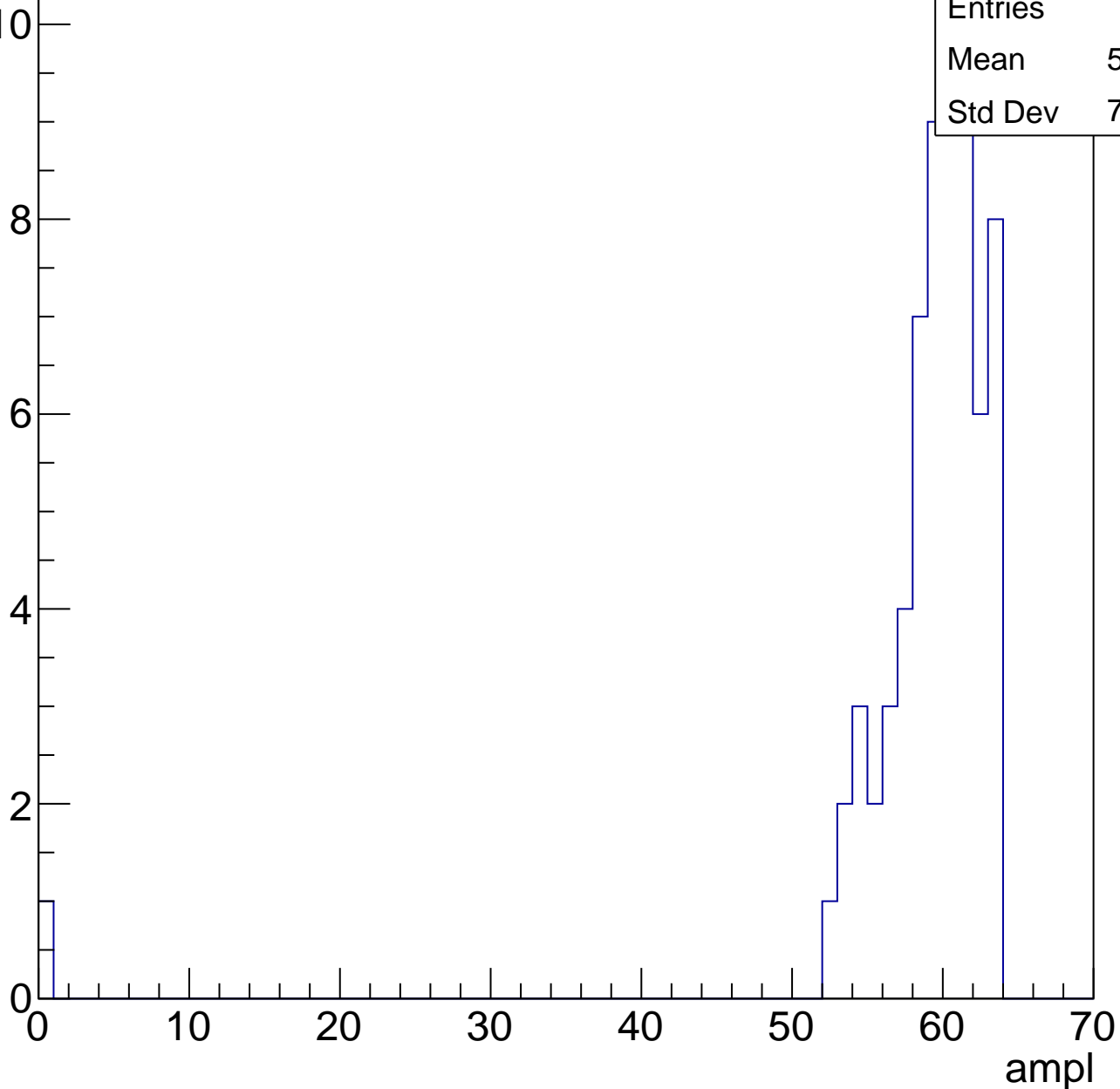
Entries	41
Mean	53.98
Std Dev	2.7

# B0L000S, U25-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

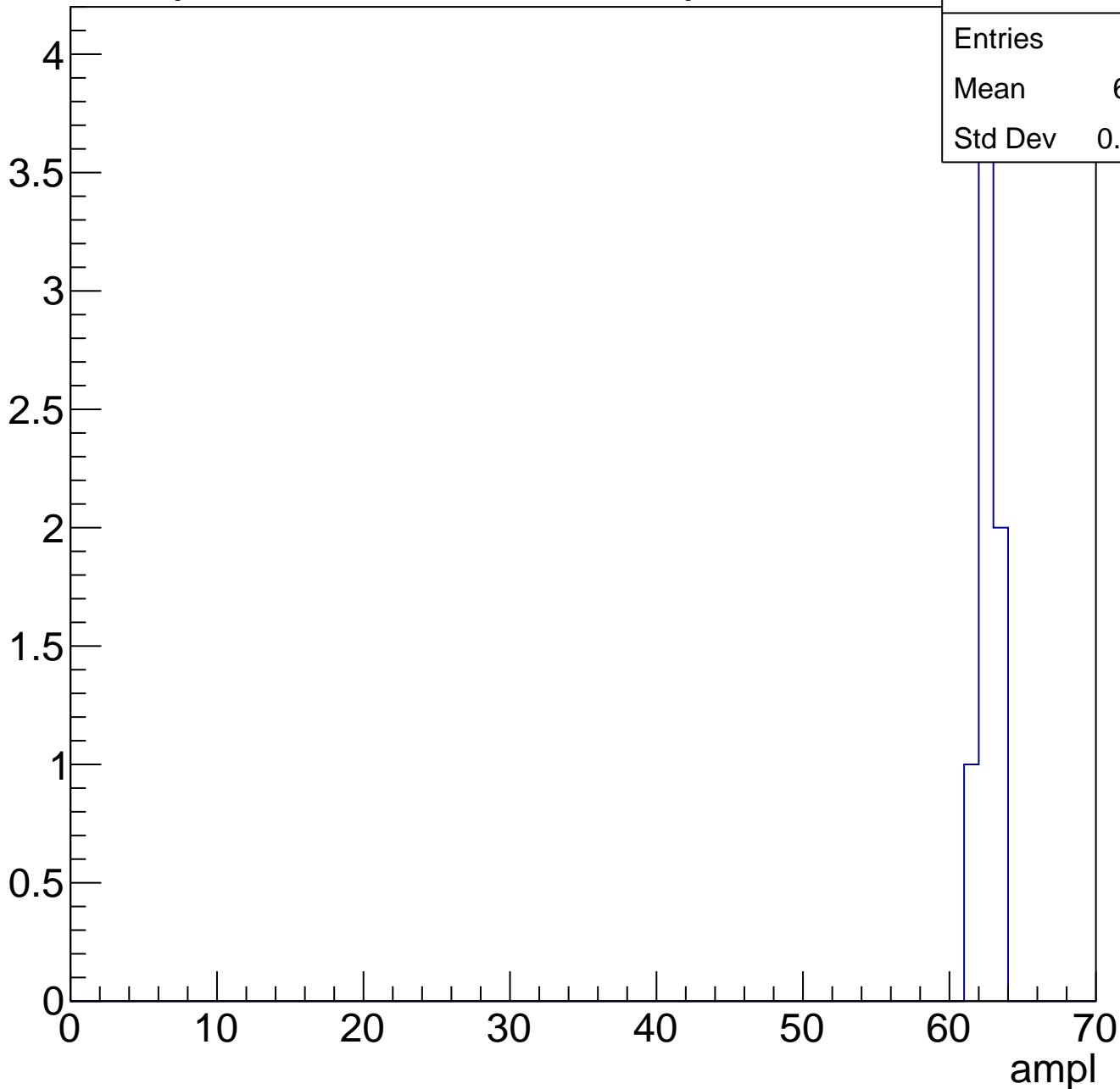
Entries	65
Mean	58.28
Std Dev	7.798



# B0L000S, U25-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch43, adc0

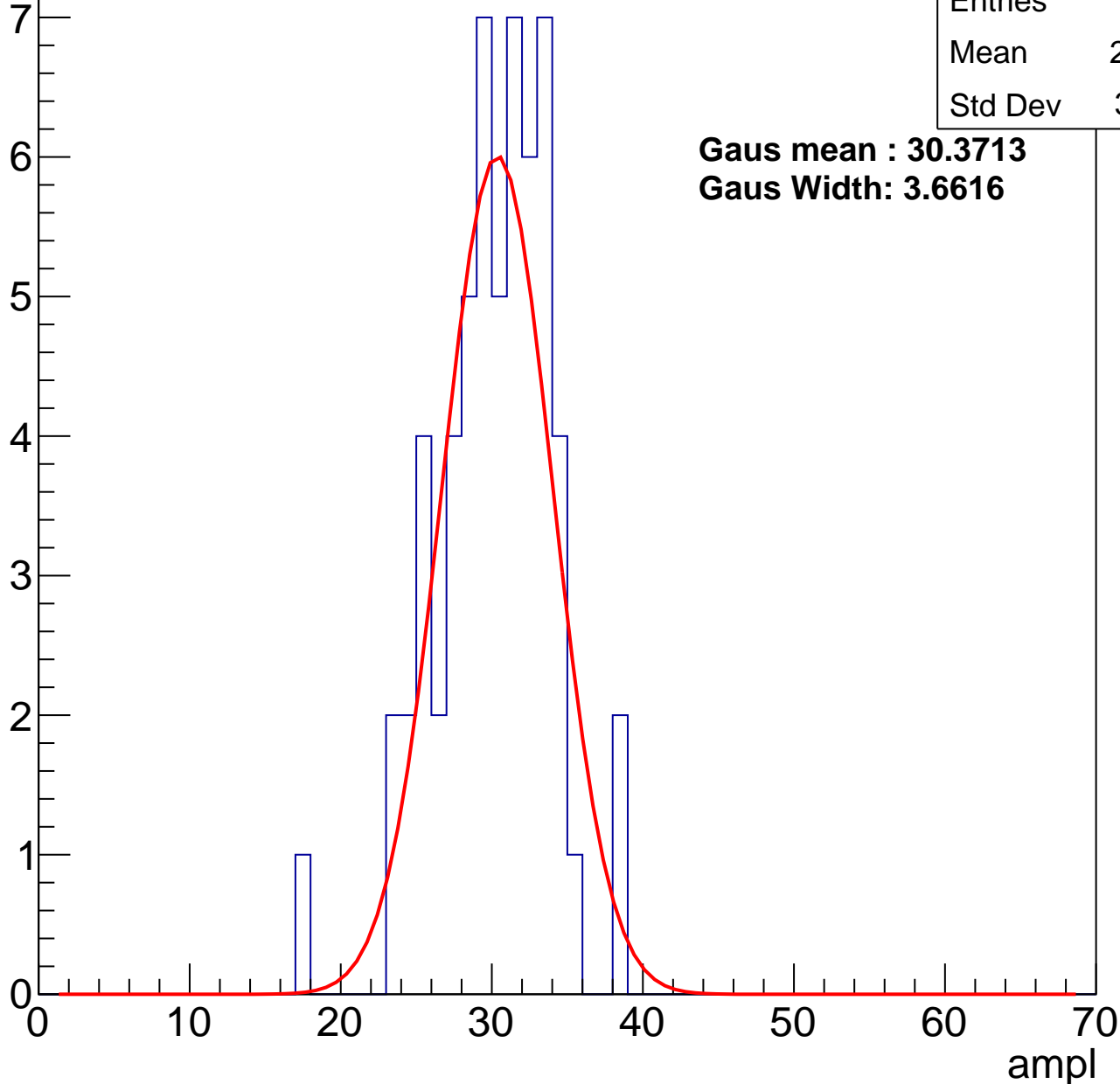
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	29.68
Std Dev	3.771

**Gaus mean : 30.3713**

**Gaus Width: 3.6616**



# B0L000S, U25-ch43, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	85
Mean	37.09
Std Dev	3.75

**Gaus mean : 37.8748**

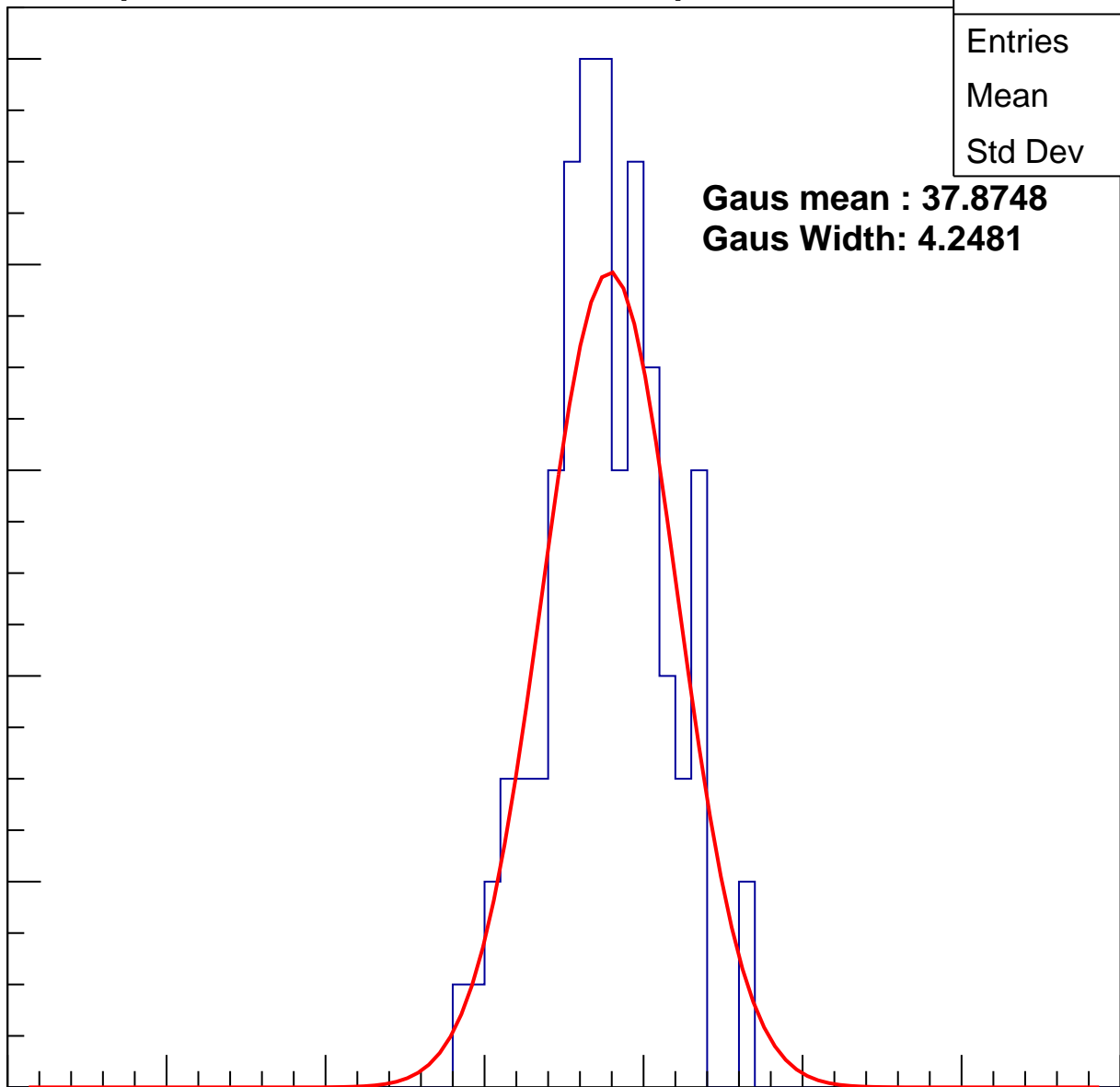
**Gaus Width: 4.2481**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch43, adc2

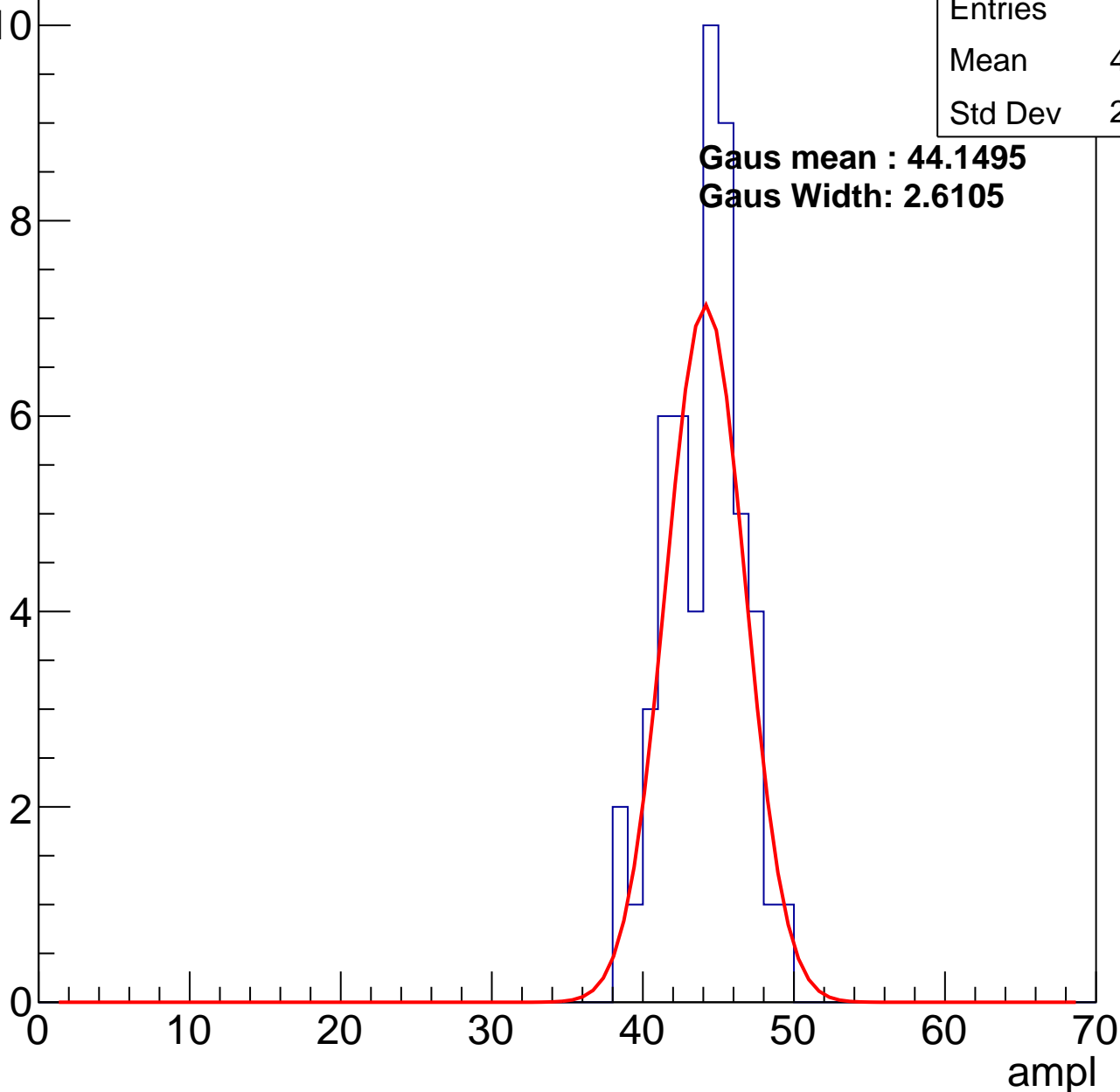
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	43.56
Std Dev	2.492

**Gaus mean : 44.1495**

**Gaus Width: 2.6105**

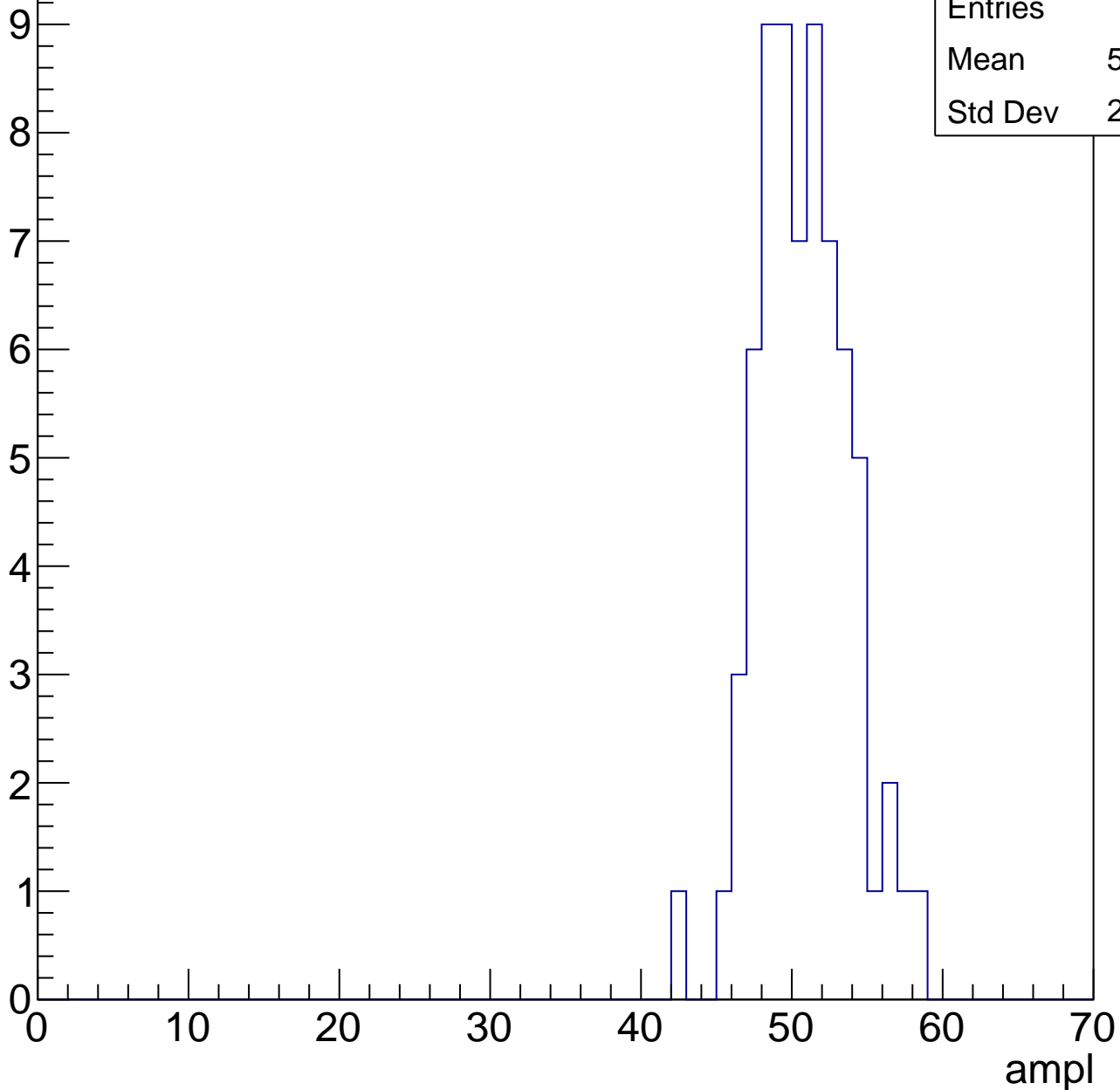


# B0L000S, U25-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

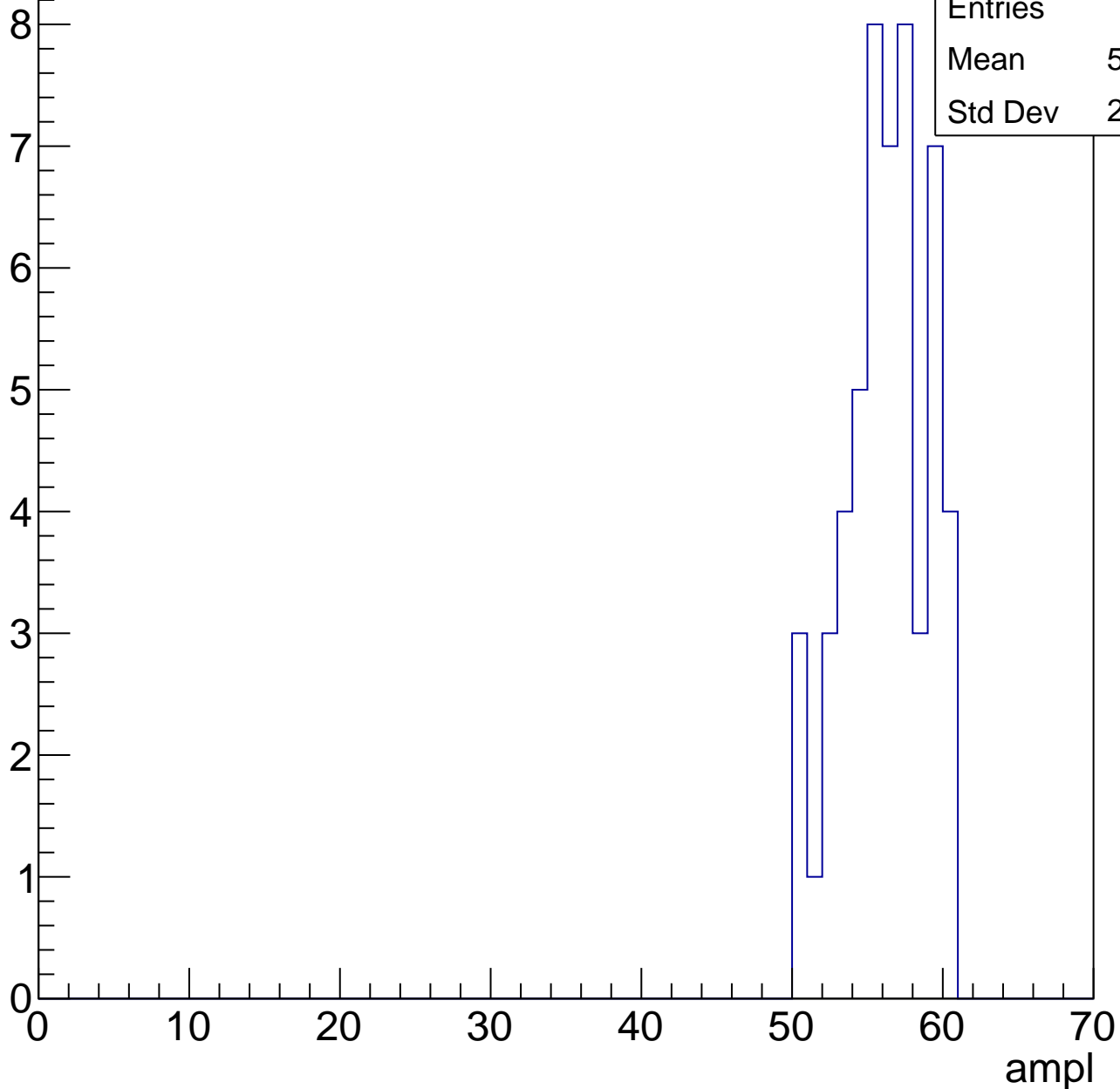
Entries	68
Mean	50.34
Std Dev	2.993



# B0L000S, U25-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

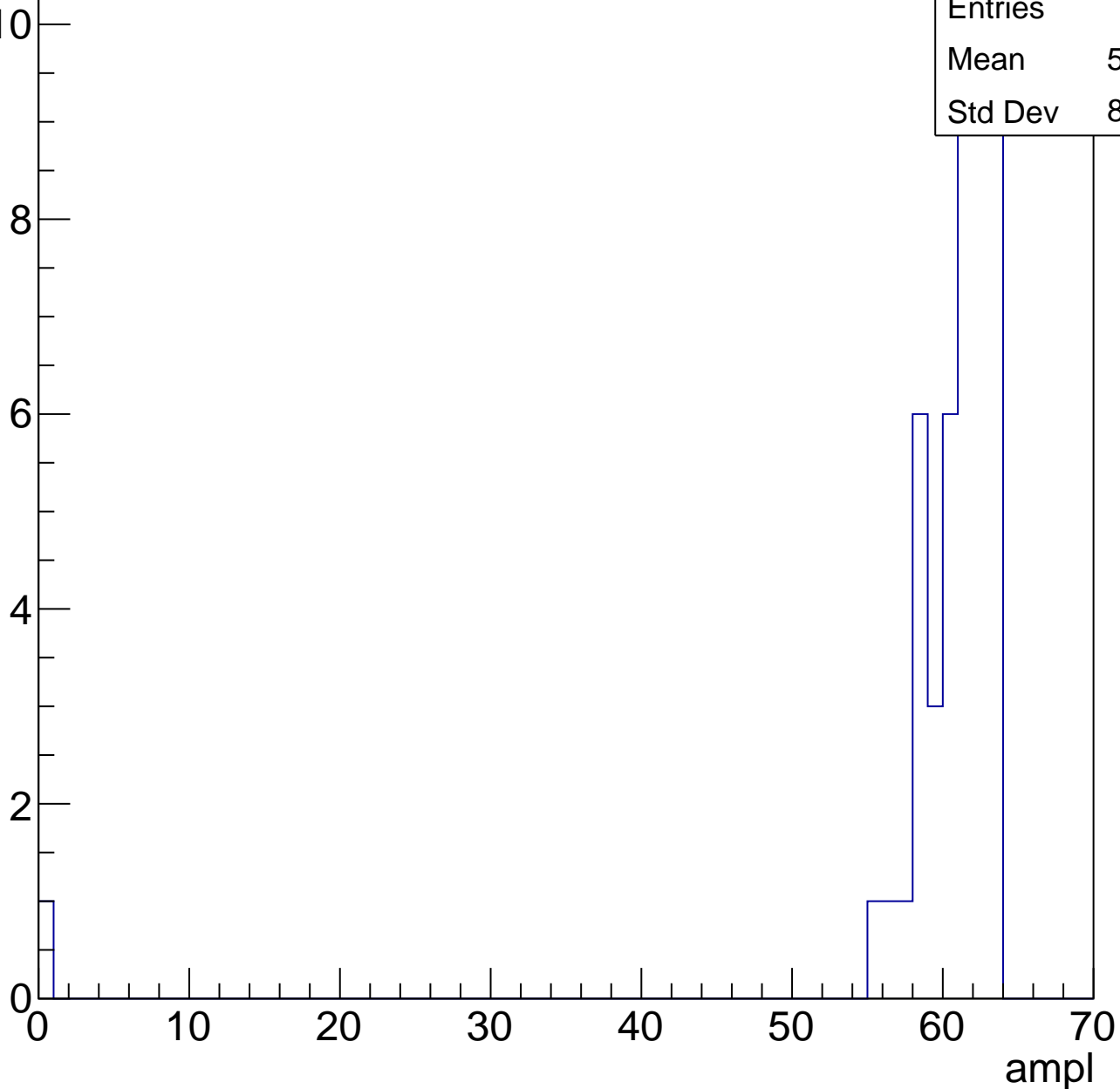


# B0L000S, U25-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	59.42
Std Dev	8.897



# B0L000S, U25-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch44, adc0

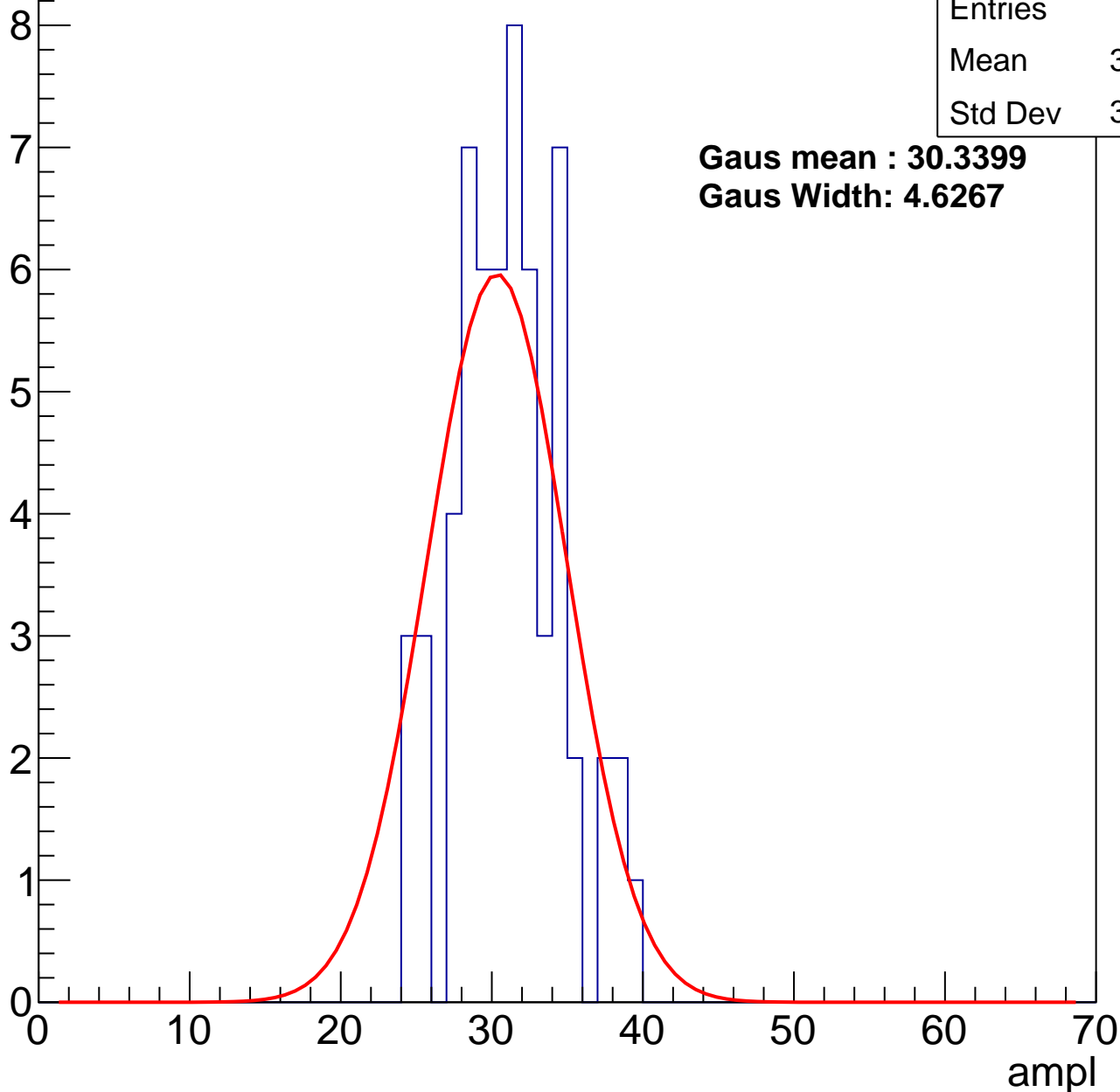
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.68
Std Dev	3.543

**Gaus mean : 30.3399**

**Gaus Width: 4.6267**



# B0L000S, U25-ch44, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	36.74
Std Dev	3.713

**Gaus mean : 37.9060**

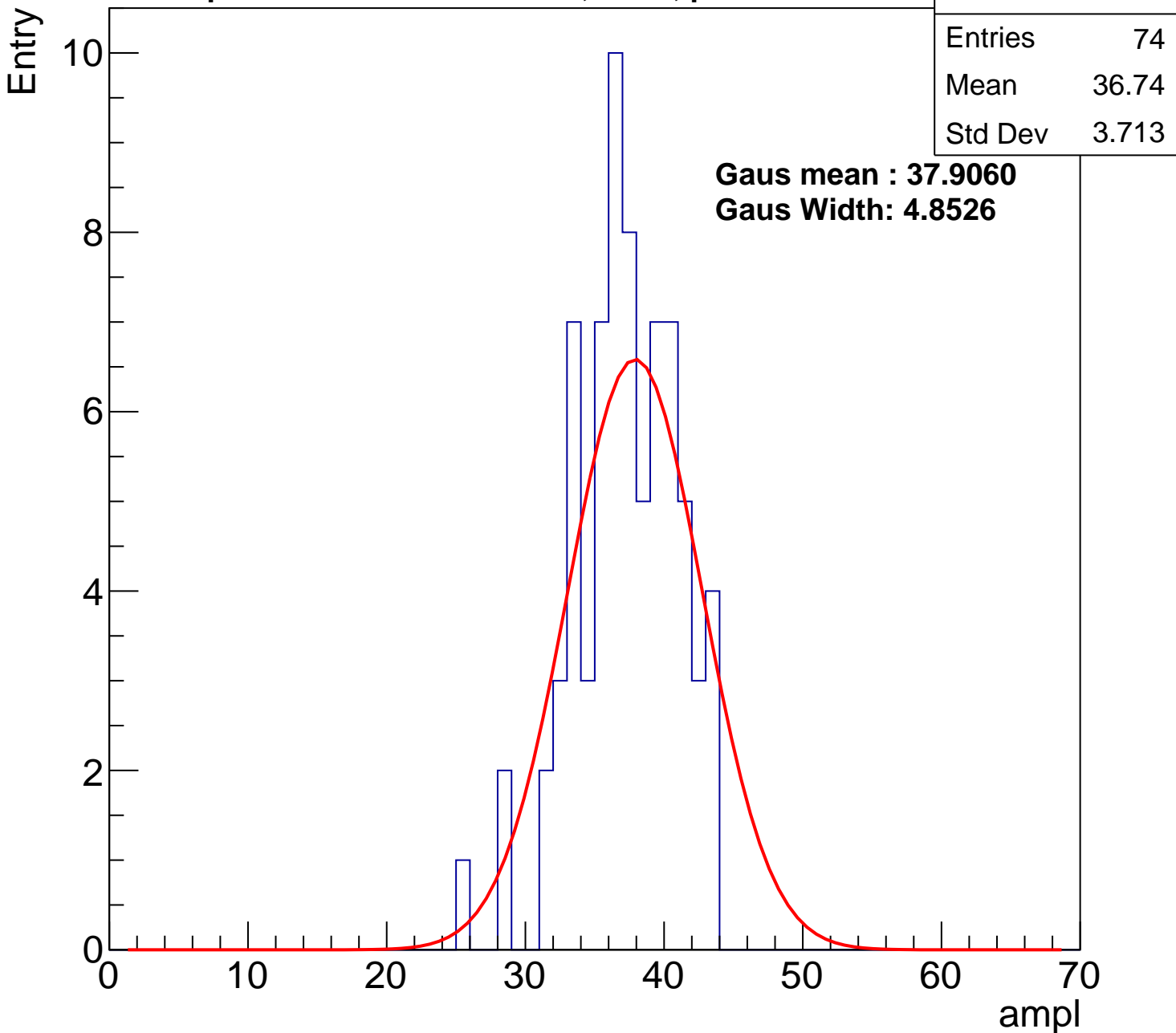
**Gaus Width: 4.8526**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch44, adc2

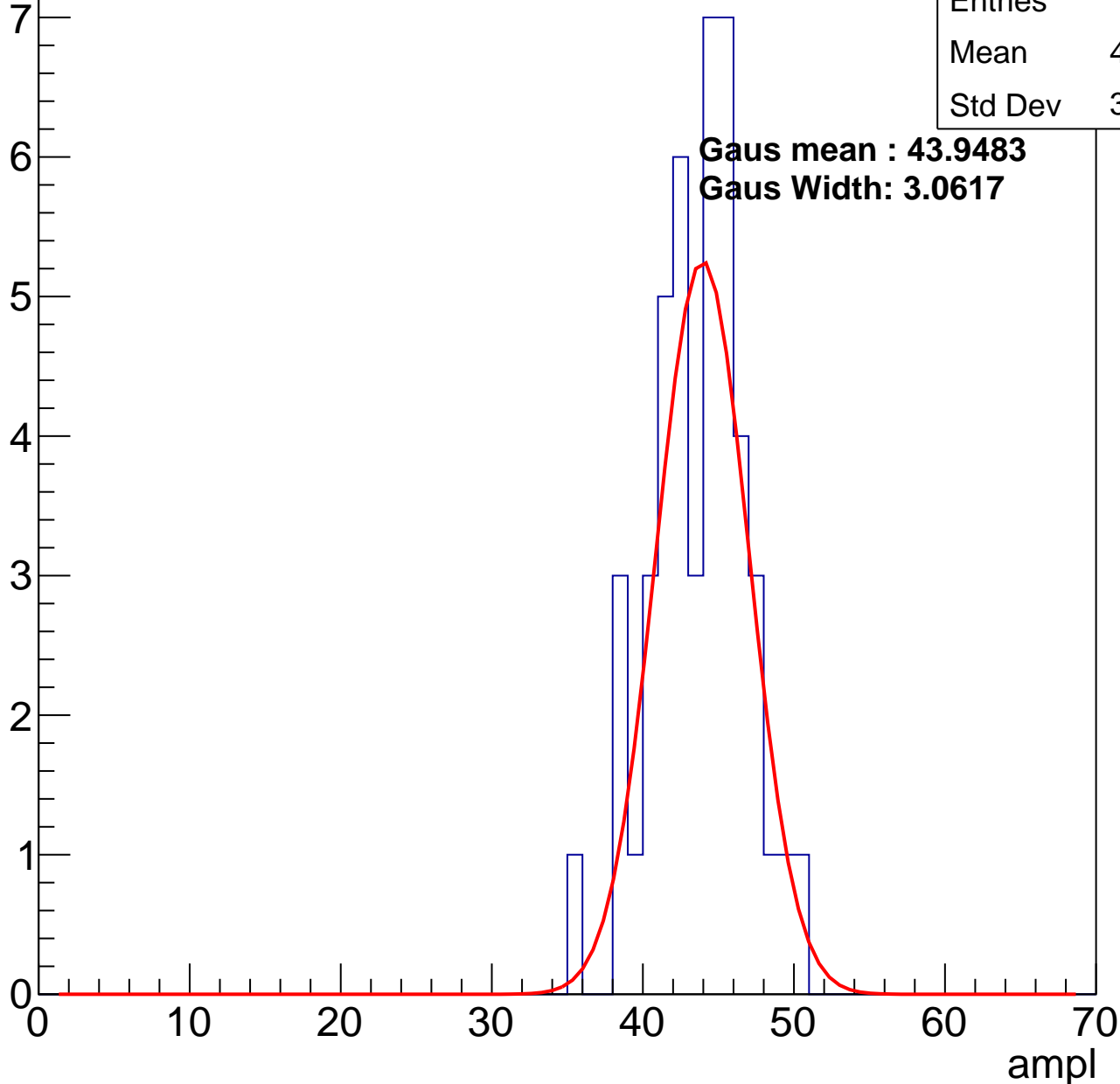
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	43.24
Std Dev	3.073

**Gaus mean : 43.9483**

**Gaus Width: 3.0617**

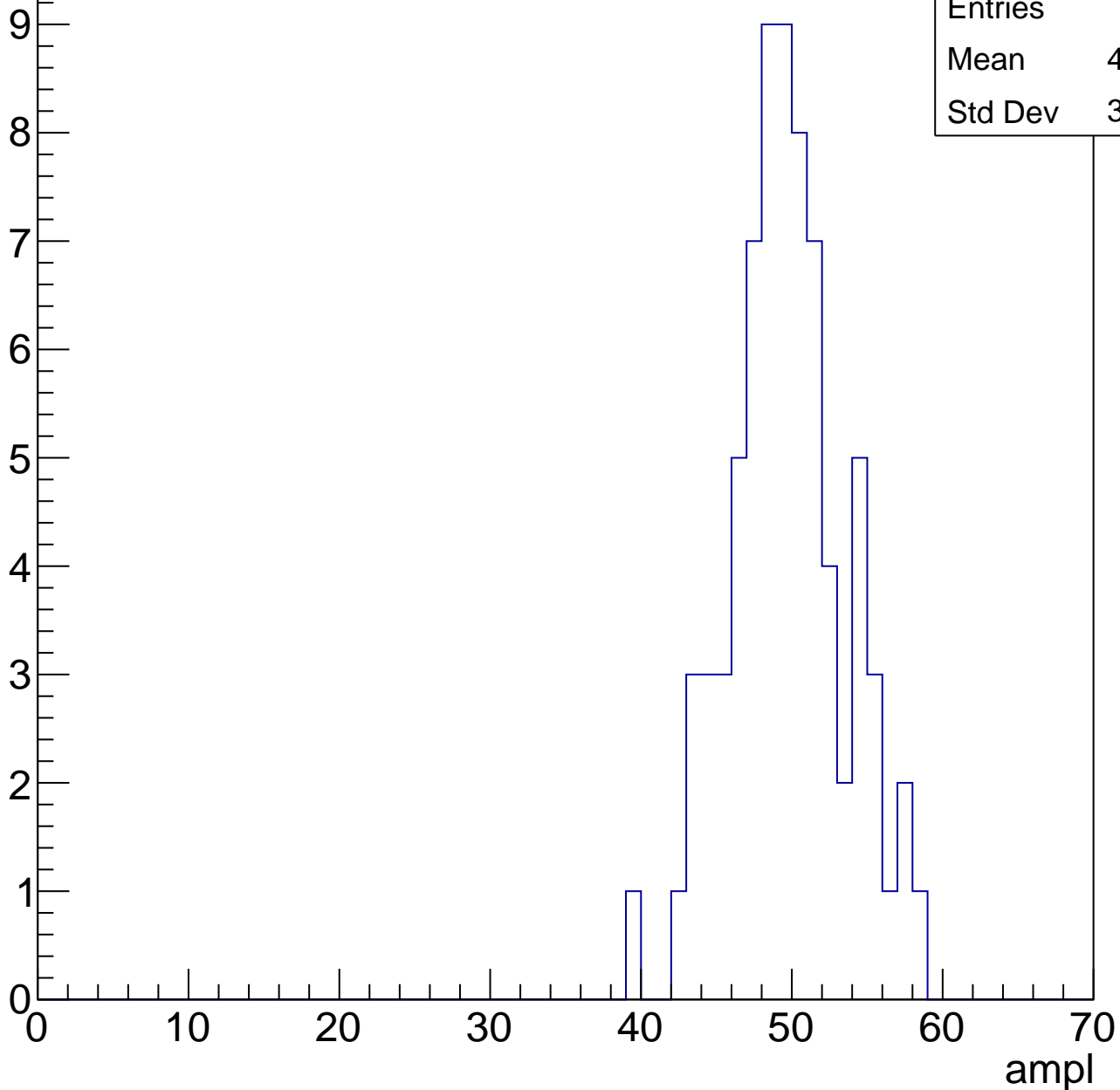


# B0L000S, U25-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	49.23
Std Dev	3.772

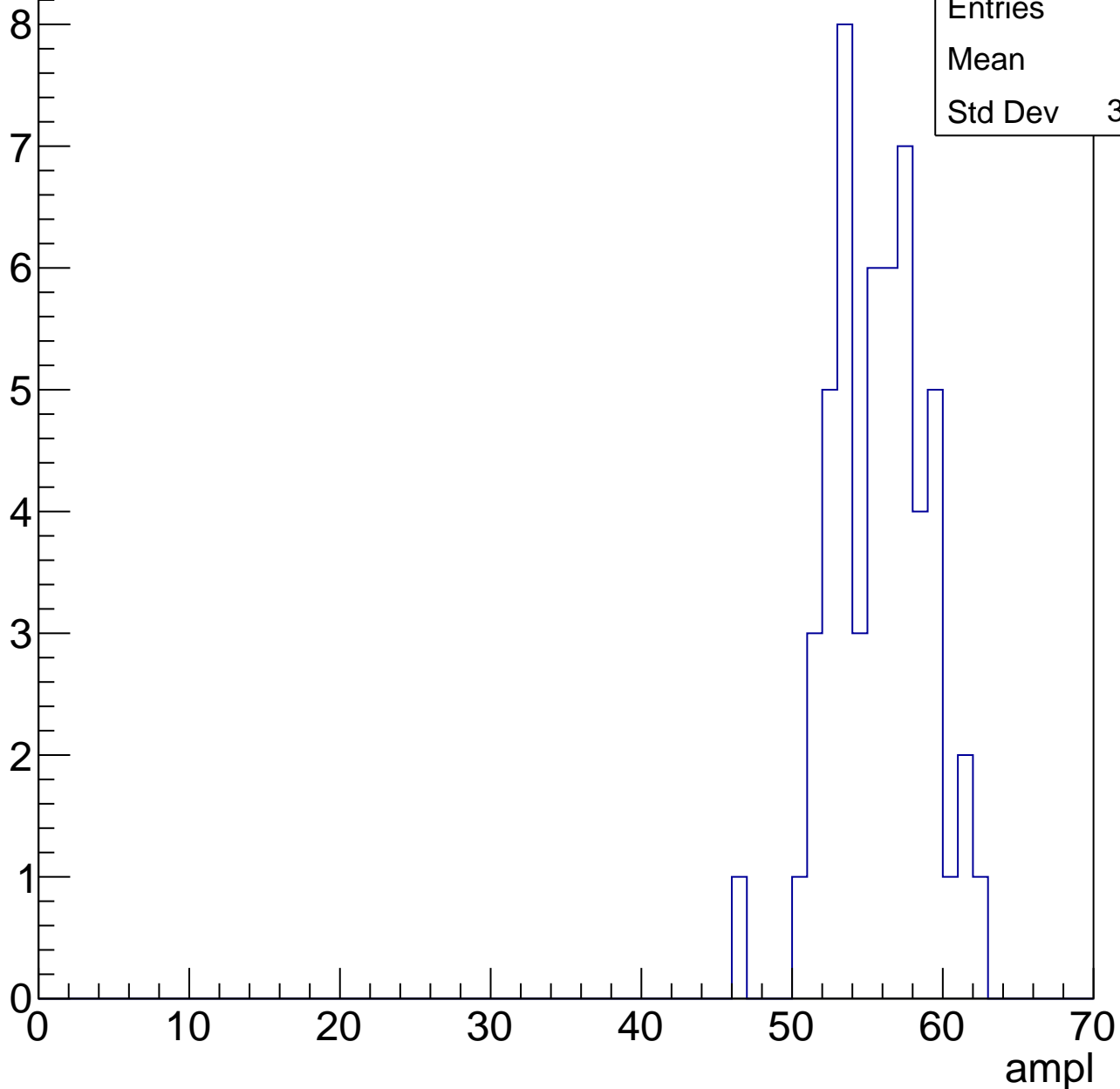


# B0L000S, U25-ch44, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.3
Std Dev	3.148

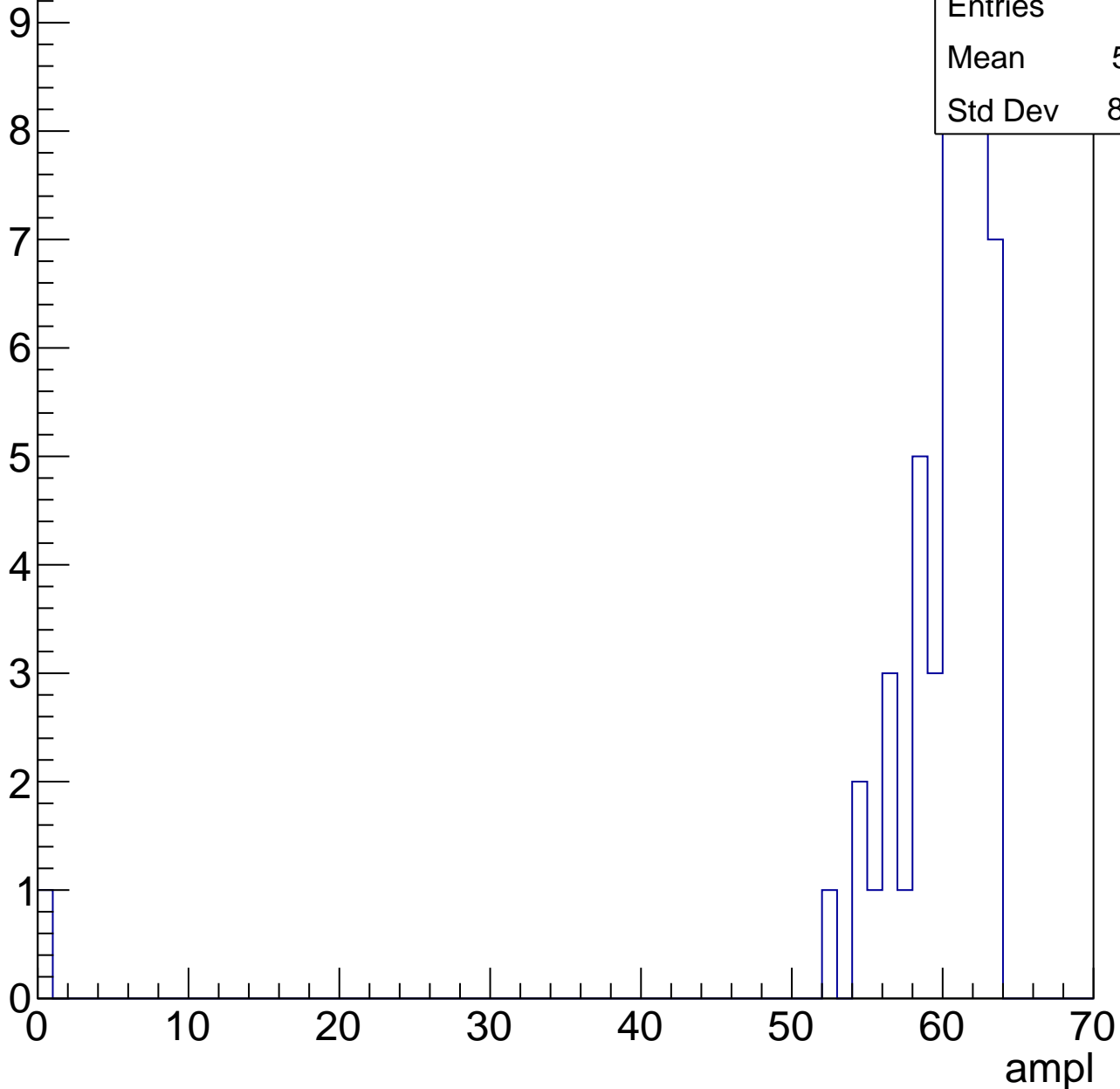


# B0L000S, U25-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

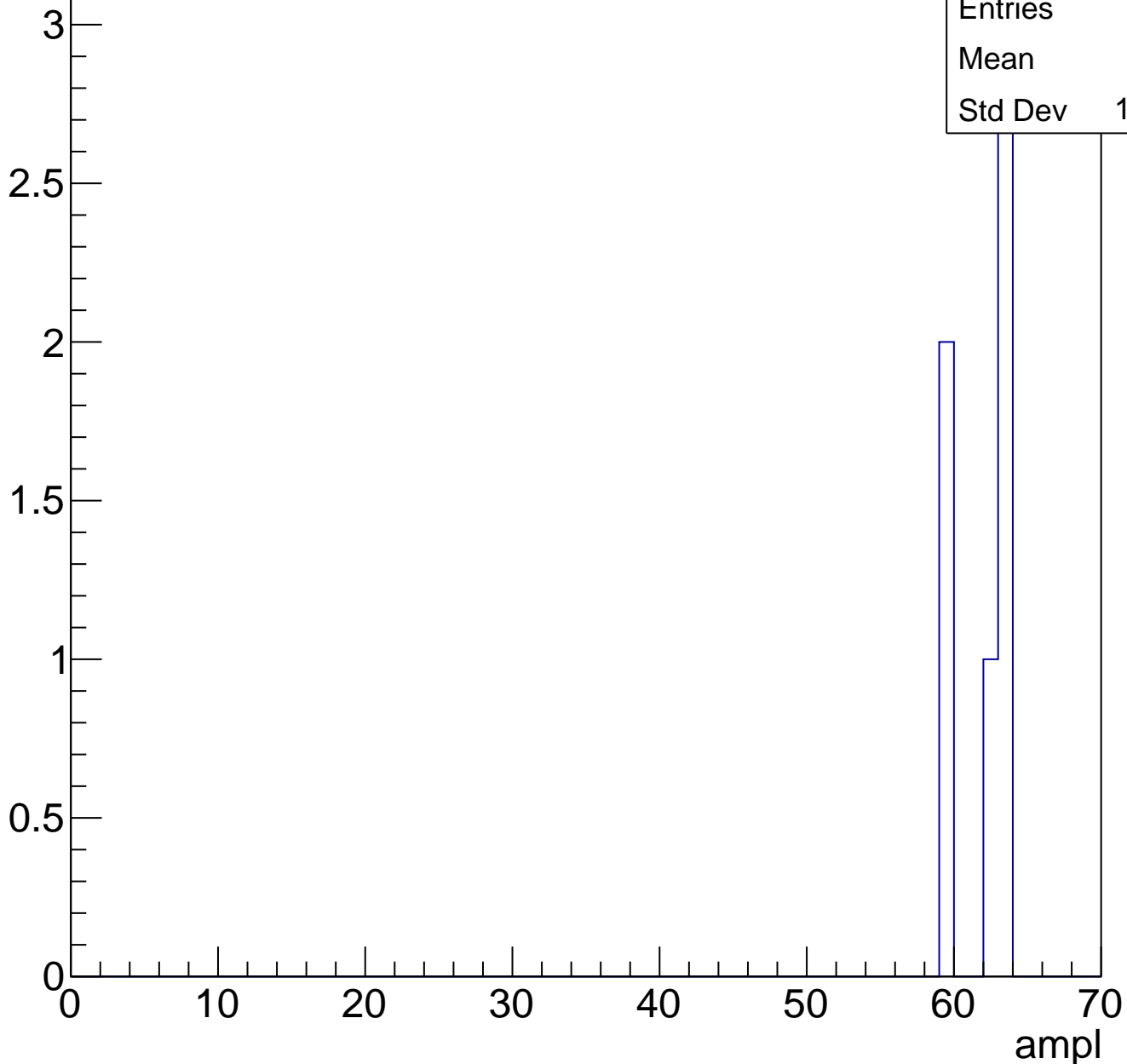
Entries	49
Mean	58.61
Std Dev	8.864



# B0L000S, U25-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

# B0L000S, U25-ch45, adc0

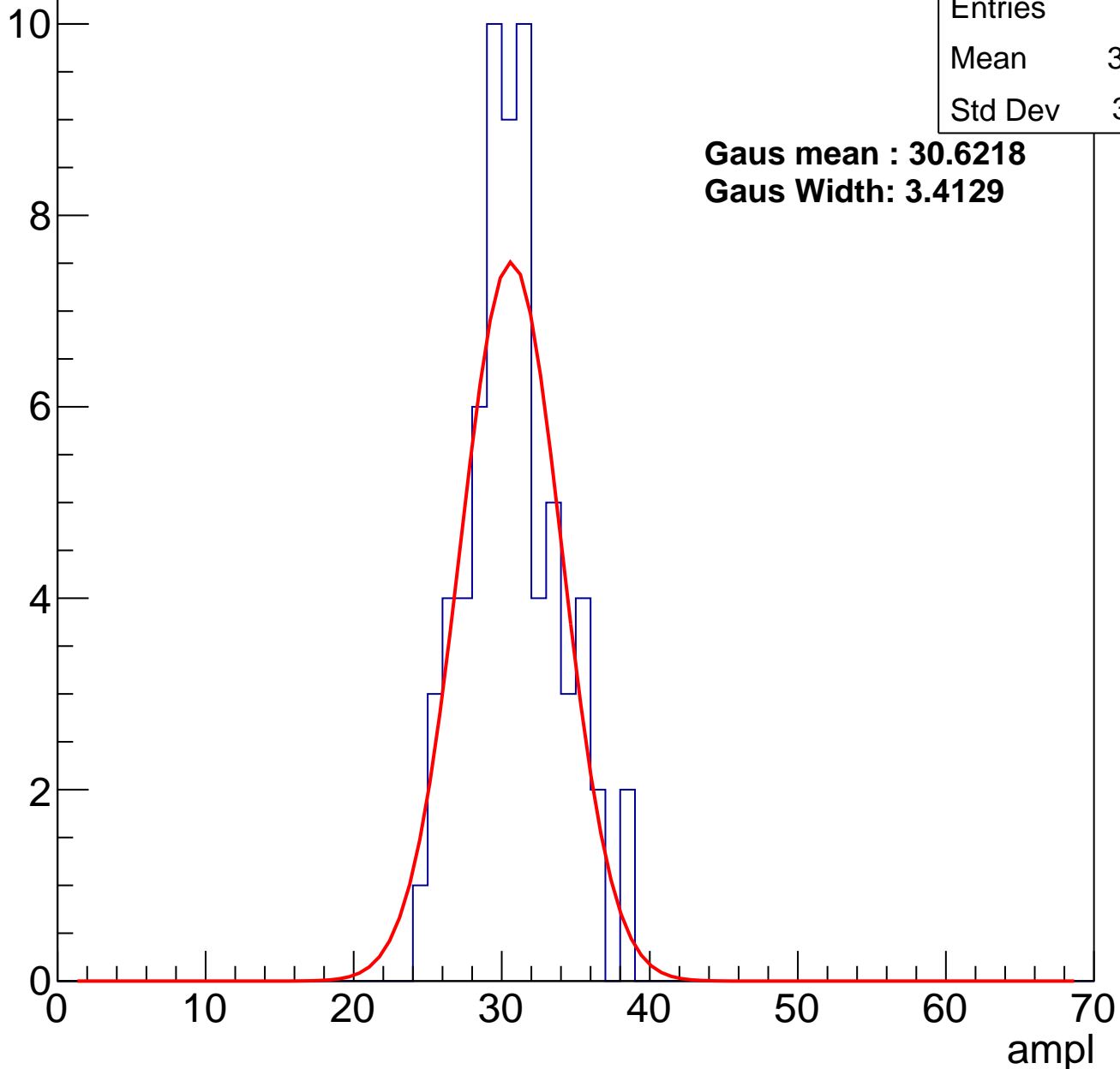
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	67
Mean	30.33
Std Dev	3.131

**Gaus mean : 30.6218**

**Gaus Width: 3.4129**

Entry



# B0L000S, U25-ch45, adc1

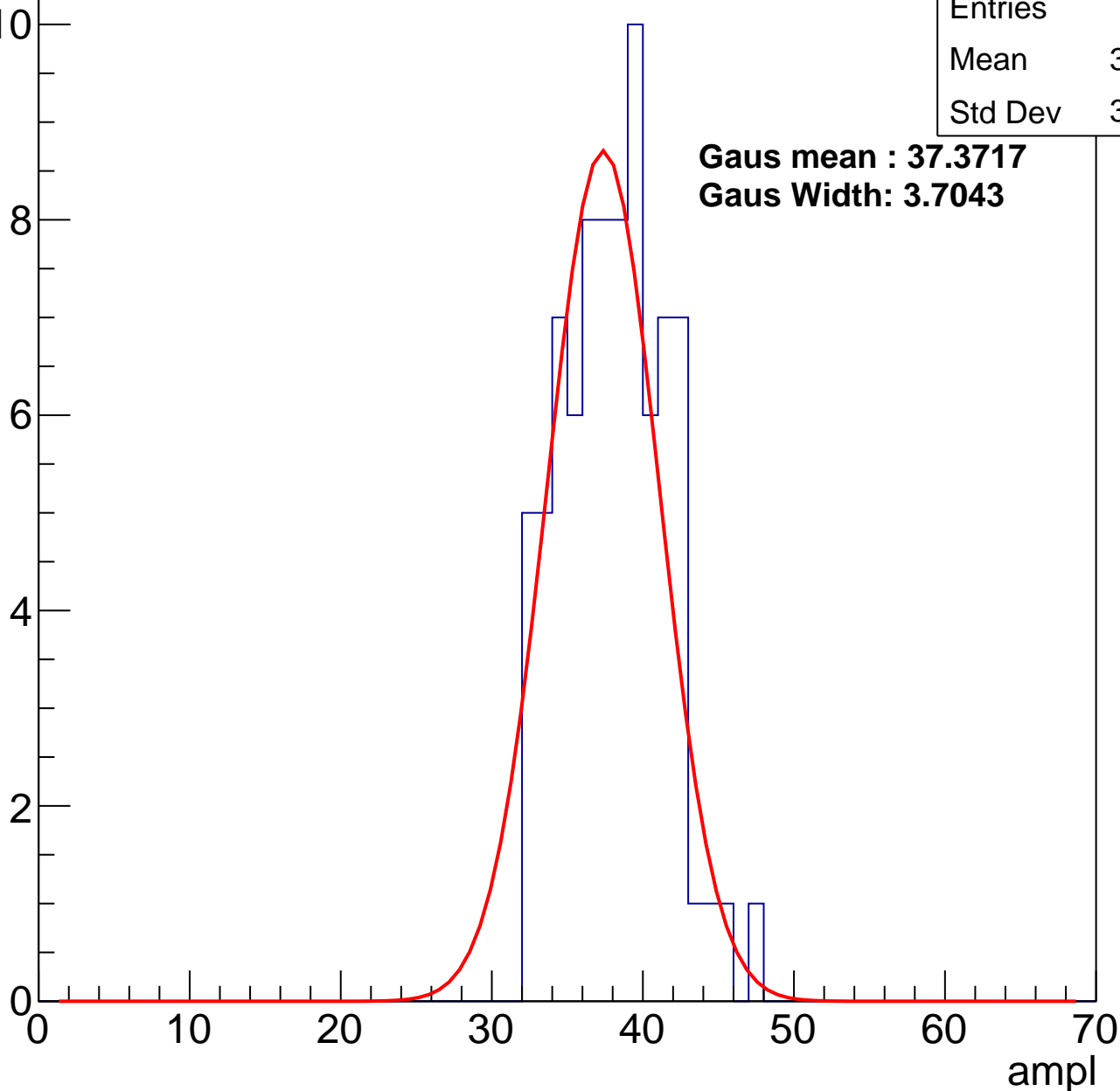
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	37.67
Std Dev	3.337

**Gaus mean : 37.3717**

**Gaus Width: 3.7043**



# B0L000S, U25-ch45, adc2

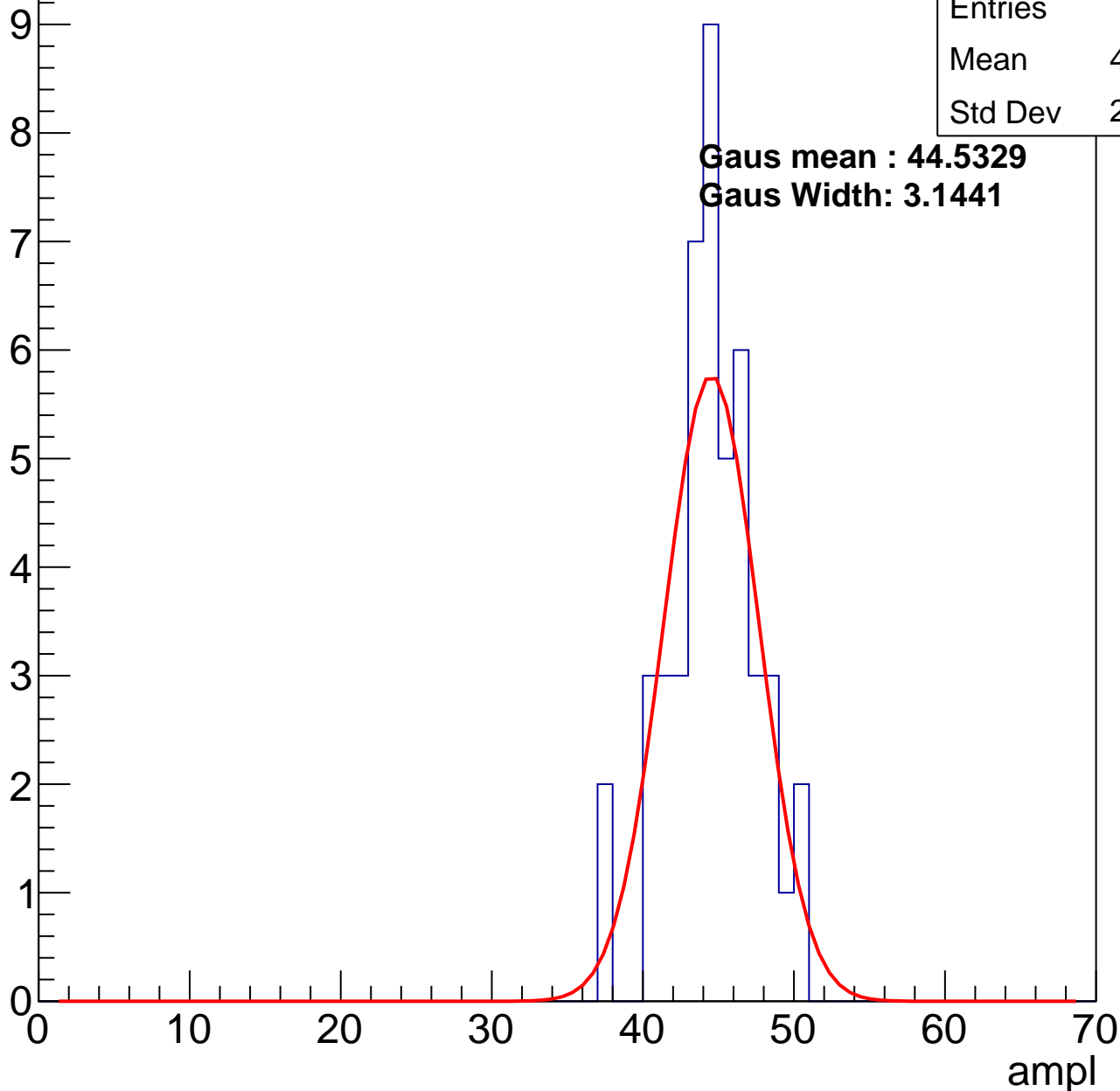
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	44.15
Std Dev	2.888

**Gaus mean : 44.5329**

**Gaus Width: 3.1441**

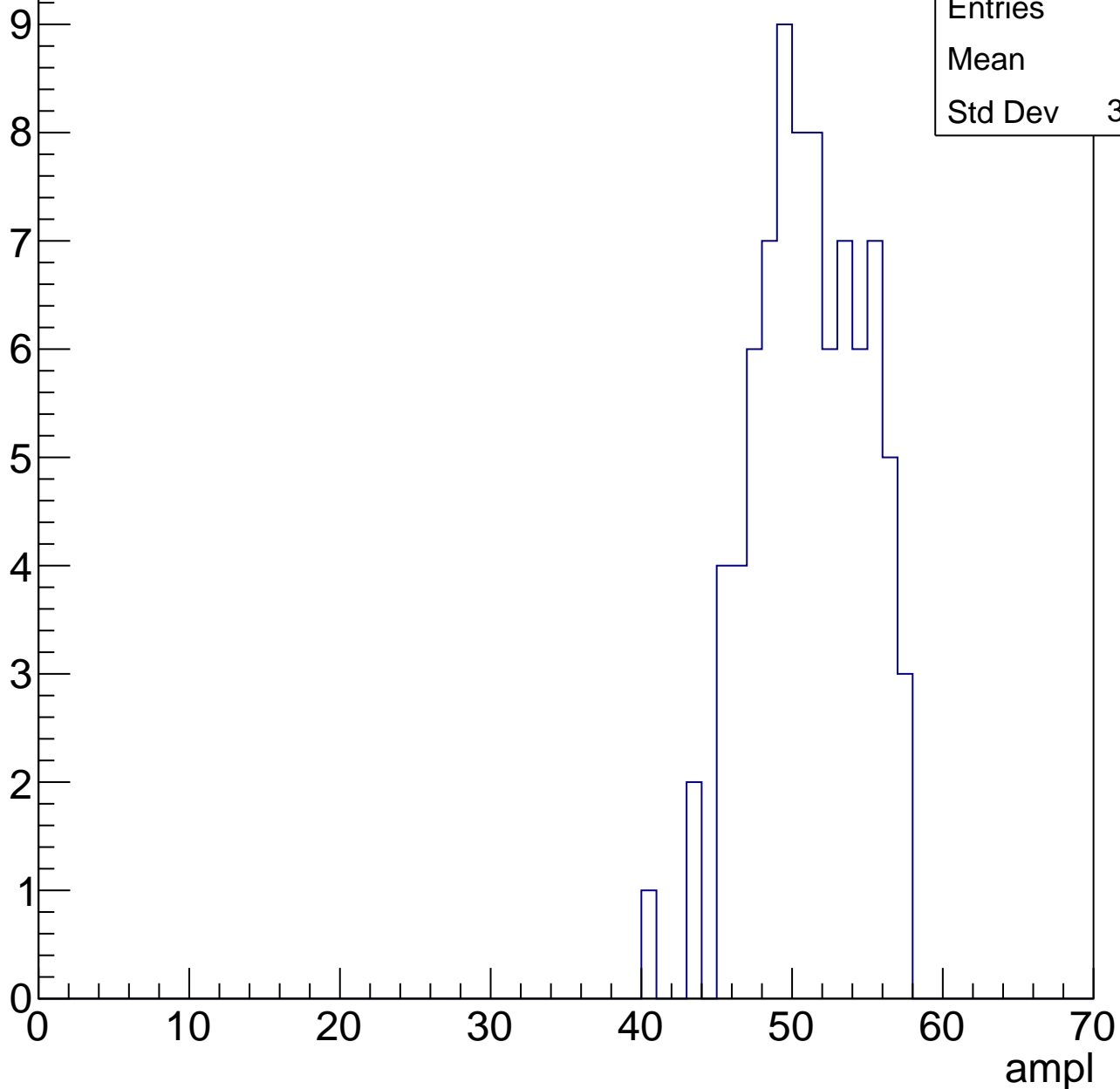


# B0L000S, U25-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	83
Mean	50.6
Std Dev	3.667

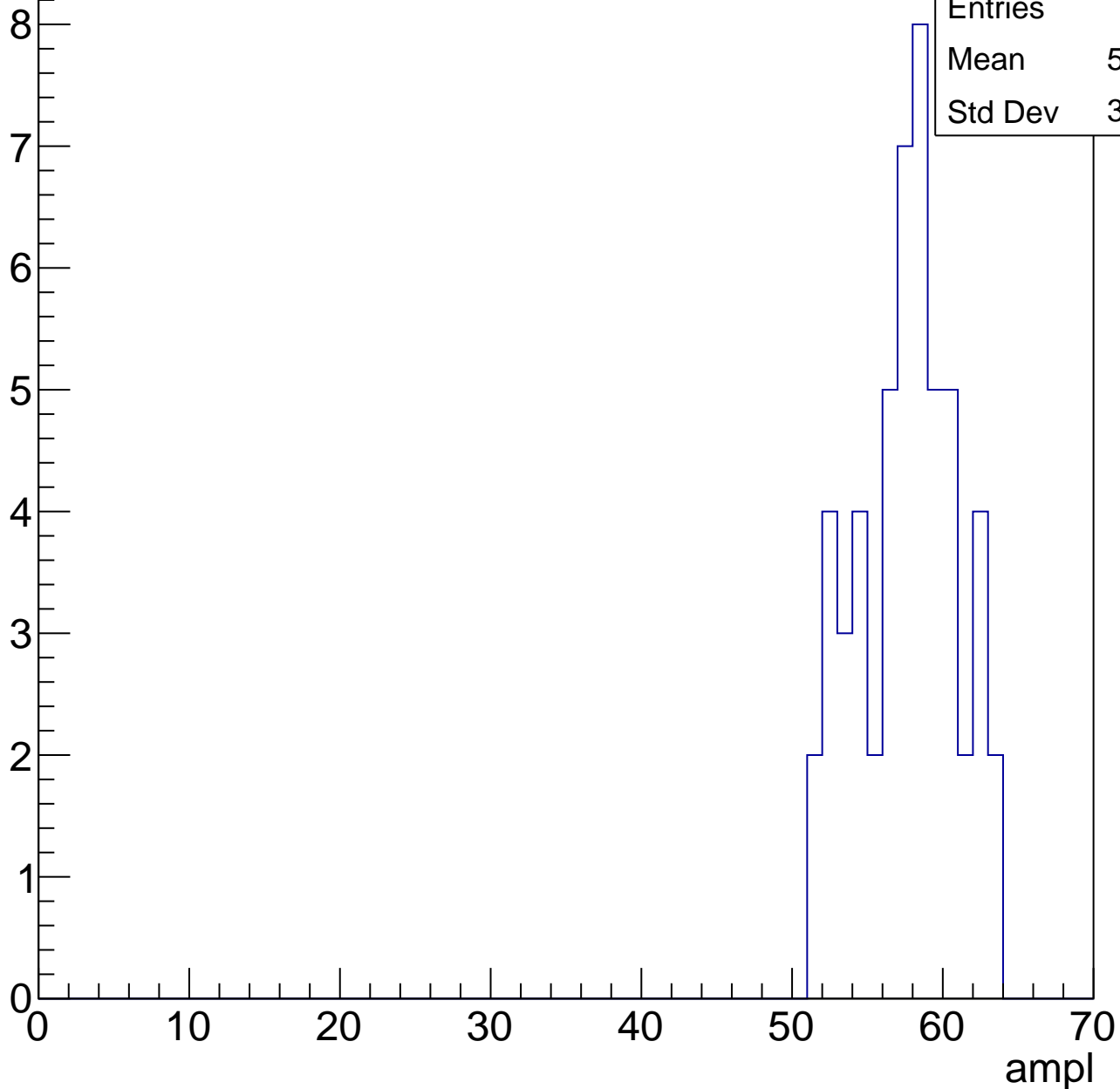


# B0L000S, U25-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.15
Std Dev	3.206

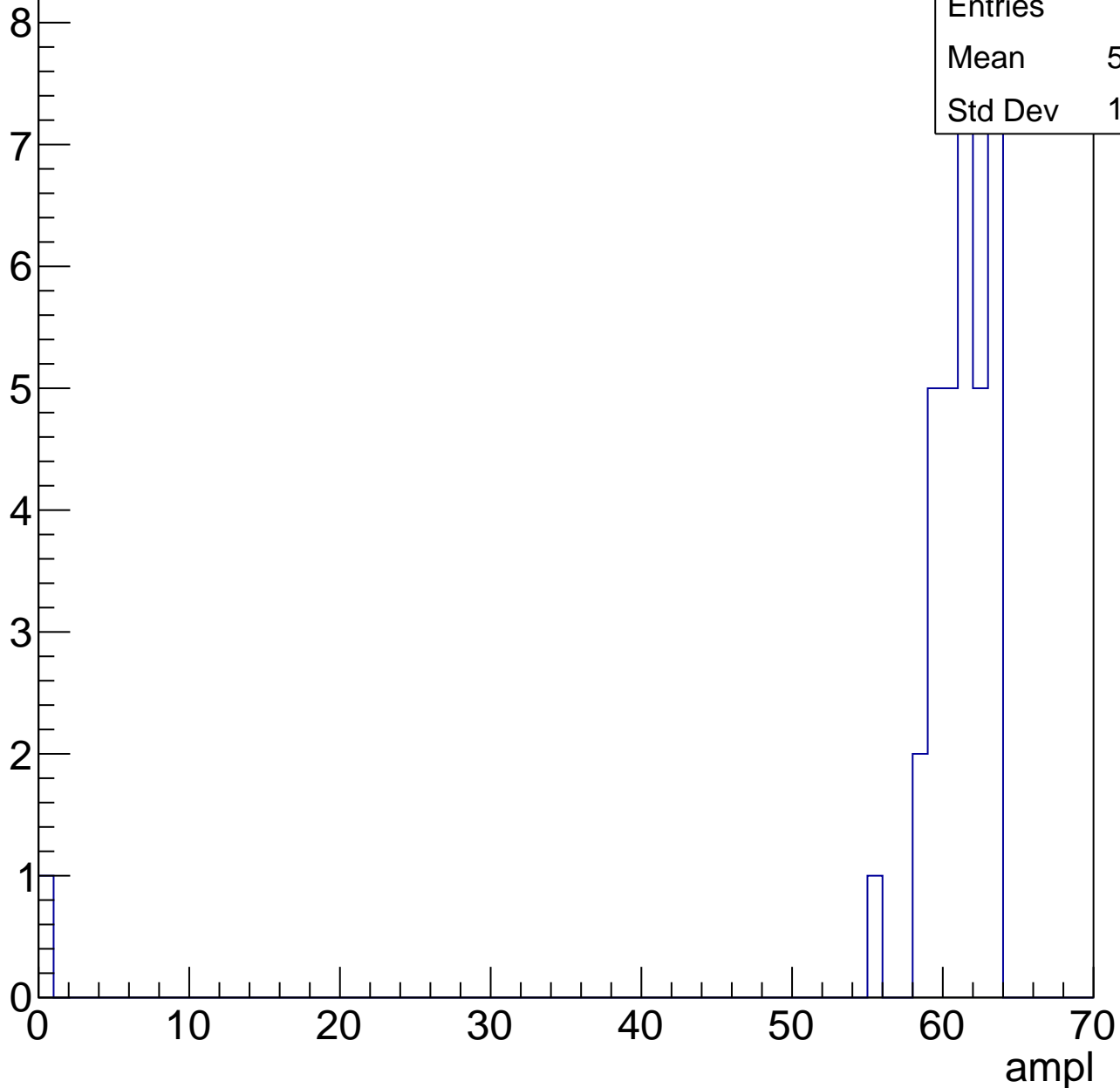


# B0L000S, U25-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	59.09
Std Dev	10.29



# B0L000S, U25-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch46, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	29.13
Std Dev	3.48

**Gaus mean : 30.0683**

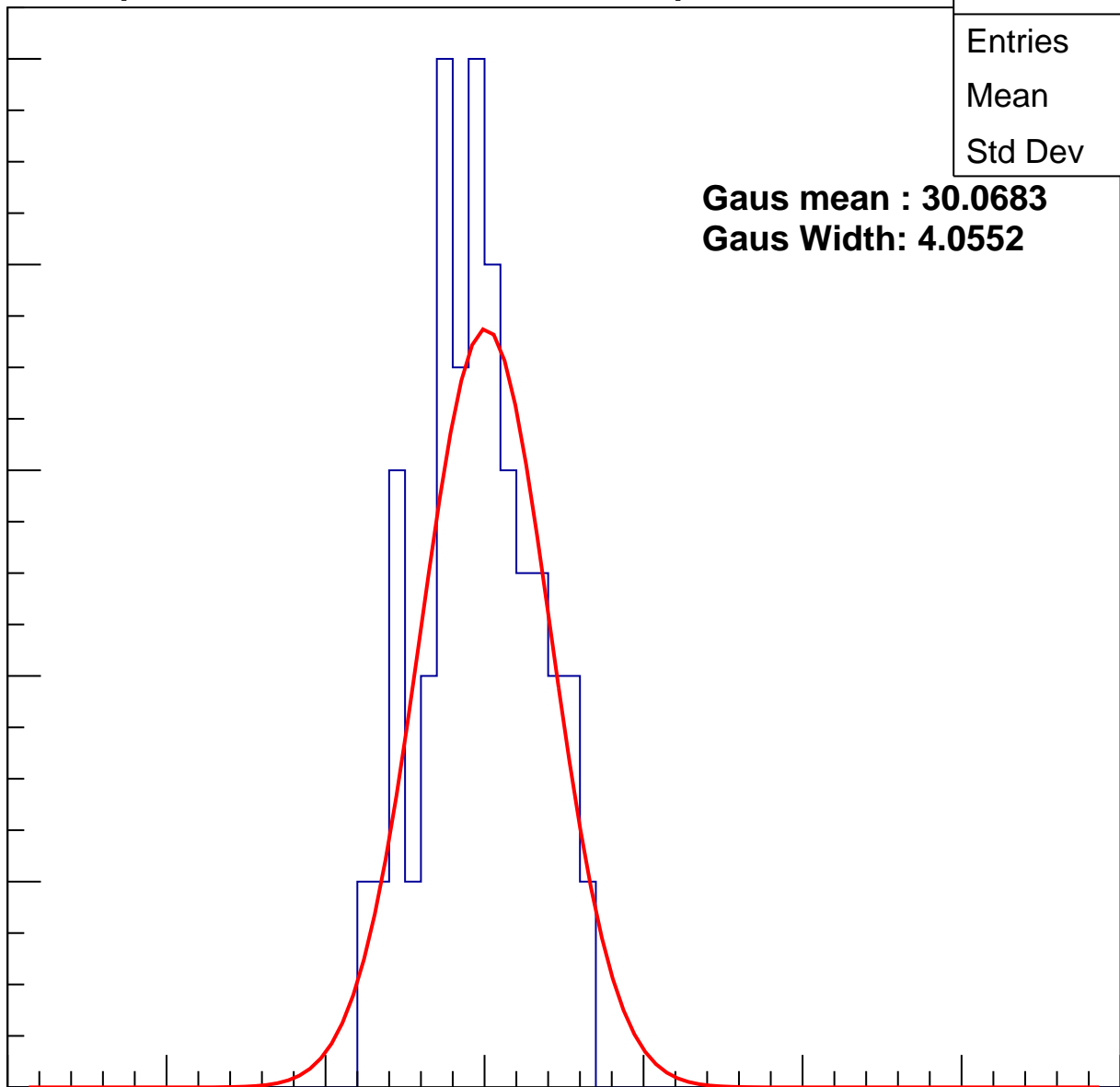
**Gaus Width: 4.0552**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch46, adc1

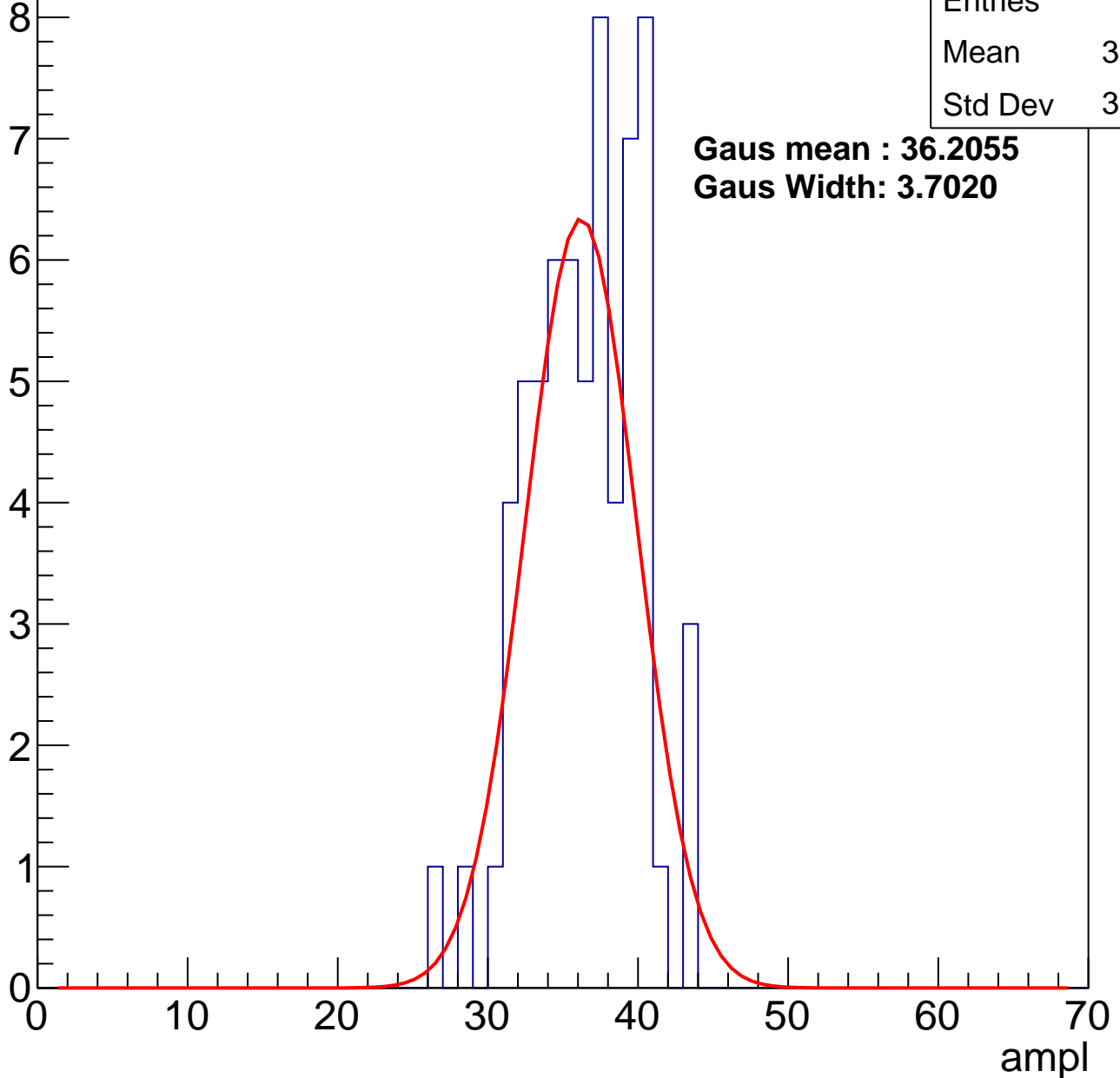
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.97
Std Dev	3.599

**Gaus mean : 36.2055**

**Gaus Width: 3.7020**



# B0L000S, U25-ch46, adc2

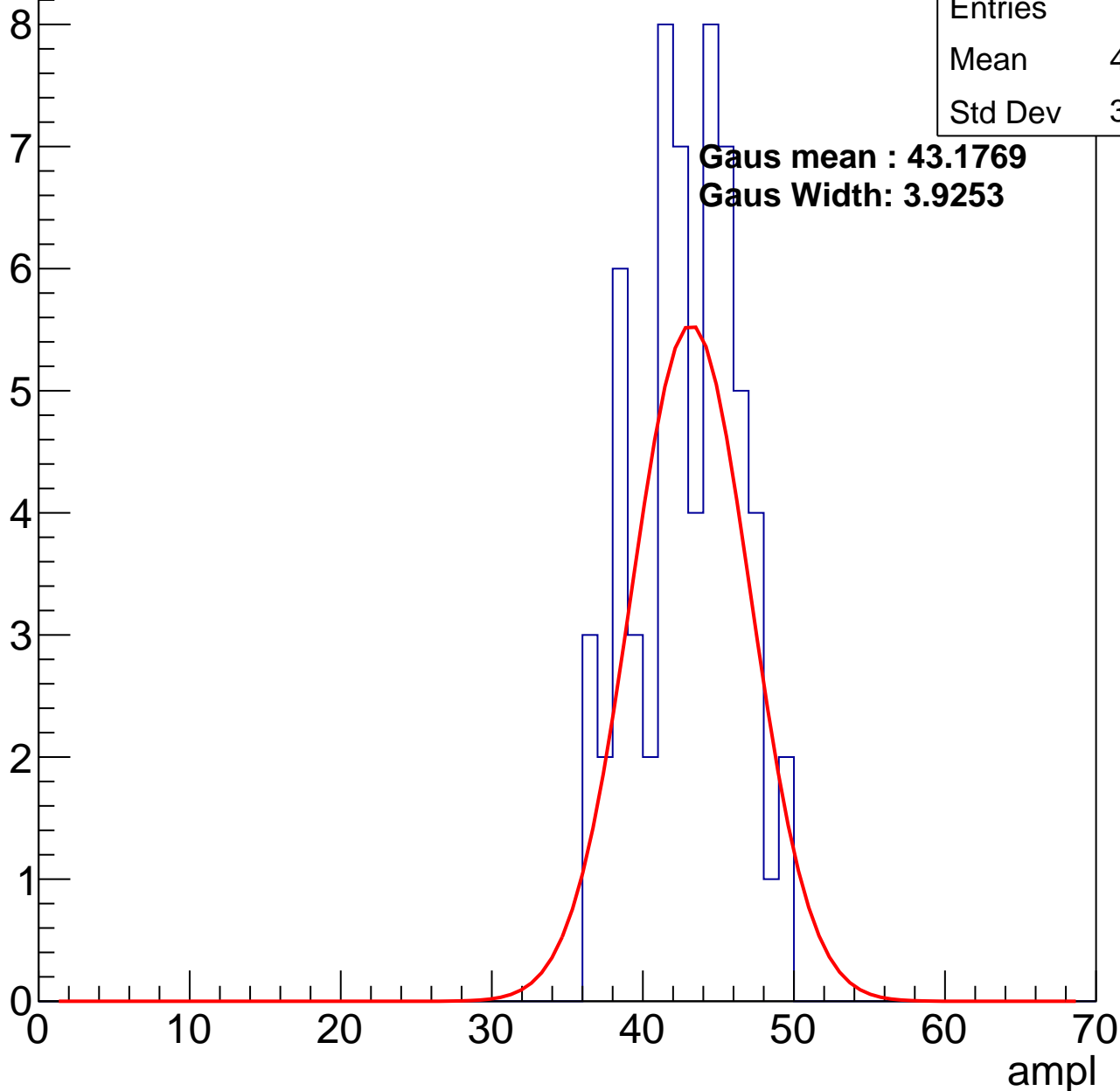
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.45
Std Dev	3.354

**Gaus mean : 43.1769**

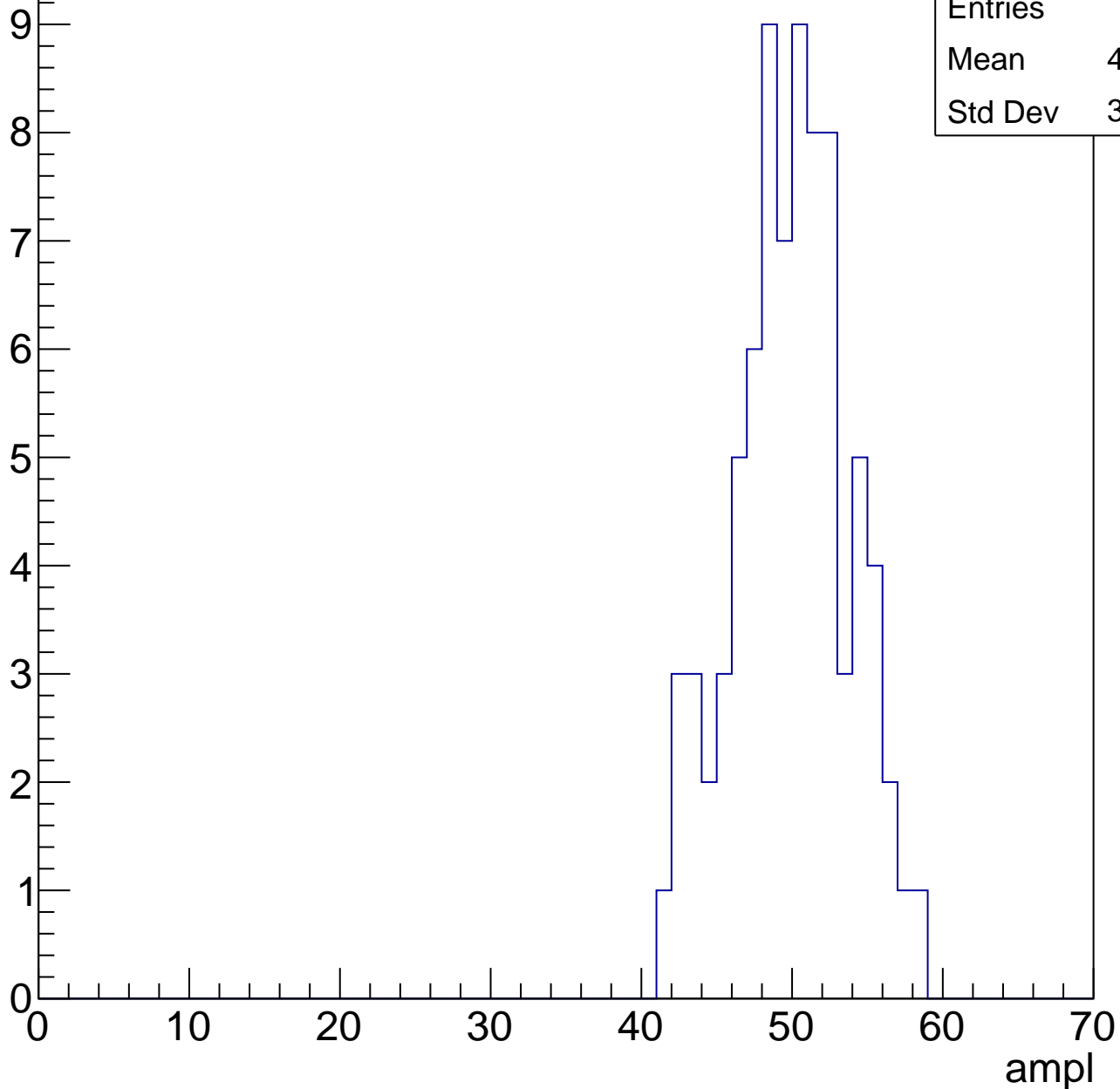
**Gaus Width: 3.9253**



# B0L000S, U25-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

6

5

4

3

2

1

0

Entries	37
Mean	55.19
Std Dev	2.817

ampl

0

10

20

30

40

50

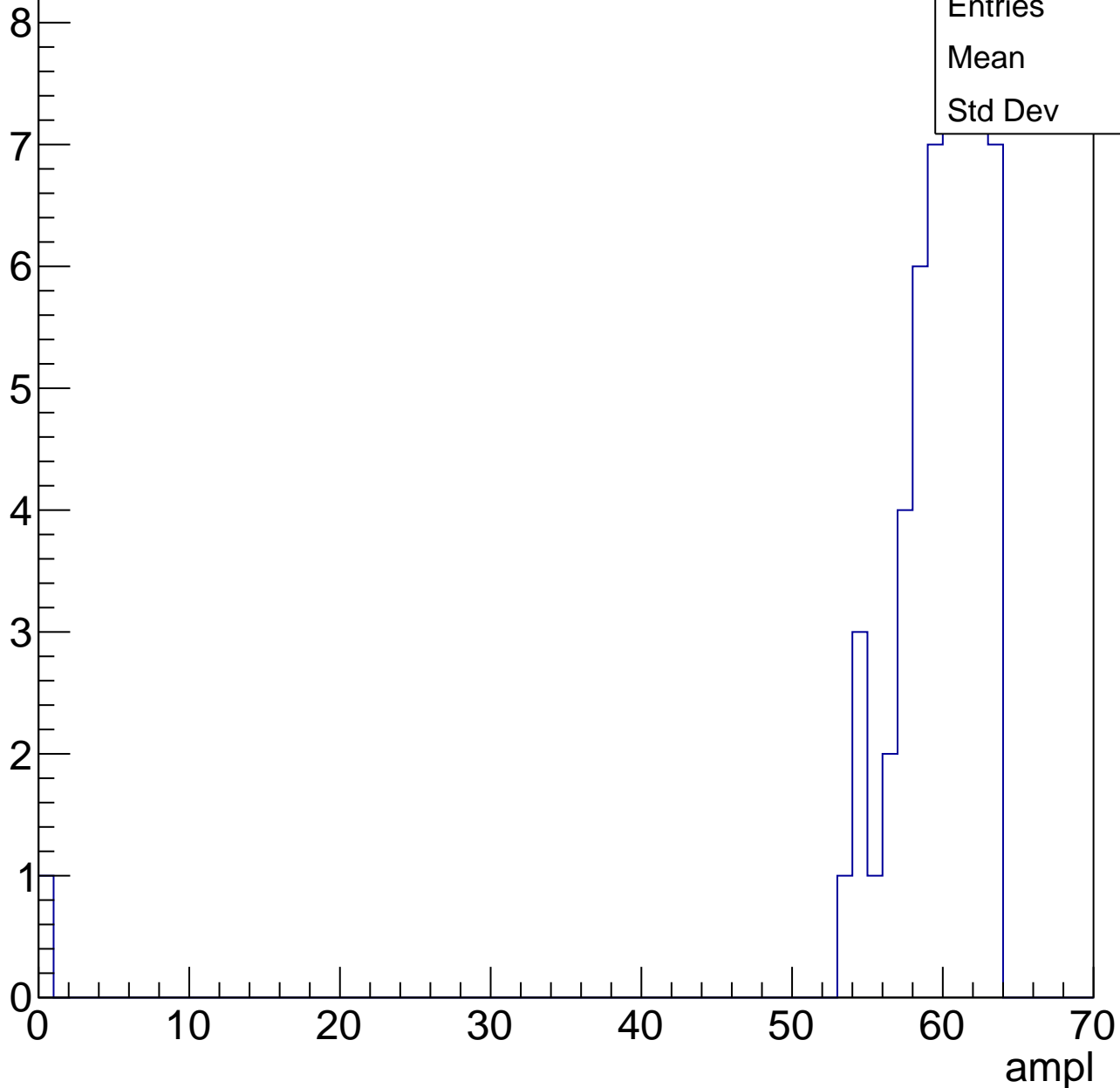
60

70

# B0L000S, U25-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

8

Mean

61.38

Std Dev

1.932

3

2.5

2

1.5

1

0.5

0

10

20

30

40

50

60

70

ampl



# B0L000S, U25-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch47, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	91
Mean	29.6
Std Dev	4.704

**Gaus mean : 30.8912**

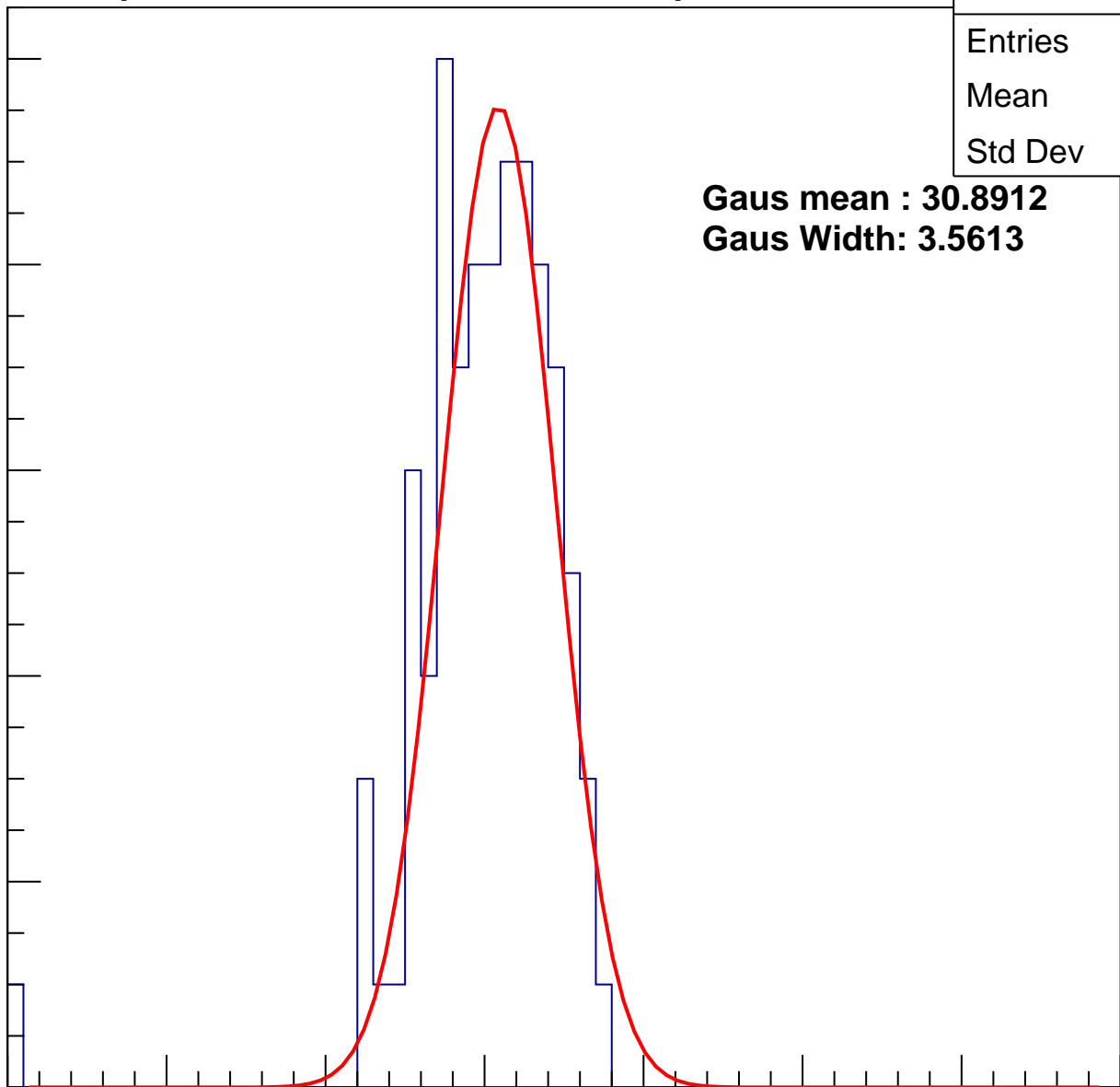
**Gaus Width: 3.5613**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



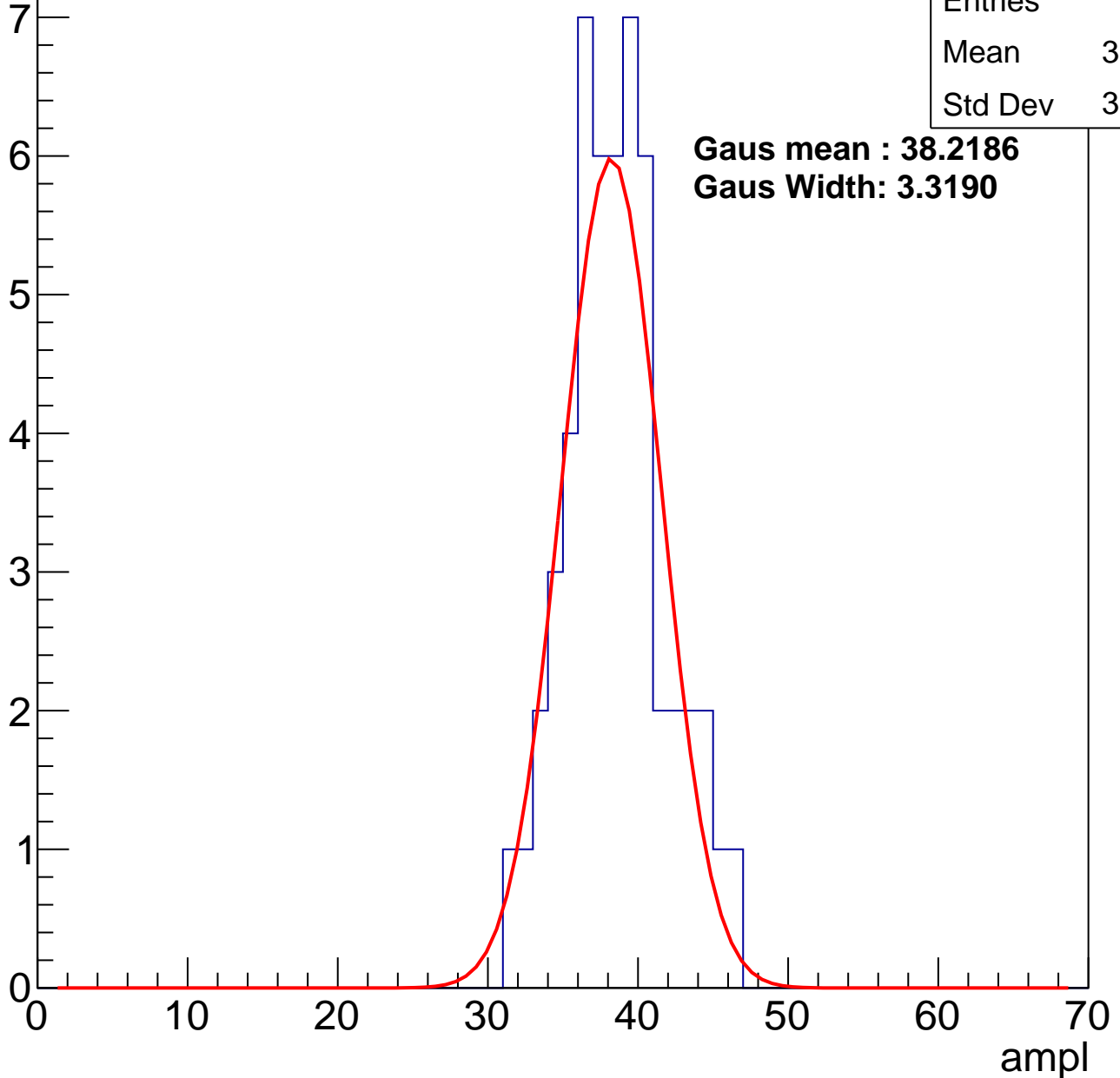
# B0L000S, U25-ch47, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	38.06
Std Dev	3.276

**Gaus mean : 38.2186**  
**Gaus Width: 3.3190**



# B0L000S, U25-ch47, adc2

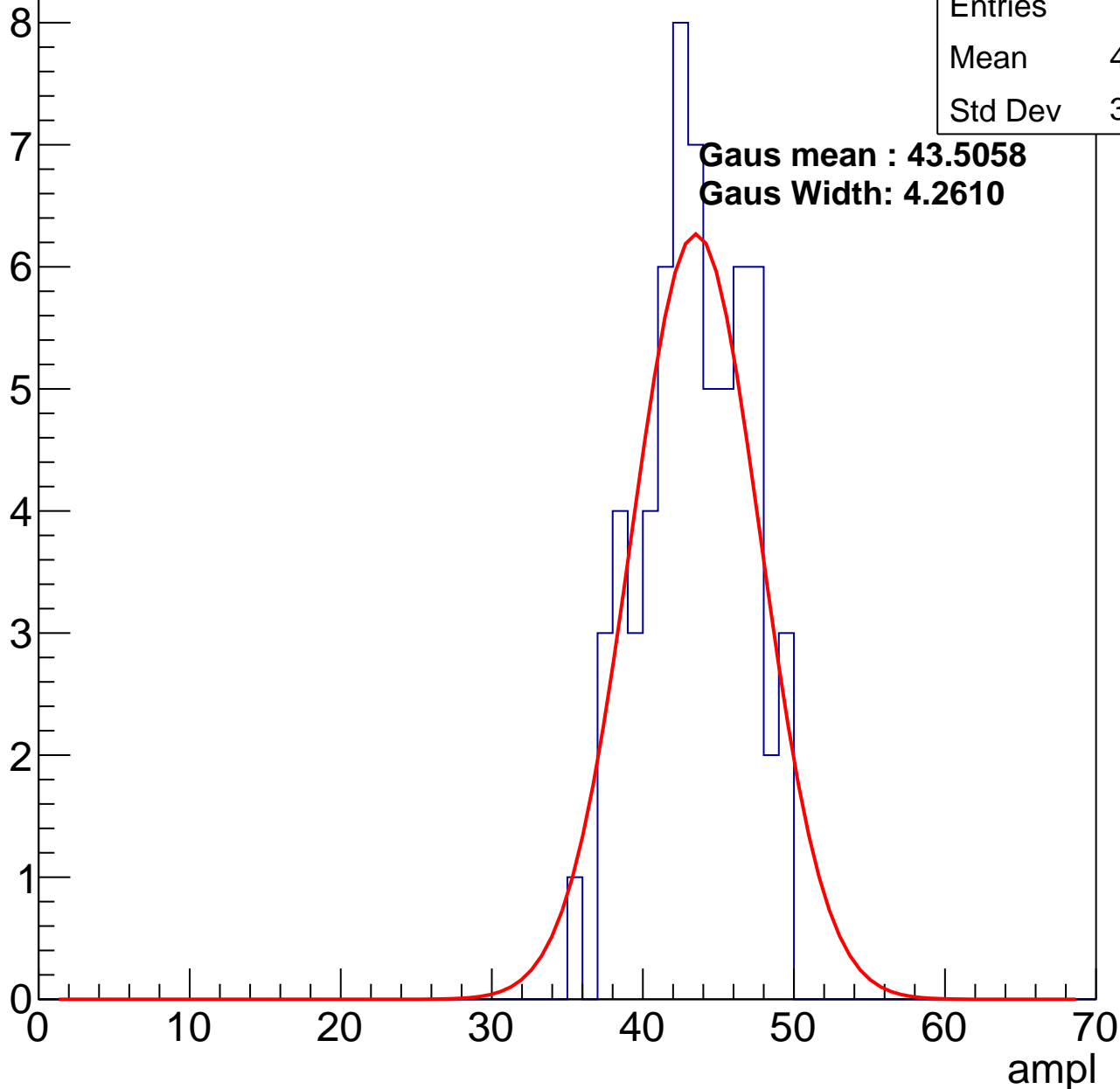
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.92
Std Dev	3.382

**Gaus mean : 43.5058**

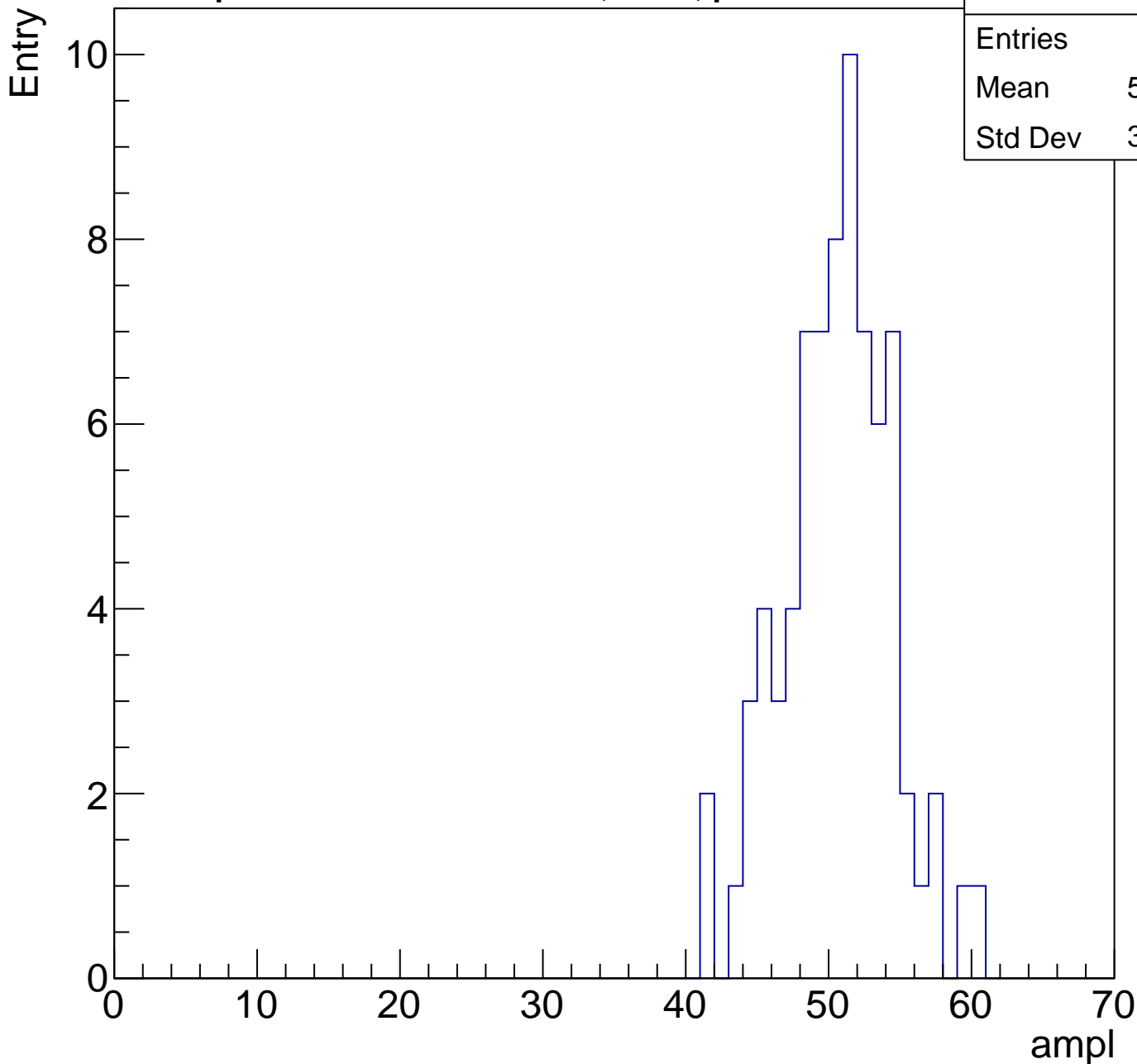
**Gaus Width: 4.2610**



# B0L000S, U25-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	76
Mean	50.14
Std Dev	3.814

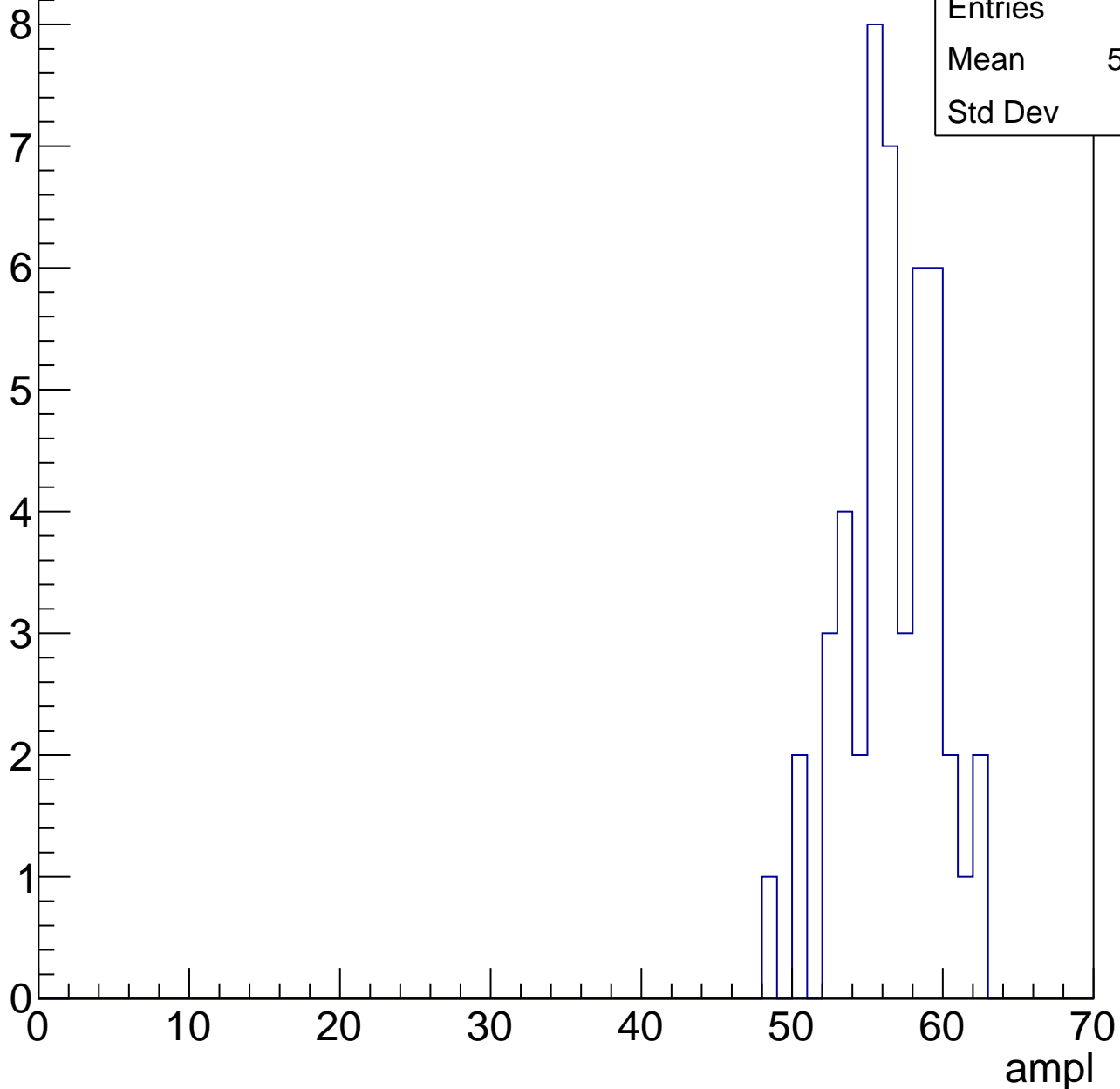


# B0L000S, U25-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	47
Mean	56.04
Std Dev	3.08

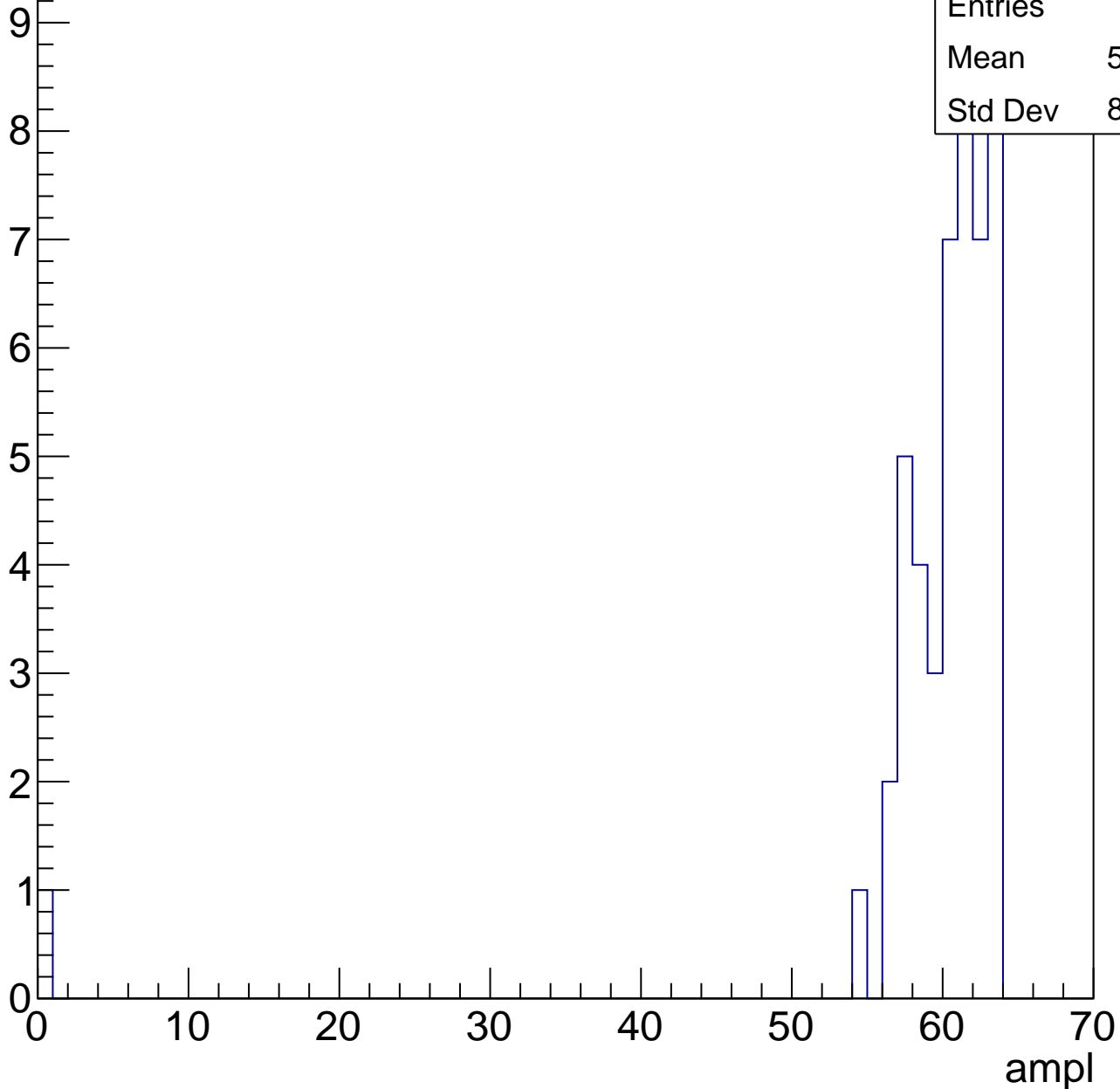


# B0L000S, U25-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	58.96
Std Dev	8.895



# B0L000S, U25-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

# B0L000S, U25-ch48, adc0

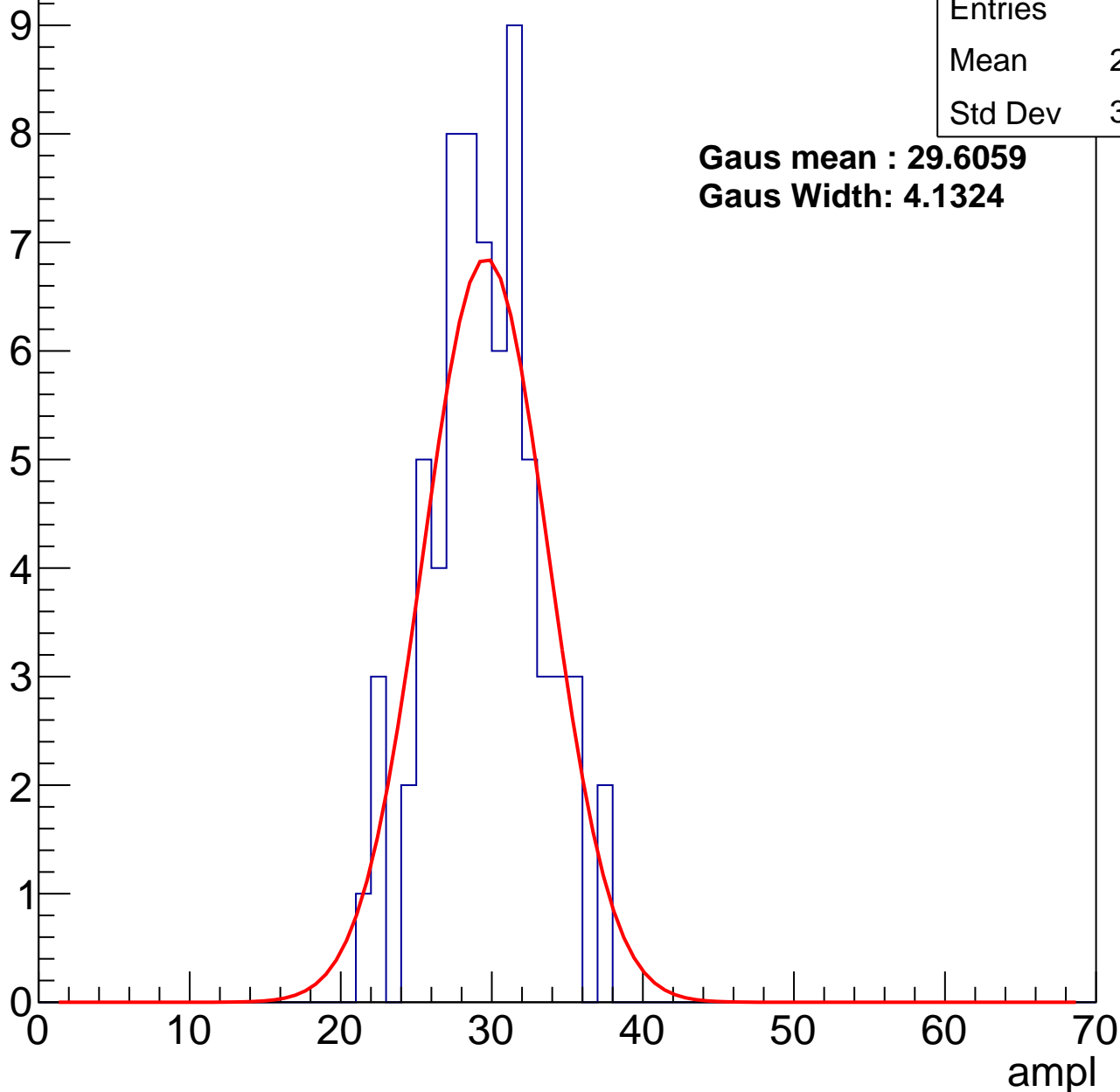
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.07
Std Dev	3.536

**Gaus mean : 29.6059**

**Gaus Width: 4.1324**



# B0L000S, U25-ch48, adc1

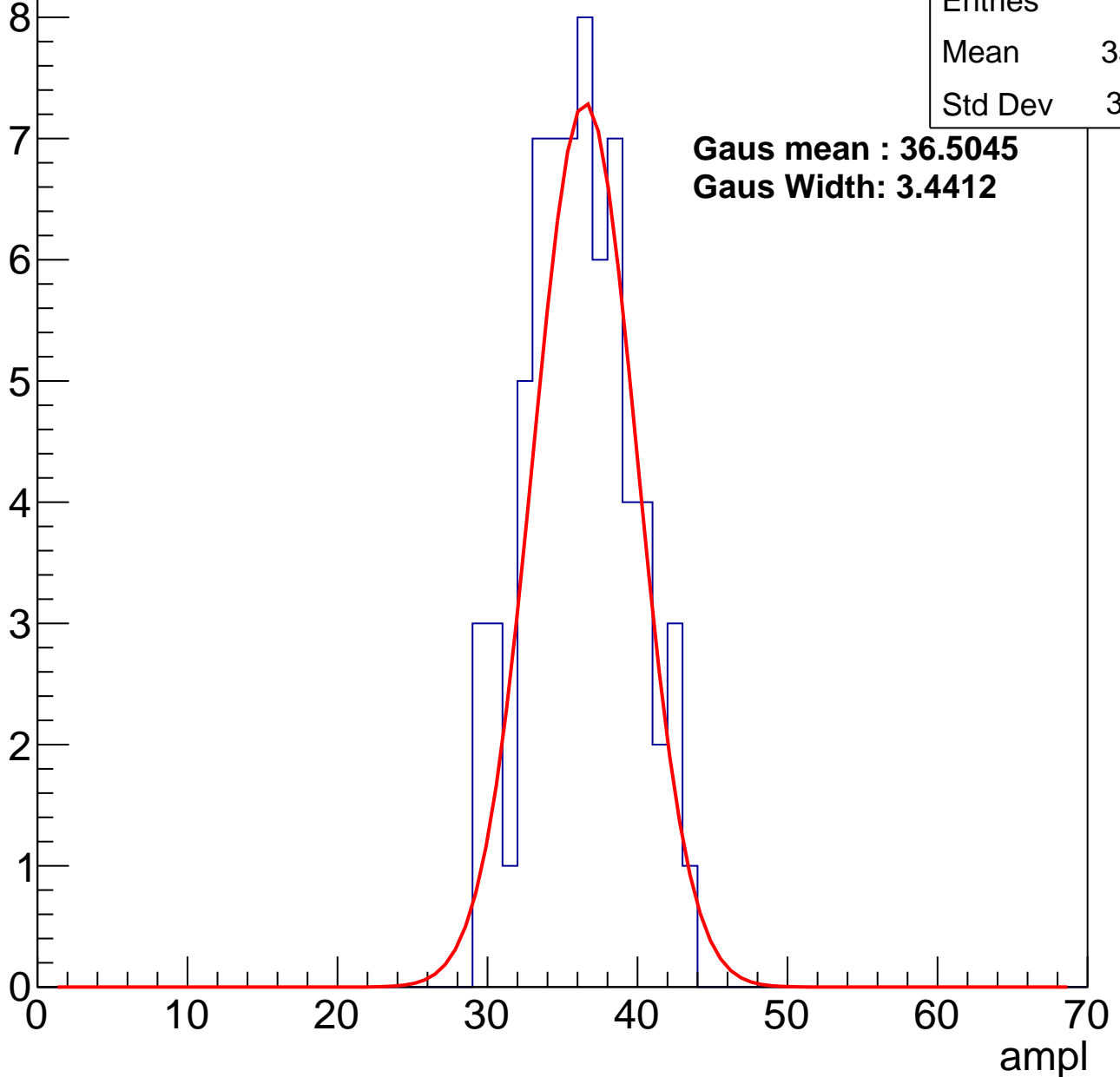
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	35.66
Std Dev	3.411

**Gaus mean : 36.5045**

**Gaus Width: 3.4412**



# B0L000S, U25-ch48, adc2

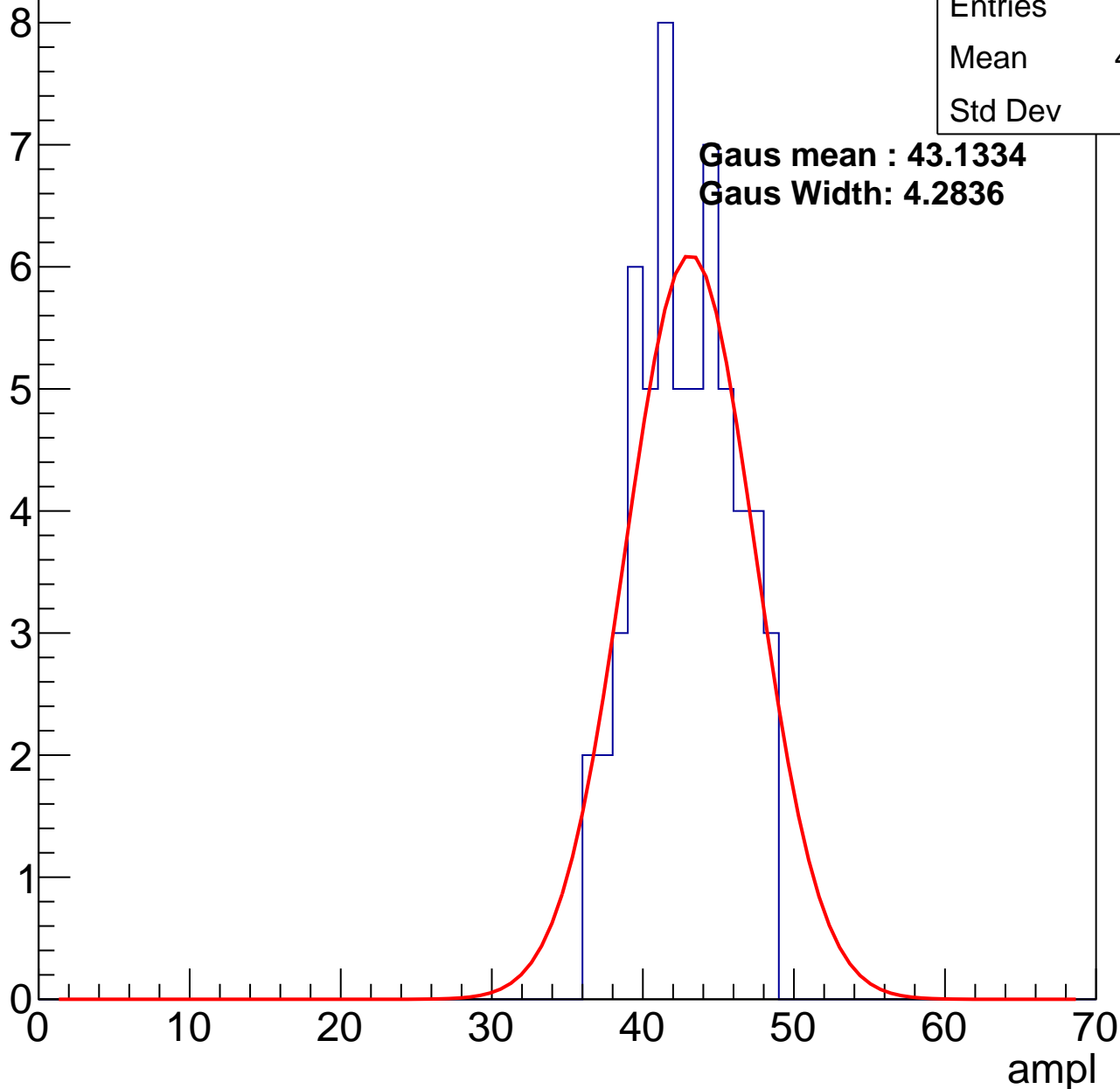
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.31
Std Dev	3.18

**Gaus mean : 43.1334**

**Gaus Width: 4.2836**



# B0L000S, U25-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

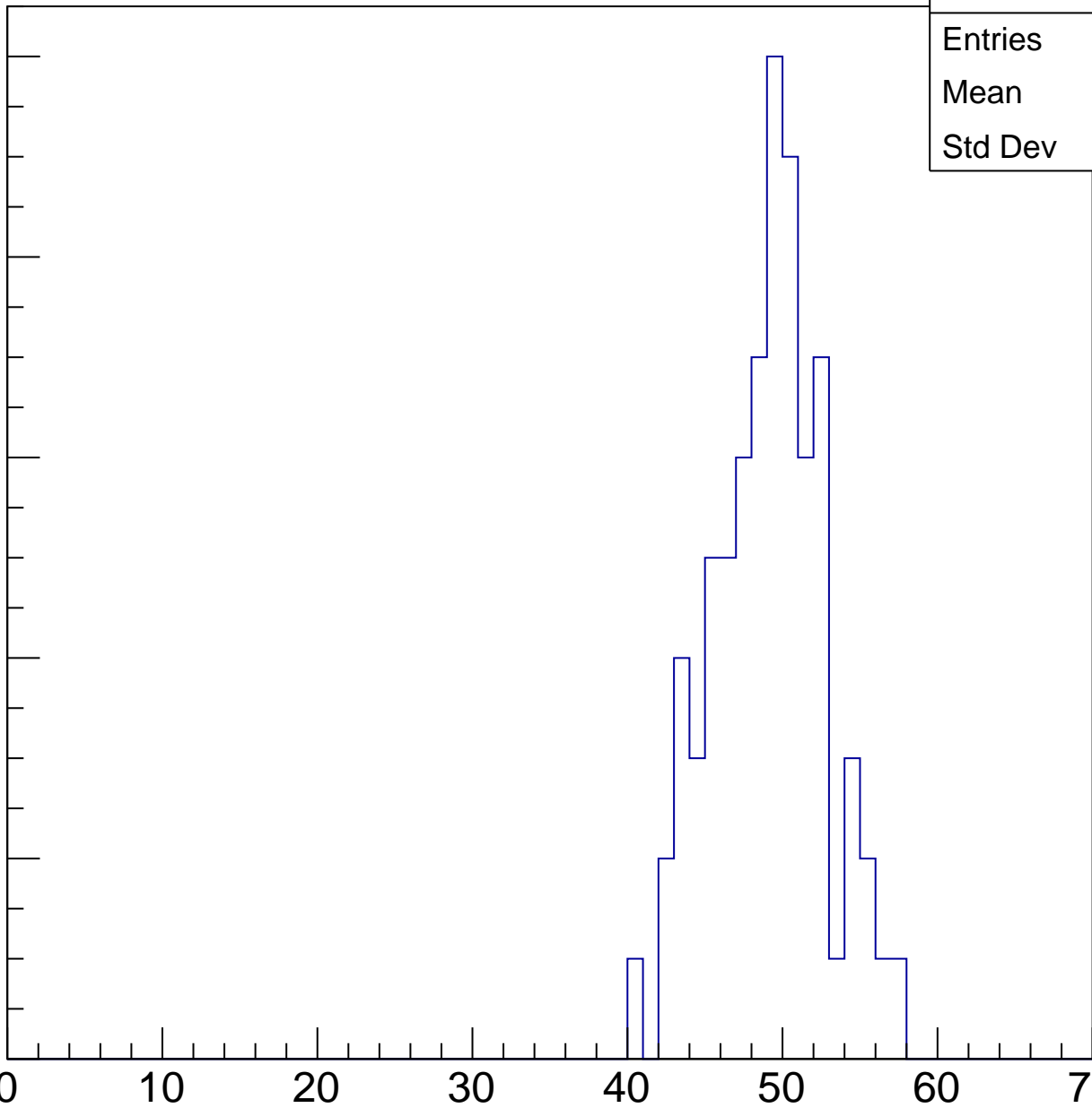
Entries	73
Mean	48.62
Std Dev	3.541

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

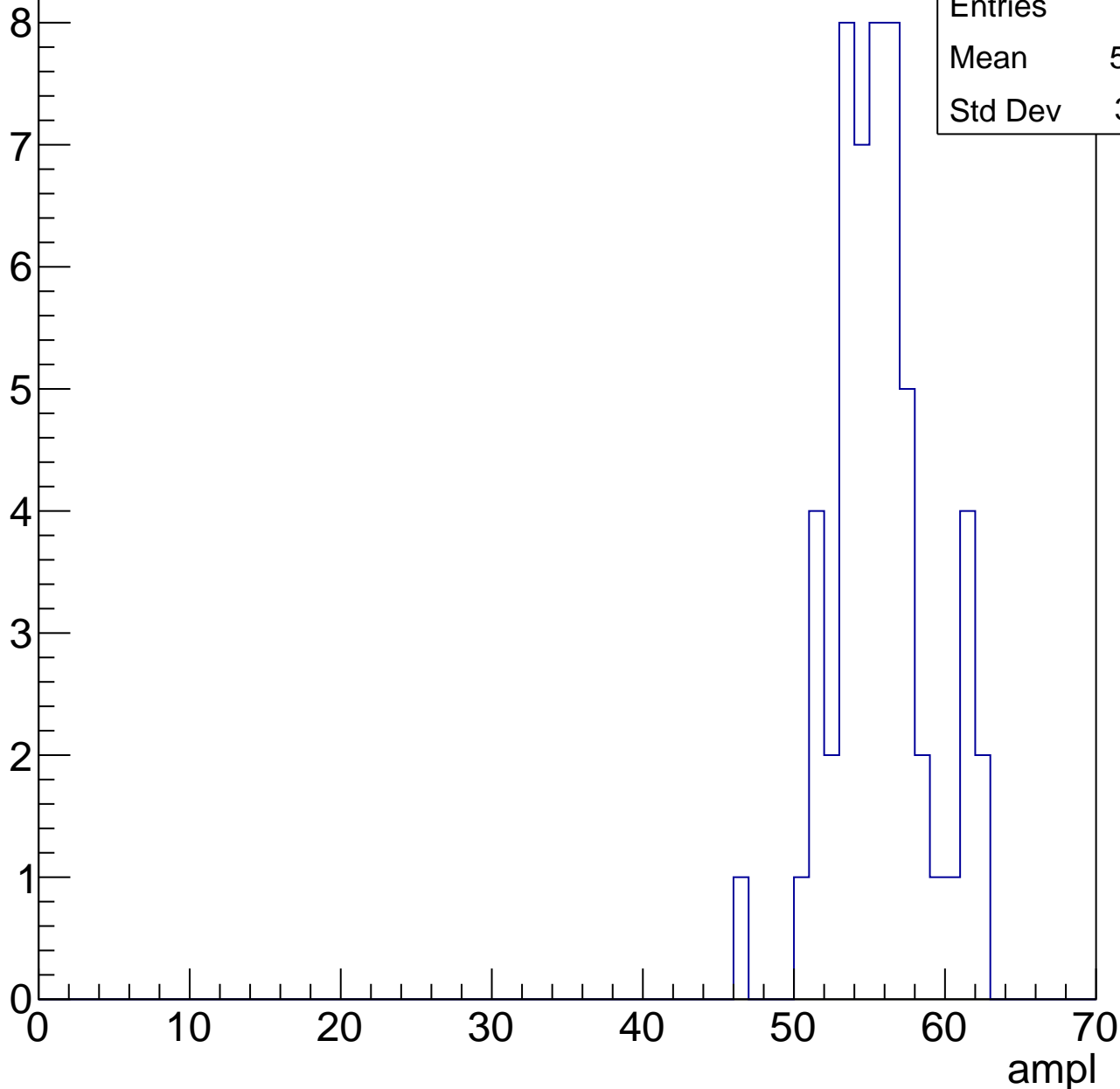


# B0L000S, U25-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	55.22
Std Dev	3.201

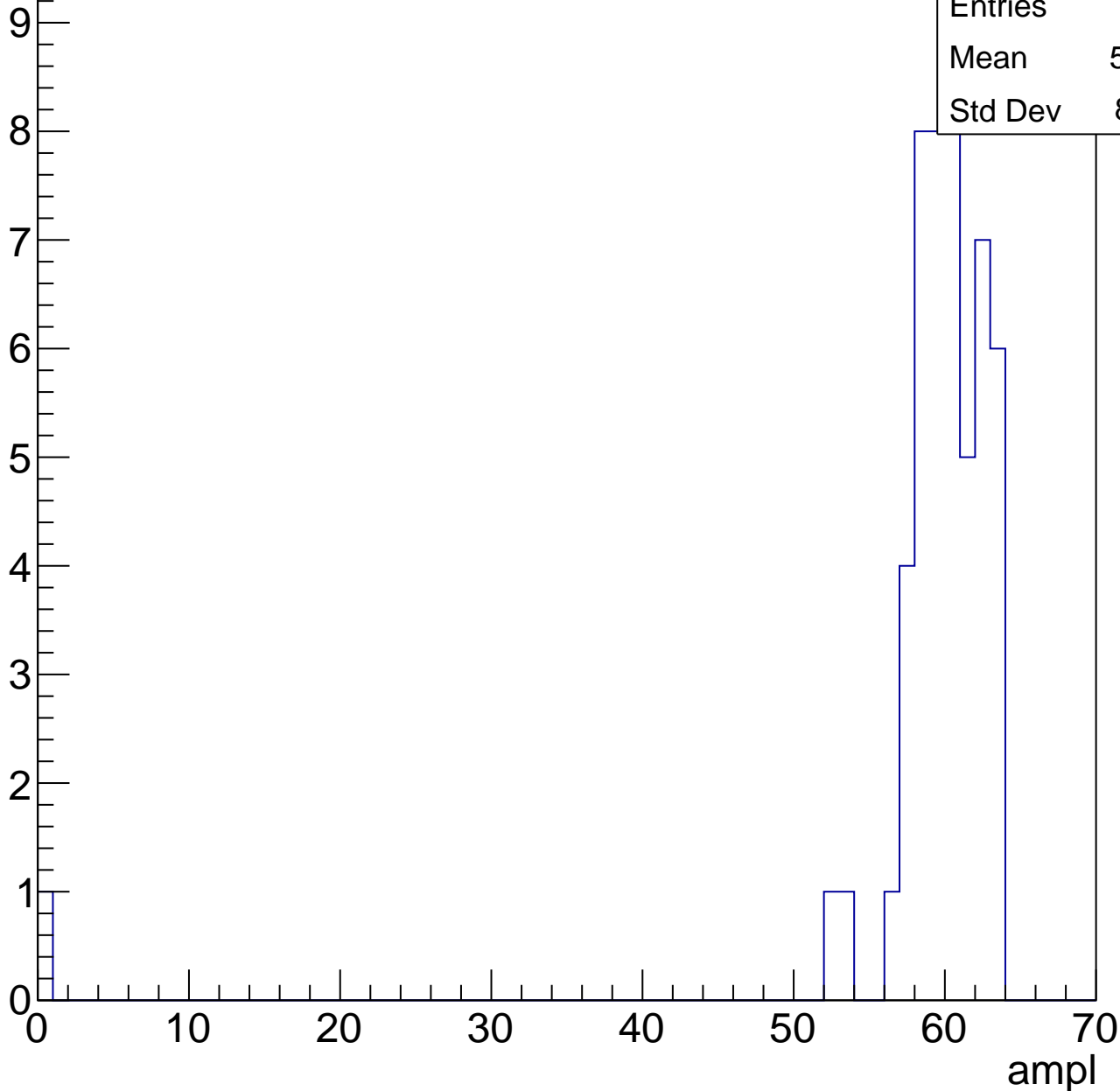


# B0L000S, U25-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

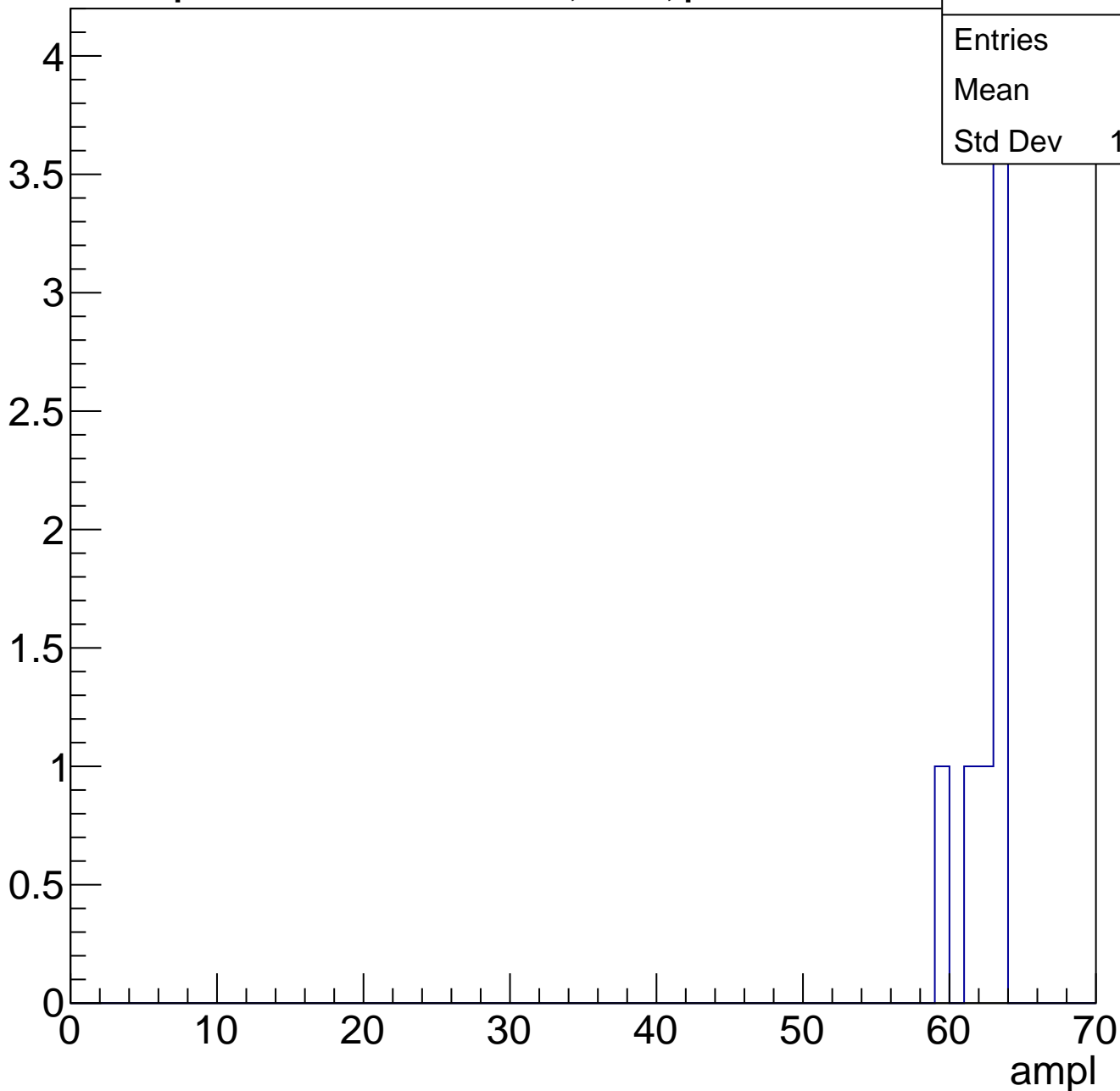
Entries	51
Mean	58.47
Std Dev	8.601



# B0L000S, U25-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch49, adc0

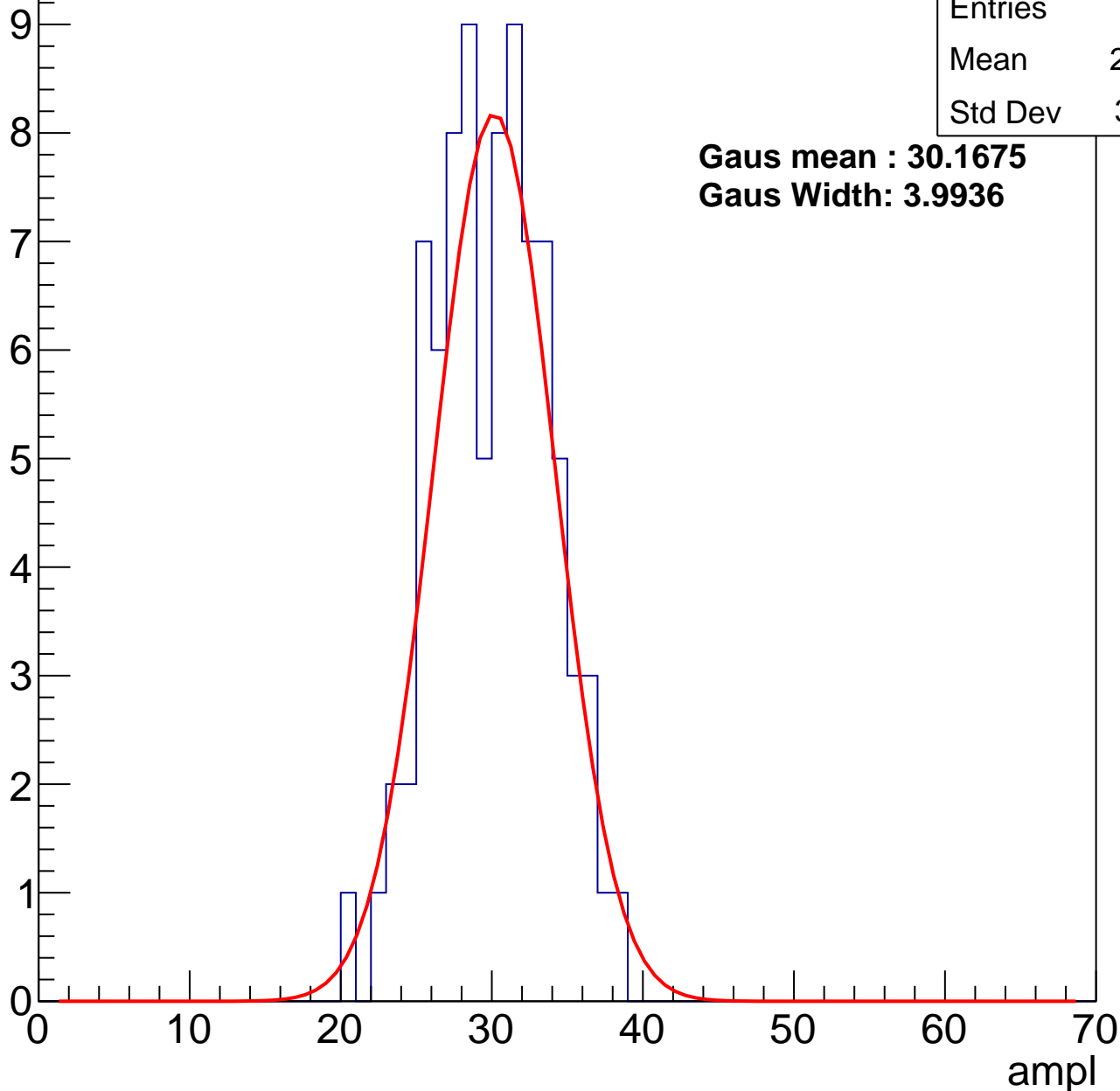
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	29.55
Std Dev	3.721

**Gaus mean : 30.1675**

**Gaus Width: 3.9936**



# B0L000S, U25-ch49, adc1

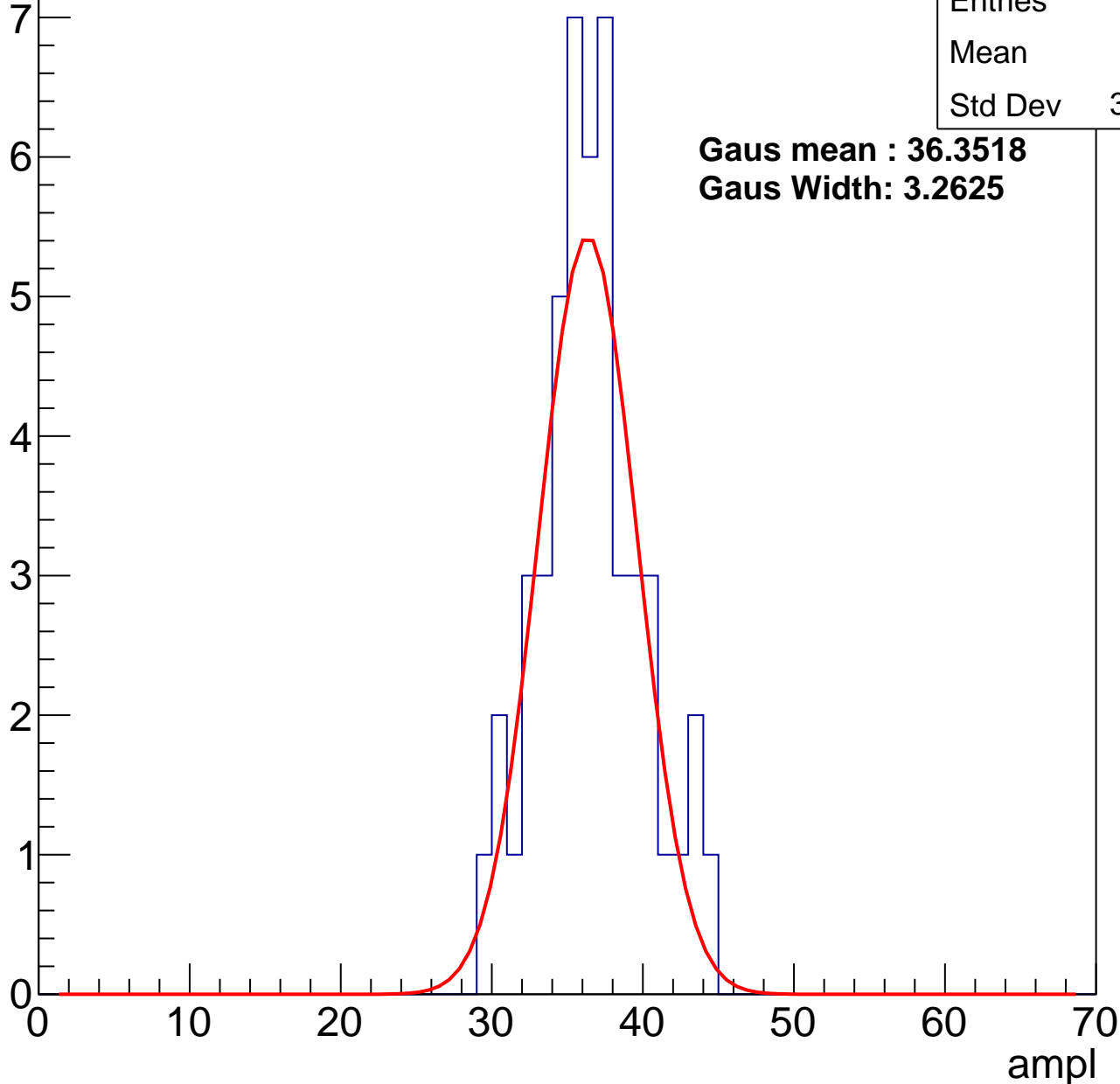
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	36.1
Std Dev	3.394

**Gaus mean : 36.3518**

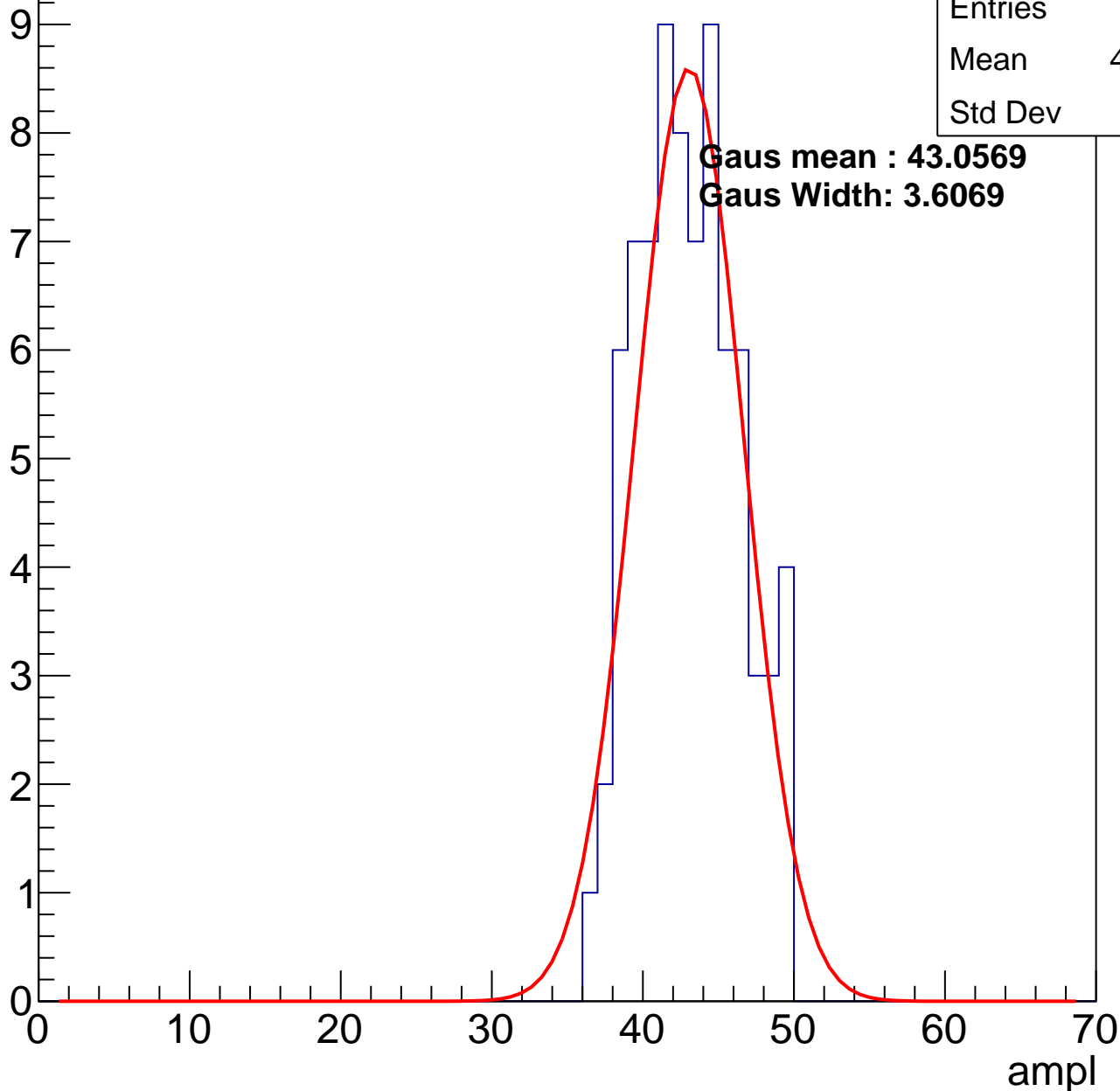
**Gaus Width: 3.2625**



# B0L000S, U25-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

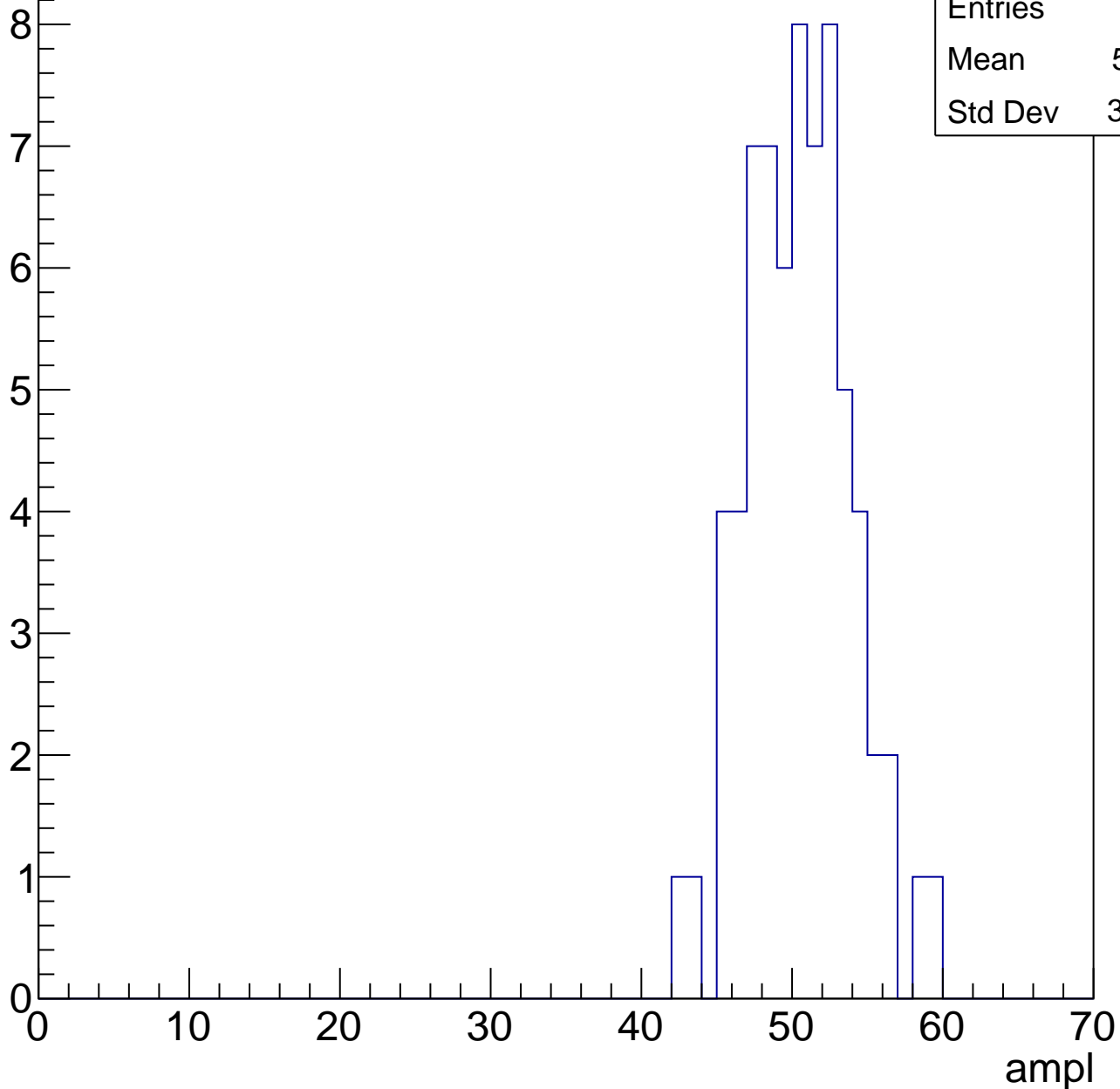


# B0L000S, U25-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.01
Std Dev	3.406

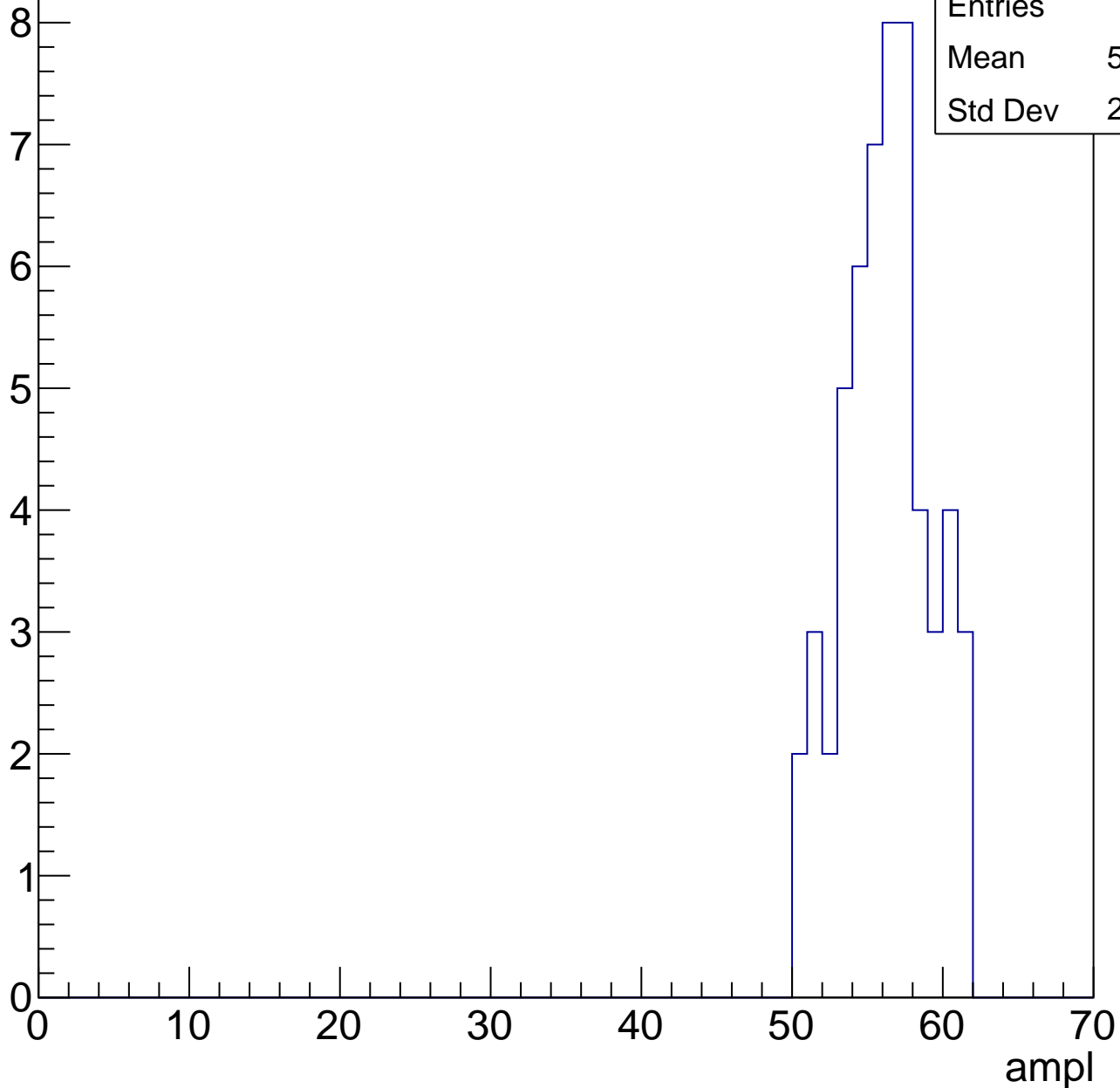


# B0L000S, U25-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.76
Std Dev	2.835

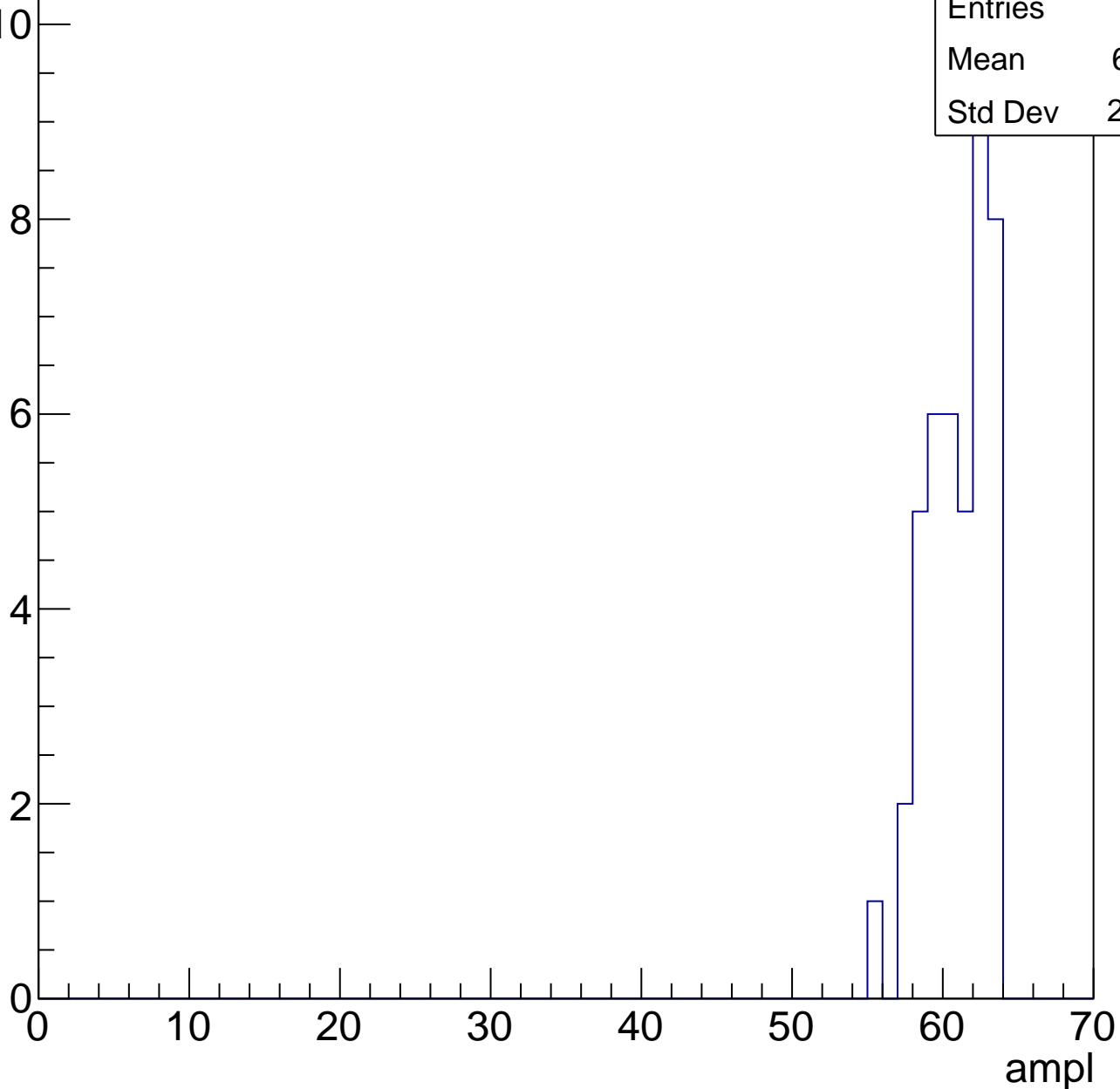


# B0L000S, U25-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

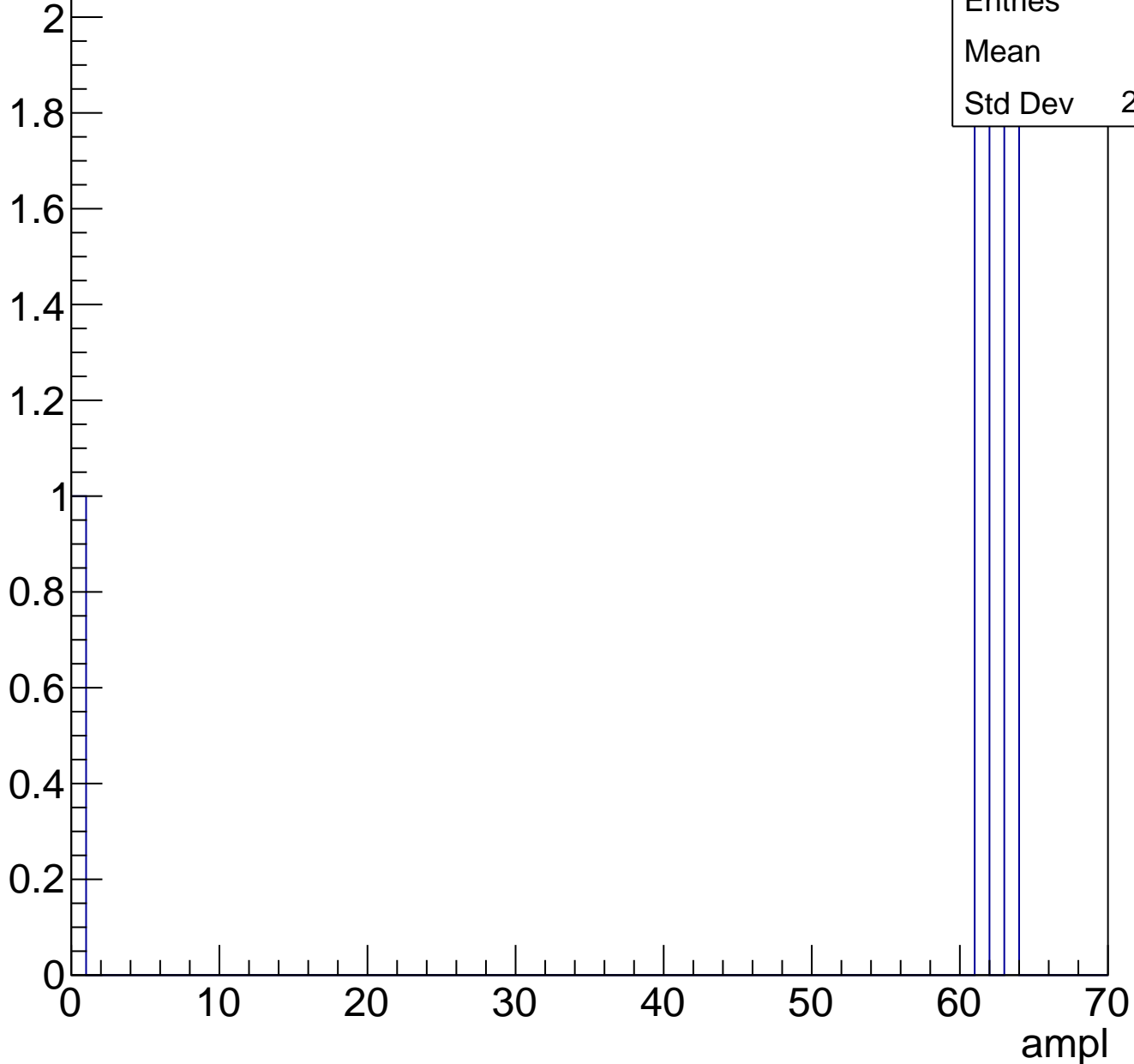
Entries	43
Mean	60.51
Std Dev	2.016



# B0L000S, U25-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	0
Std Dev	0

# B0L000S, U25-ch50, adc0

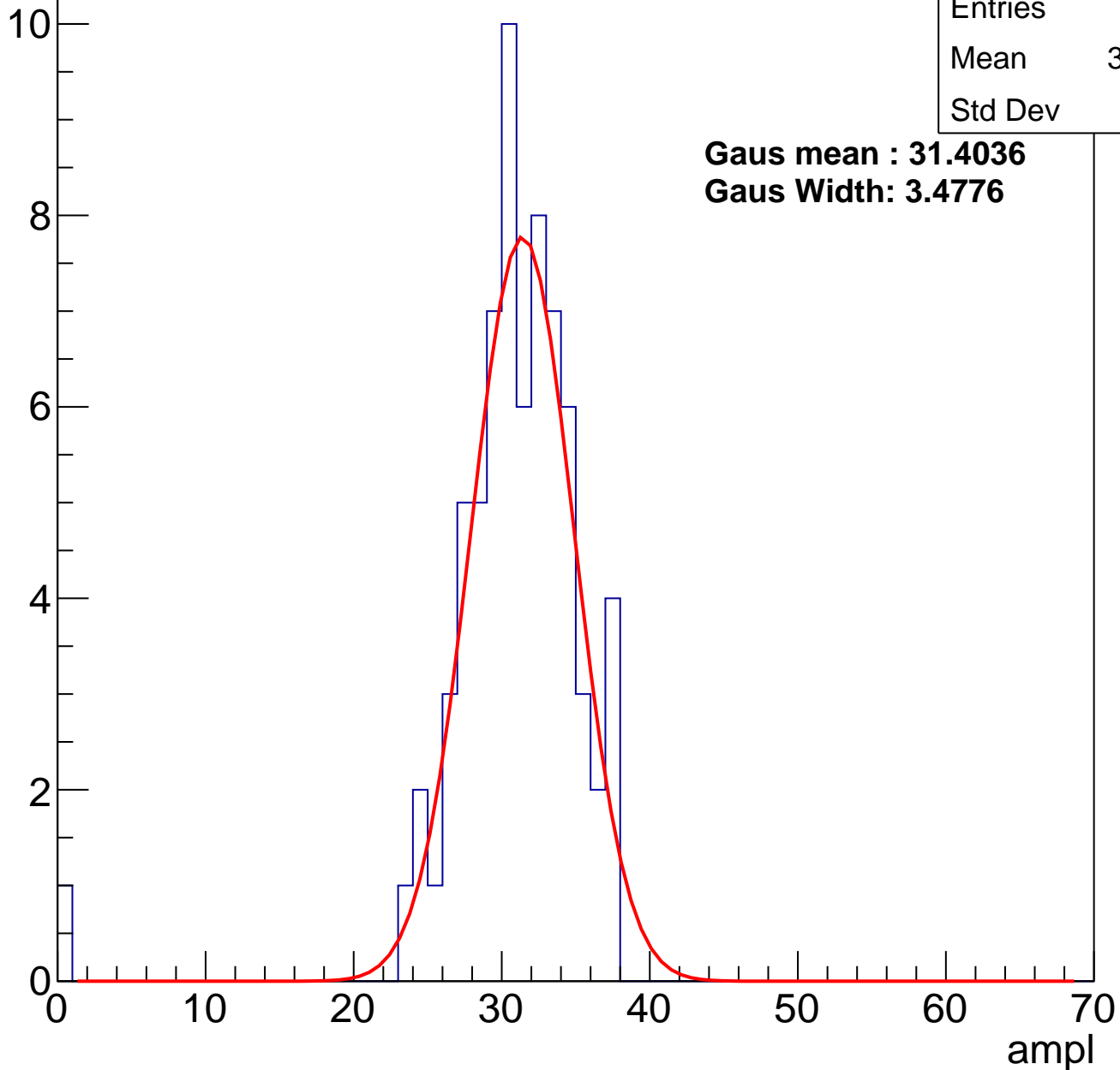
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	30.34
Std Dev	4.89

**Gaus mean : 31.4036**

**Gaus Width: 3.4776**

Entry



# B0L000S, U25-ch50, adc1

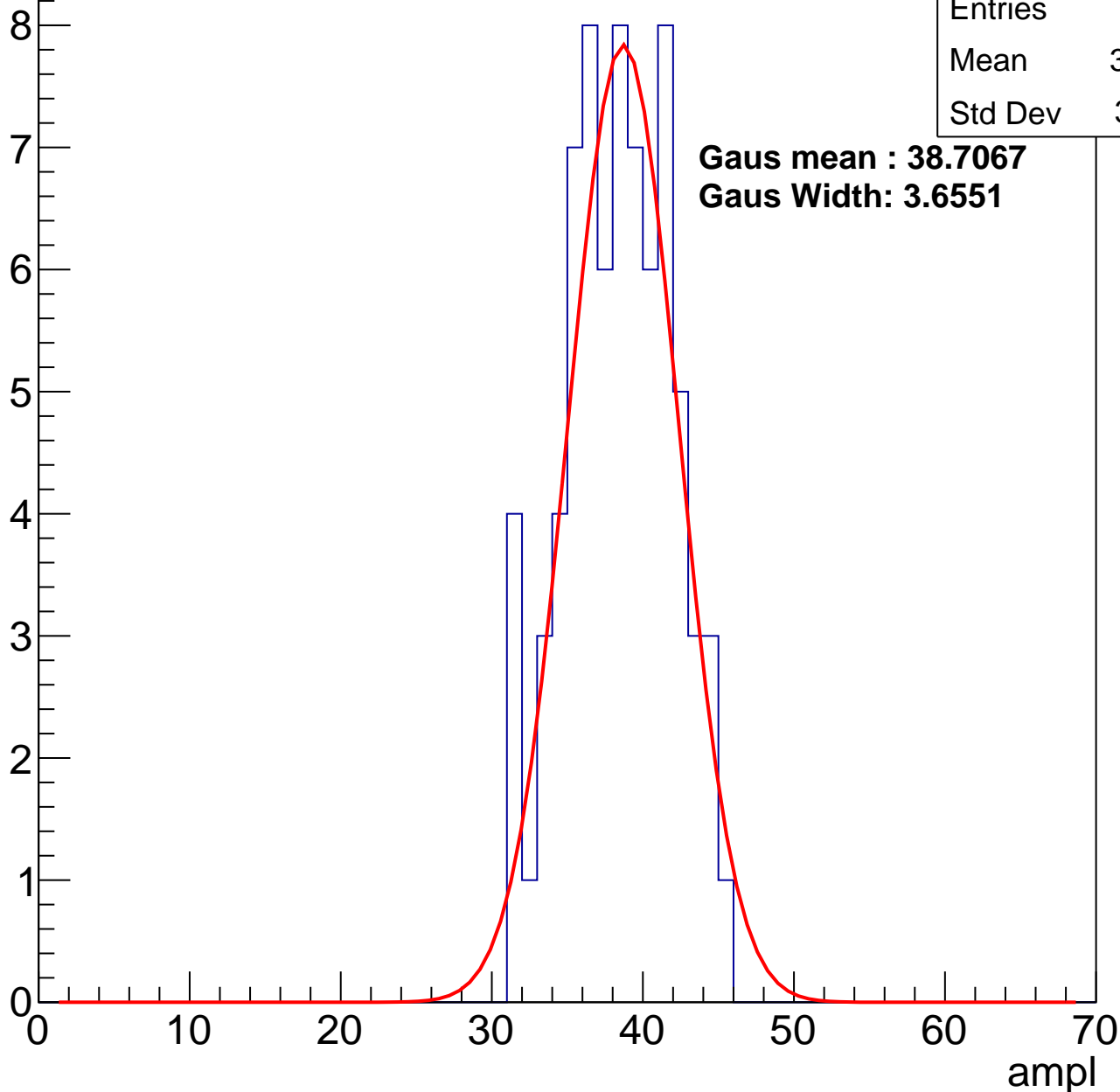
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	37.93
Std Dev	3.461

**Gaus mean : 38.7067**

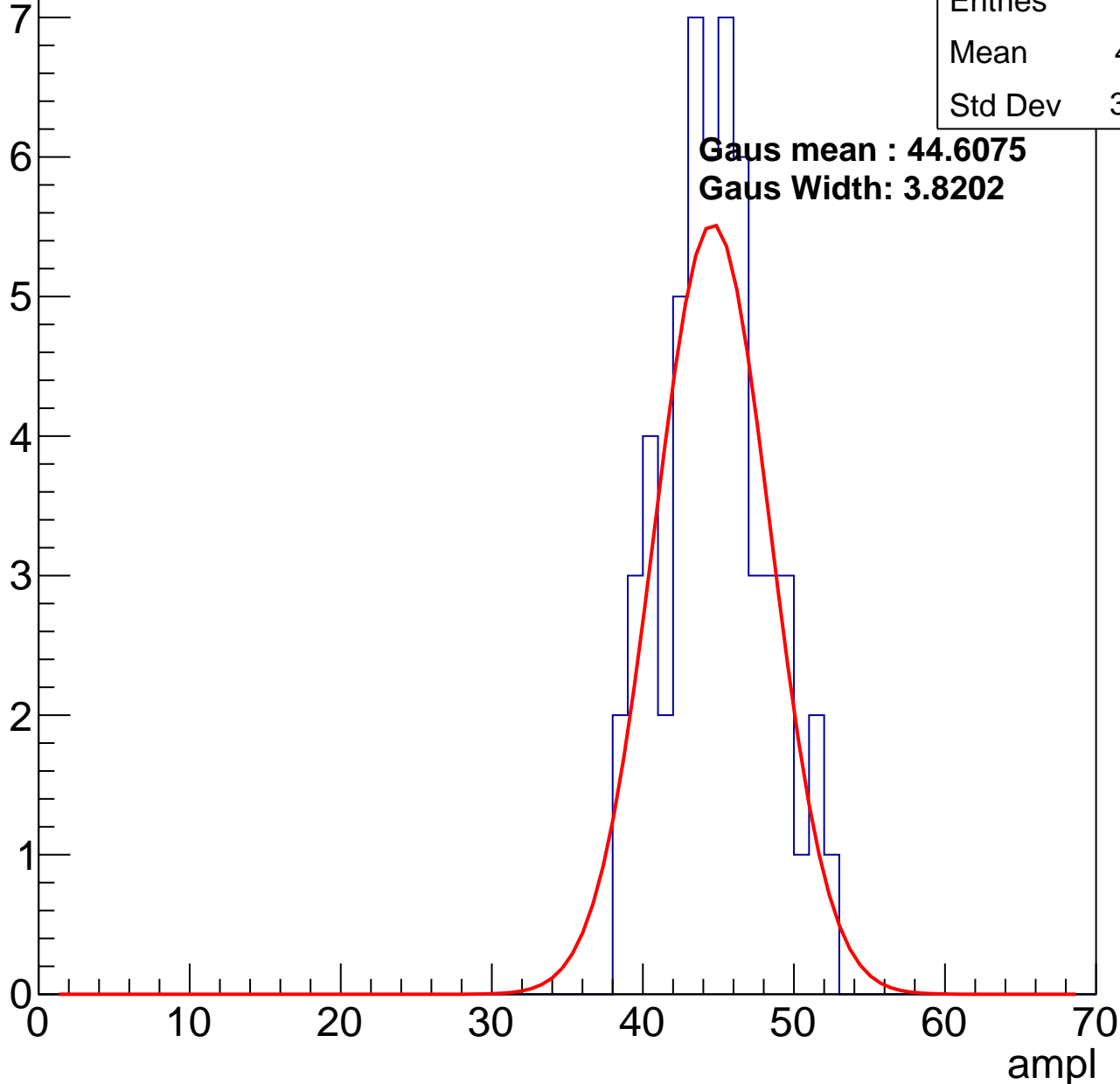
**Gaus Width: 3.6551**



# B0L000S, U25-ch50, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

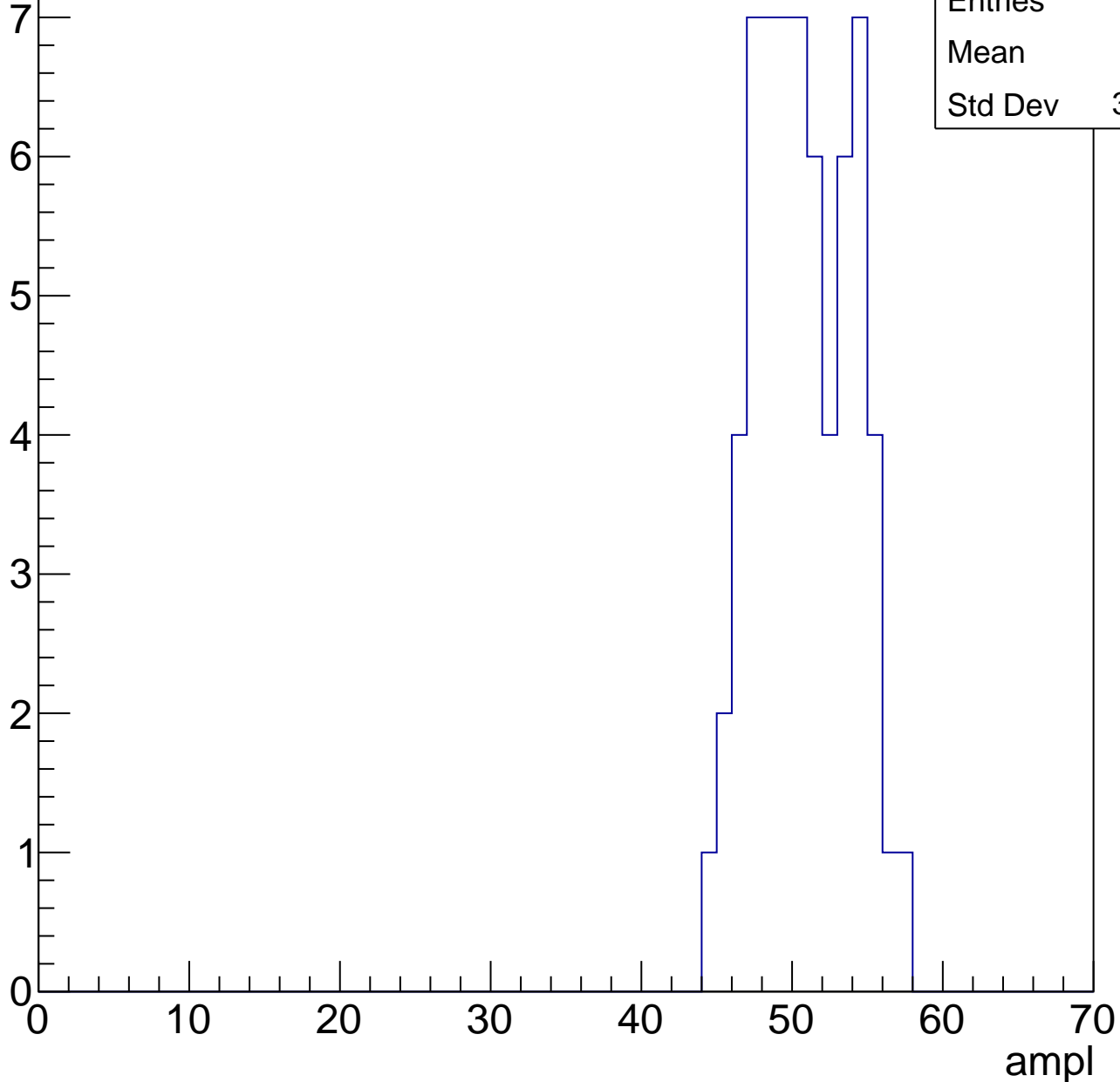
Entry



# B0L000S, U25-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

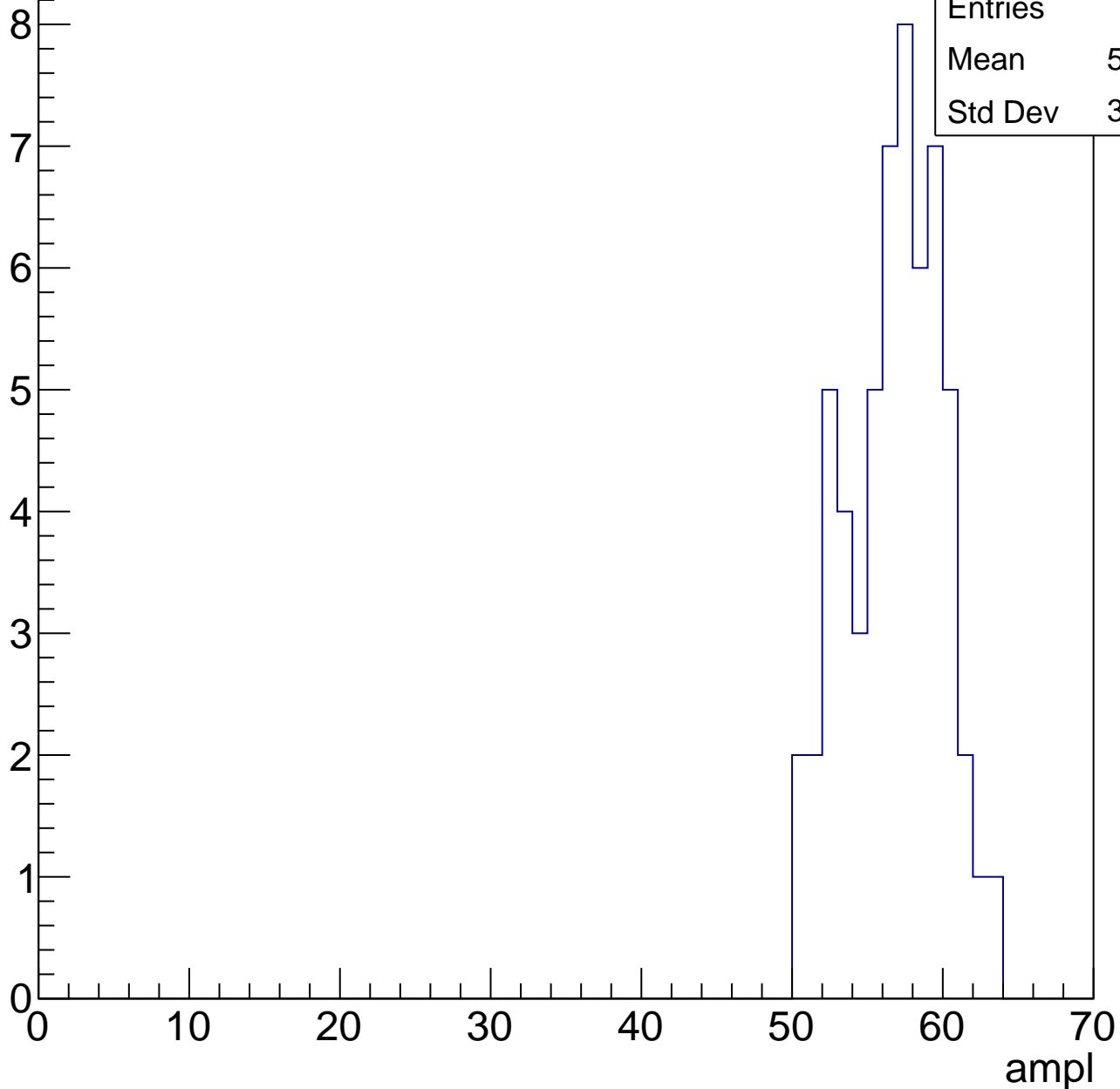


Entries	64
Mean	50.3
Std Dev	3.101

# B0L000S, U25-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	58
Mean	56.33
Std Dev	3.104

# B0L000S, U25-ch50, adc5

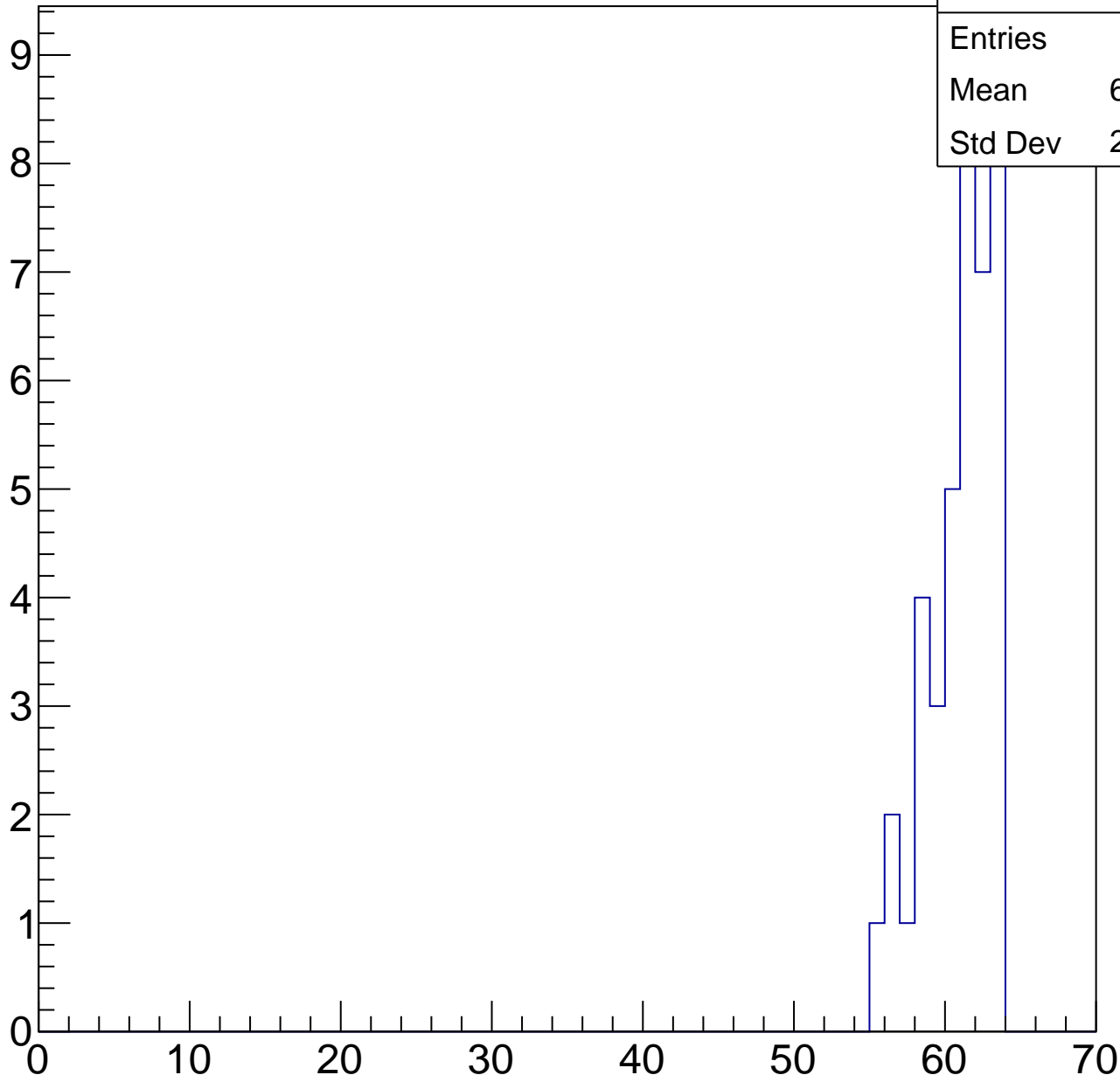
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	40
Mean	60.55
Std Dev	2.179

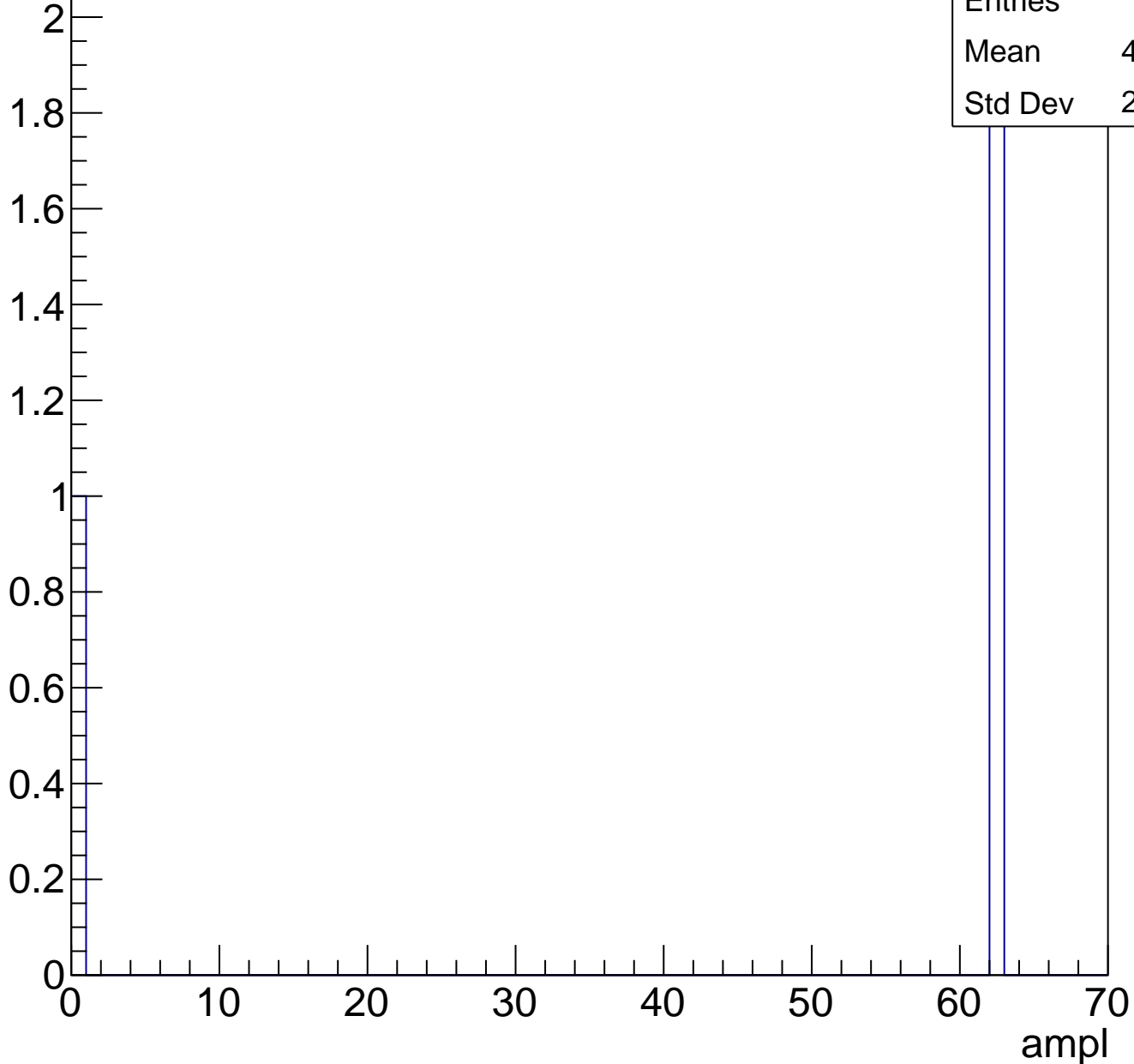
ampl



# B0L000S, U25-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch51, adc0

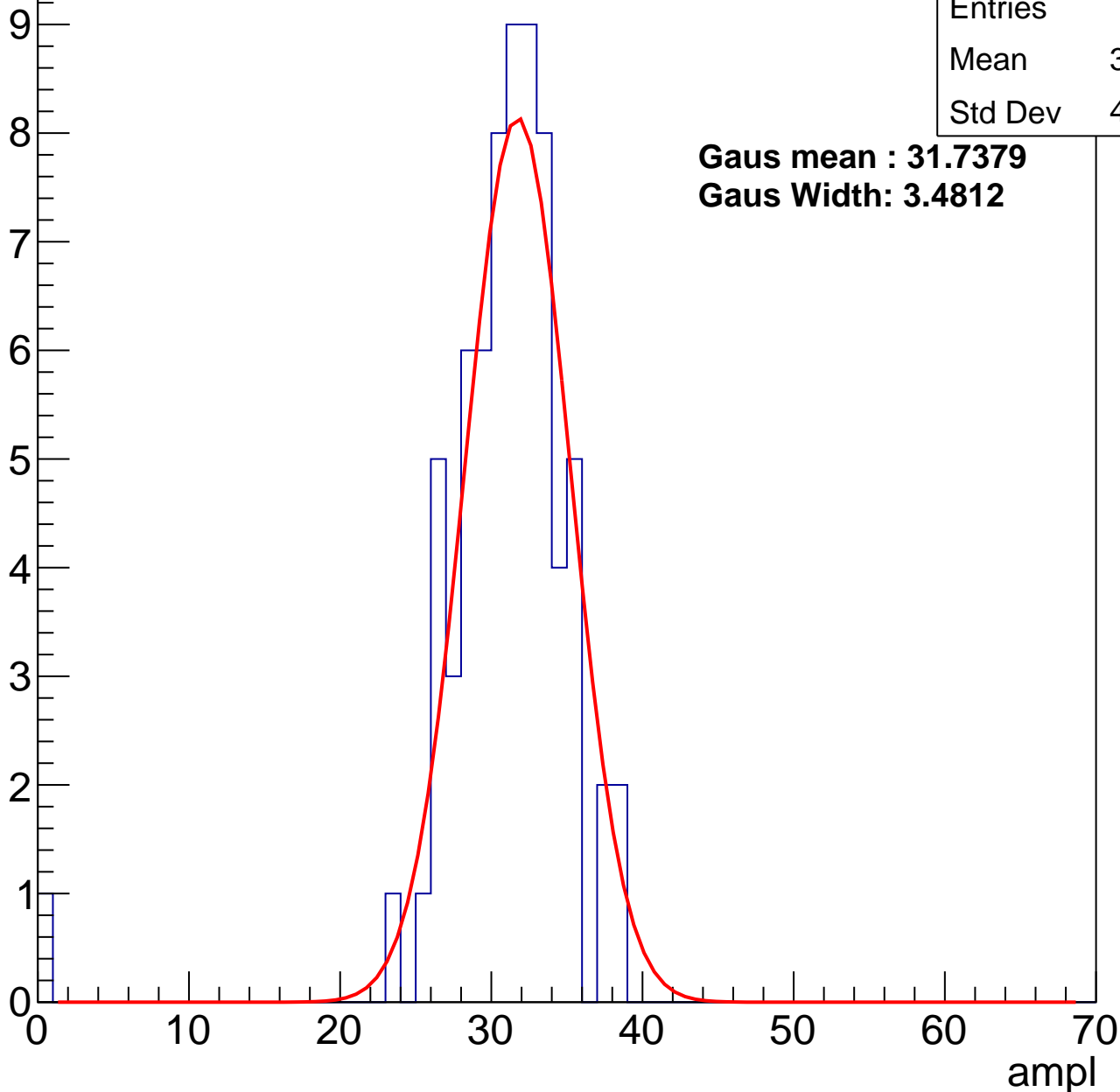
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.47
Std Dev	4.822

**Gaus mean : 31.7379**

**Gaus Width: 3.4812**

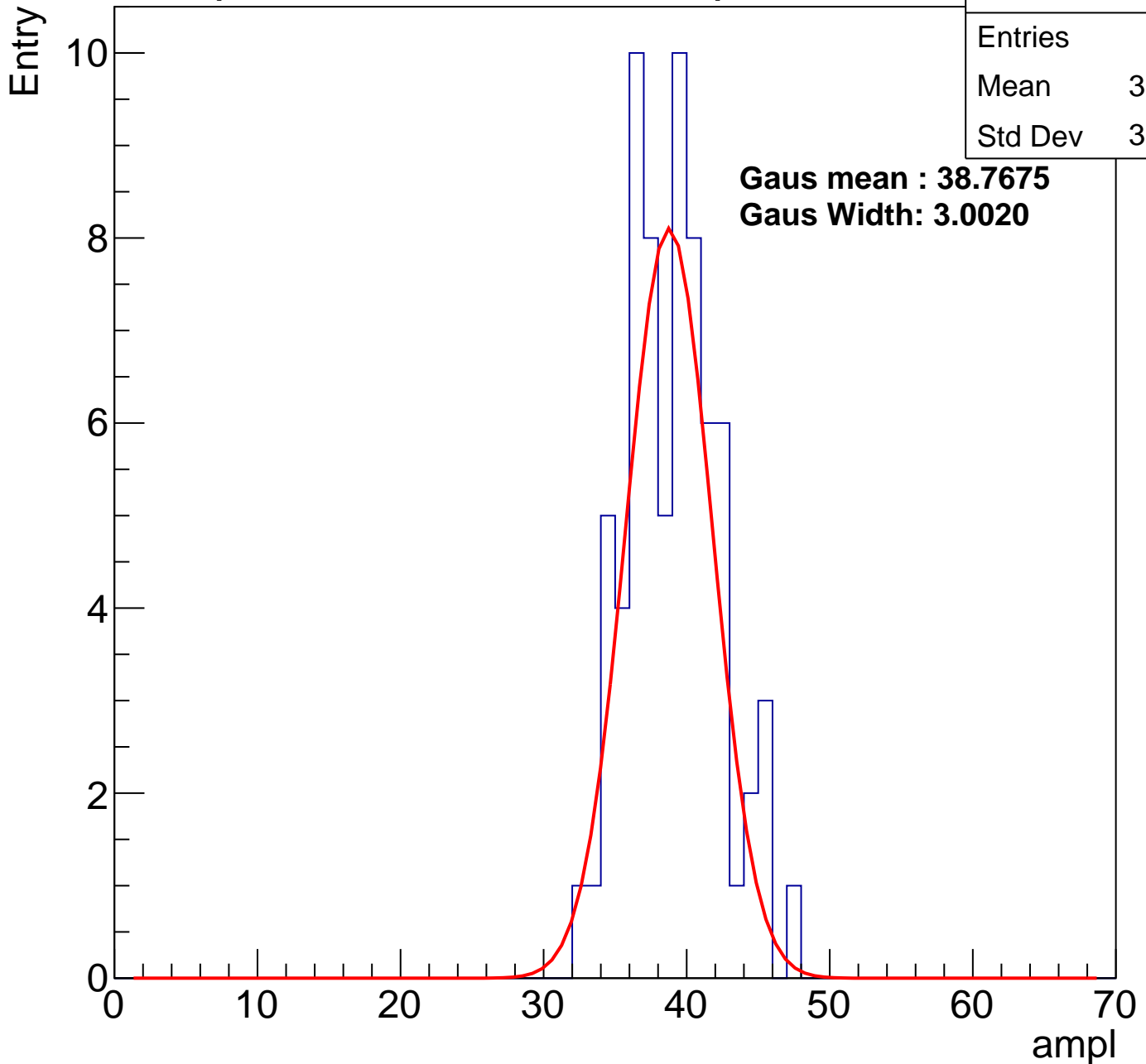


# B0L000S, U25-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	71
Mean	38.62
Std Dev	3.173

**Gaus mean : 38.7675**  
**Gaus Width: 3.0020**



# B0L000S, U25-ch51, adc2

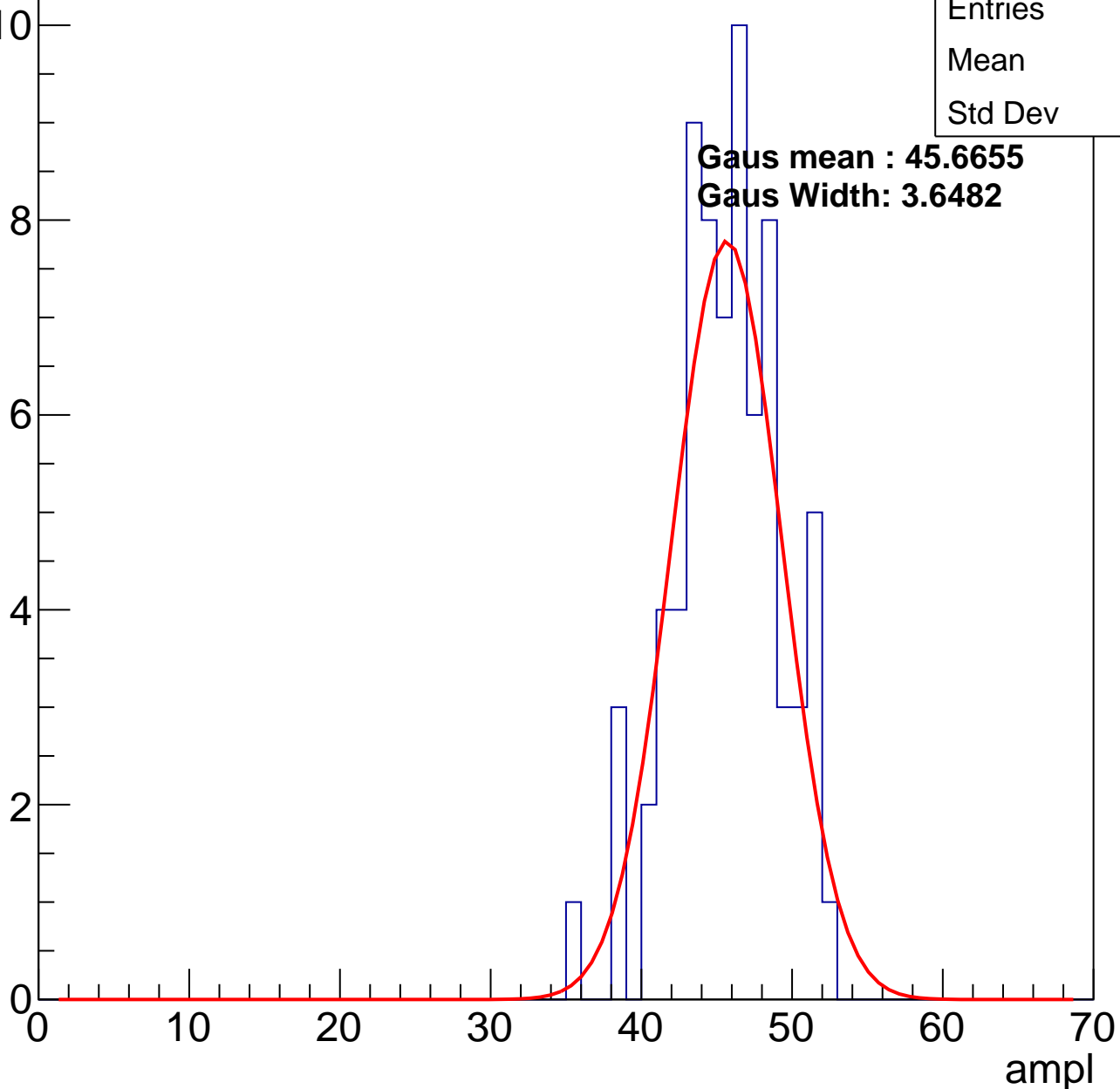
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	45.2
Std Dev	3.48

**Gaus mean : 45.6655**

**Gaus Width: 3.6482**

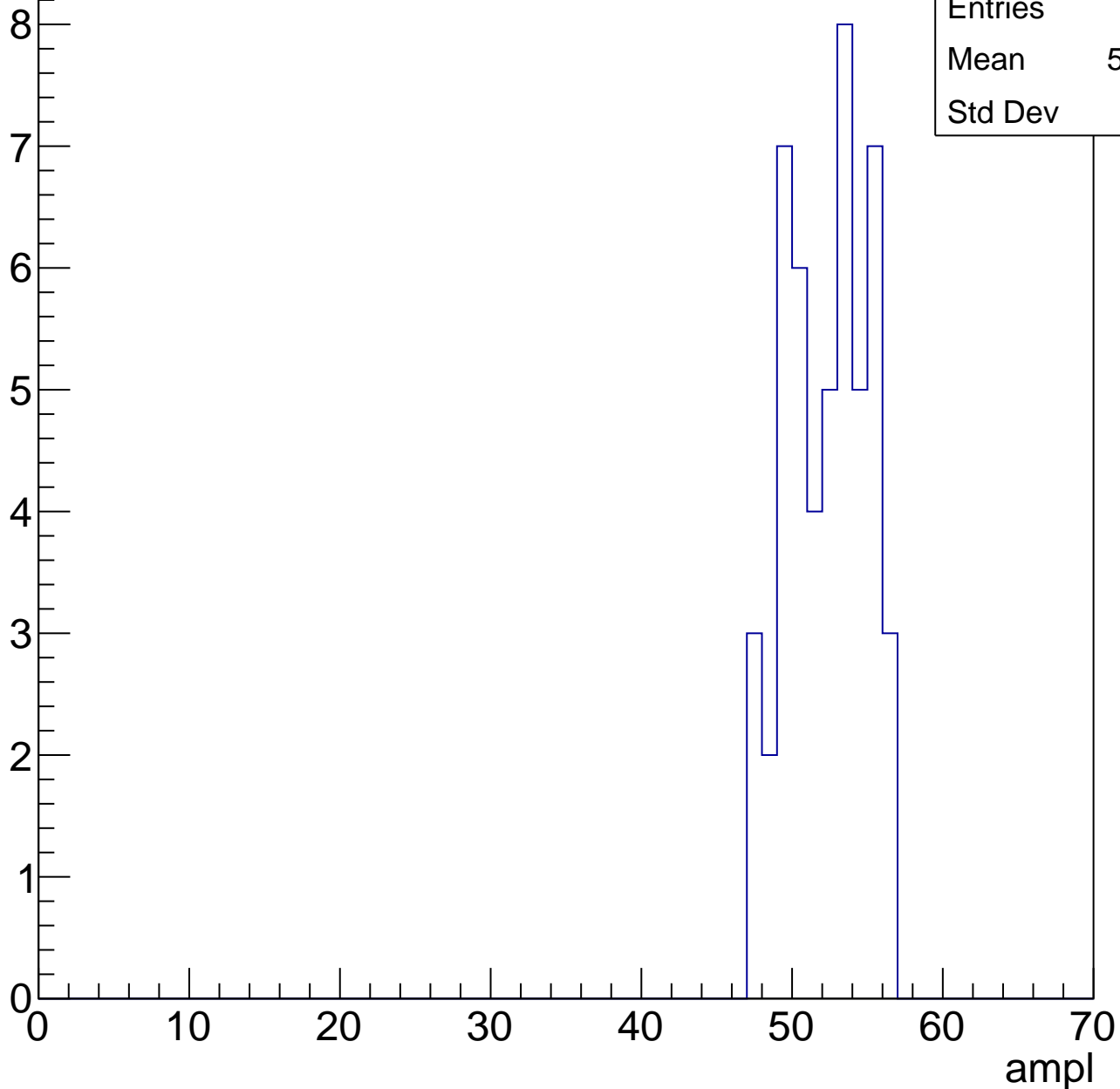


# B0L000S, U25-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

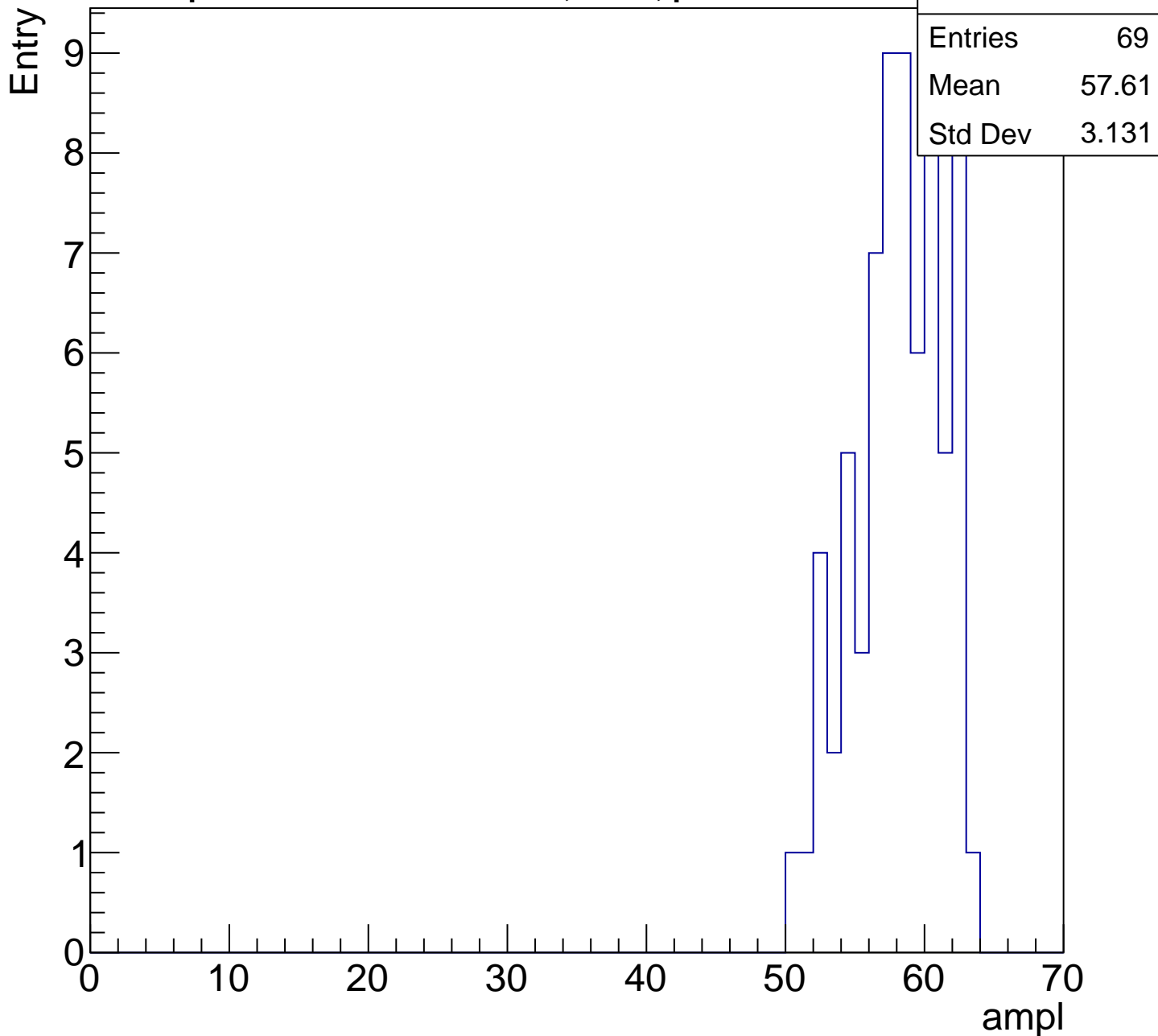
Entry

Entries	50
Mean	51.82
Std Dev	2.59



# B0L000S, U25-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

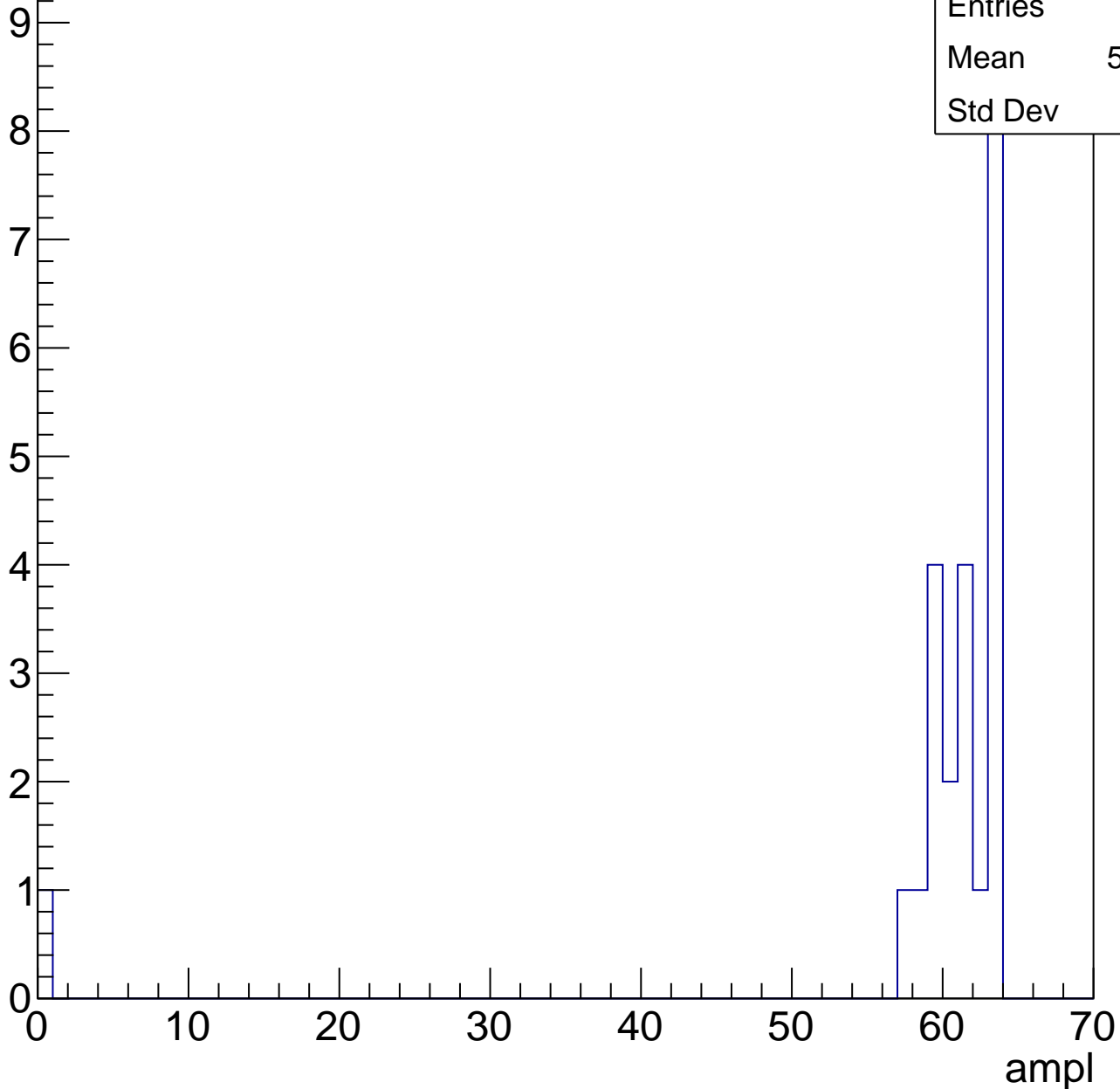


# B0L000S, U25-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	23
Mean	58.43
Std Dev	12.6



# B0L000S, U25-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch52, adc0

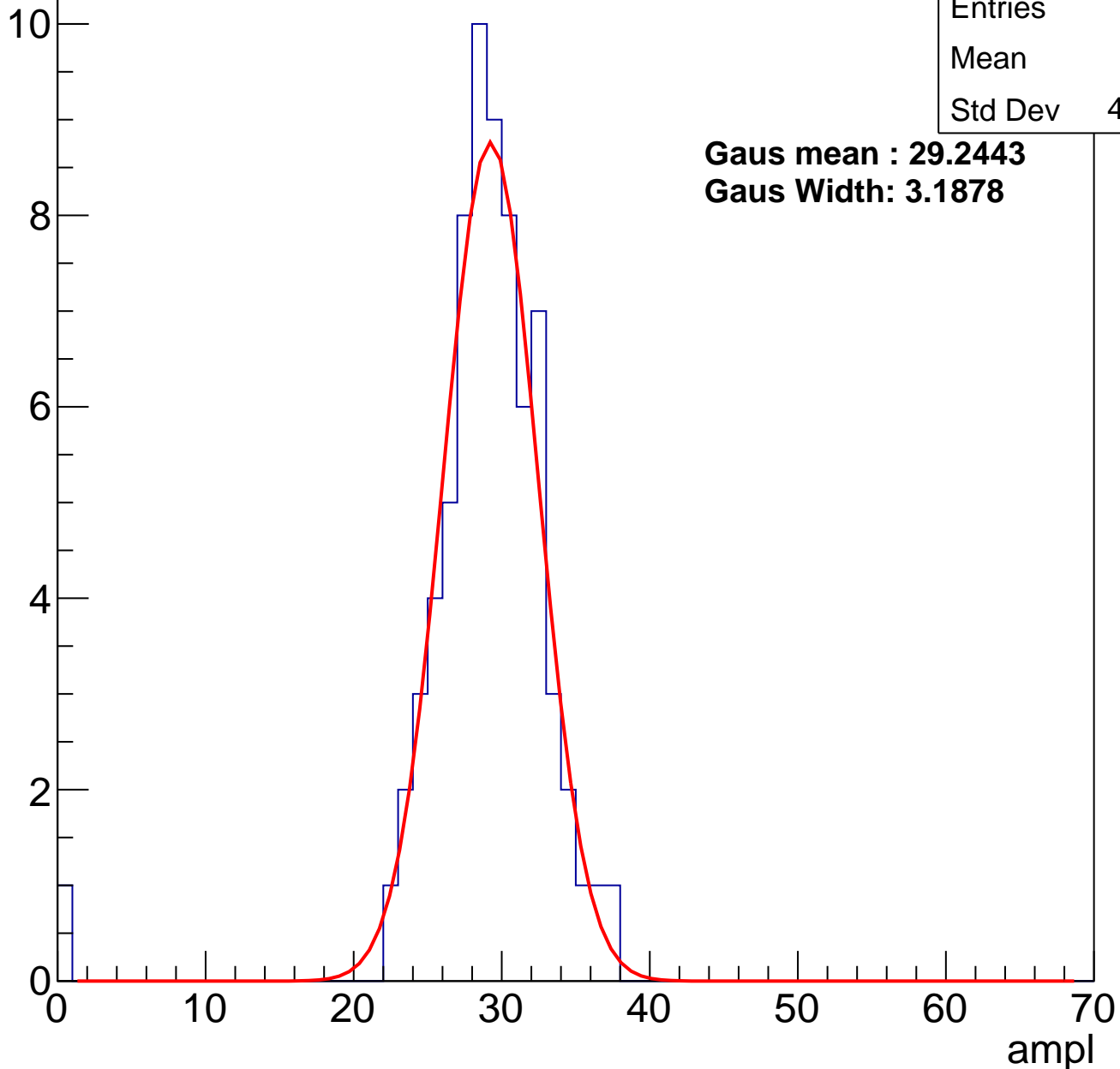
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	28.5
Std Dev	4.583

**Gaus mean : 29.2443**

**Gaus Width: 3.1878**

Entry



# B0L000S, U25-ch52, adc1

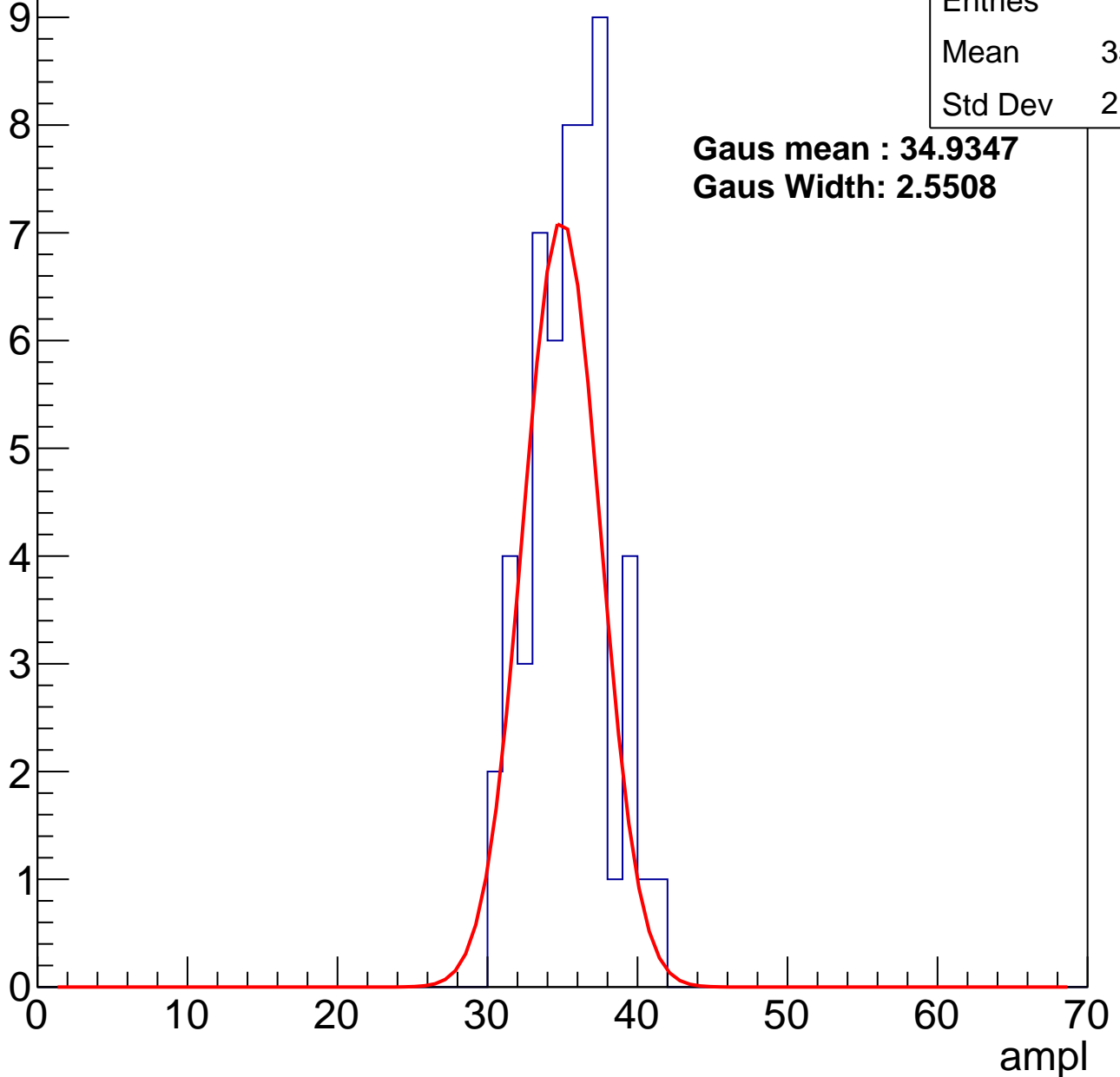
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	35.02
Std Dev	2.557

**Gaus mean : 34.9347**

**Gaus Width: 2.5508**



# B0L000S, U25-ch52, adc2

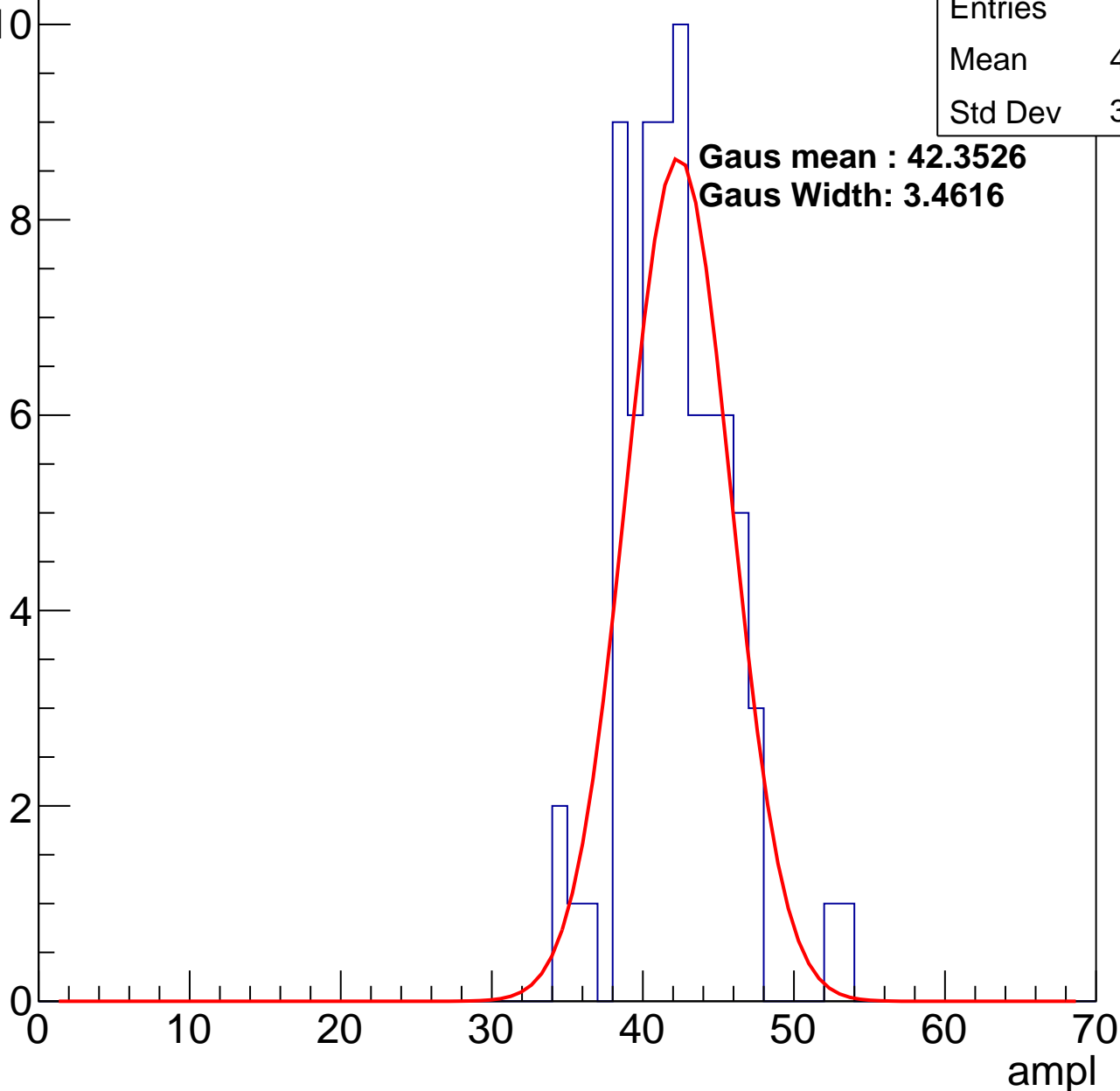
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	41.76
Std Dev	3.487

**Gaus mean : 42.3526**

**Gaus Width: 3.4616**

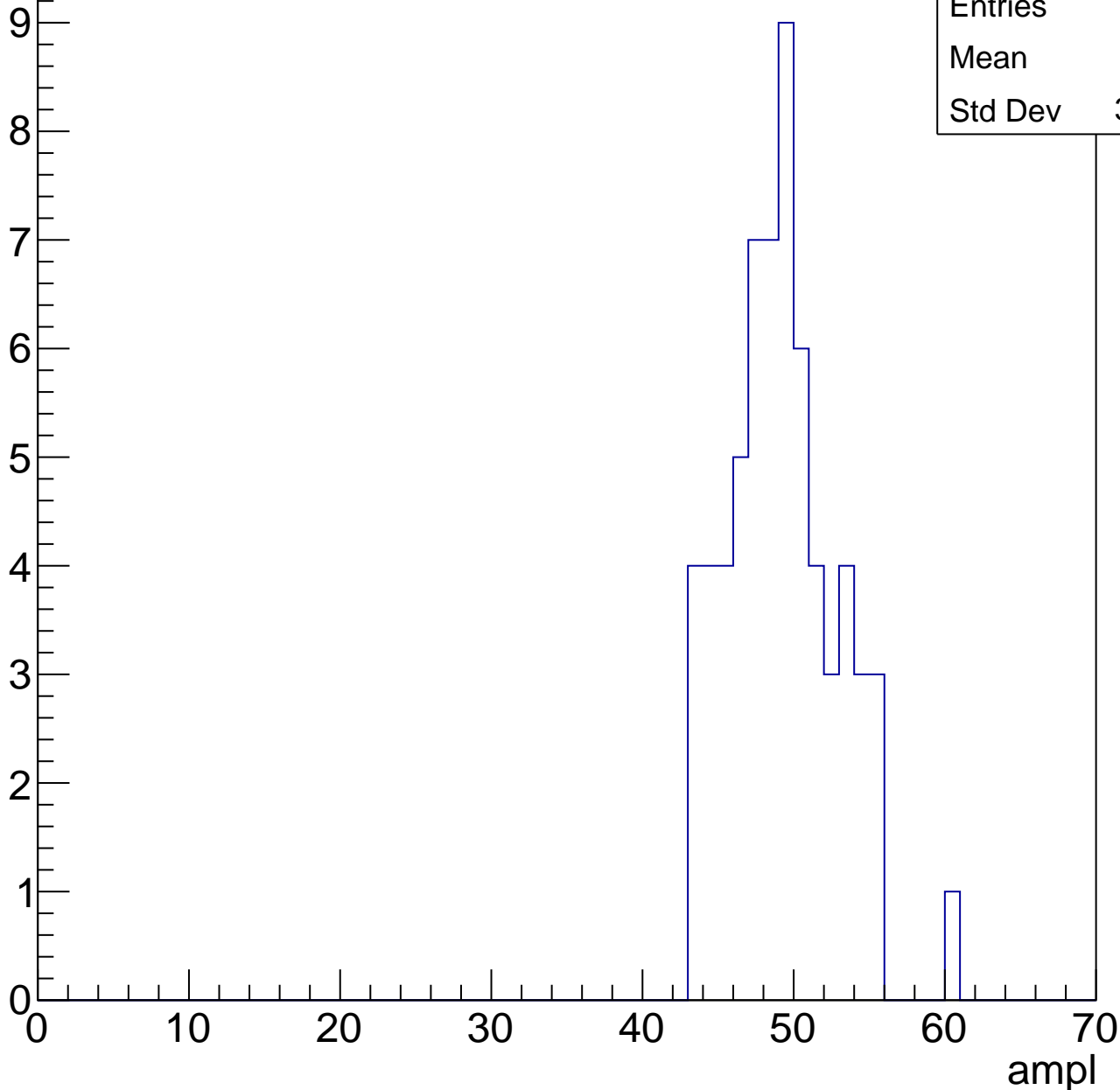


# B0L000S, U25-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

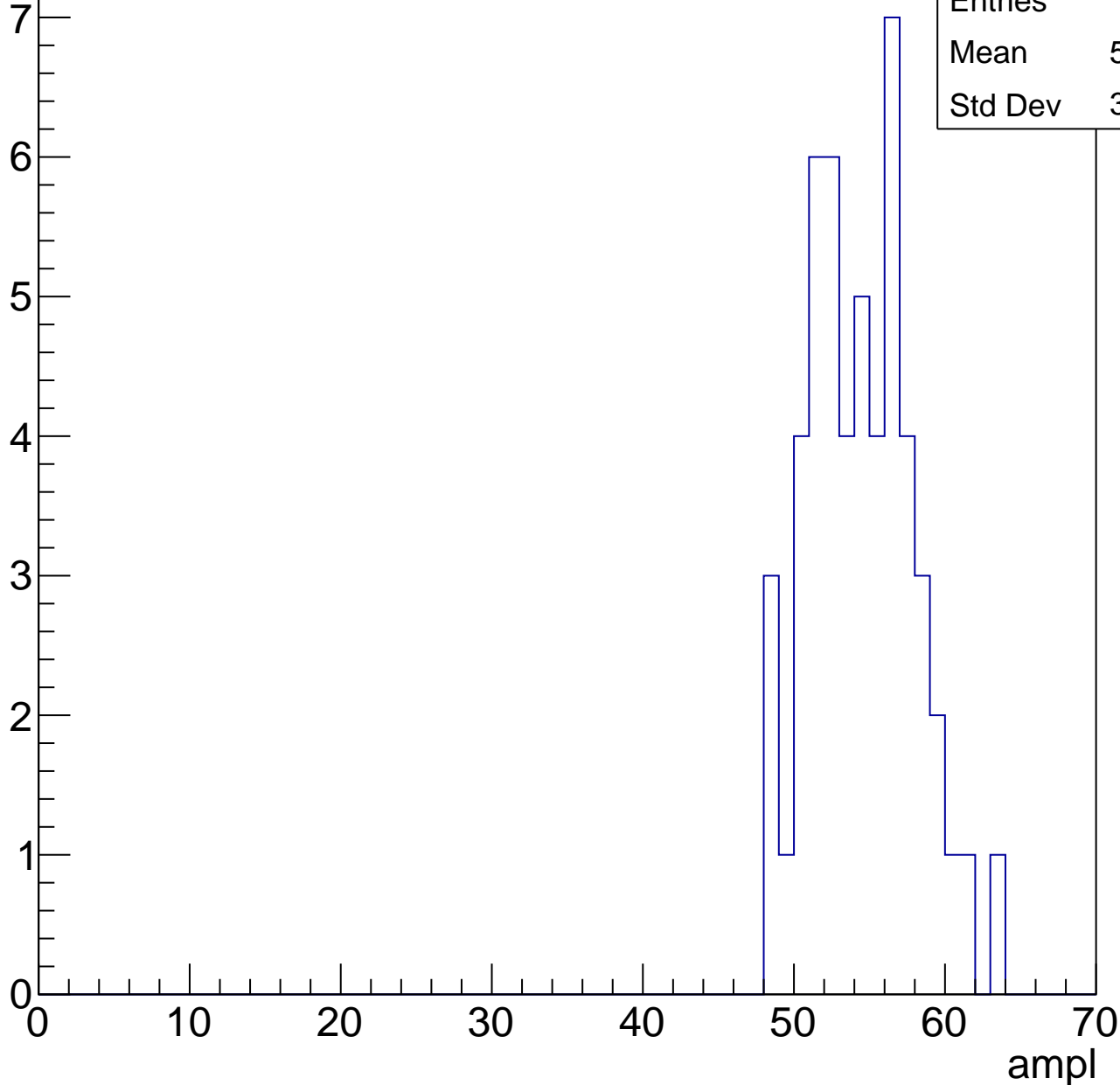
Entries	64
Mean	48.8
Std Dev	3.541



# B0L000S, U25-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	57.59
Std Dev	8.675

ampl

0

10

20

30

40

50

60

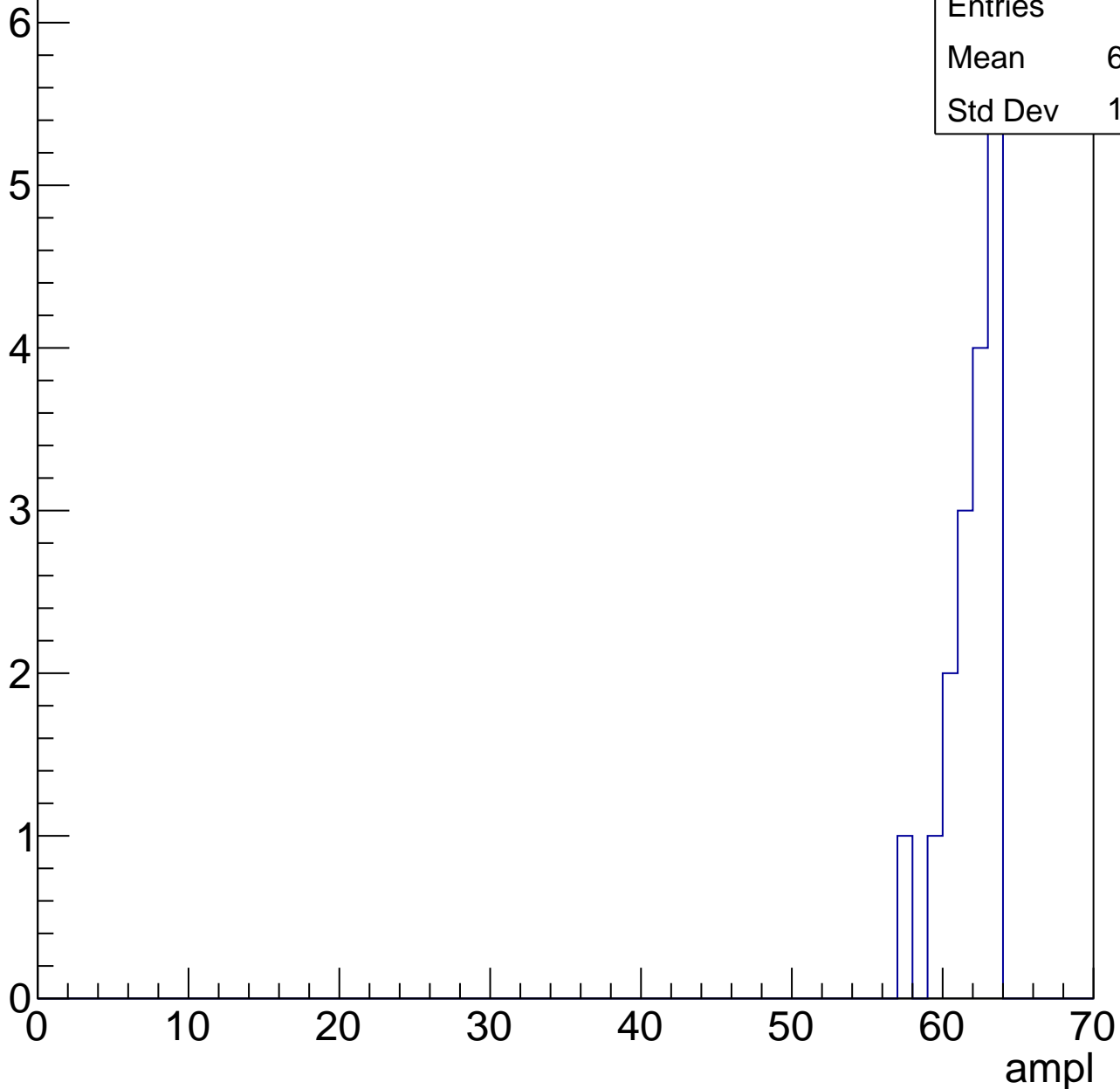
70

# B0L000S, U25-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	17
Mean	61.47
Std Dev	1.649





# B0L000S, U25-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch53, adc0

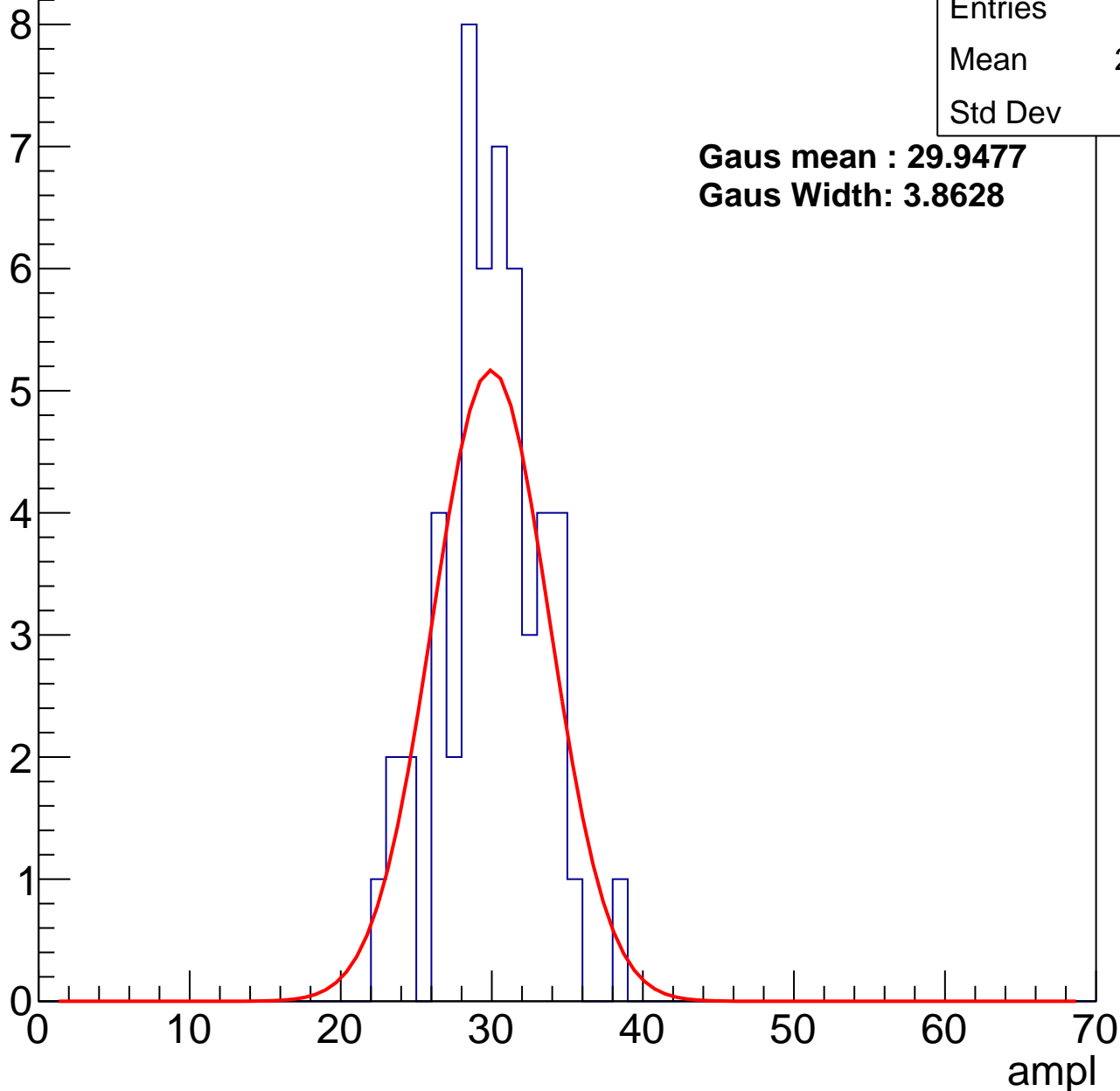
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	29.51
Std Dev	3.28

**Gaus mean : 29.9477**

**Gaus Width: 3.8628**



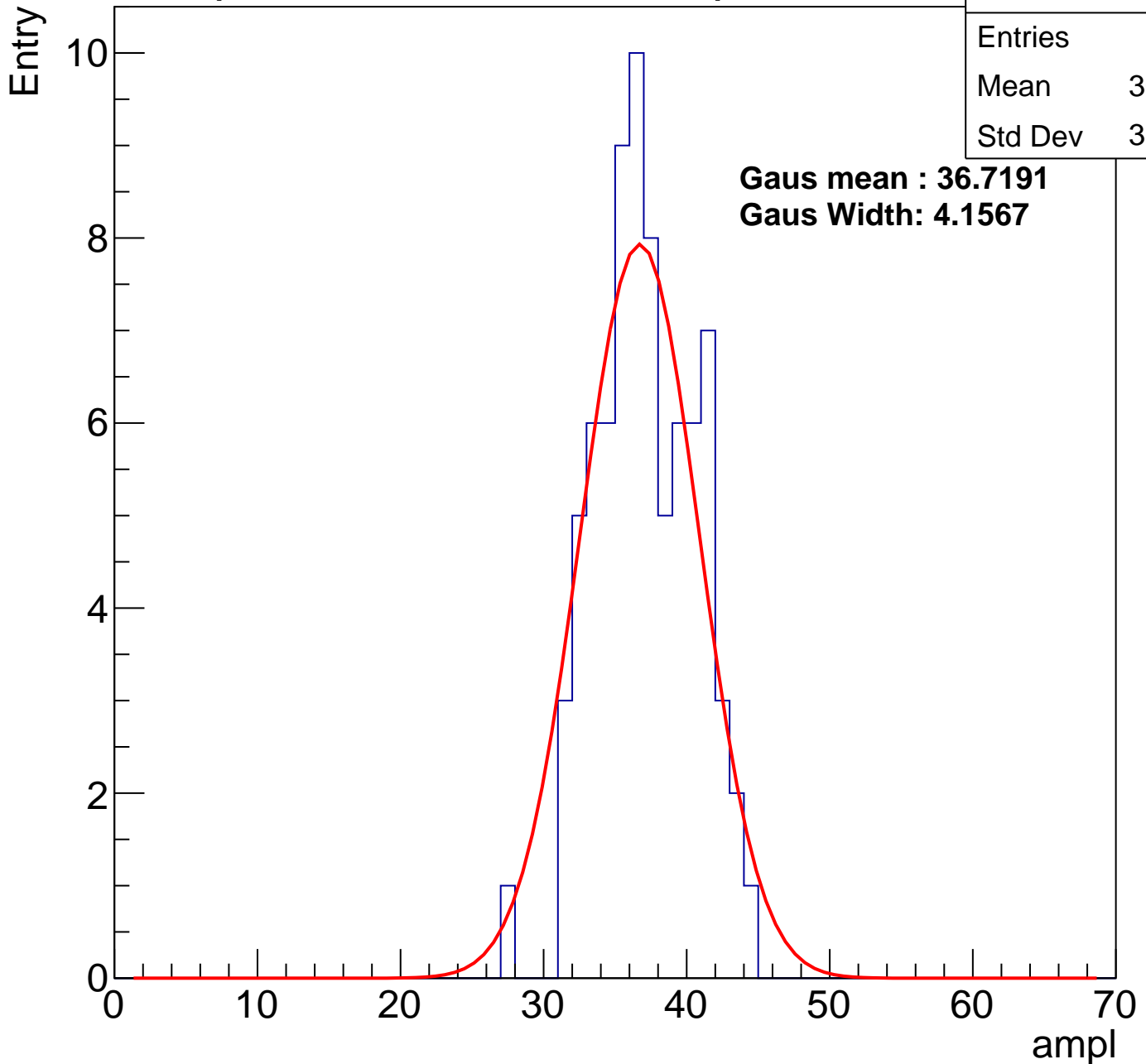
# B0L000S, U25-ch53, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	36.67
Std Dev	3.418

**Gaus mean : 36.7191**

**Gaus Width: 4.1567**



# B0L000S, U25-ch53, adc2

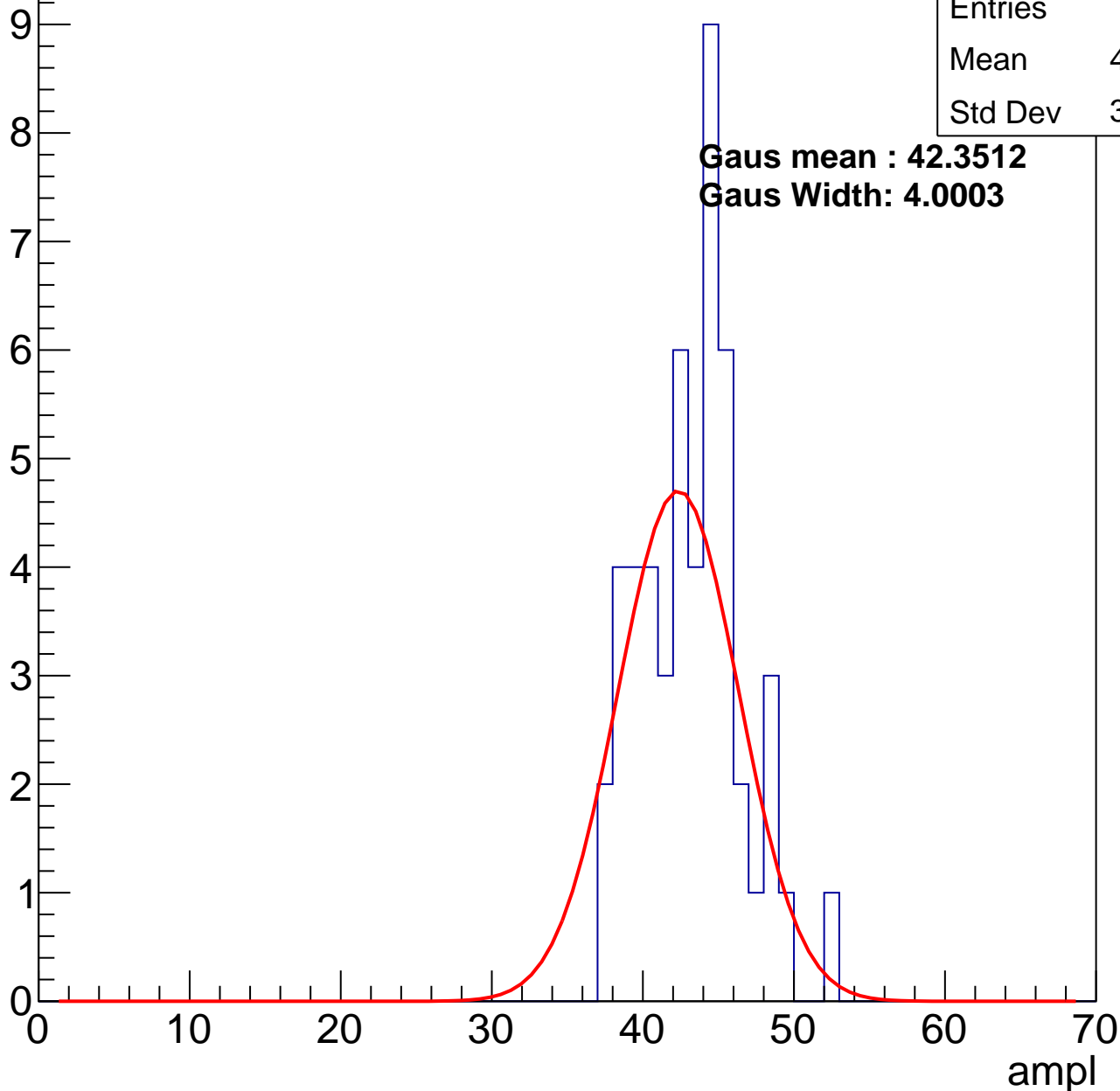
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	42.78
Std Dev	3.306

**Gaus mean : 42.3512**

**Gaus Width: 4.0003**

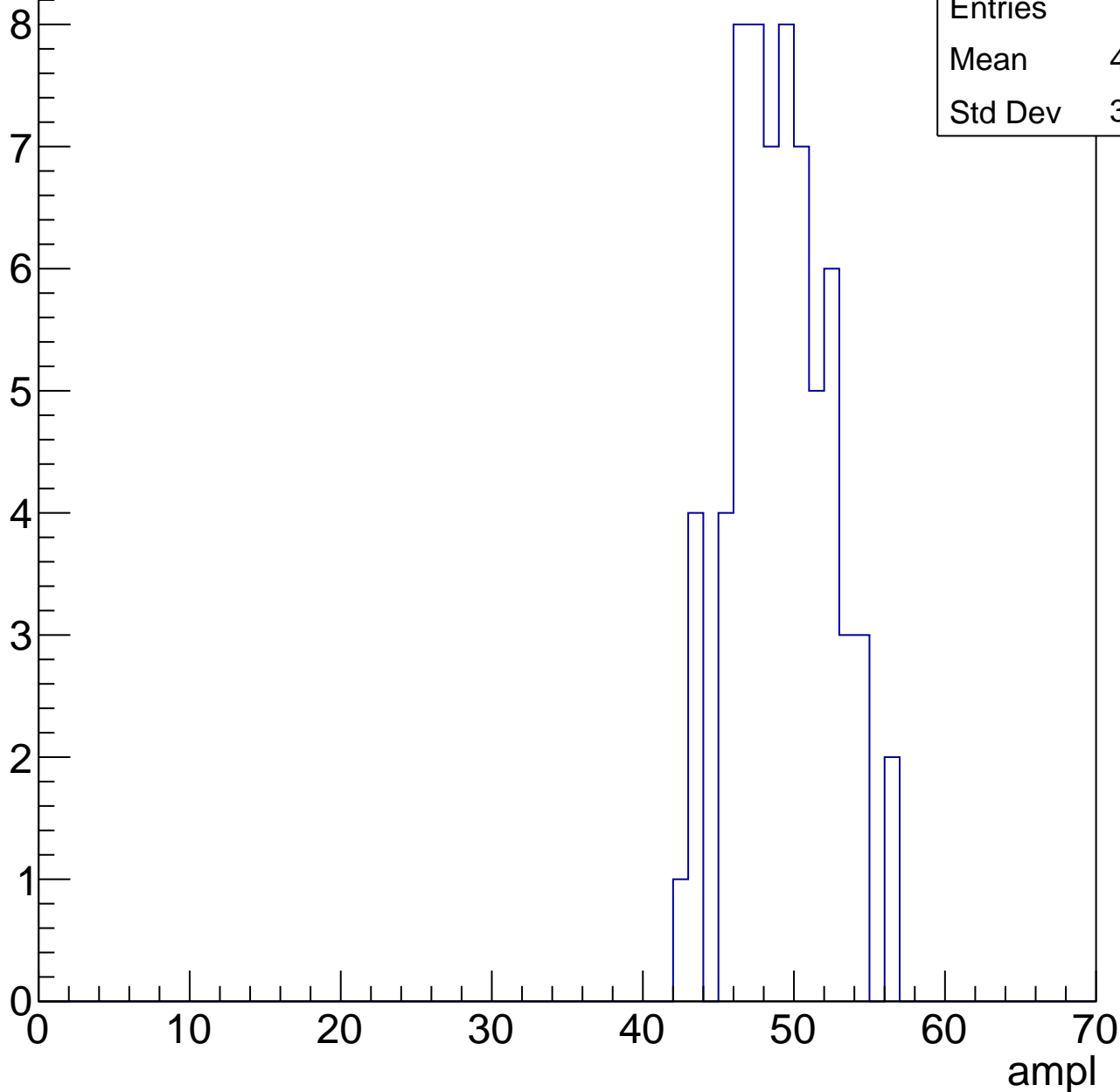


# B0L000S, U25-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.73
Std Dev	3.174

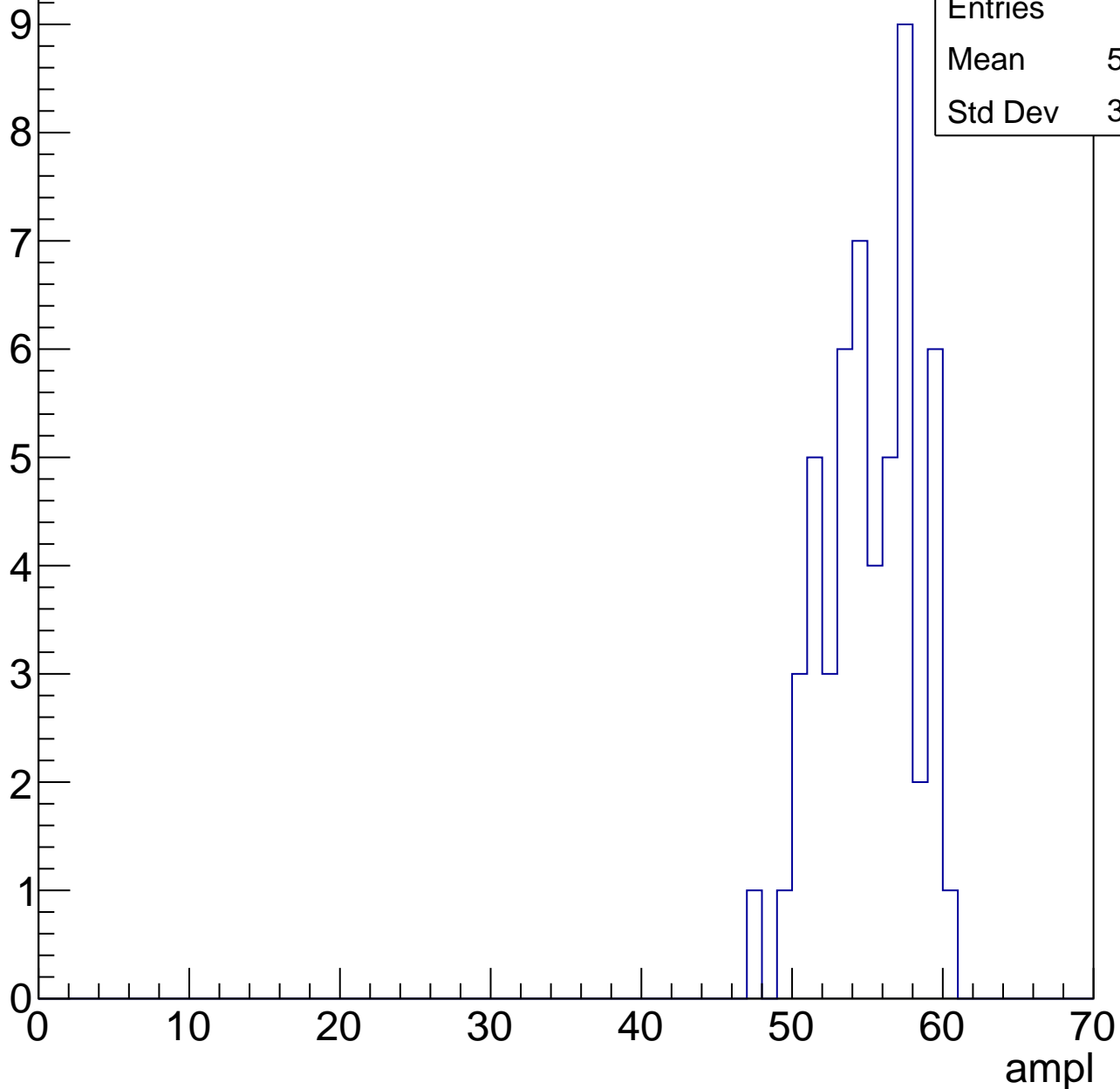


# B0L000S, U25-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

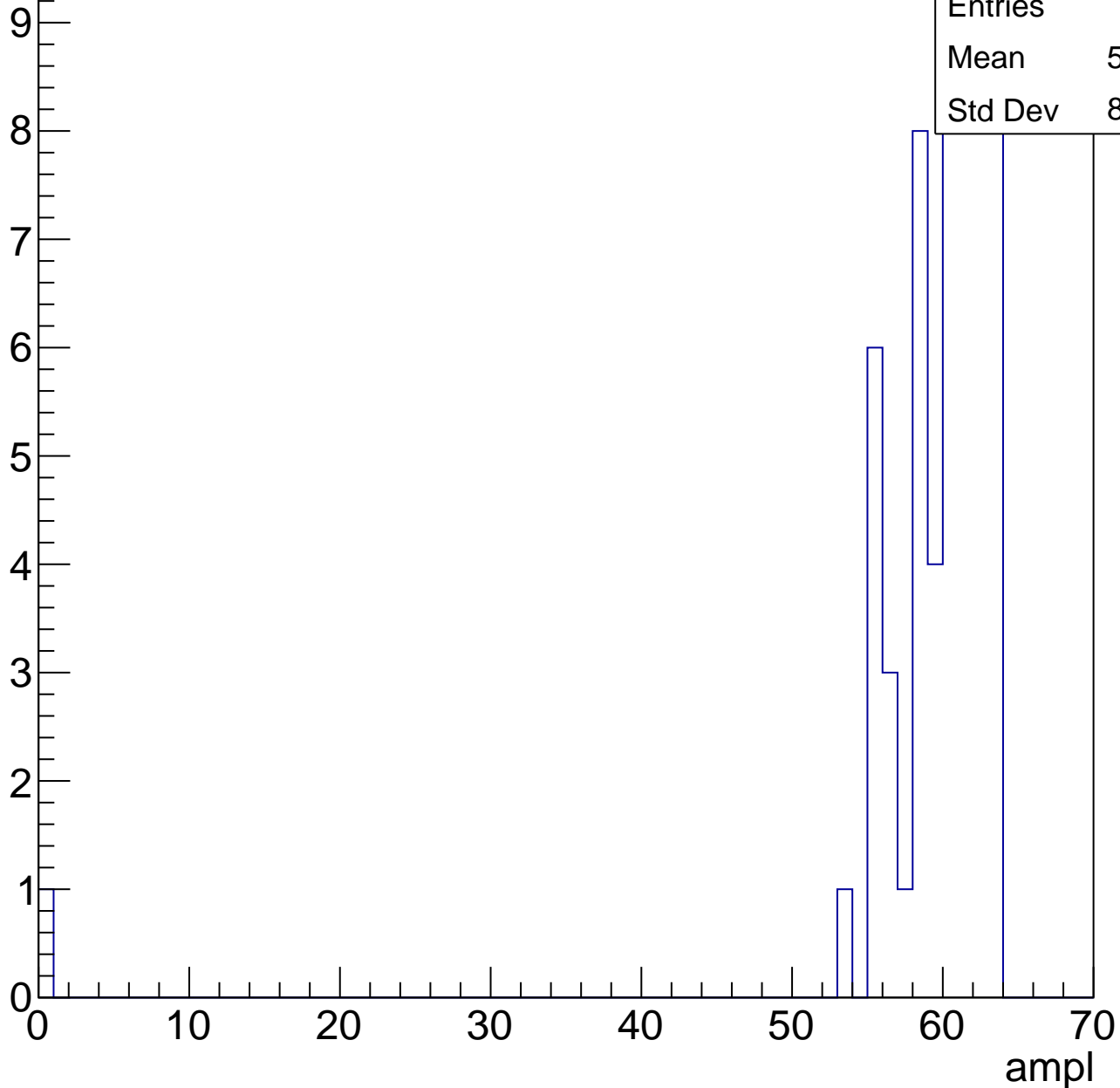
Entries	53
Mean	54.64
Std Dev	3.029



# B0L000S, U25-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch54, adc0

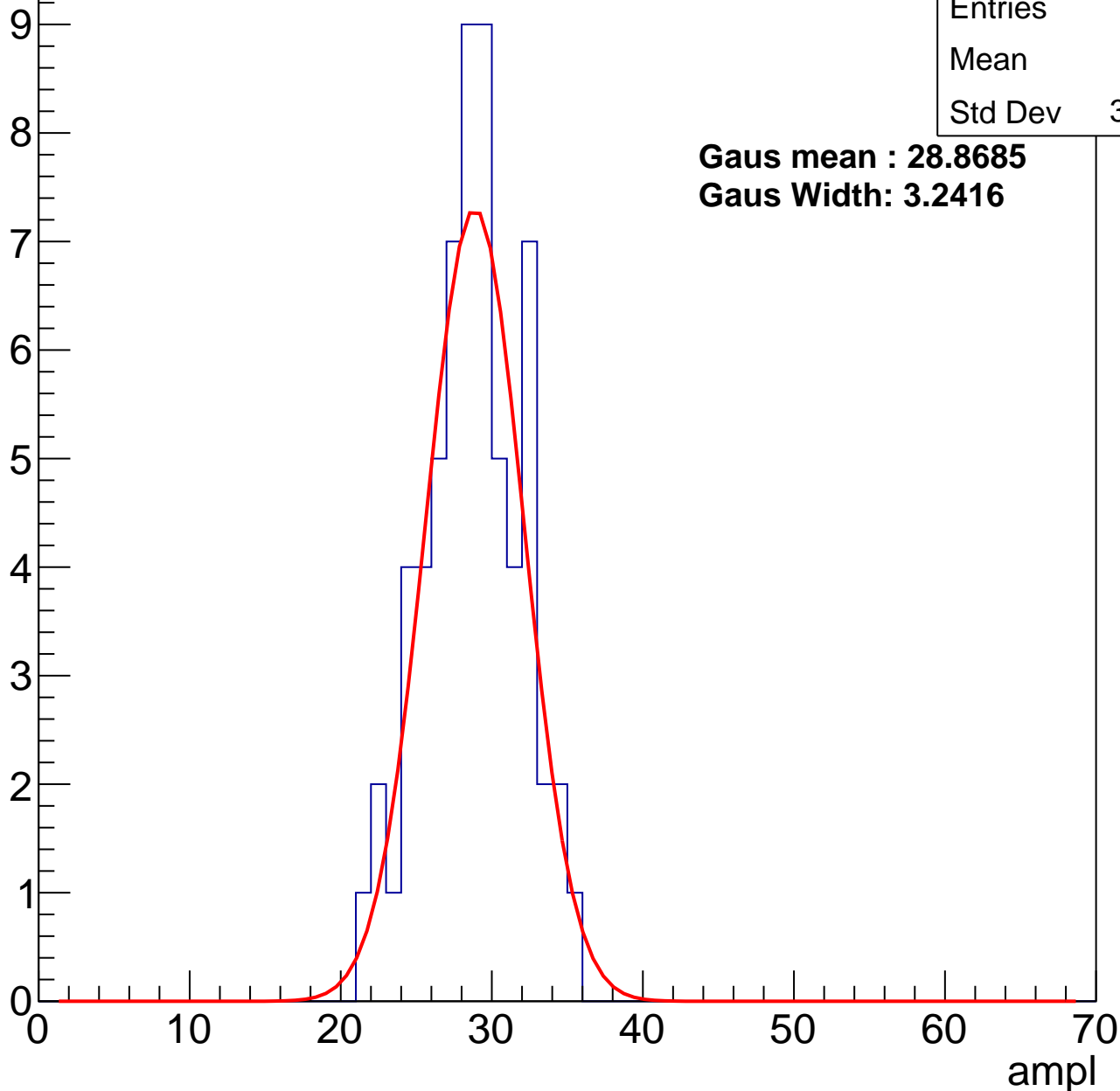
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	28.3
Std Dev	3.125

**Gaus mean : 28.8685**

**Gaus Width: 3.2416**

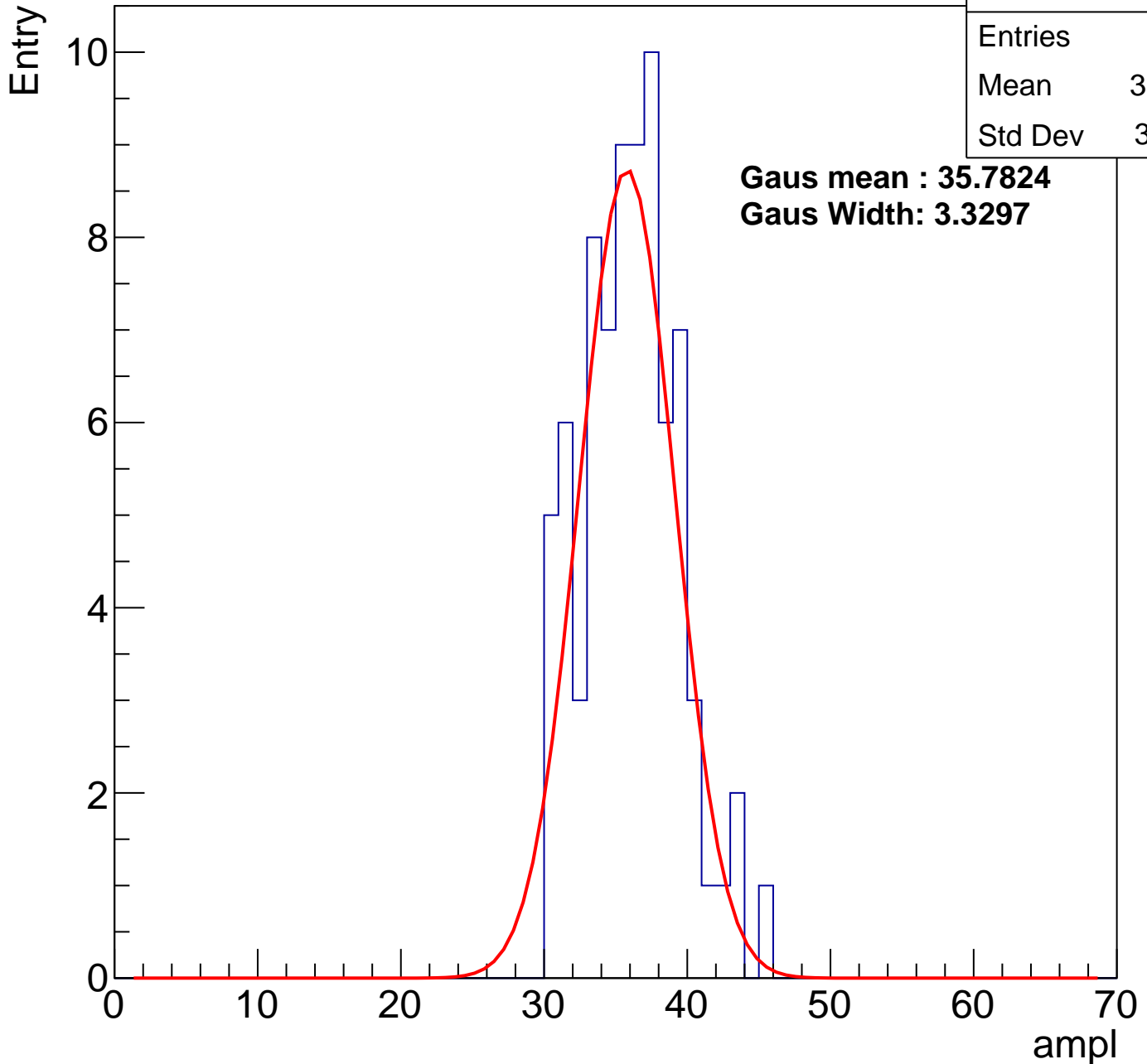


# B0L000S, U25-ch54, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	78
Mean	35.62
Std Dev	3.321

**Gaus mean : 35.7824**  
**Gaus Width: 3.3297**



# B0L000S, U25-ch54, adc2

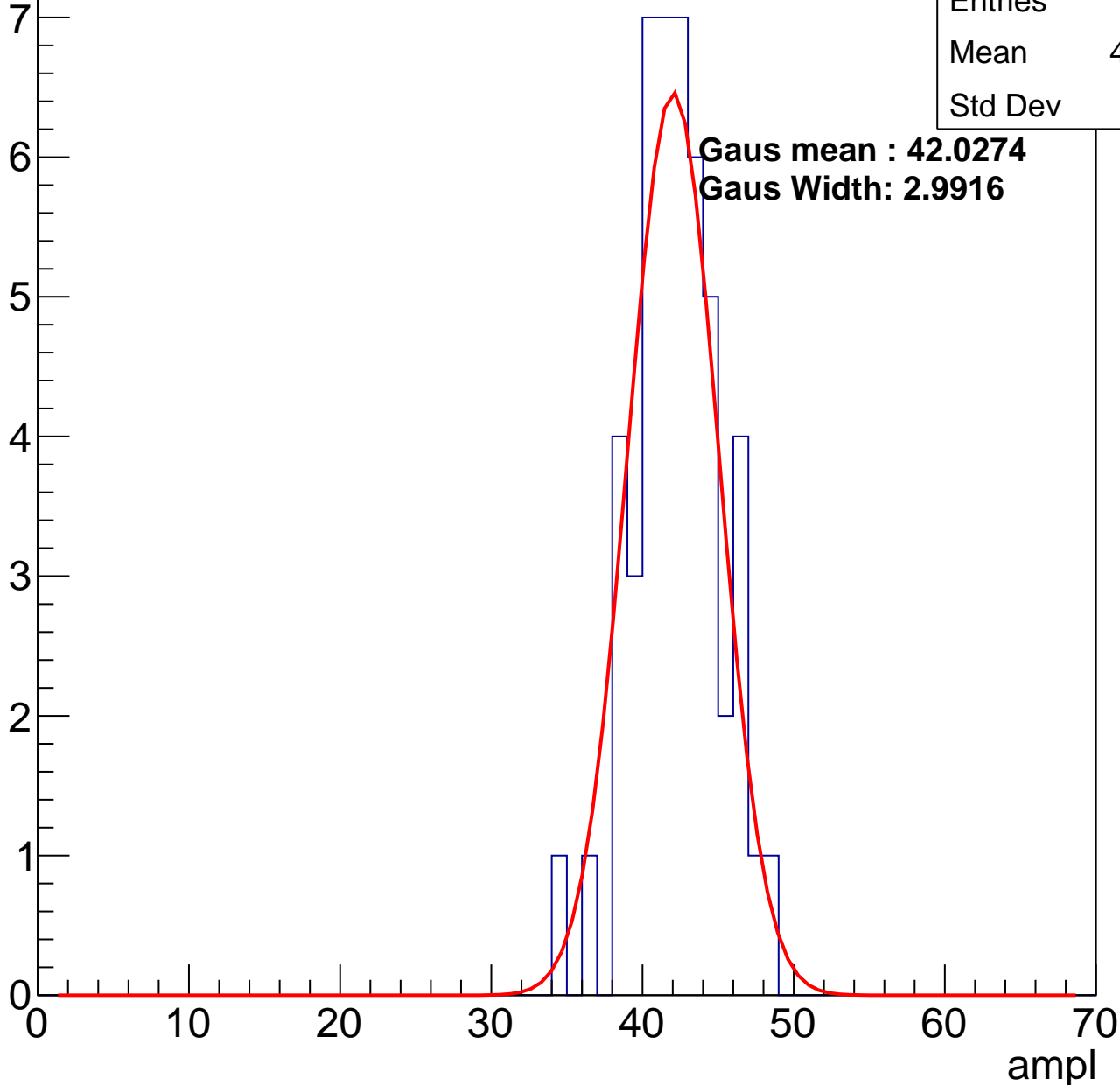
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	41.78
Std Dev	2.83

Gaus mean : 42.0274

Gaus Width: 2.9916

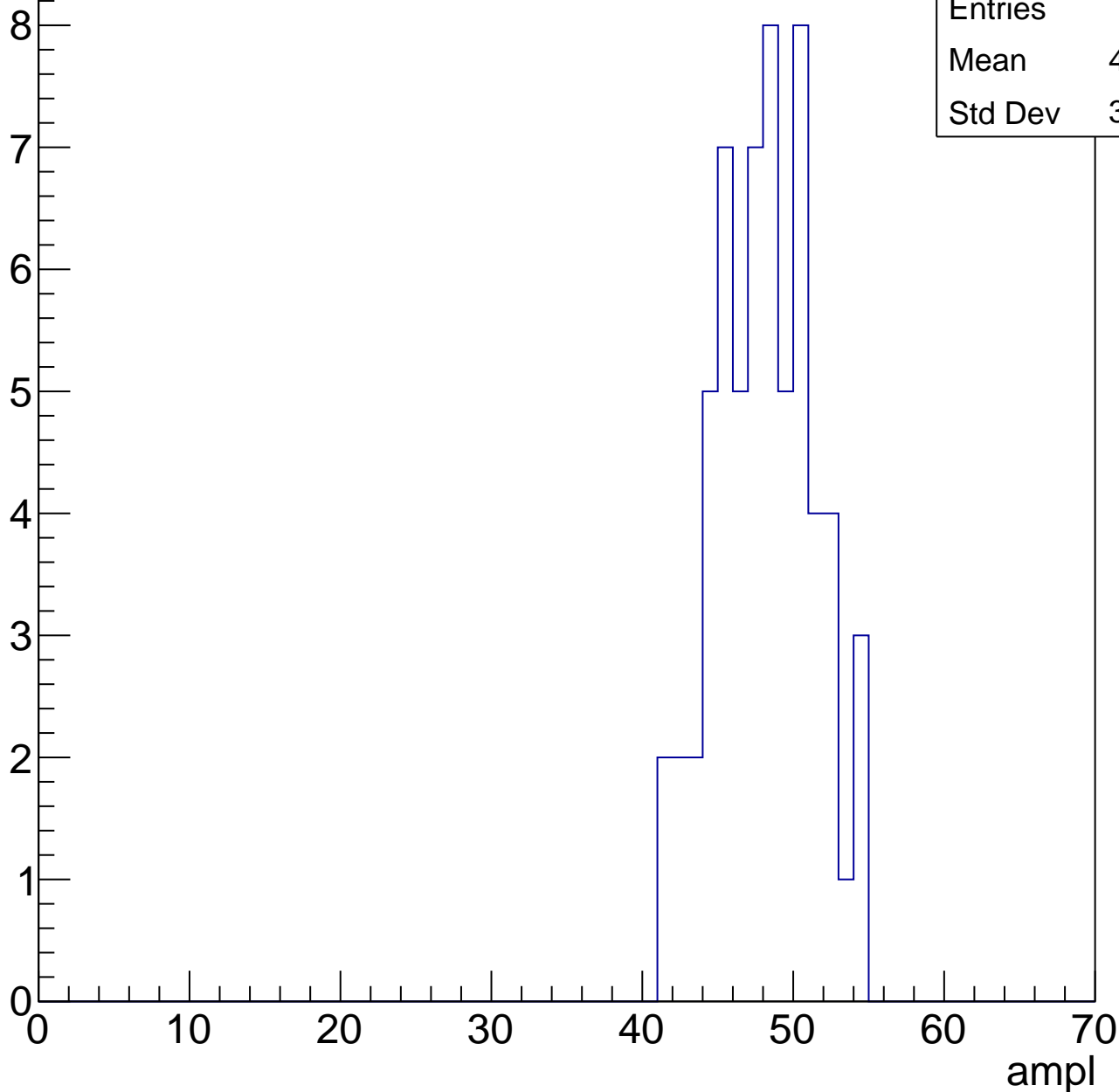


# B0L000S, U25-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	47.65
Std Dev	3.218

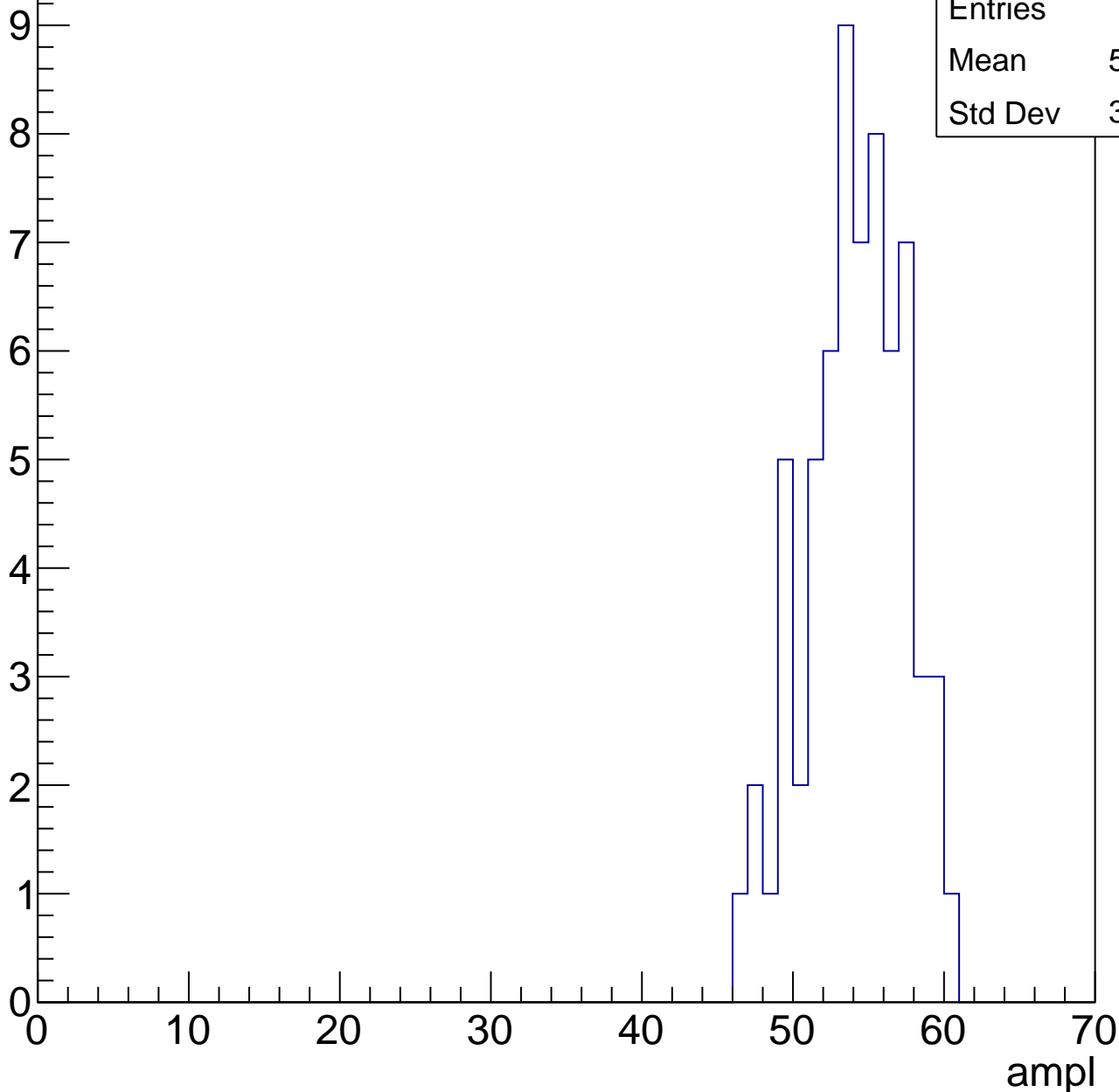


# B0L000S, U25-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	53.65
Std Dev	3.207

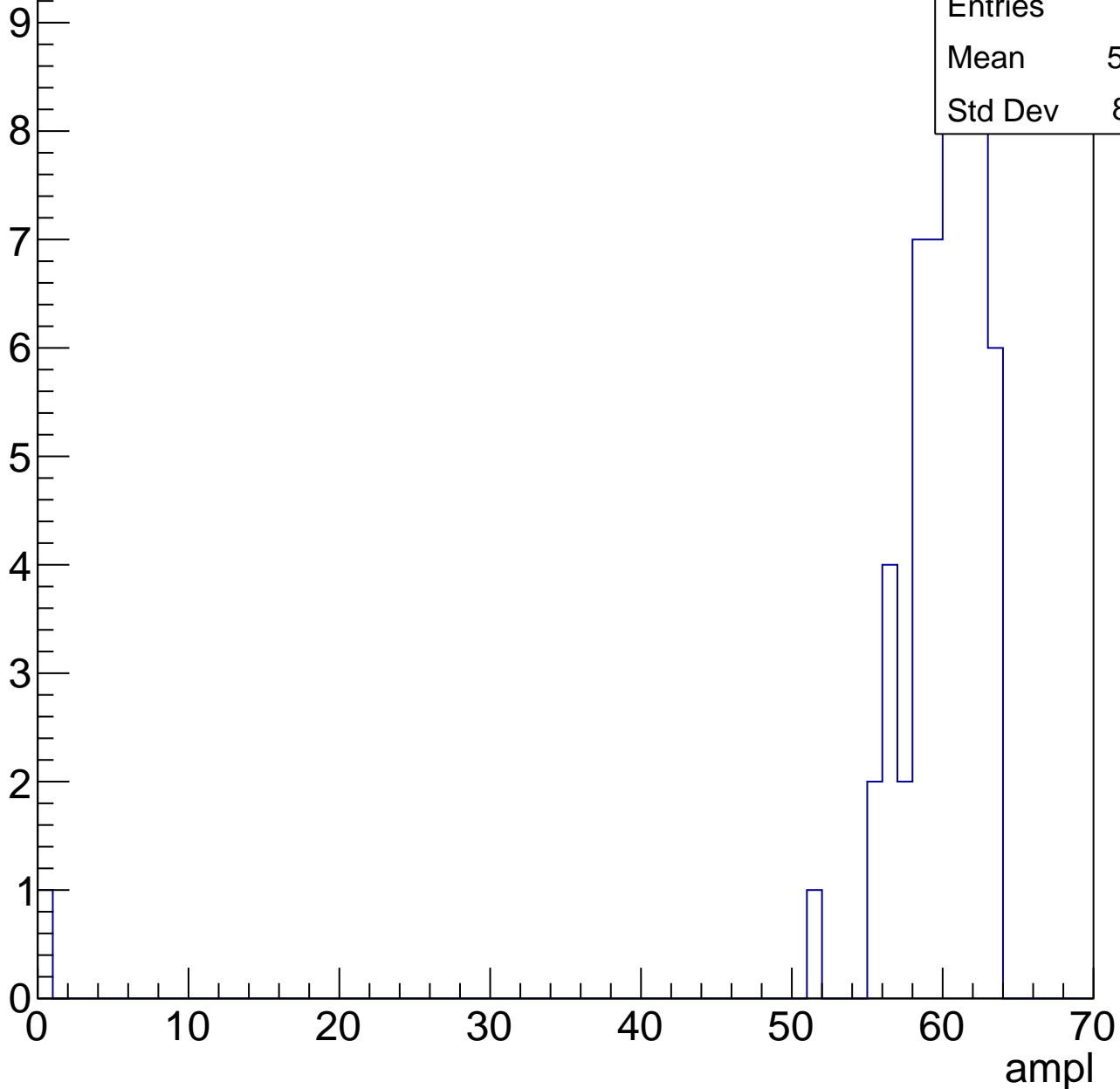


# B0L000S, U25-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

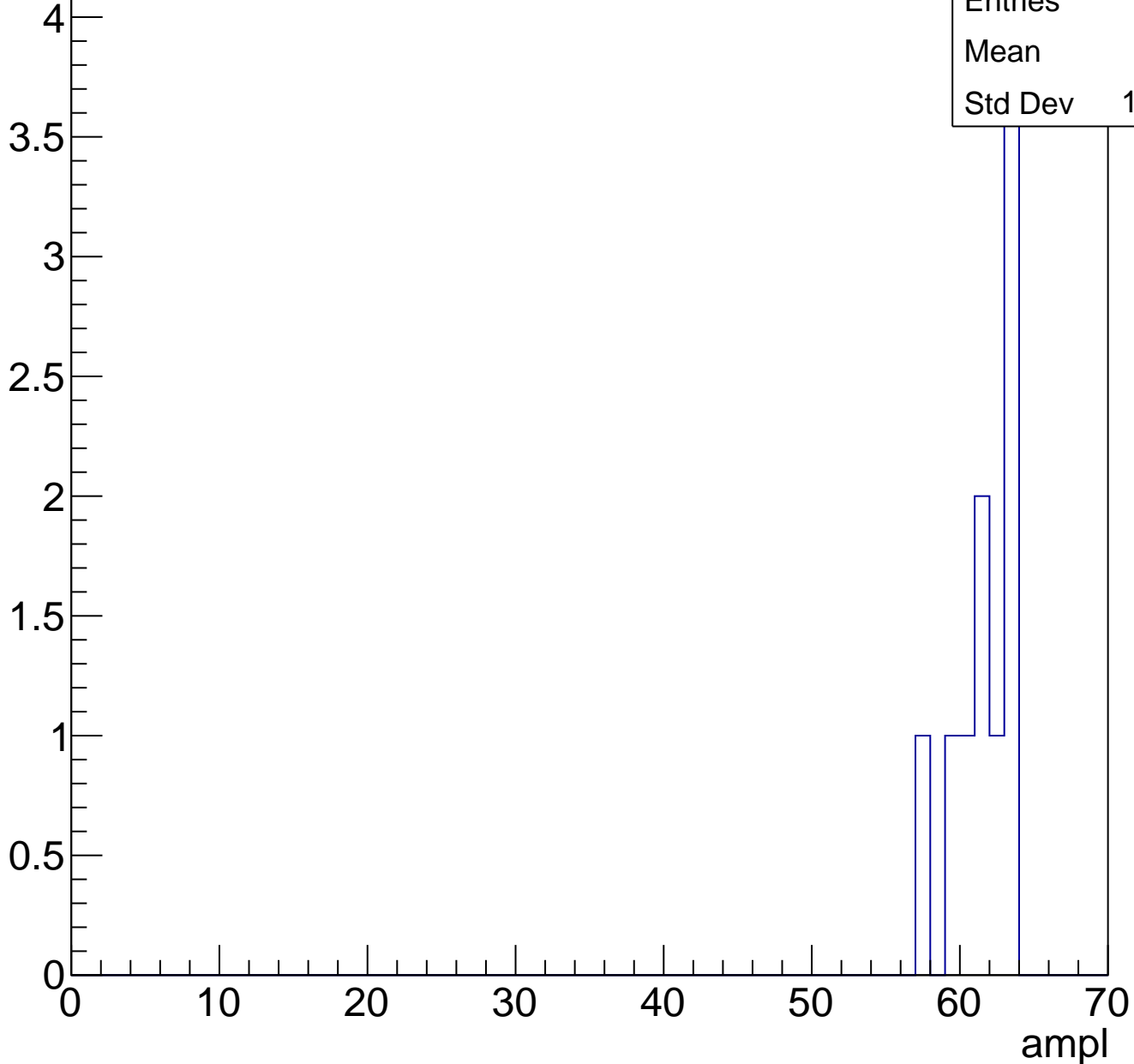
Entries	55
Mean	58.58
Std Dev	8.351



# B0L000S, U25-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch55, adc0

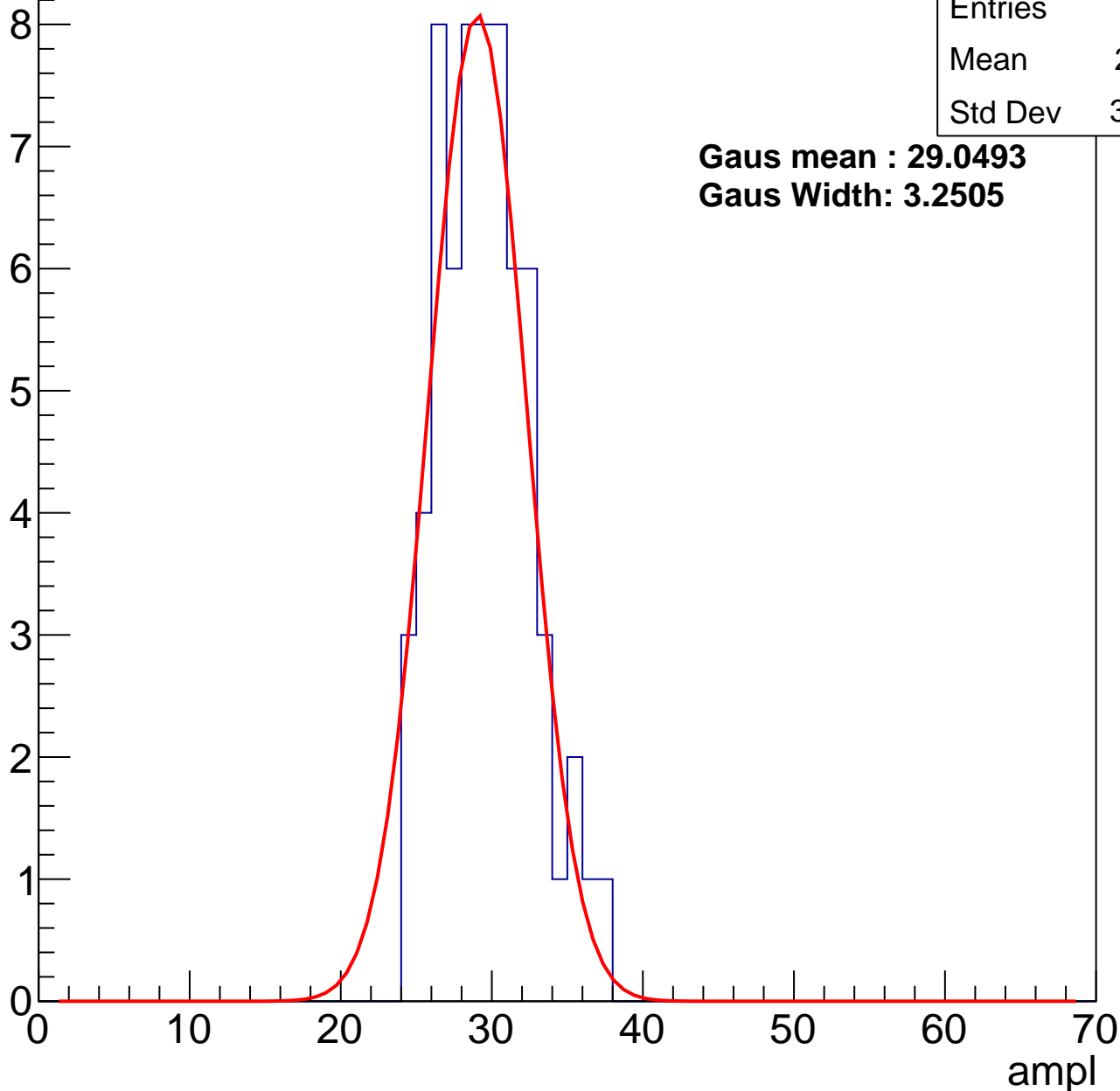
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.11
Std Dev	3.003

**Gaus mean : 29.0493**

**Gaus Width: 3.2505**



# B0L000S, U25-ch55, adc1

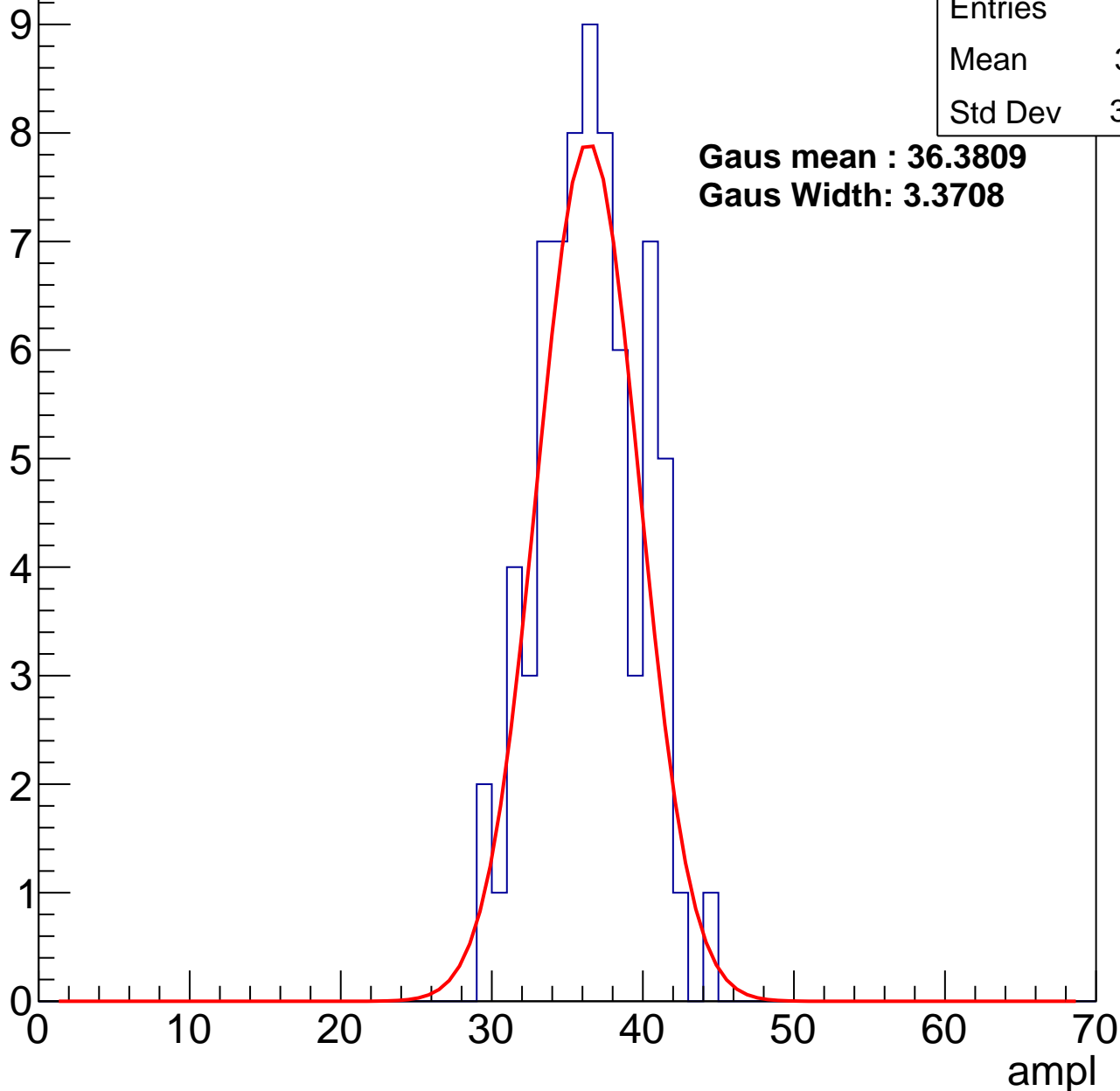
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.01
Std Dev	3.285

**Gaus mean : 36.3809**

**Gaus Width: 3.3708**



# B0L000S, U25-ch55, adc2

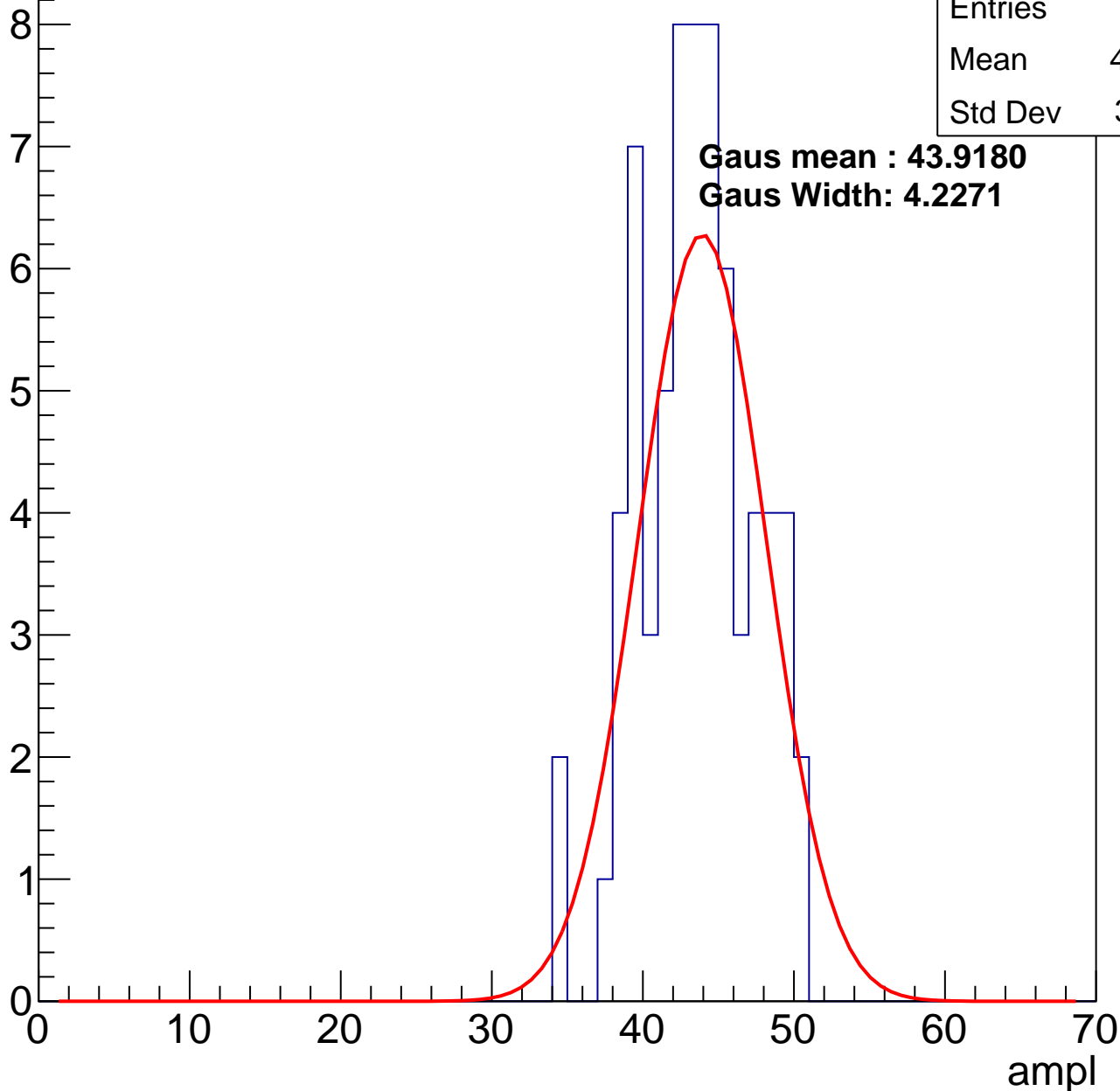
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.06
Std Dev	3.671

**Gaus mean : 43.9180**

**Gaus Width: 4.2271**

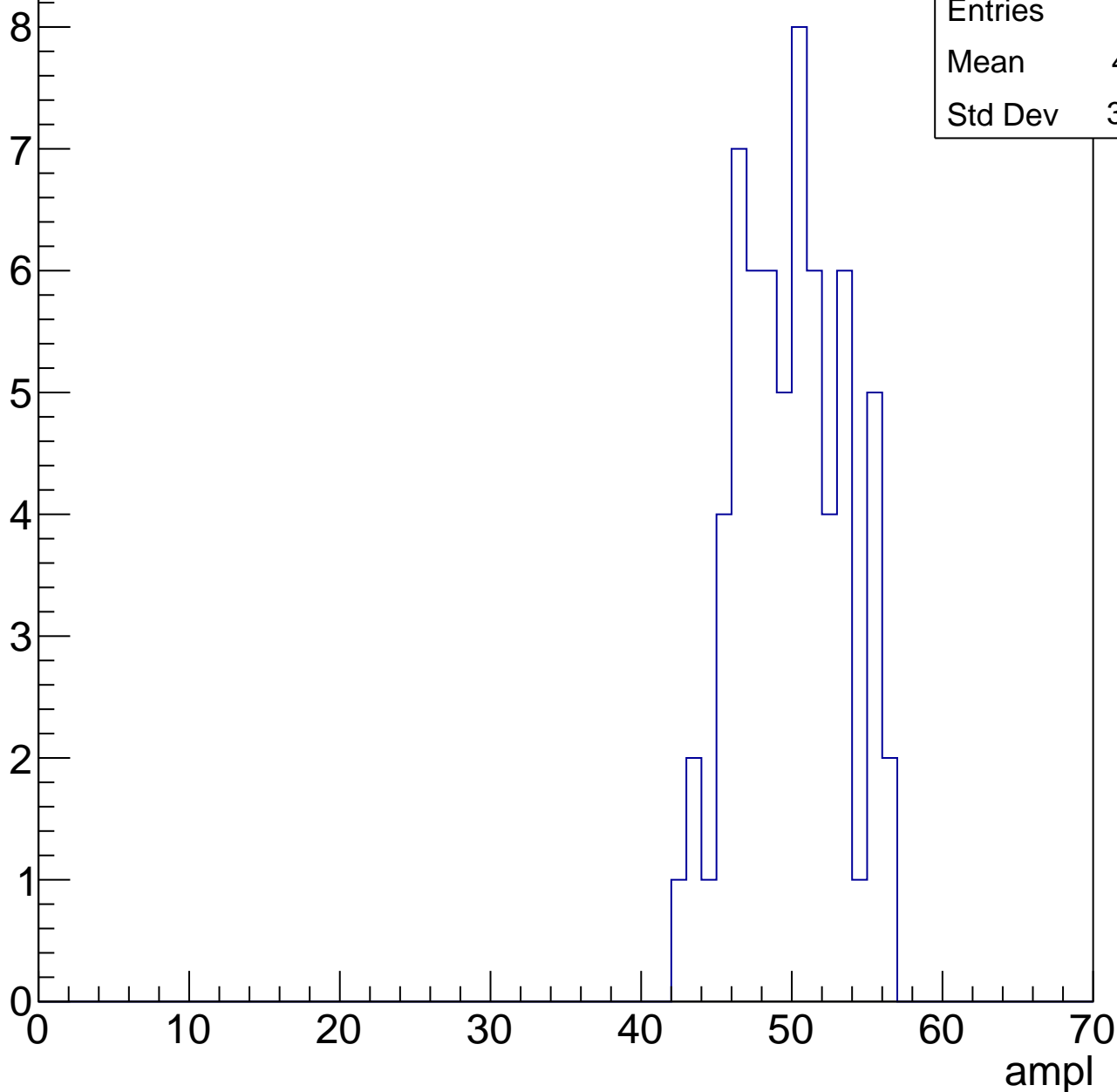


# B0L000S, U25-ch55, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

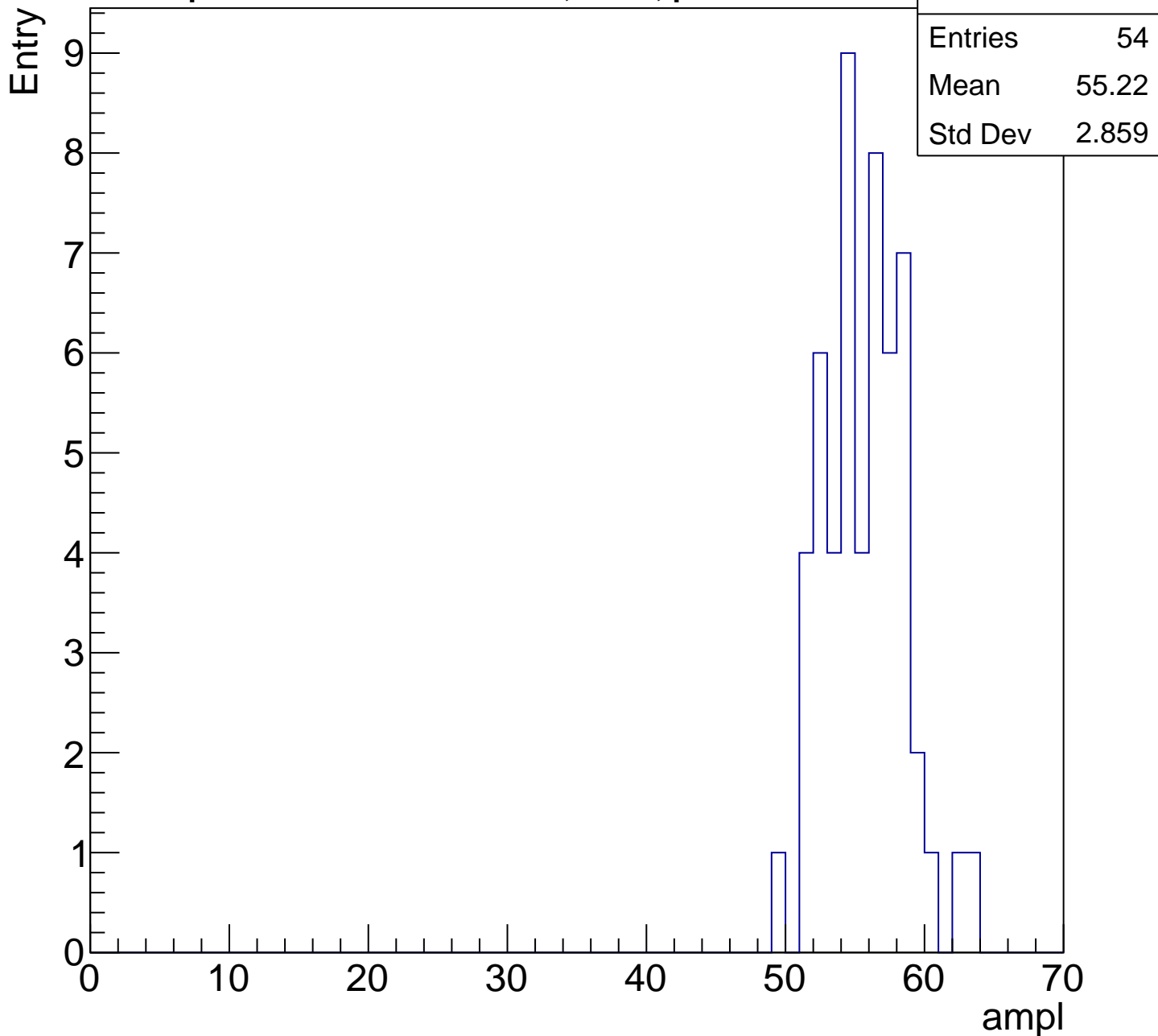
Entry

Entries	64
Mean	49.41
Std Dev	3.445



# B0L000S, U25-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

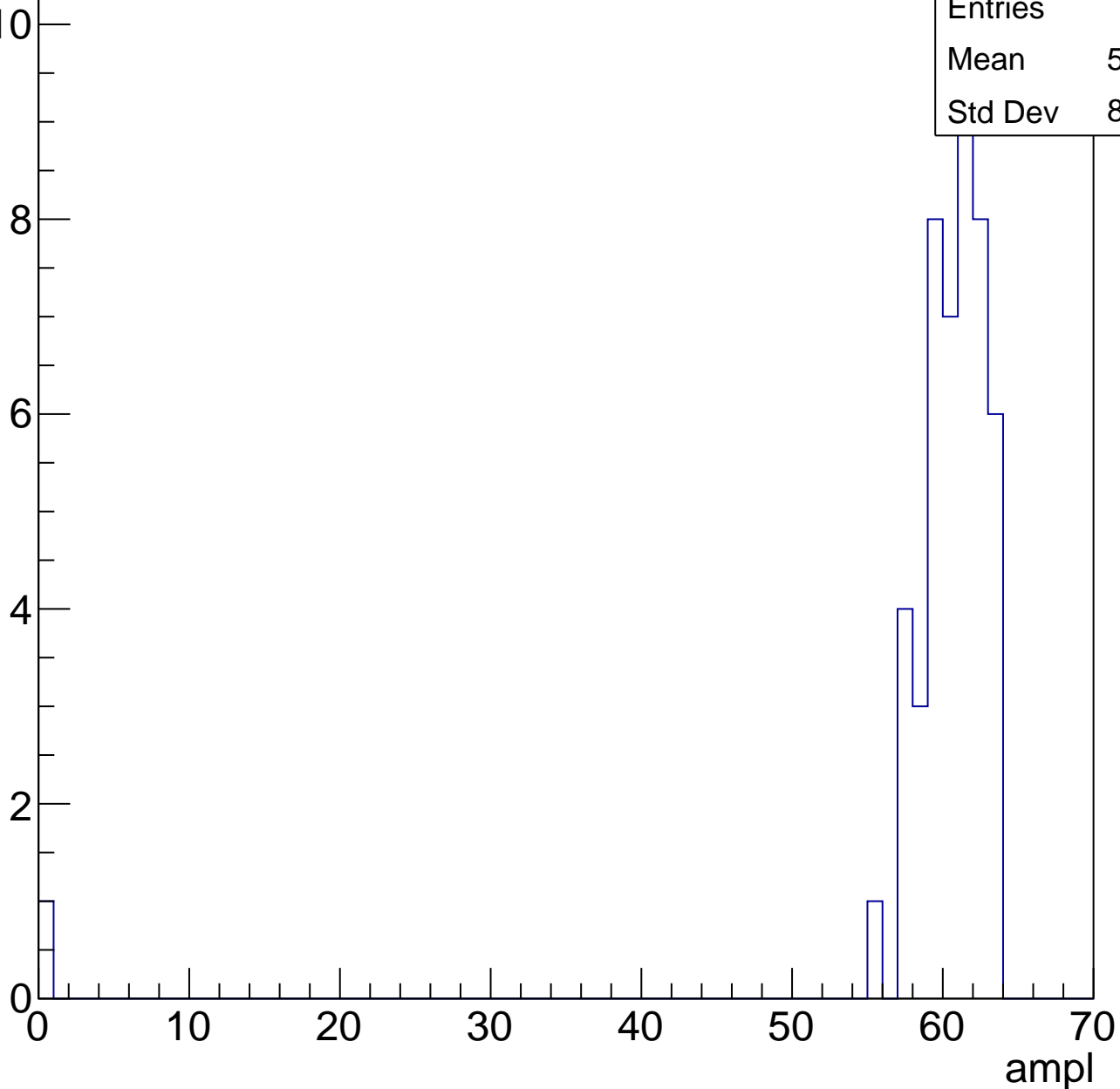


# B0L000S, U25-ch55, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

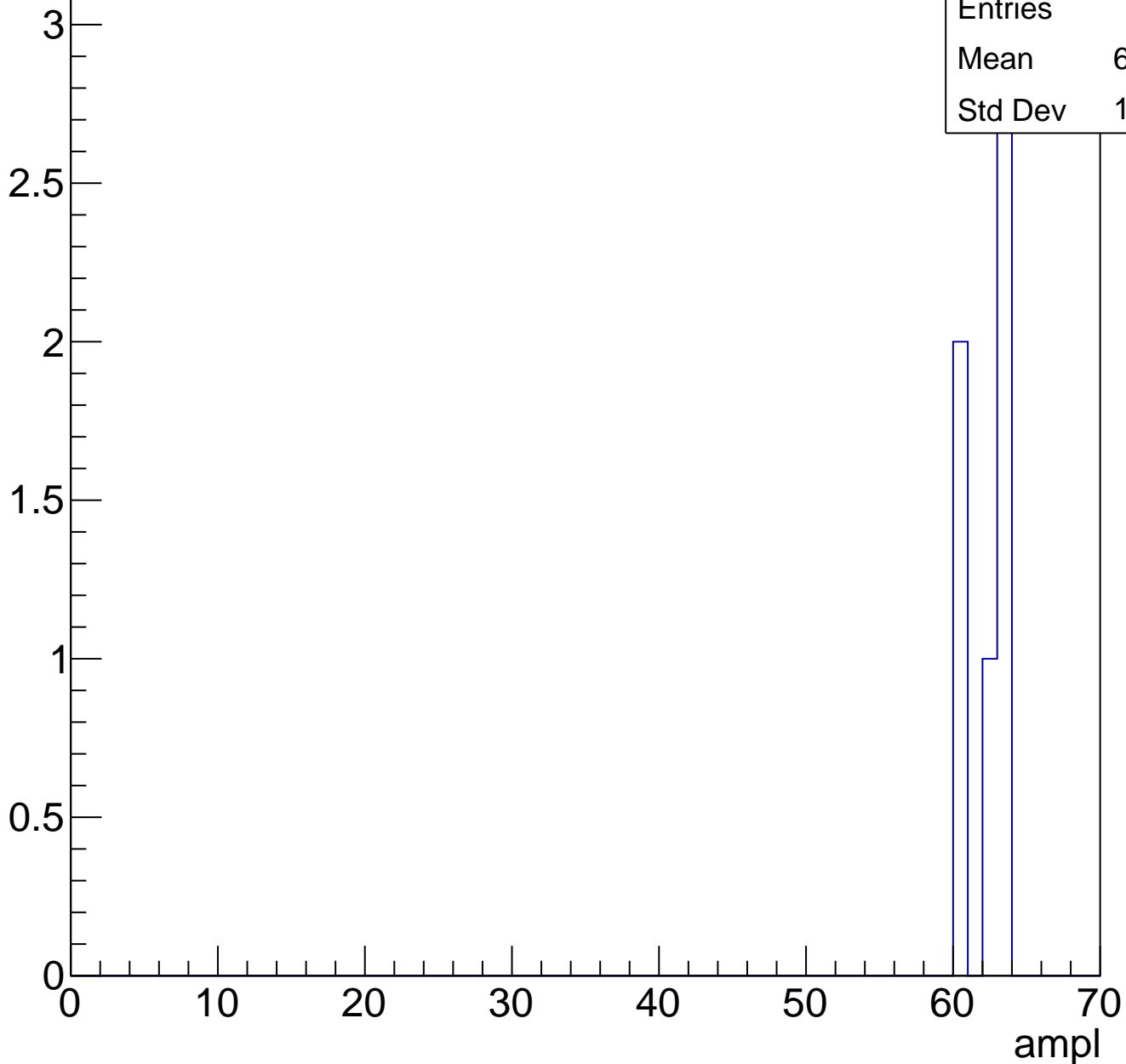
Entries	48
Mean	59.02
Std Dev	8.816



# B0L000S, U25-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U25-ch56, adc0

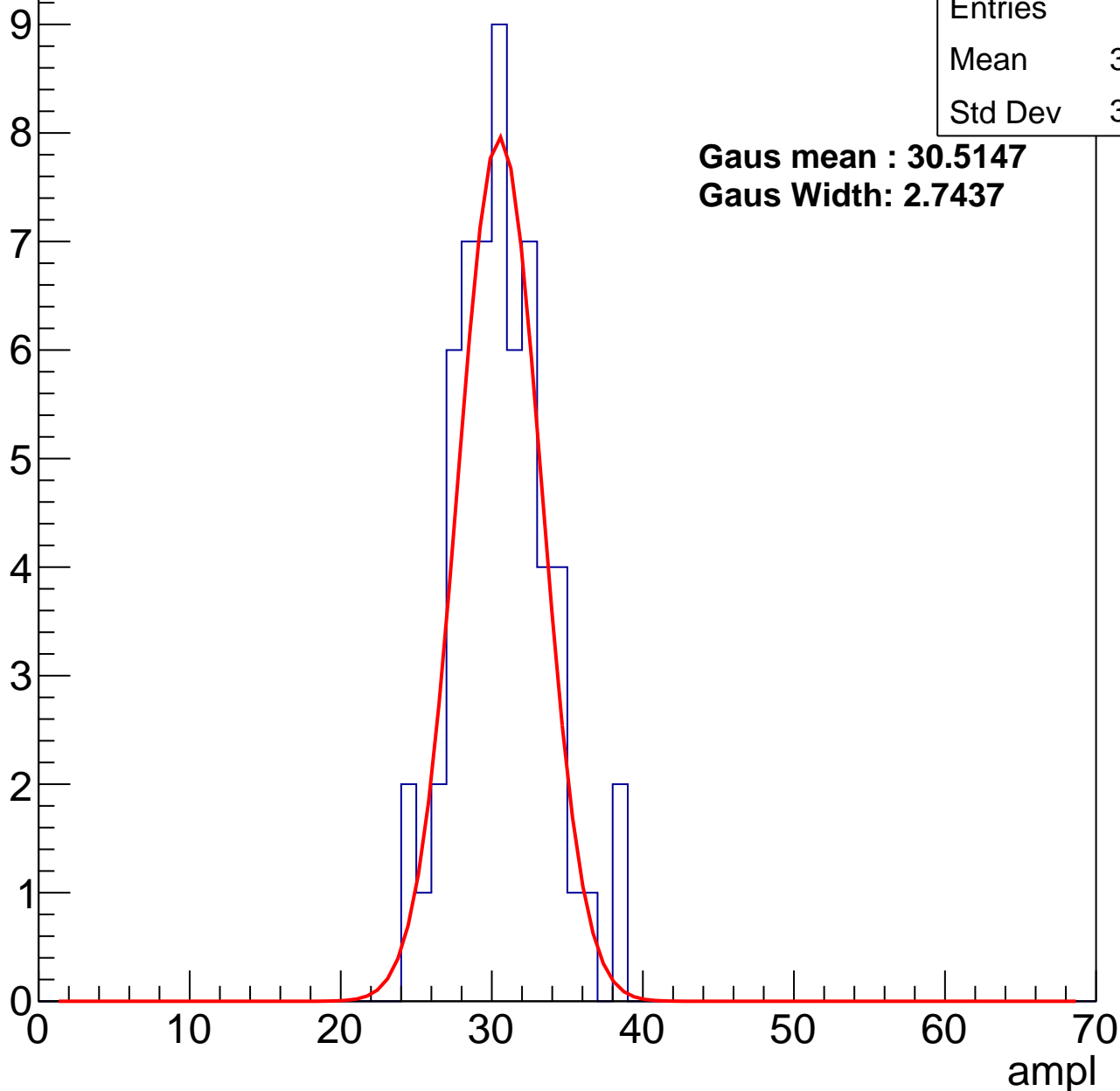
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	30.19
Std Dev	3.022

**Gaus mean : 30.5147**

**Gaus Width: 2.7437**



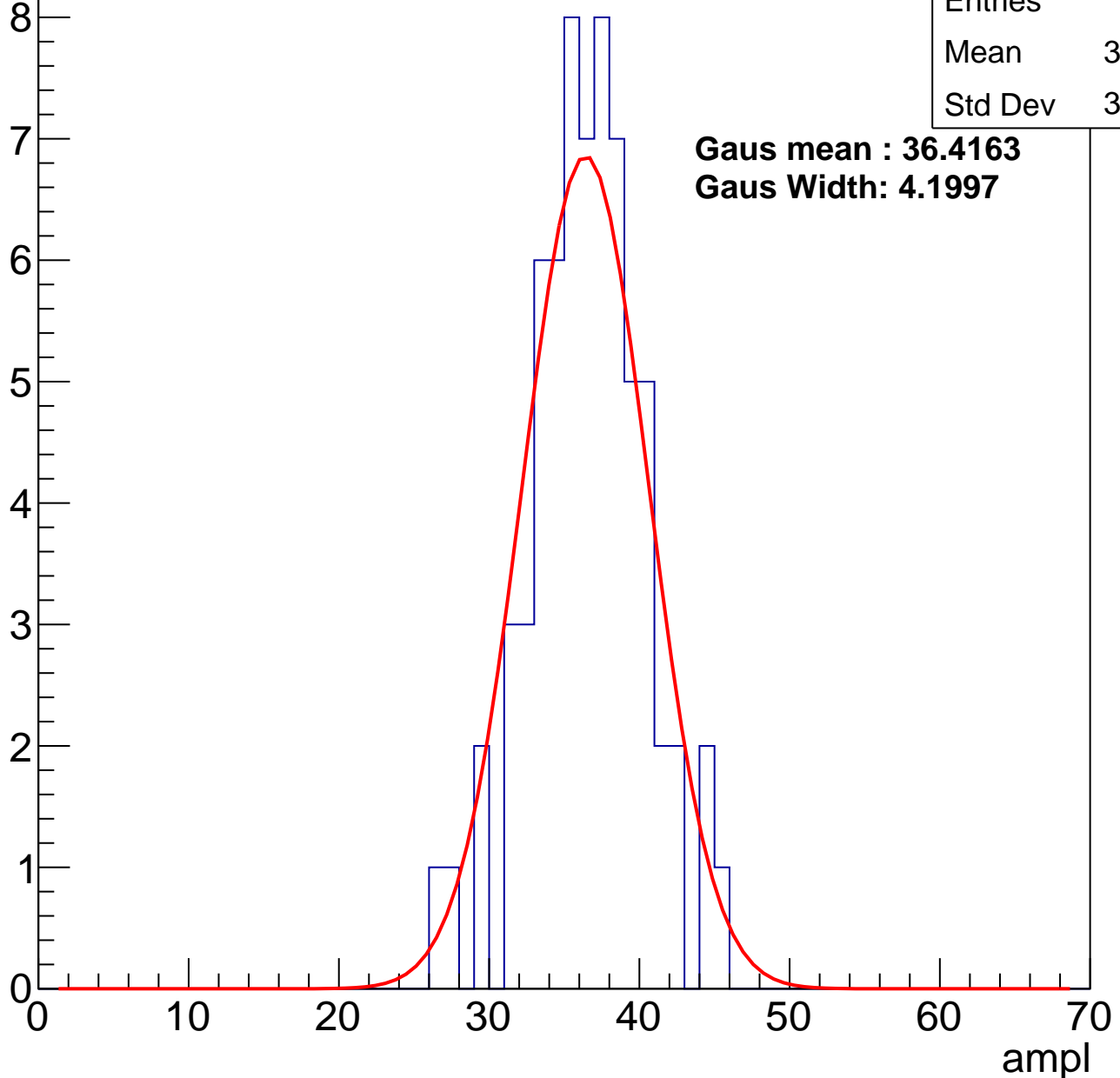
# B0L000S, U25-ch56, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.09
Std Dev	3.768

**Gaus mean : 36.4163**  
**Gaus Width: 4.1997**



# B0L000S, U25-ch56, adc2

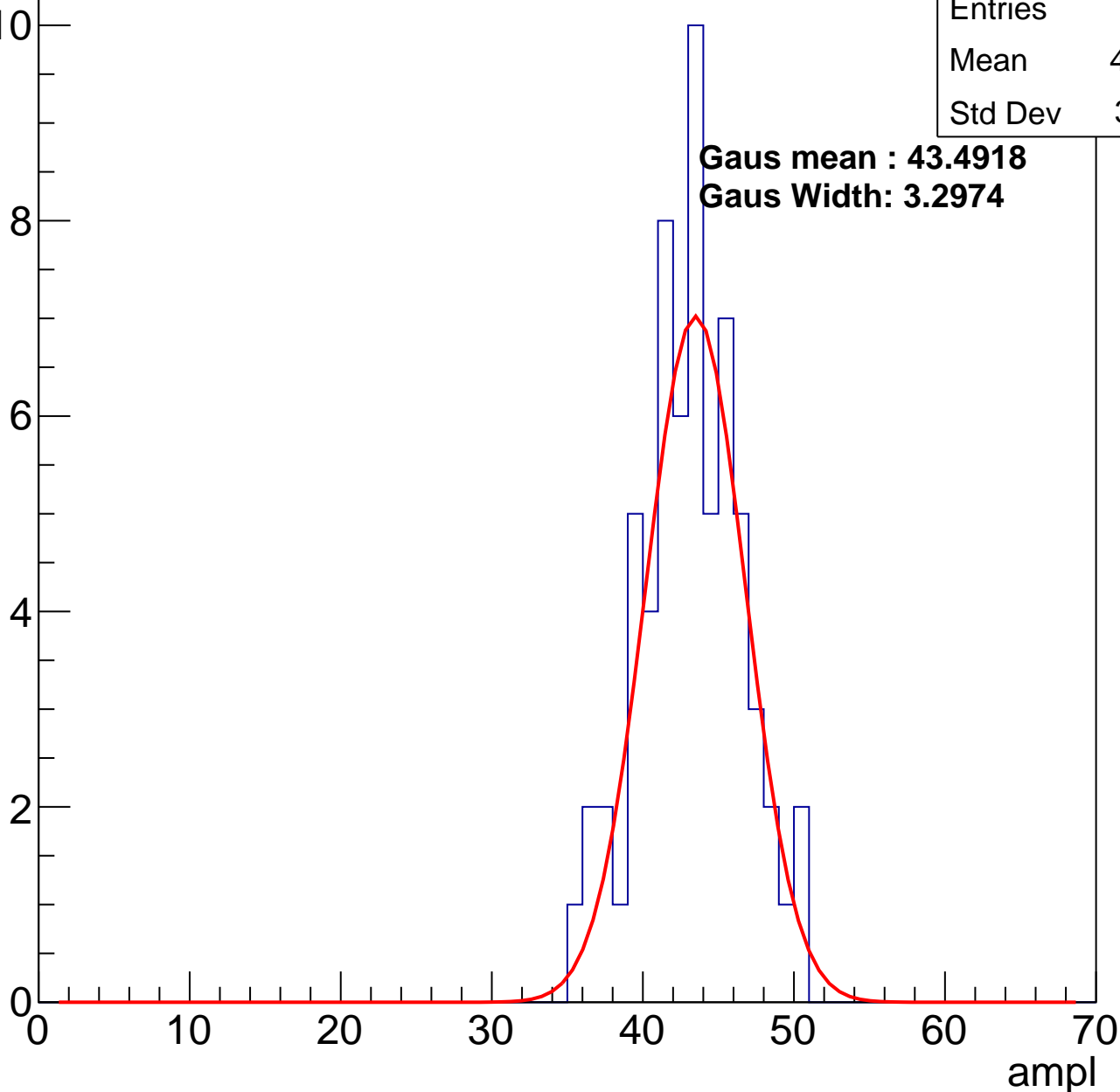
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.73
Std Dev	3.351

**Gaus mean : 43.4918**

**Gaus Width: 3.2974**

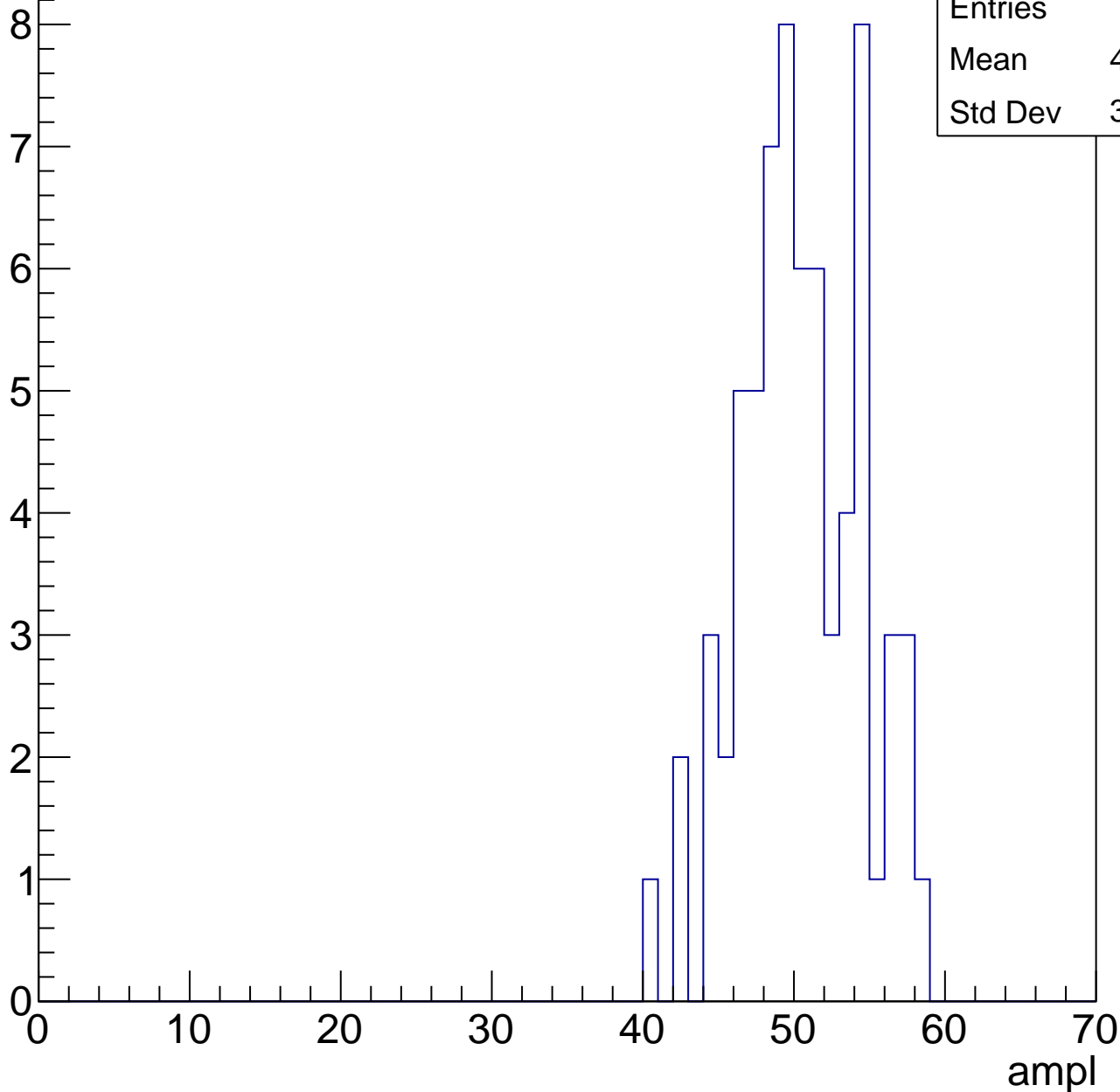


# B0L000S, U25-ch56, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.96
Std Dev	3.972

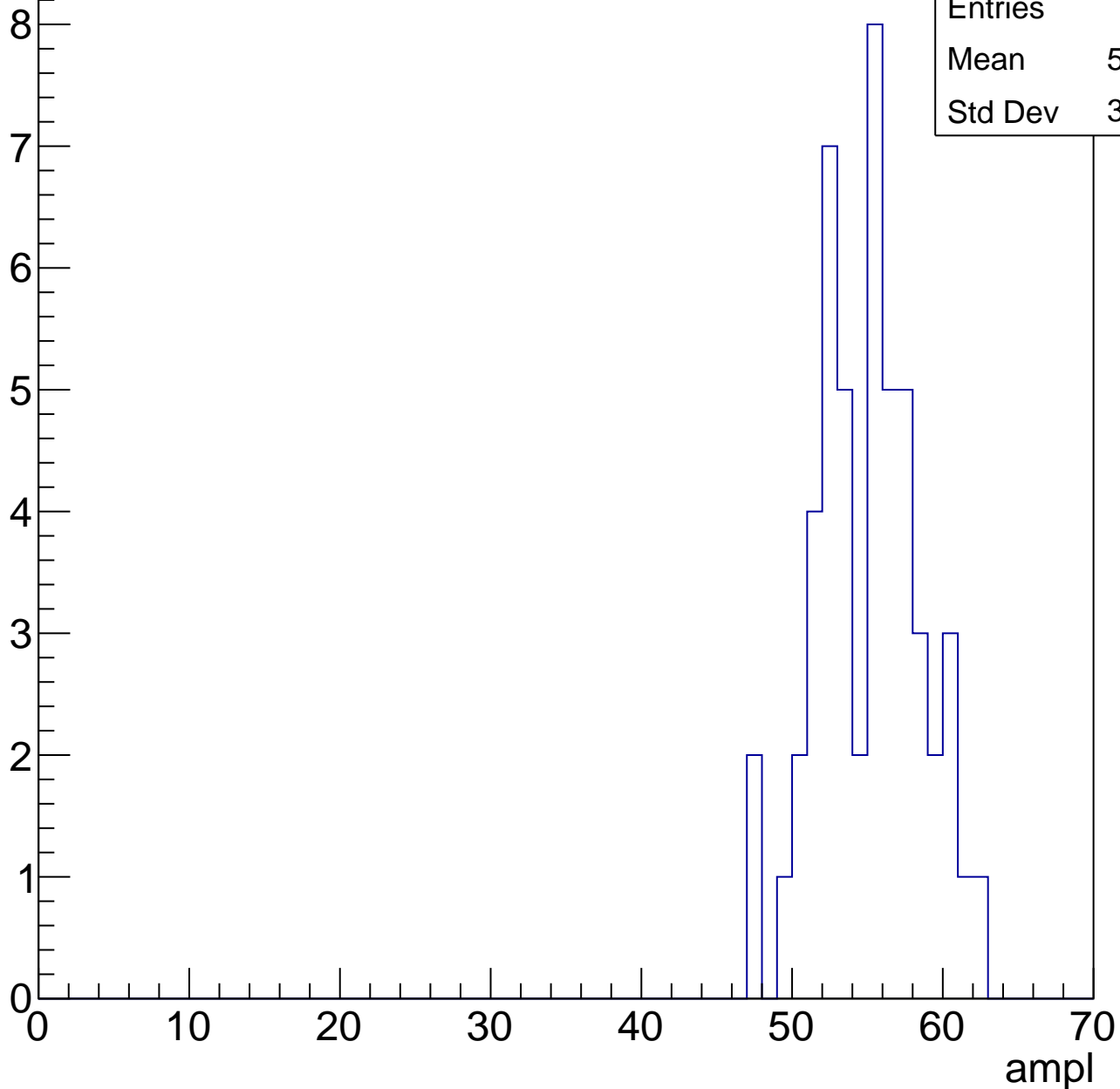


# B0L000S, U25-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.59
Std Dev	3.425

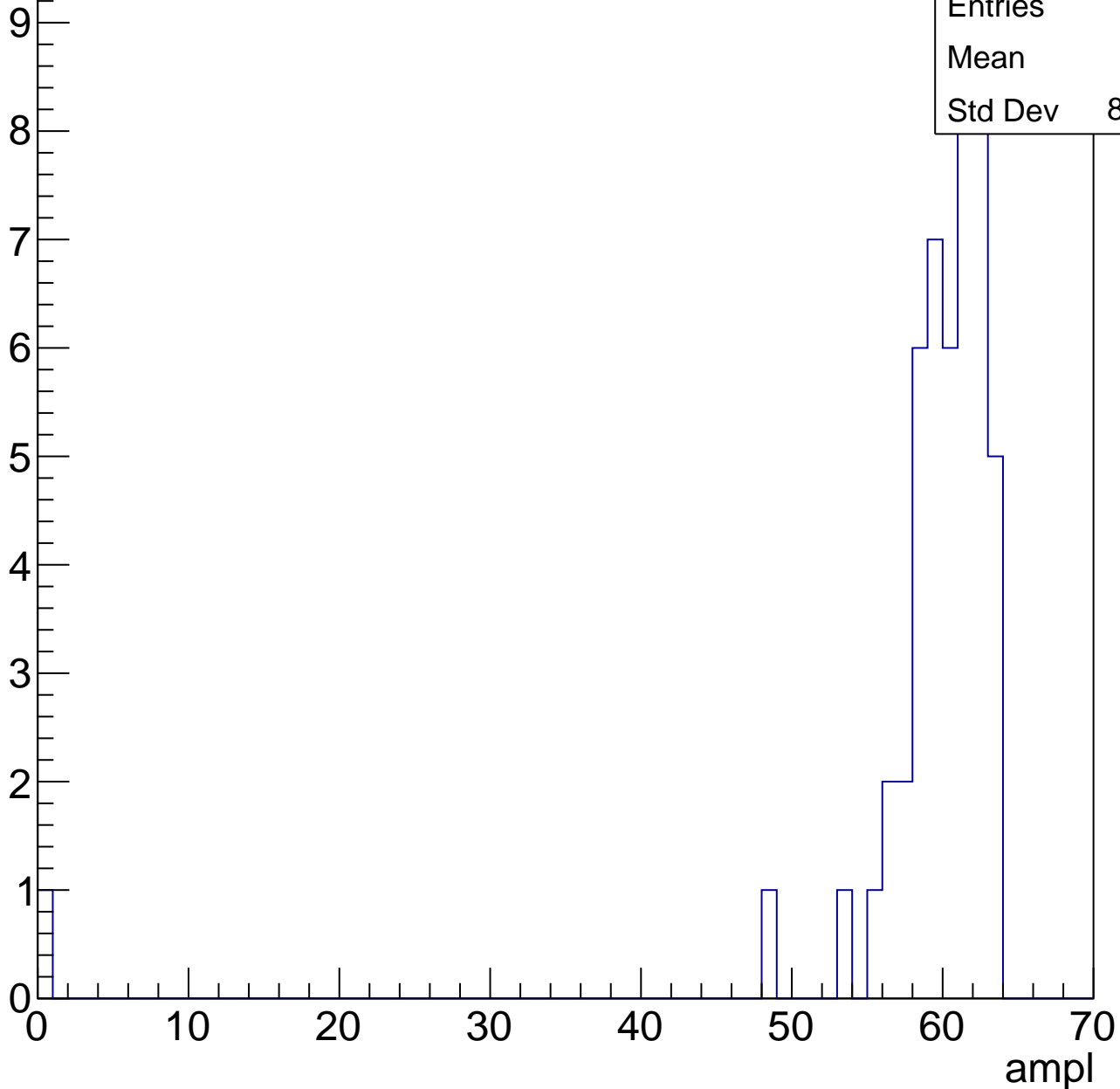


# B0L000S, U25-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

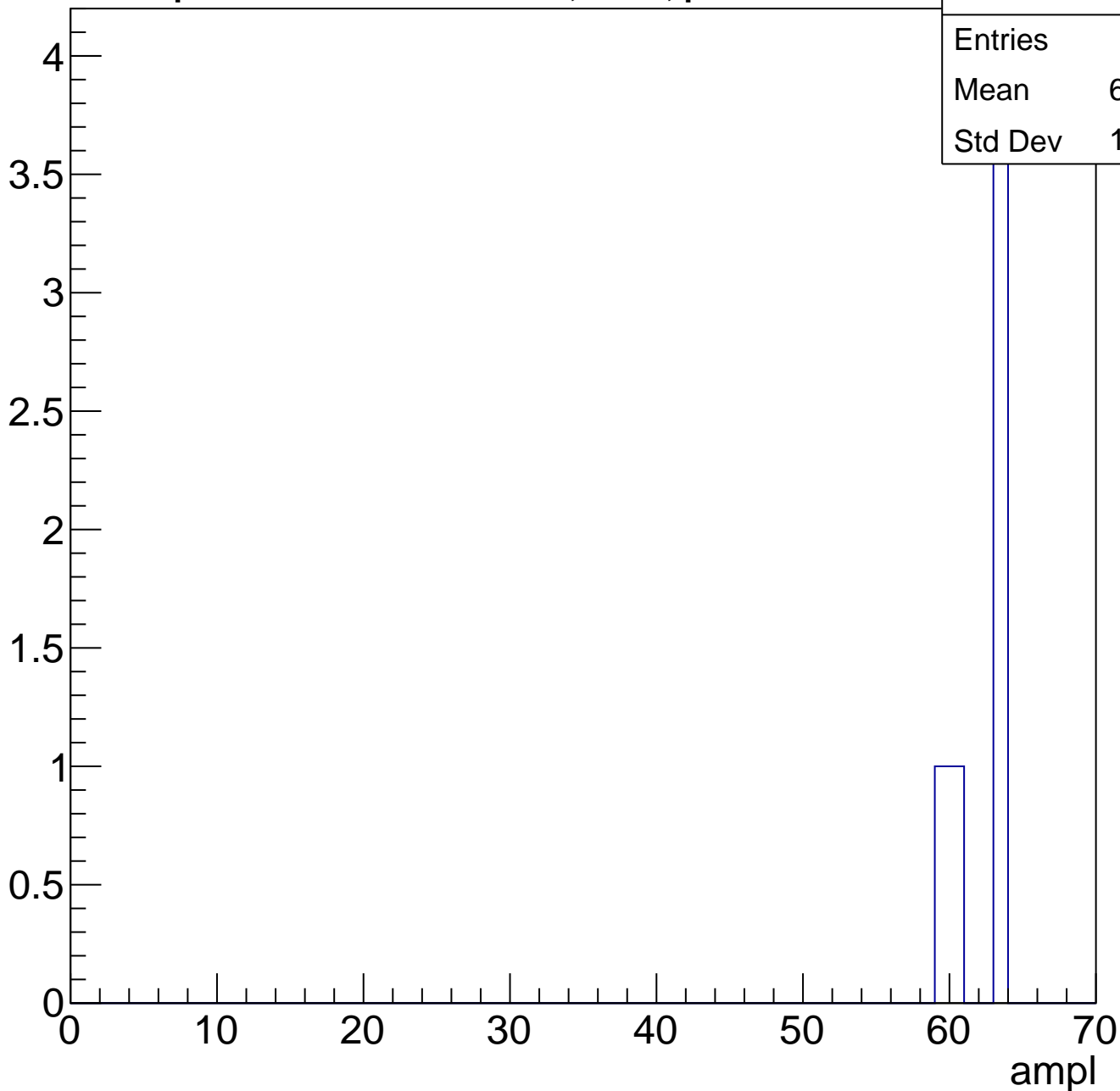
Entries	50
Mean	58.5
Std Dev	8.805



# B0L000S, U25-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	21
Std Dev	29.7

# B0L000S, U25-ch57, adc0

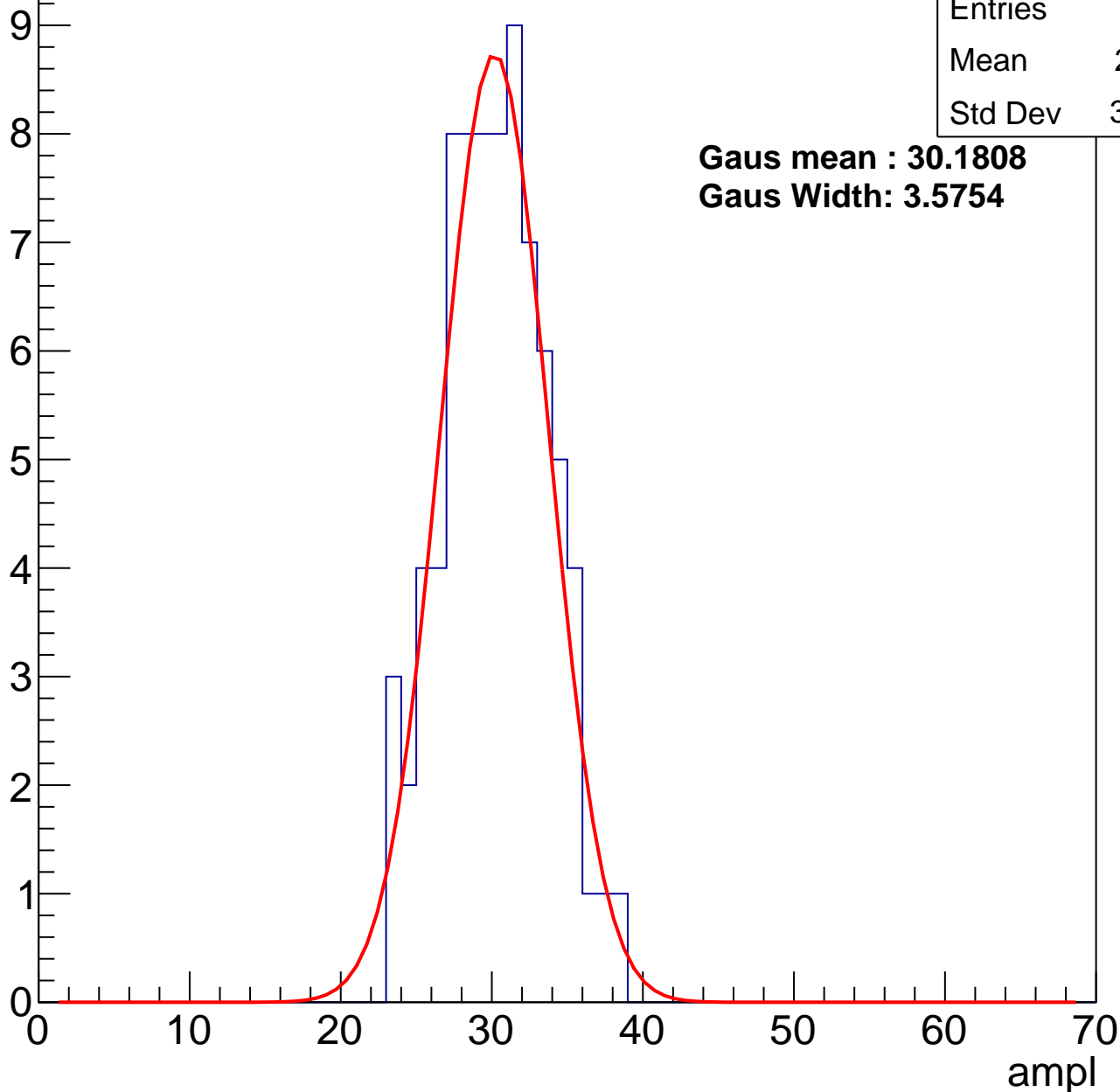
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	29.81
Std Dev	3.398

**Gaus mean : 30.1808**

**Gaus Width: 3.5754**



# B0L000S, U25-ch57, adc1

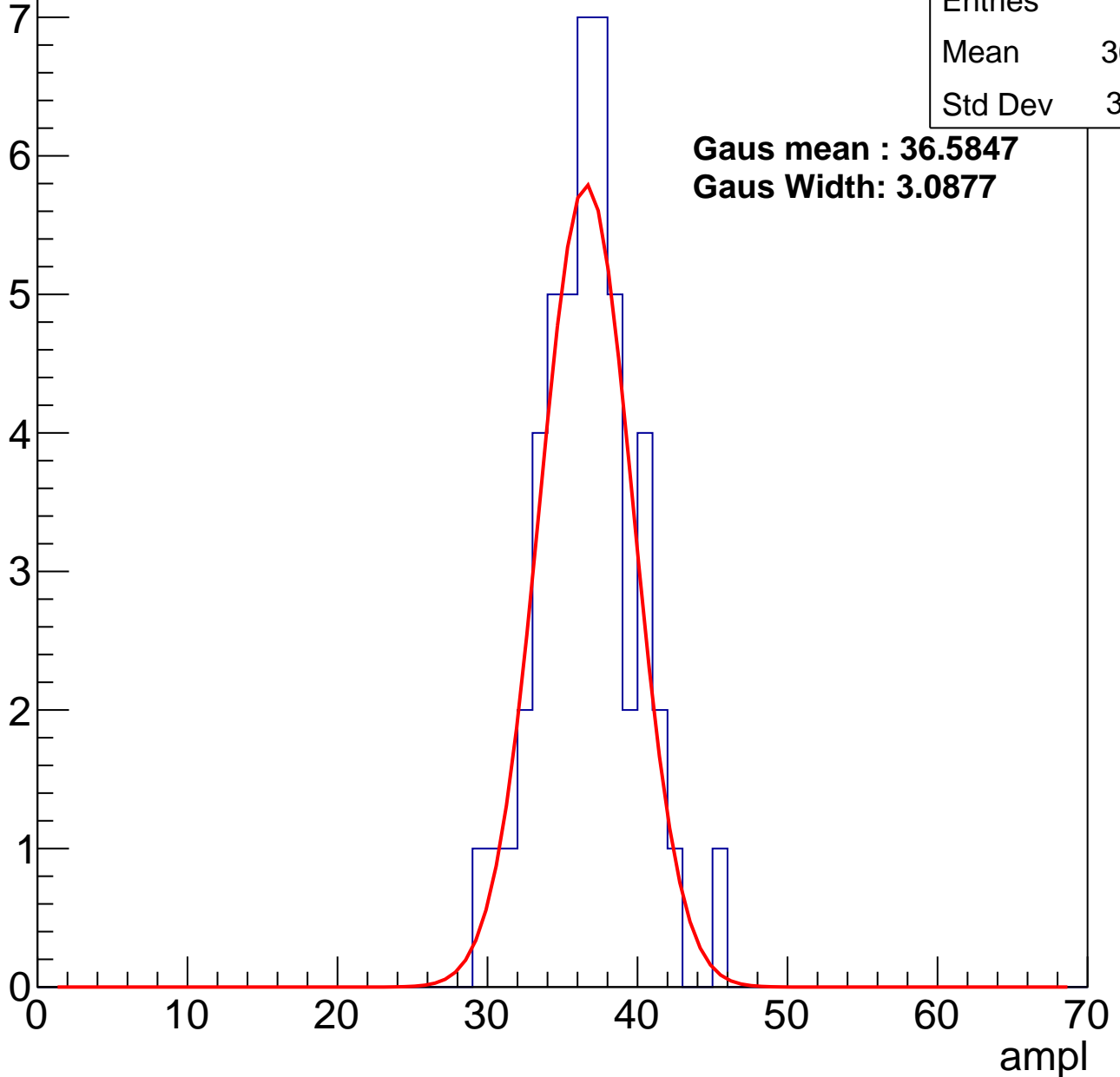
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	36.23
Std Dev	3.151

**Gaus mean : 36.5847**

**Gaus Width: 3.0877**



# B0L000S, U25-ch57, adc2

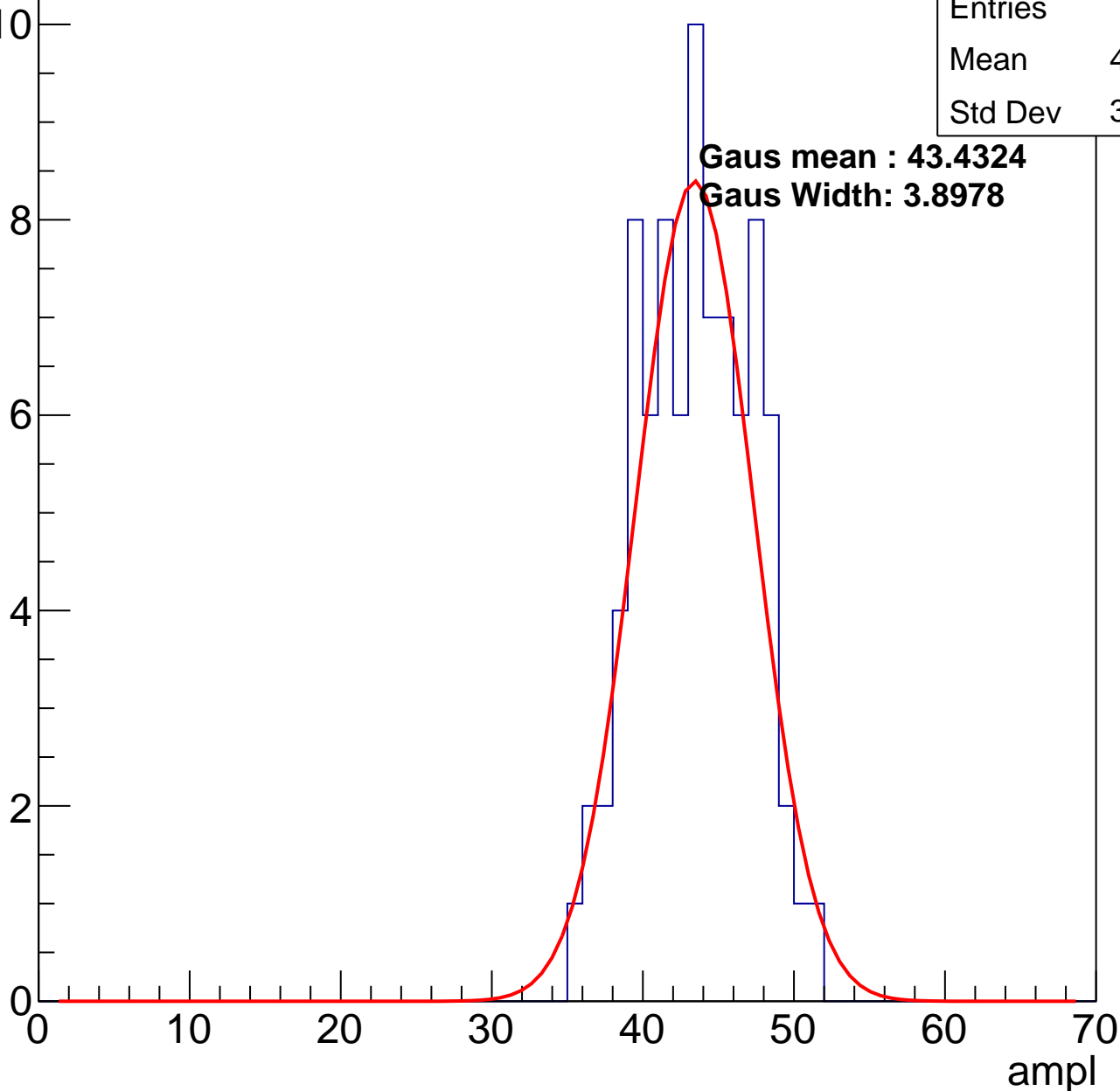
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	85
Mean	43.02
Std Dev	3.607

**Gaus mean : 43.4324**

**Gaus Width: 3.8978**

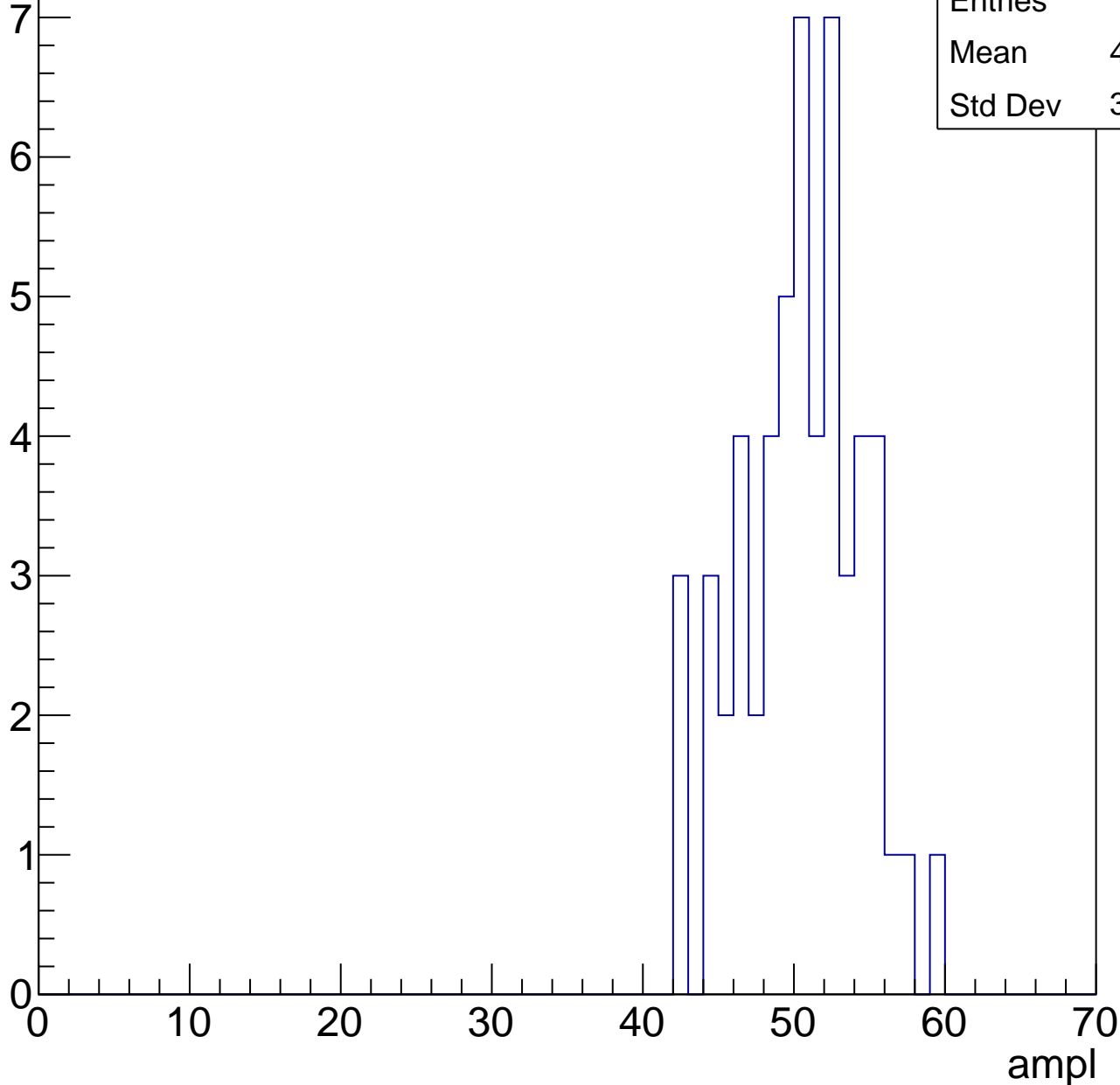


# B0L000S, U25-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	49.96
Std Dev	3.913

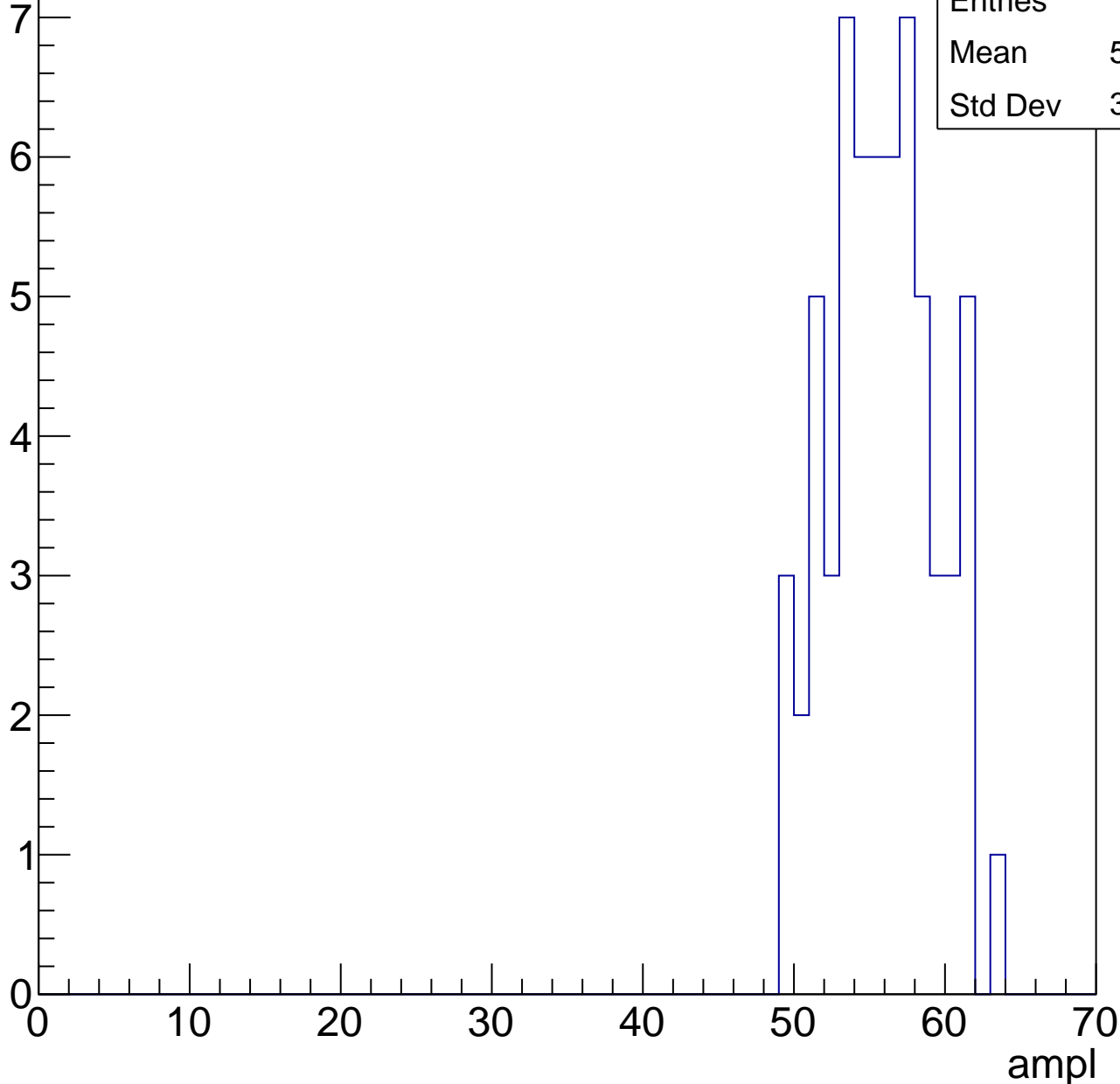


# B0L000S, U25-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.37
Std Dev	3.447

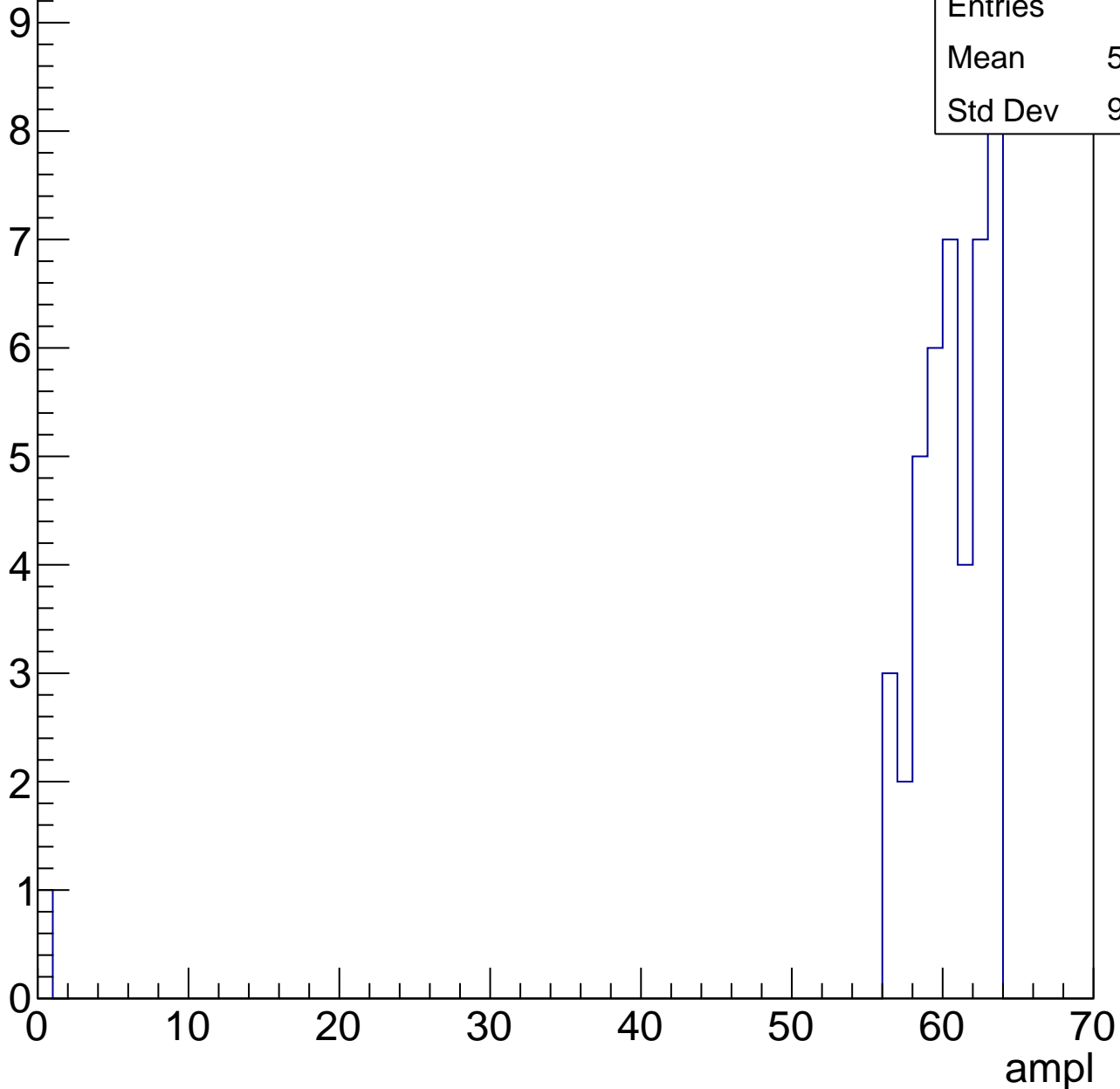


# B0L000S, U25-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.89
Std Dev	9.232



# B0L000S, U25-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U25-ch58, adc0

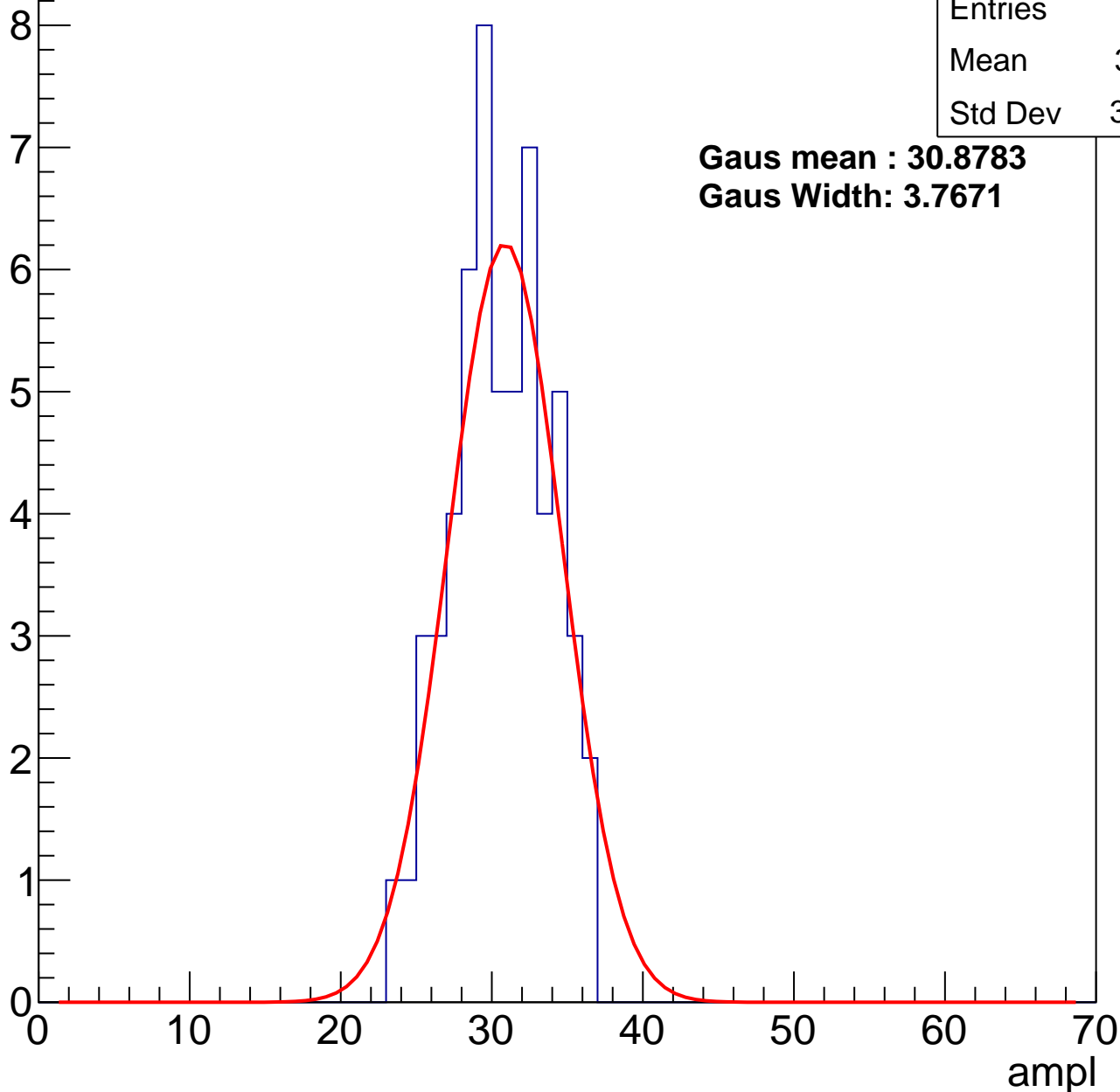
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	30.11
Std Dev	3.166

**Gaus mean : 30.8783**

**Gaus Width: 3.7671**



# B0L000S, U25-ch58, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	92
Mean	37.33
Std Dev	3.839

**Gaus mean : 37.8178**

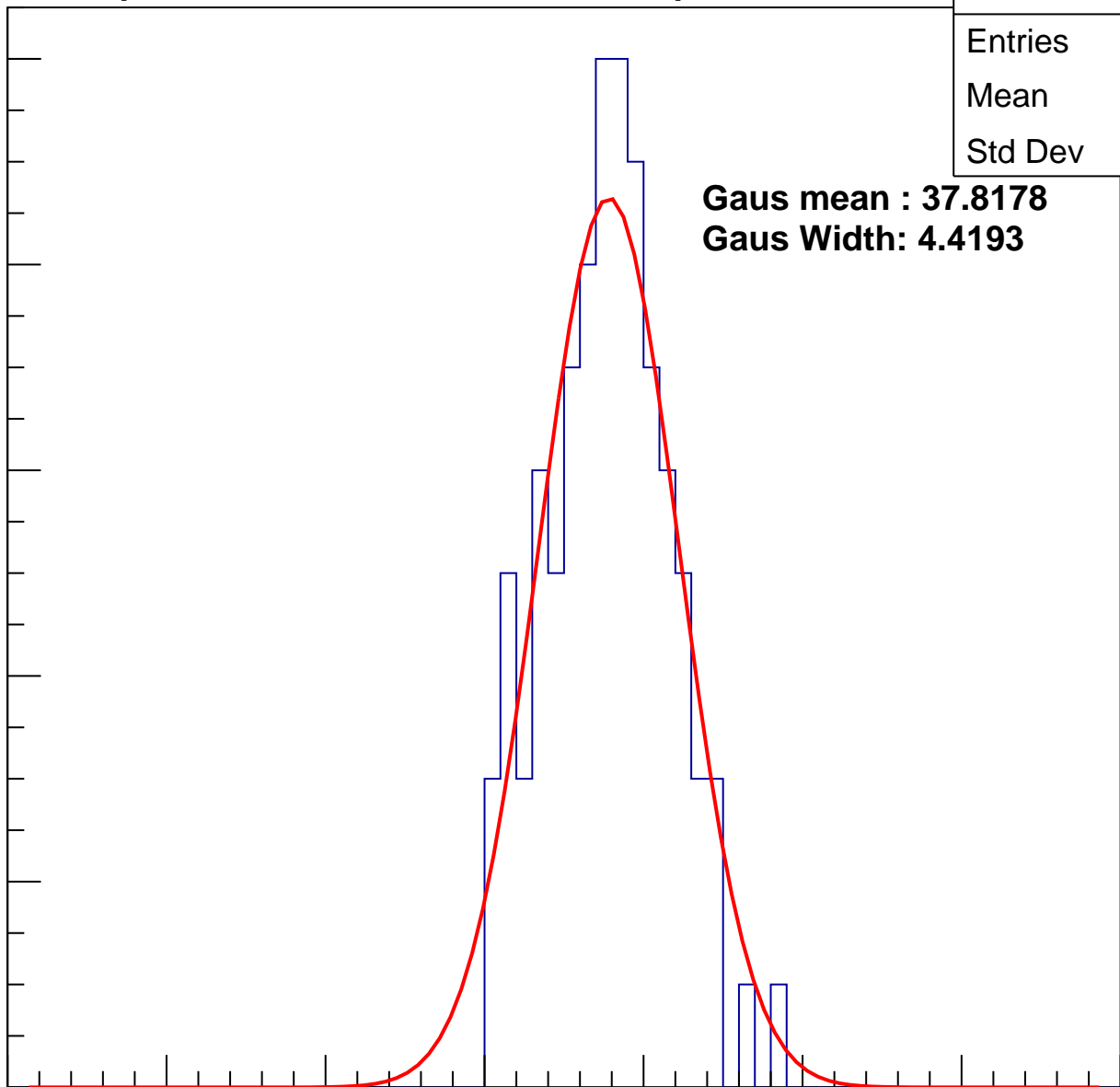
**Gaus Width: 4.4193**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch58, adc2

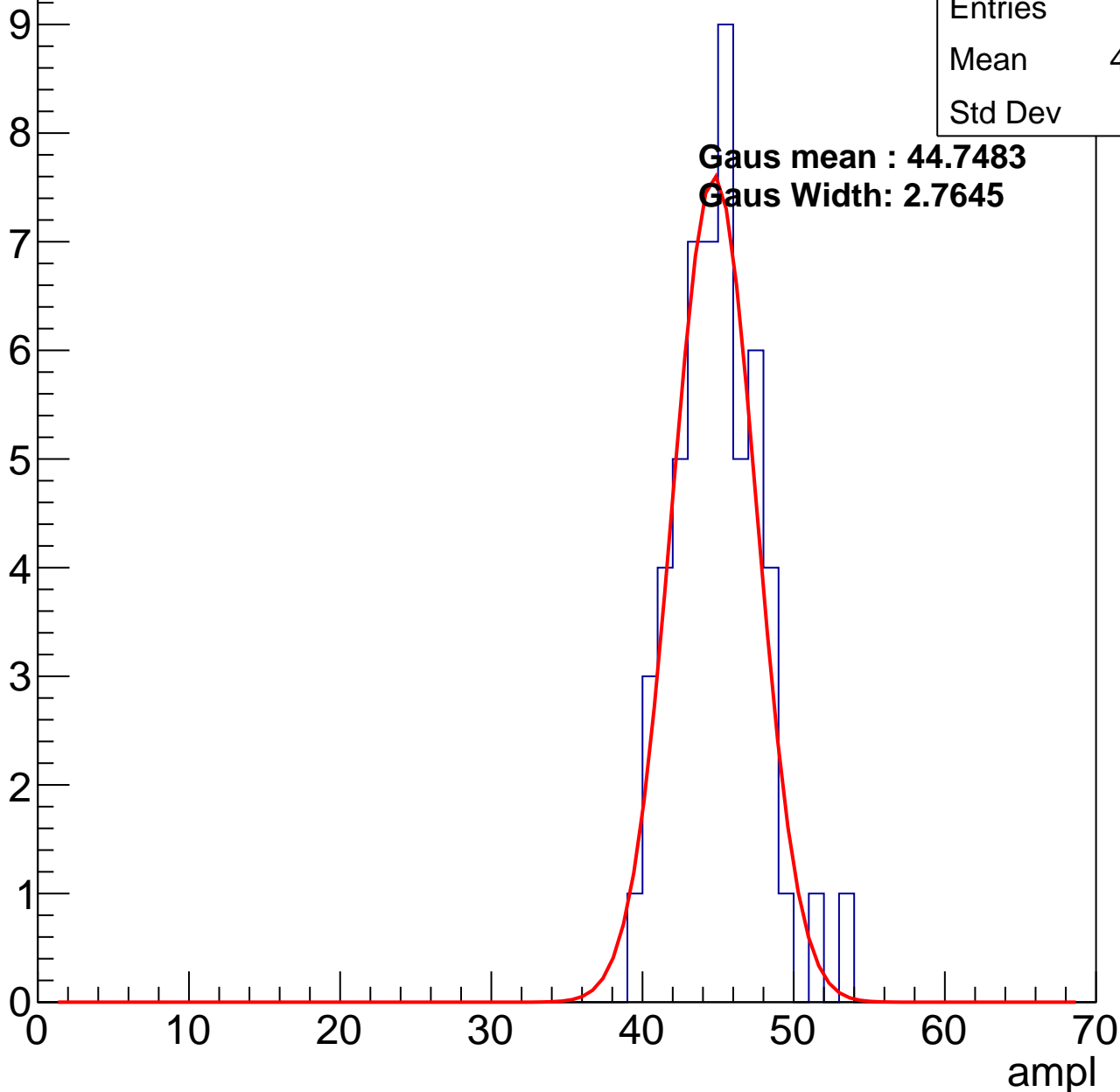
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	44.52
Std Dev	2.8

**Gaus mean : 44.7483**

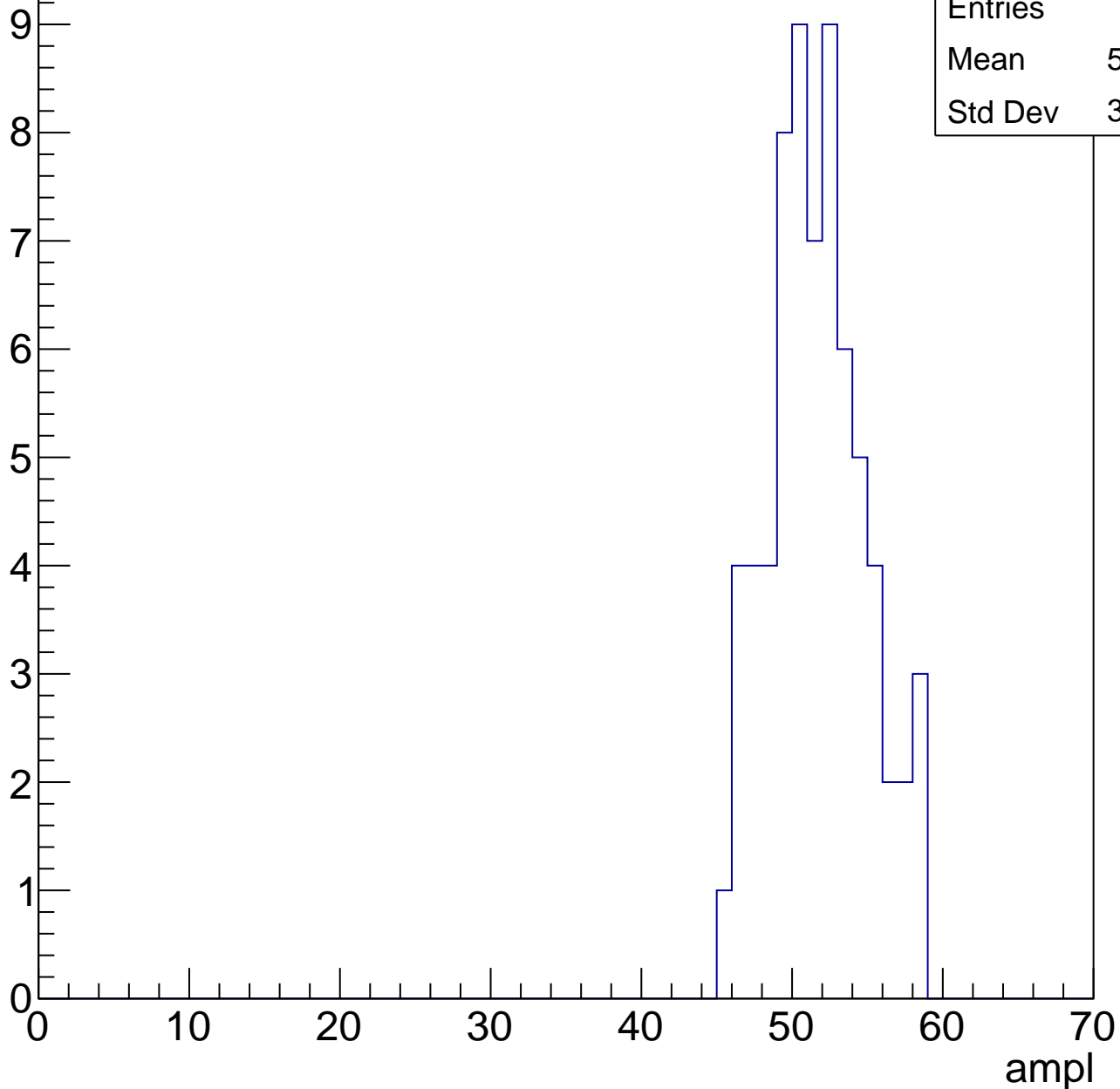
**Gaus Width: 2.7645**



# B0L000S, U25-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



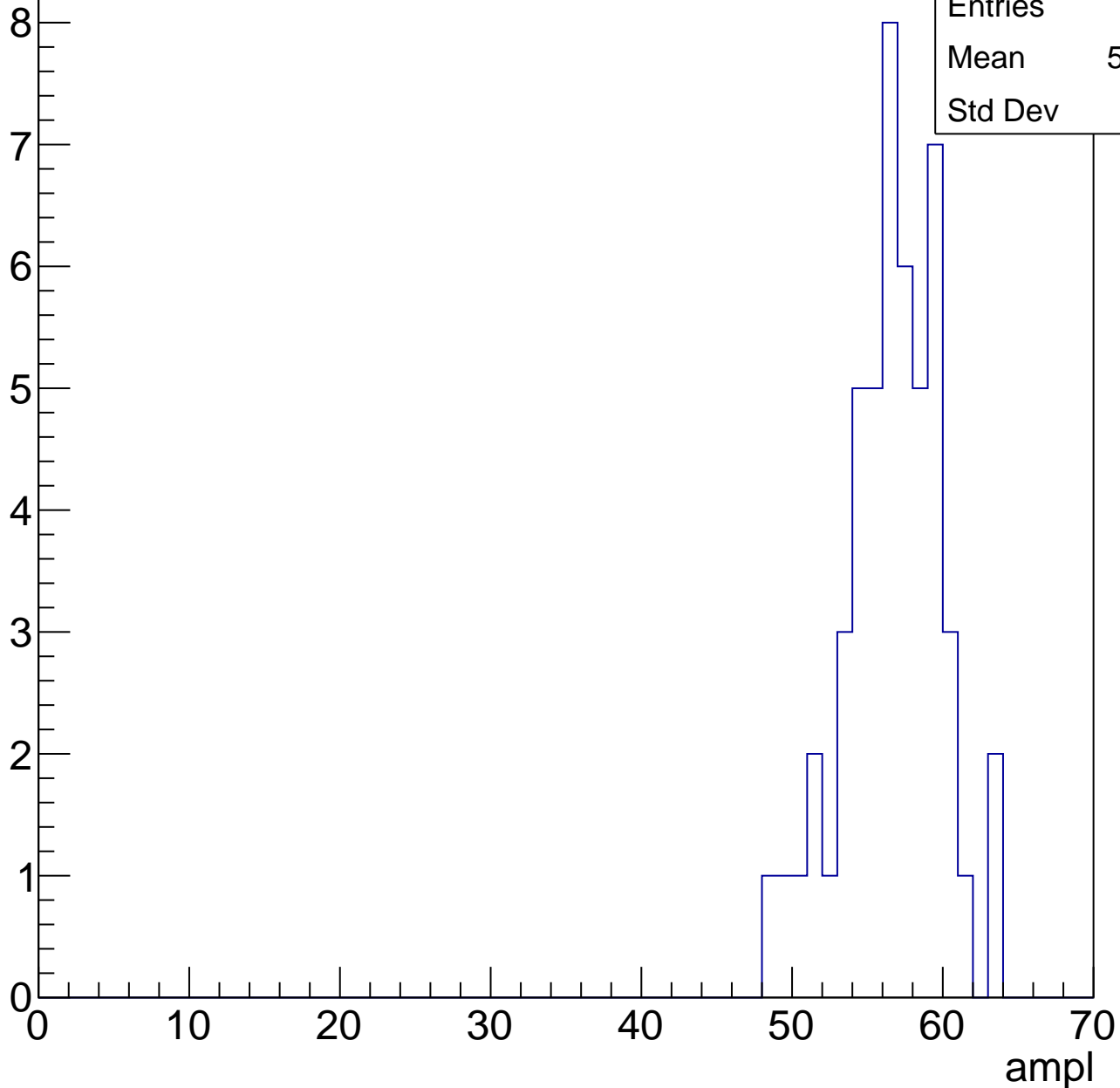
Entries	68
Mean	51.24
Std Dev	3.172

# B0L000S, U25-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	56.18
Std Dev	3.21

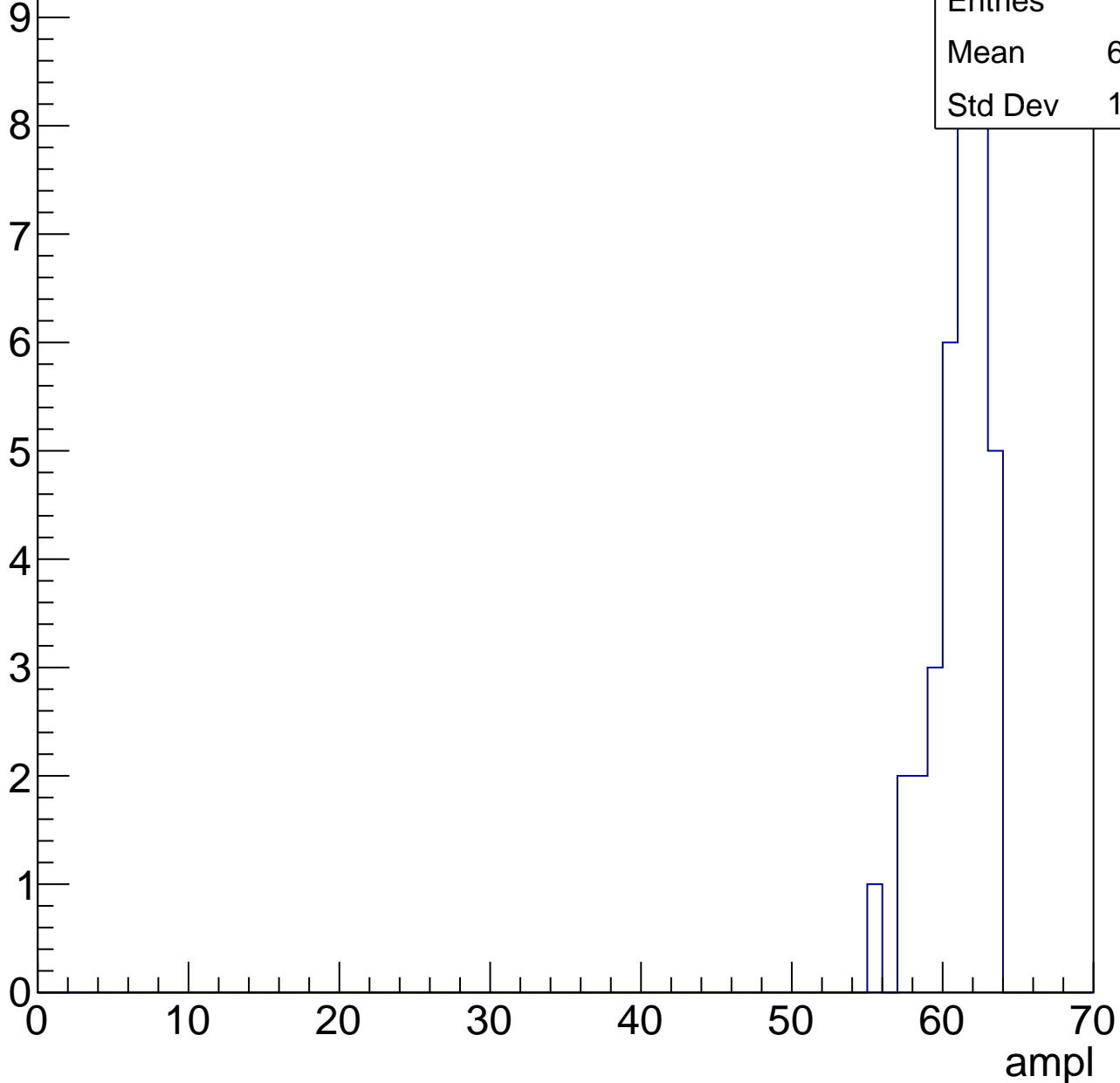


# B0L000S, U25-ch58, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

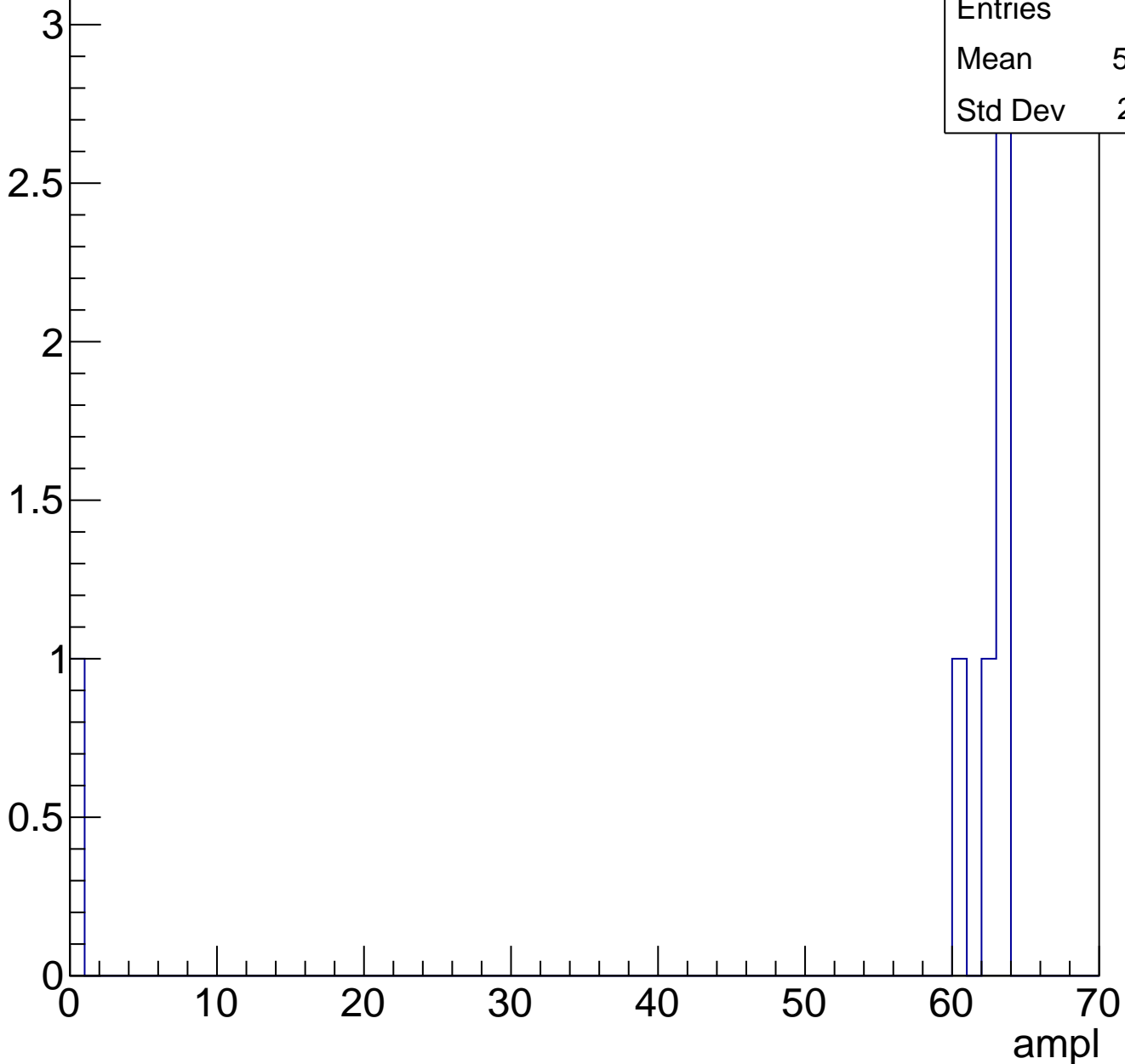
Entries	37
Mean	60.65
Std Dev	1.863



# B0L000S, U25-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	23
Std Dev	0

# B0L000S, U25-ch59, adc0

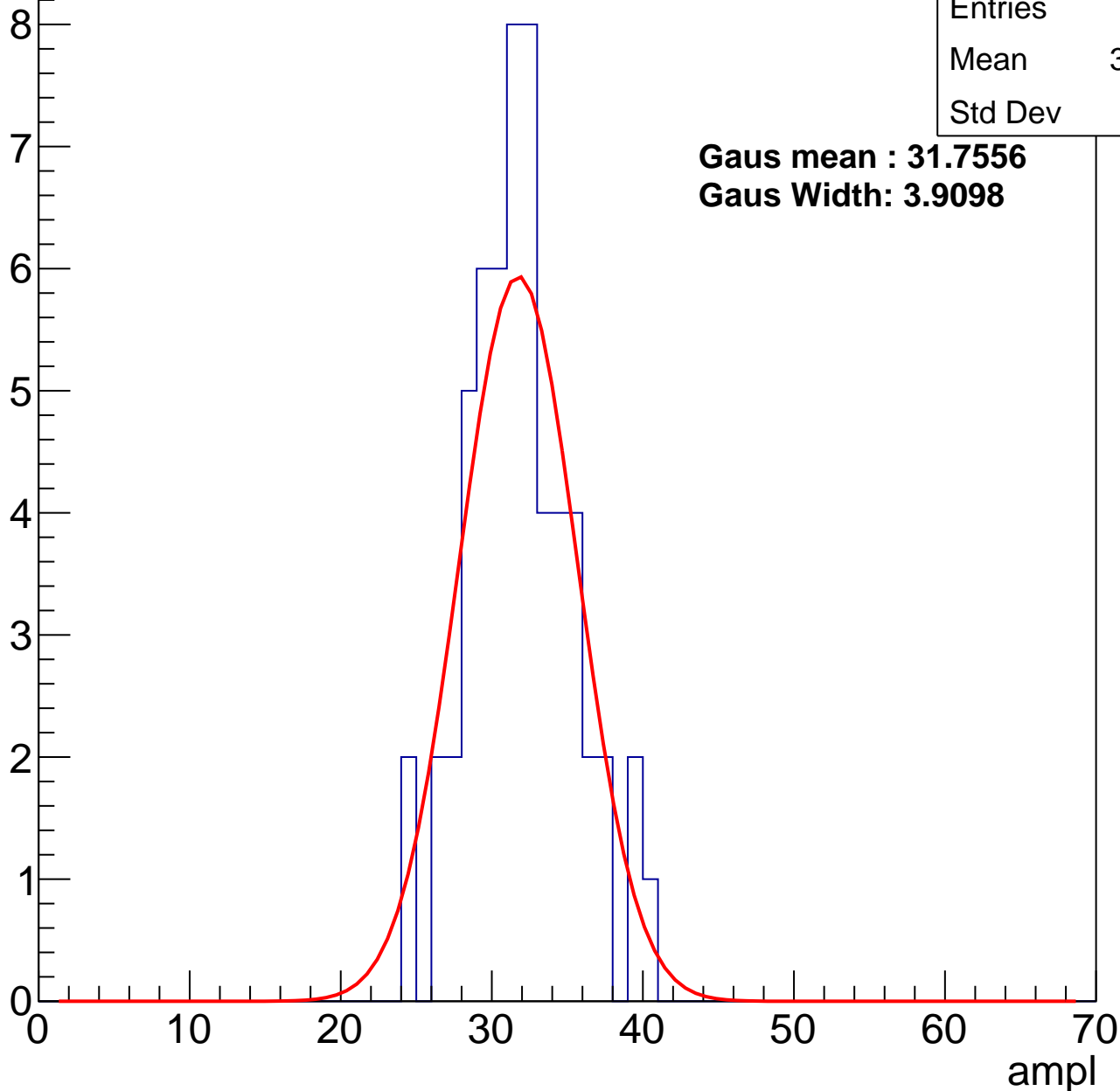
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	31.45
Std Dev	3.47

**Gaus mean : 31.7556**

**Gaus Width: 3.9098**



# B0L000S, U25-ch59, adc1

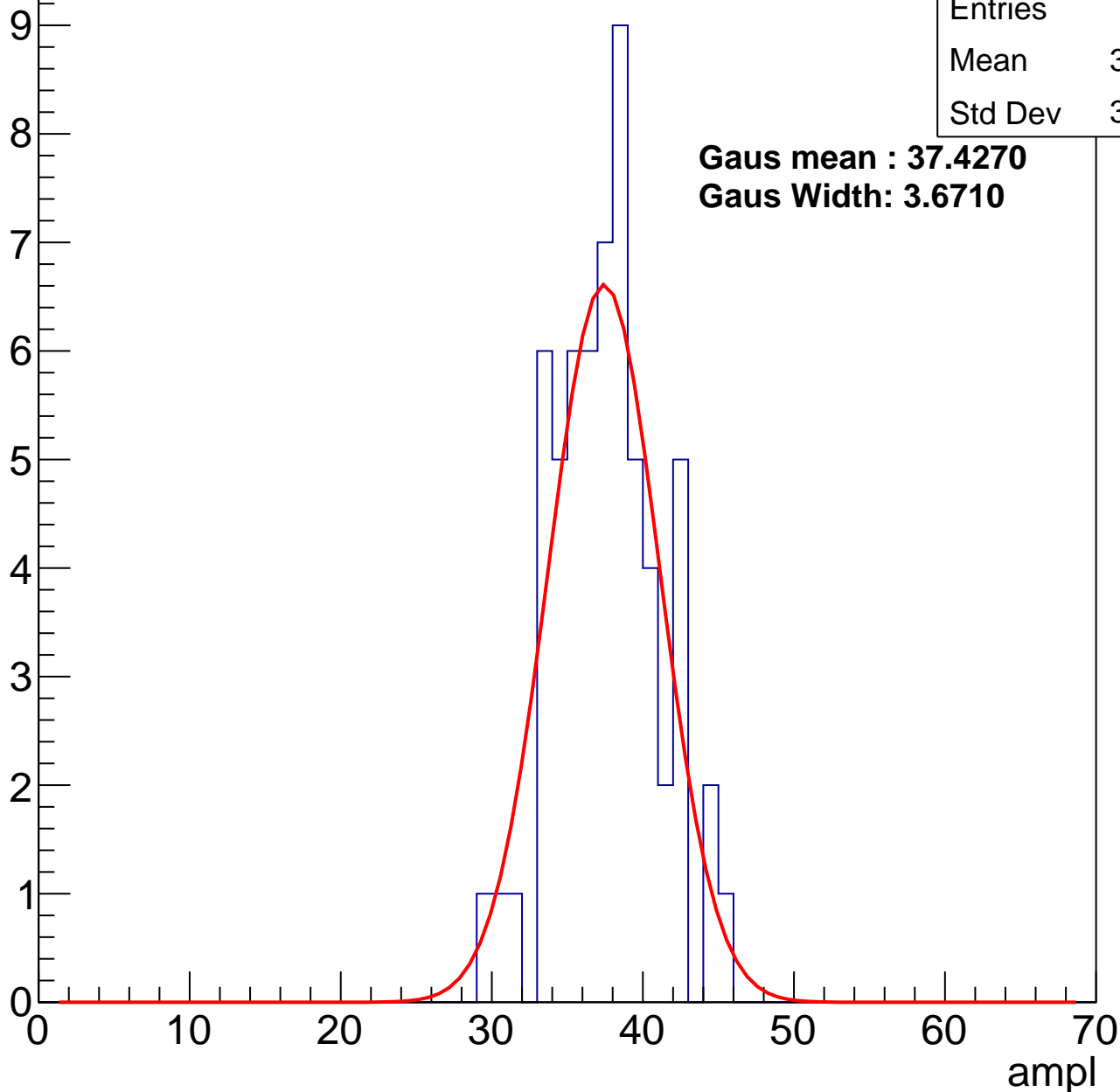
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.13
Std Dev	3.395

**Gaus mean : 37.4270**

**Gaus Width: 3.6710**



# B0L000S, U25-ch59, adc2

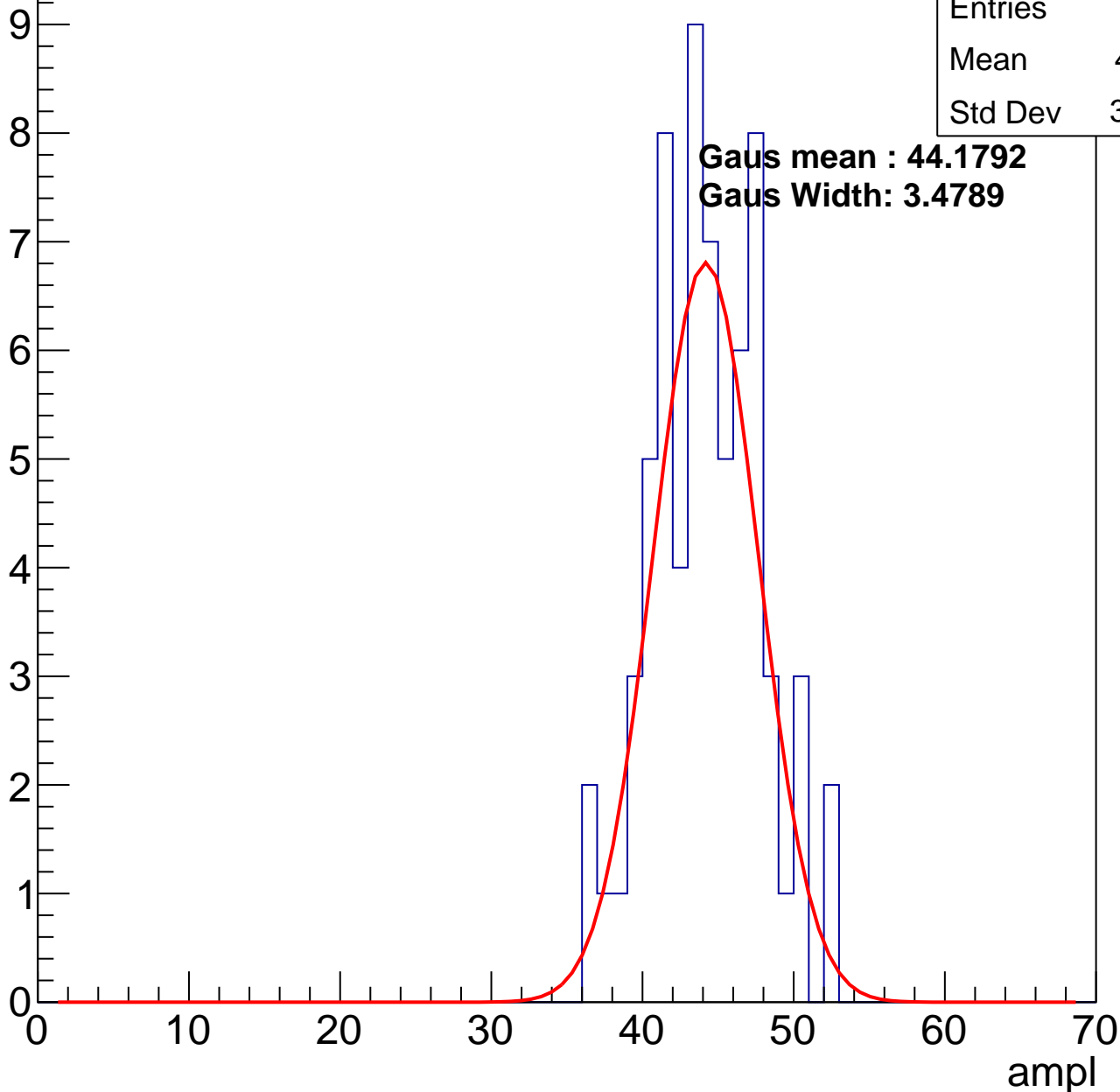
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	43.81
Std Dev	3.582

**Gaus mean : 44.1792**

**Gaus Width: 3.4789**



# B0L000S, U25-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

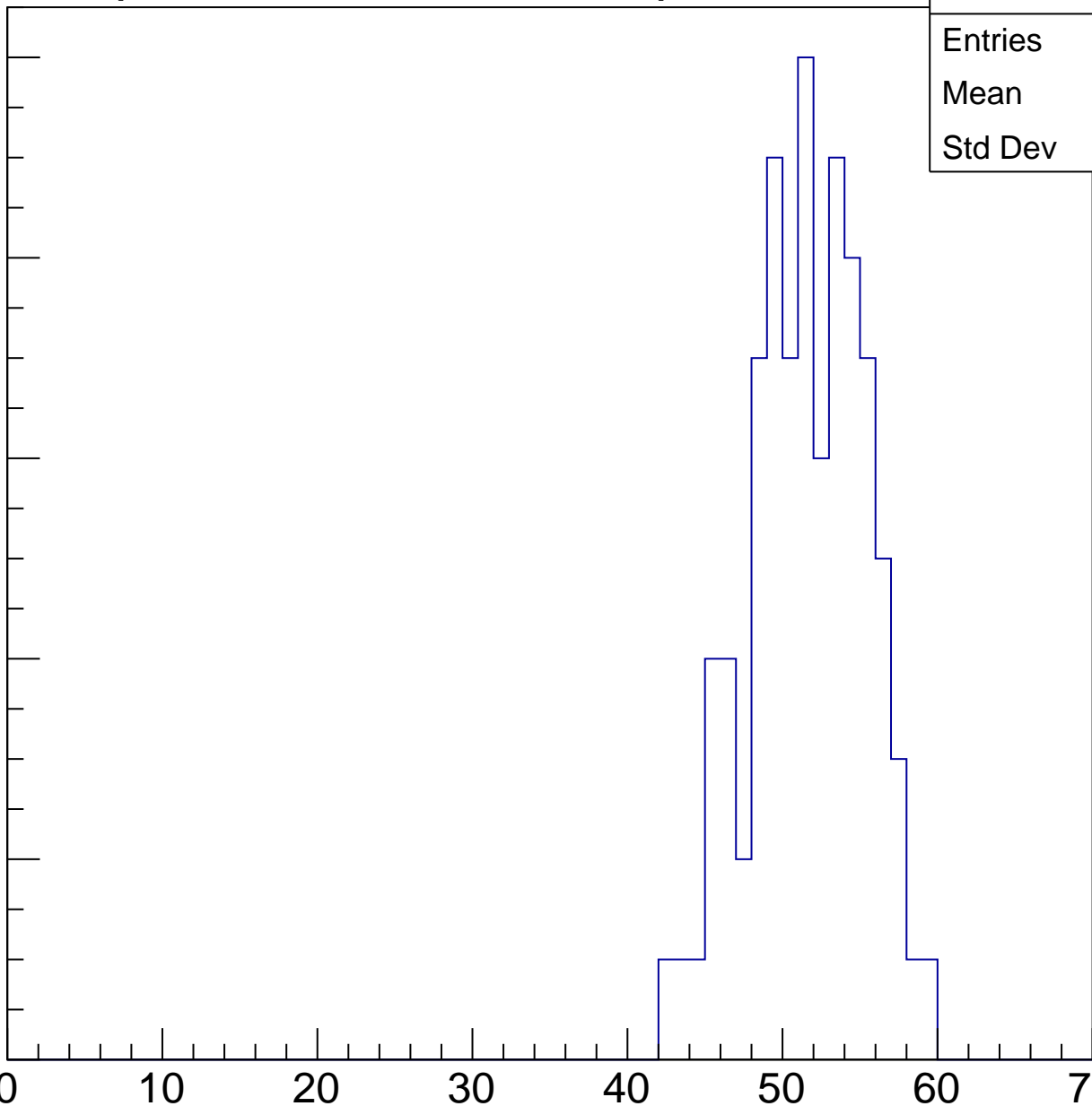
Entries	86
Mean	51.14
Std Dev	3.651

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

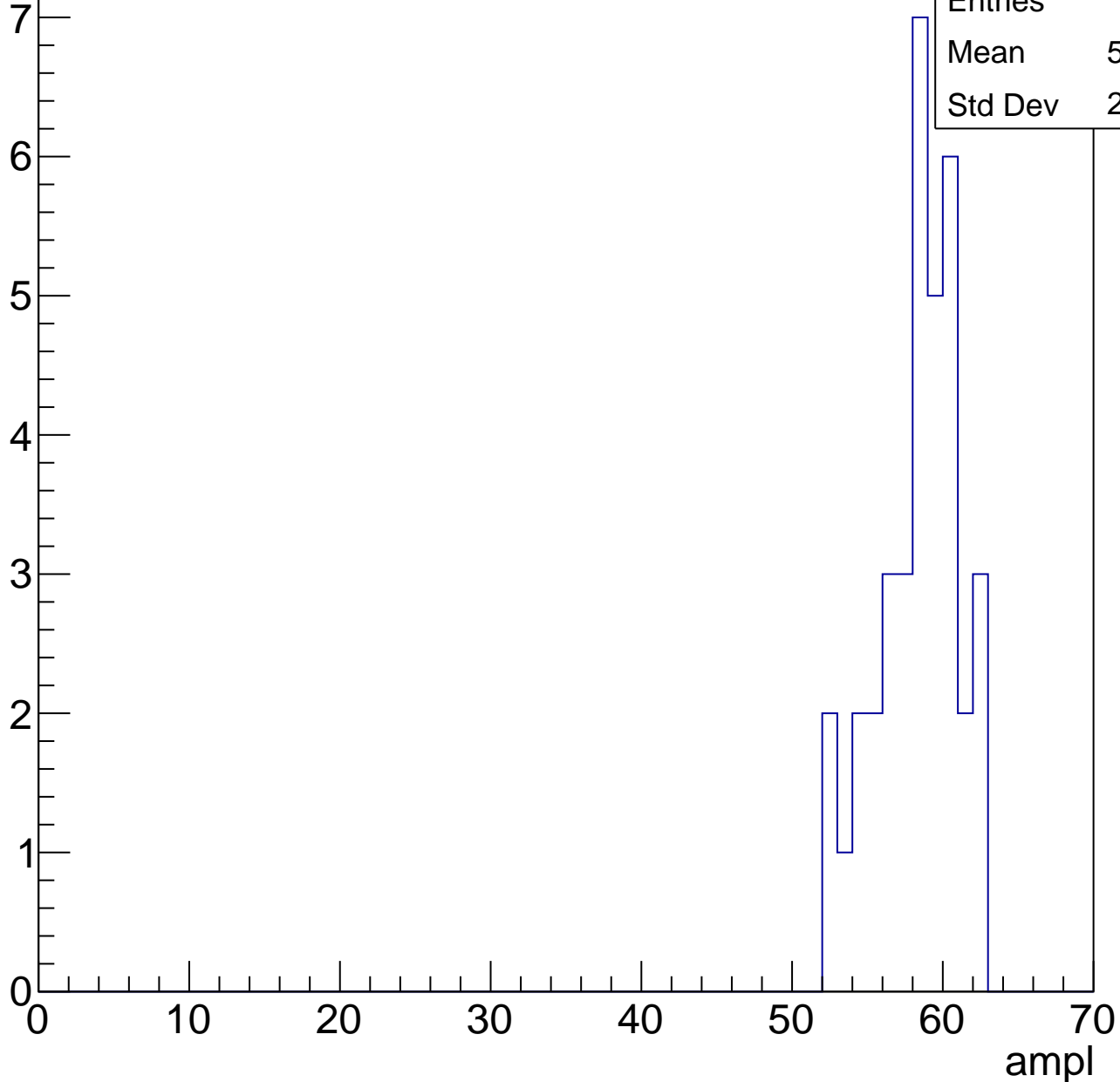
ampl



# B0L000S, U25-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



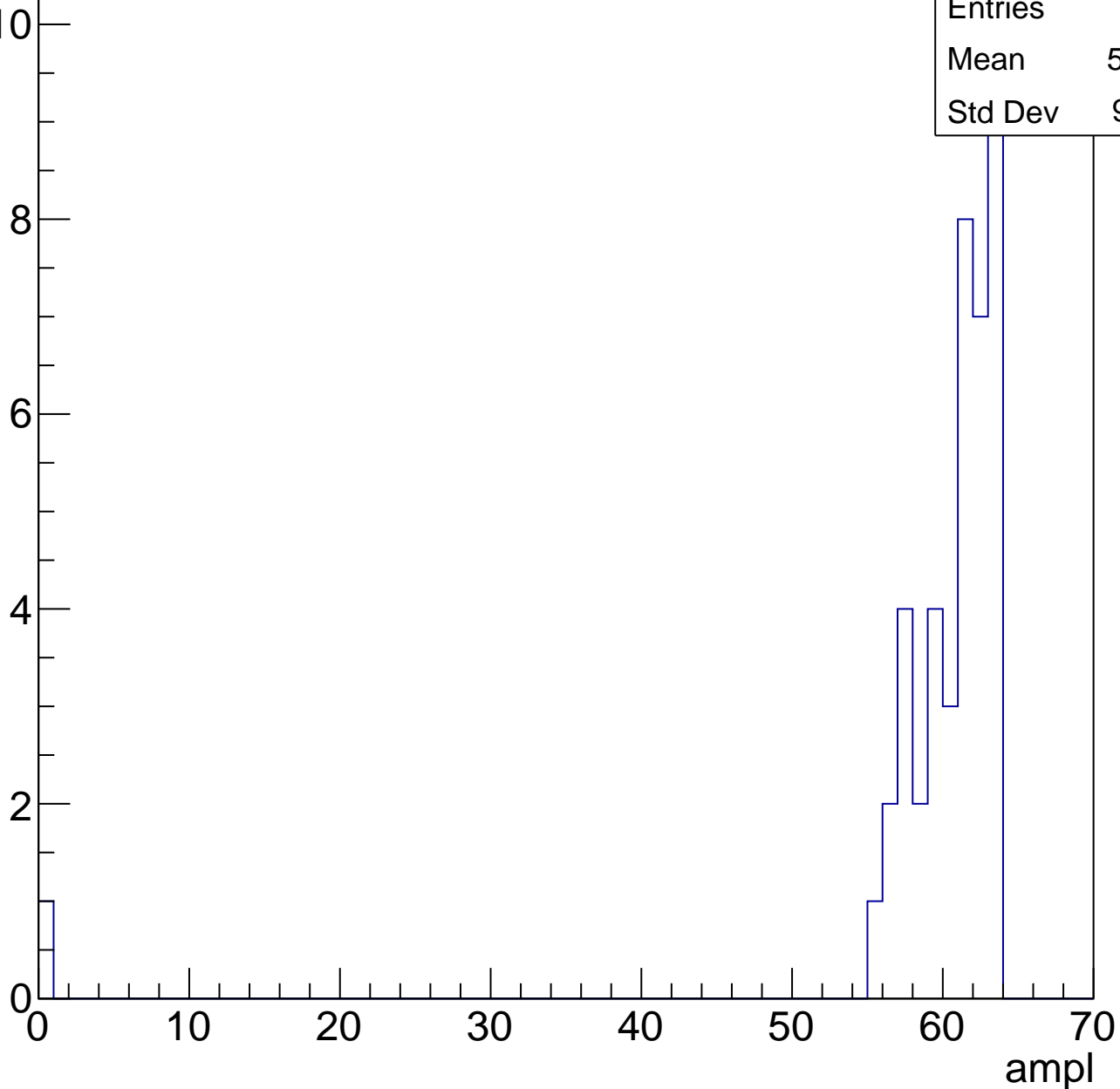
Entries	36
Mean	57.86
Std Dev	2.668

# B0L000S, U25-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	59.02
Std Dev	9.501



# B0L000S, U25-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch60, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	29.61
Std Dev	3.468

**Gaus mean : 30.4034**

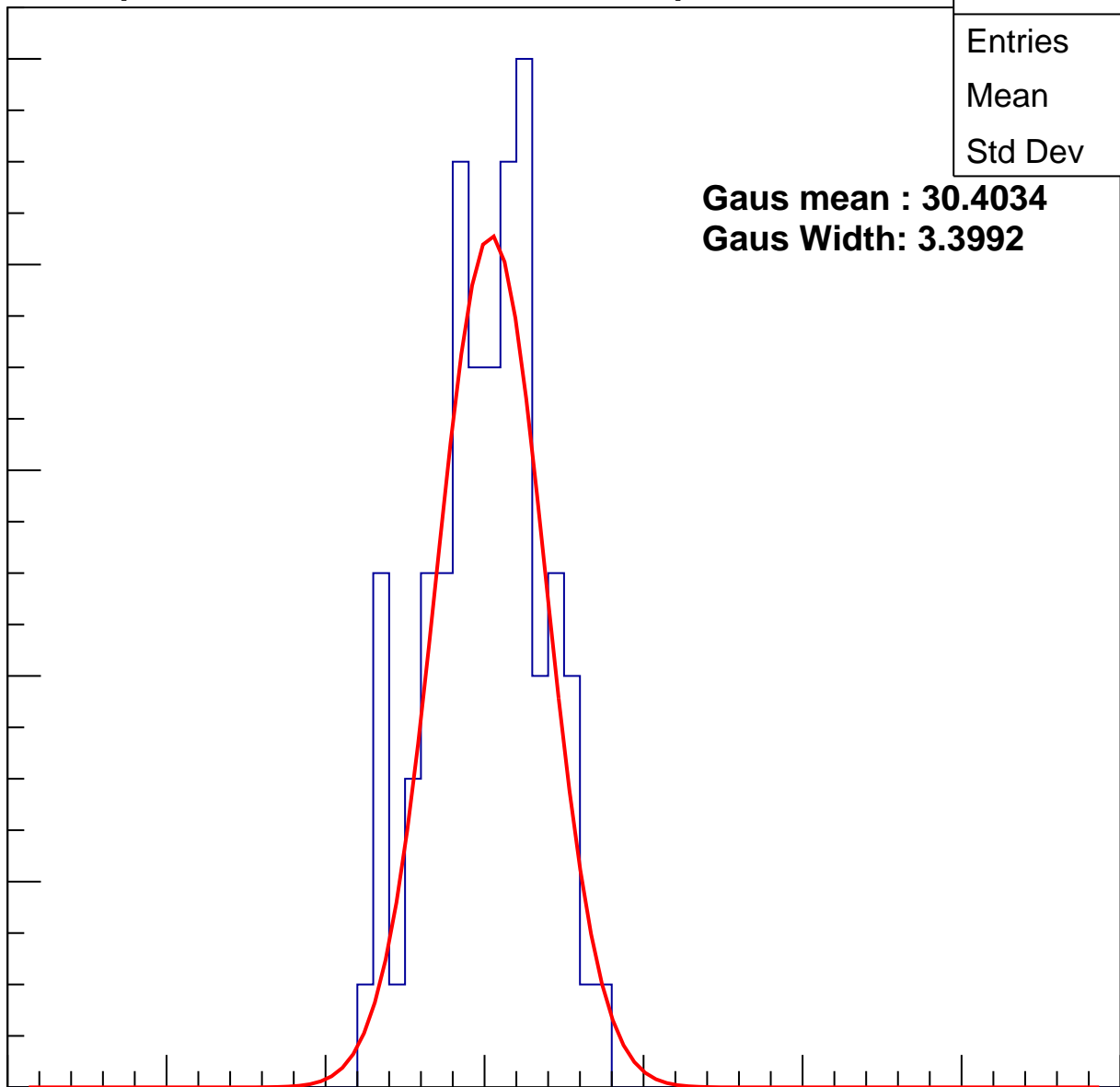
**Gaus Width: 3.3992**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch60, adc1

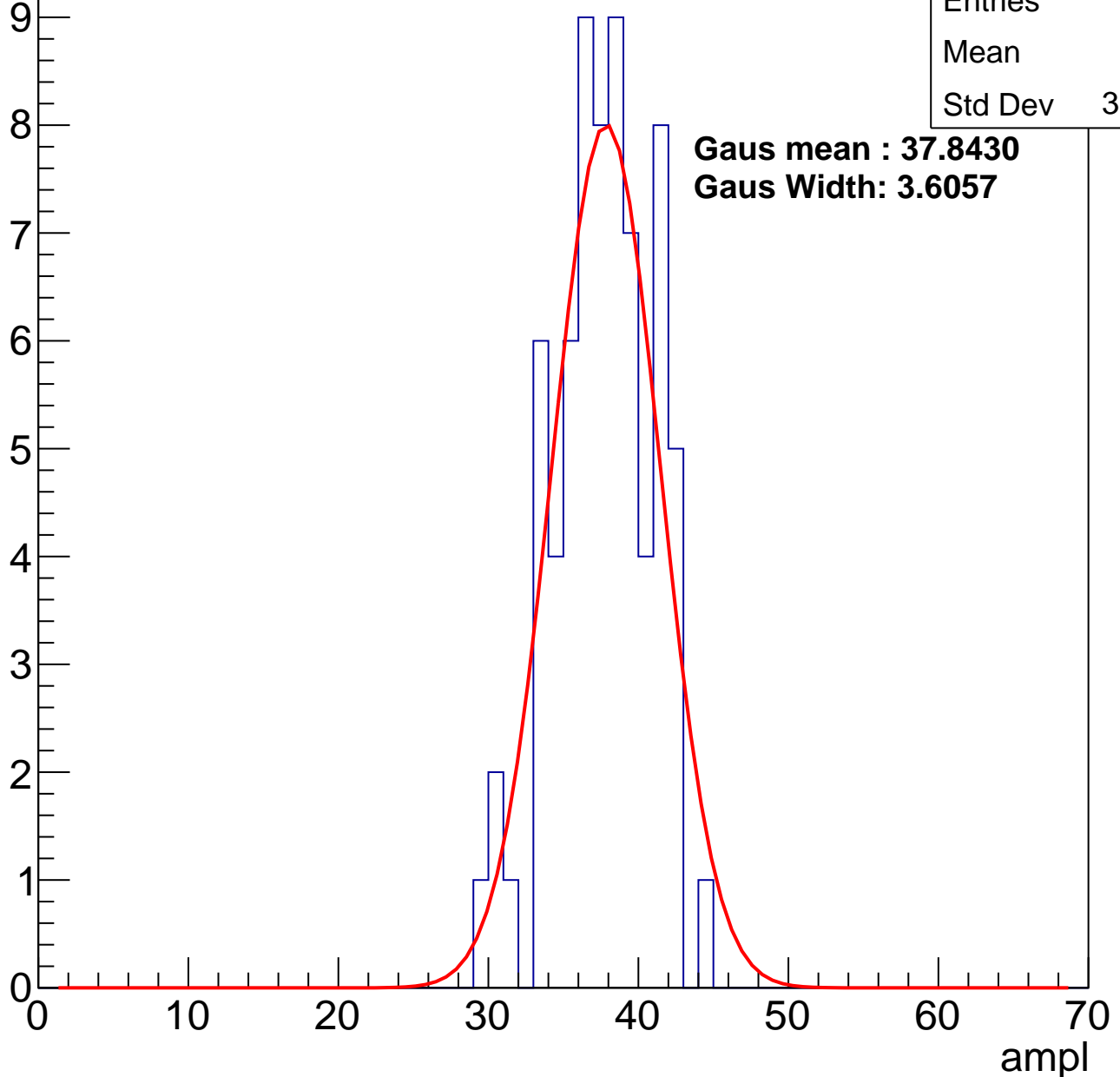
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	37.2
Std Dev	3.218

**Gaus mean : 37.8430**

**Gaus Width: 3.6057**



# B0L000S, U25-ch60, adc2

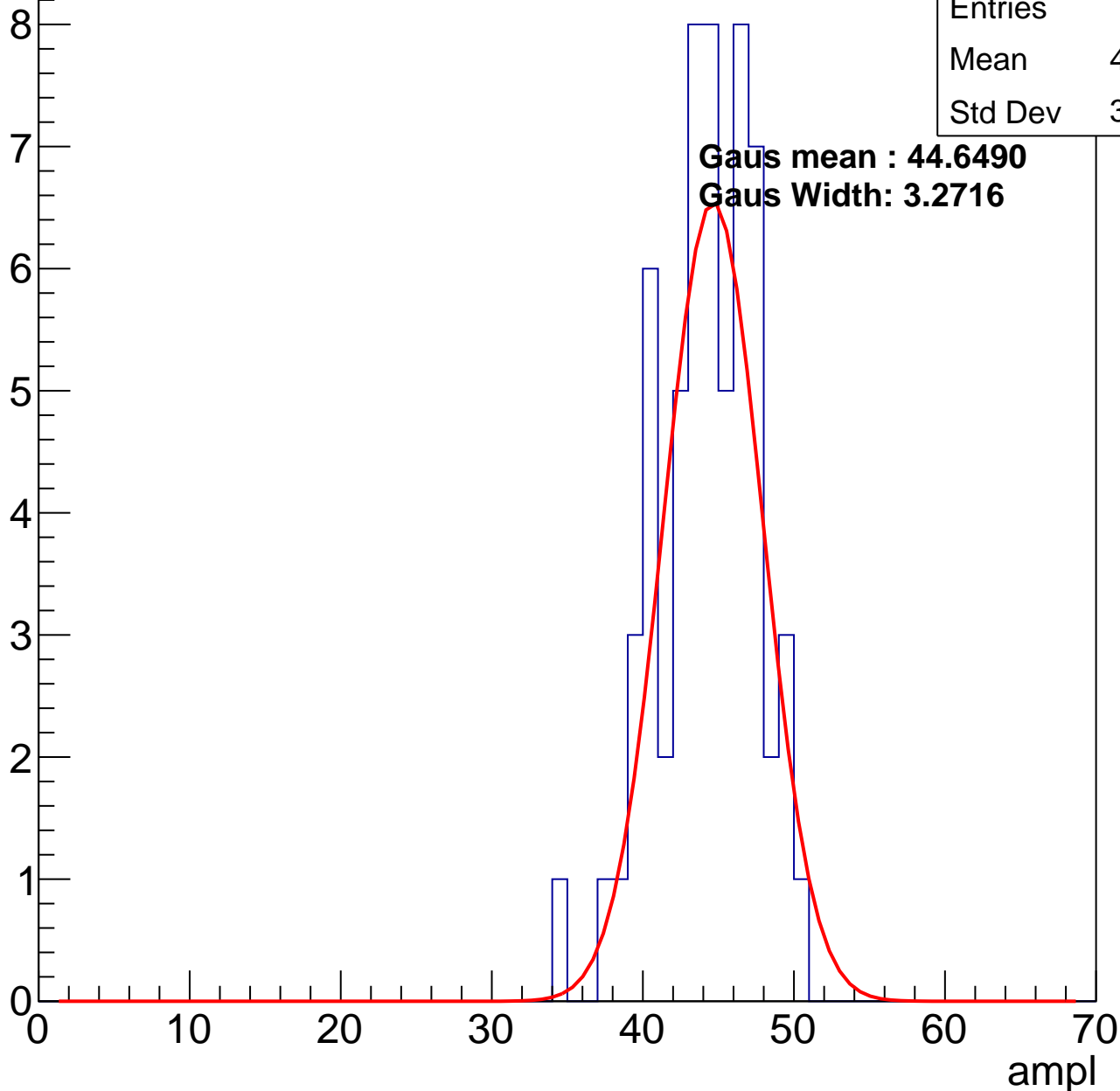
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	43.75
Std Dev	3.242

**Gaus mean : 44.6490**

**Gaus Width: 3.2716**

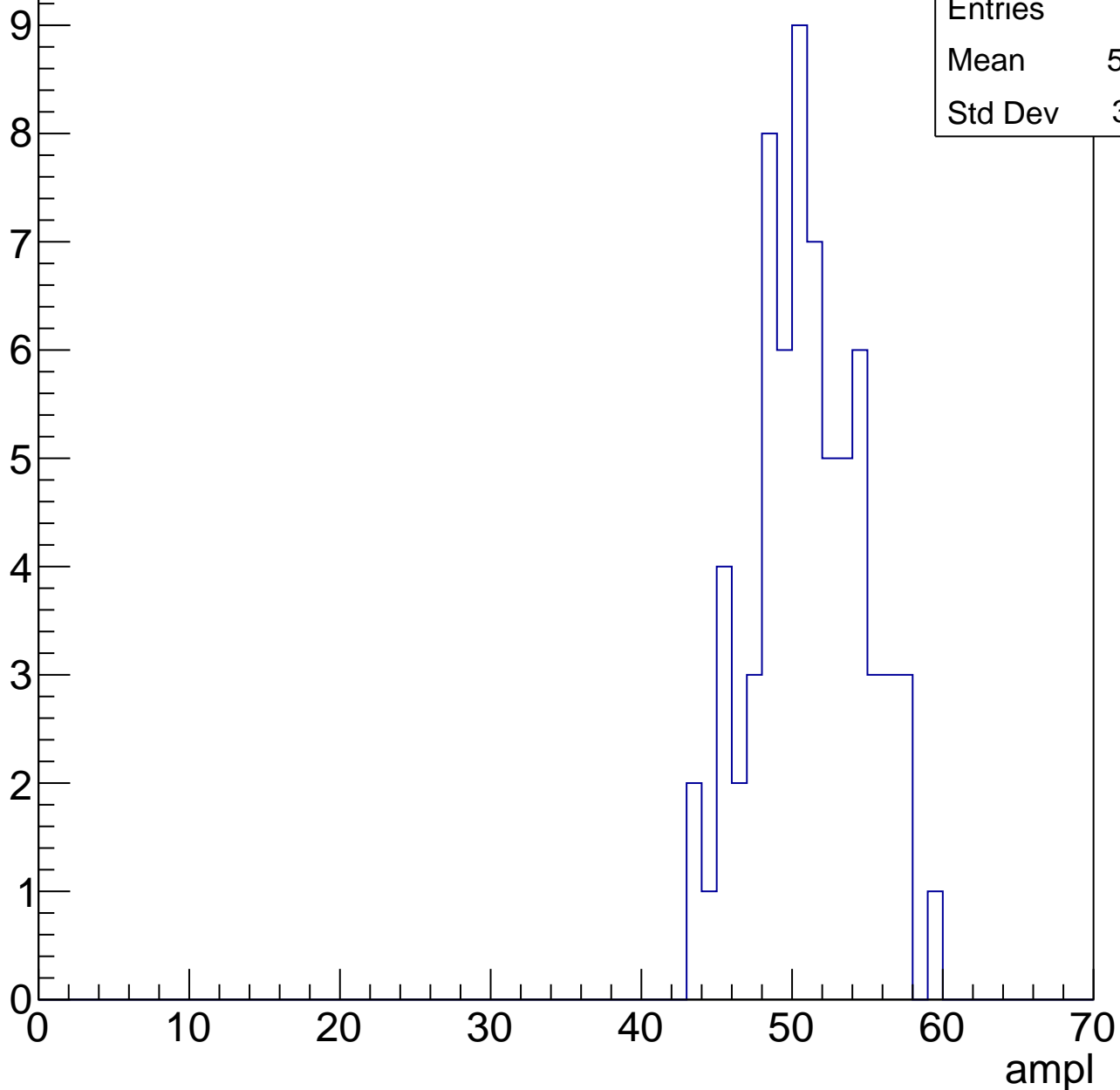


# B0L000S, U25-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	50.59
Std Dev	3.611

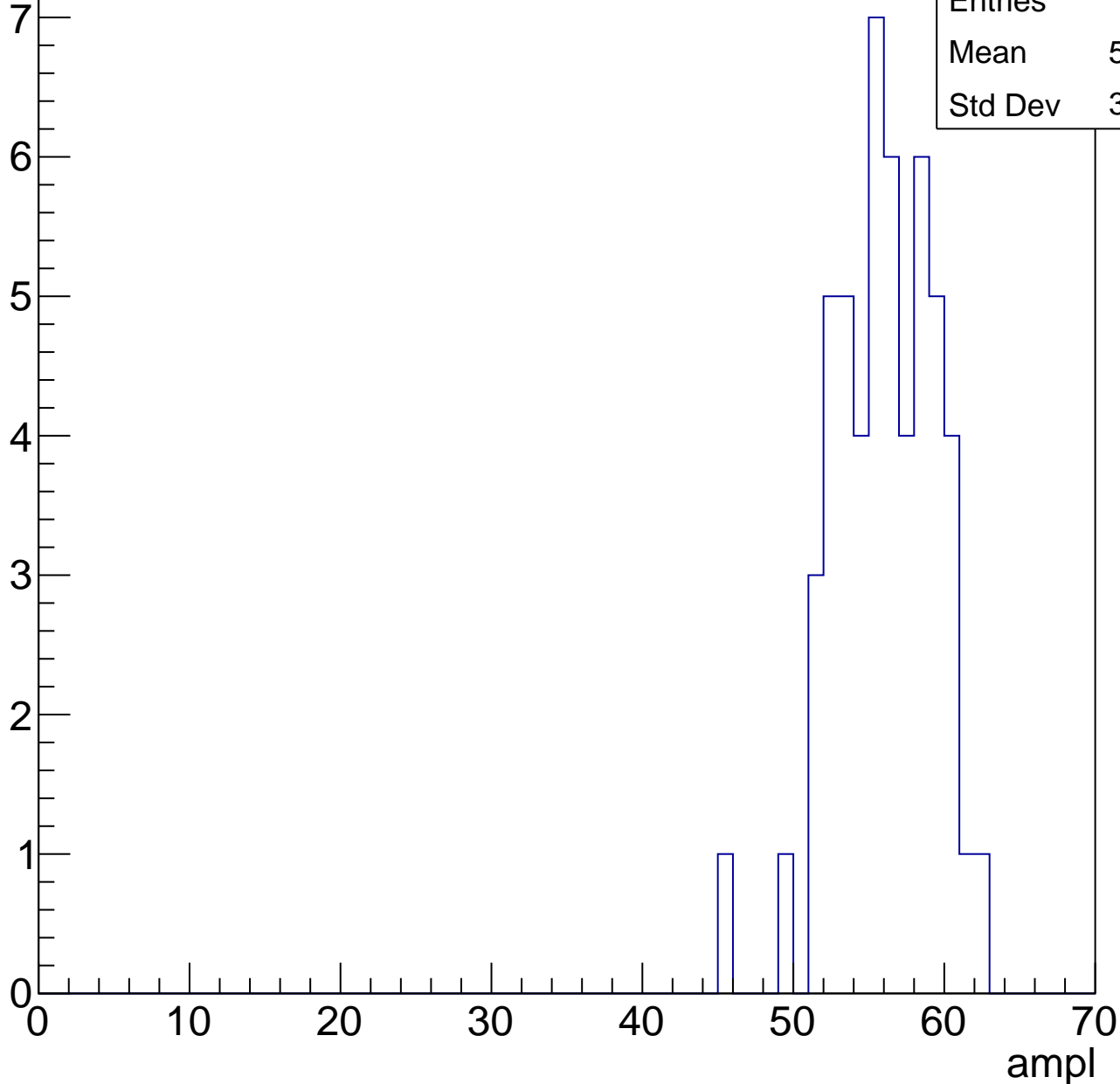


# B0L000S, U25-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	55.53
Std Dev	3.306

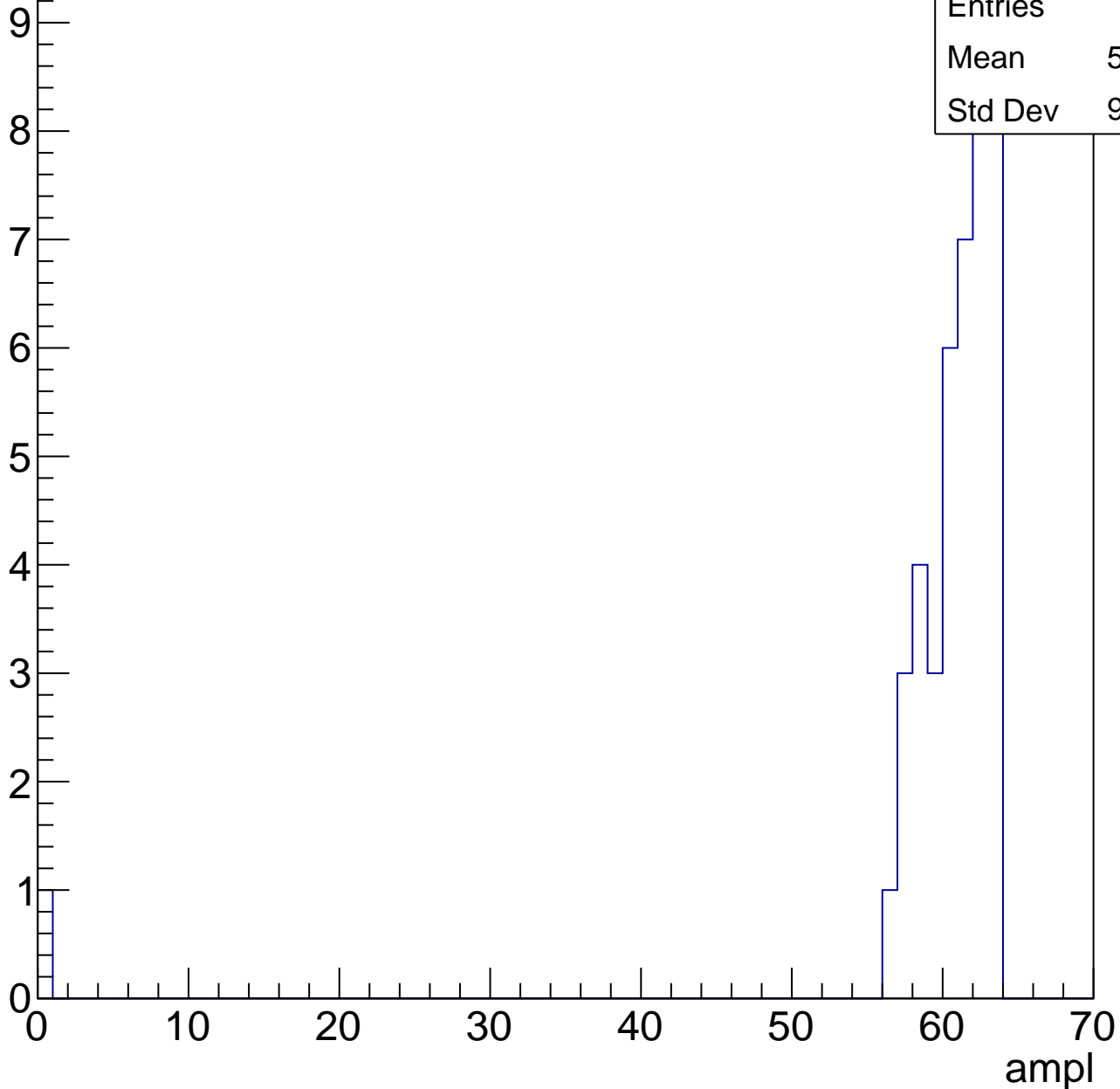


# B0L000S, U25-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

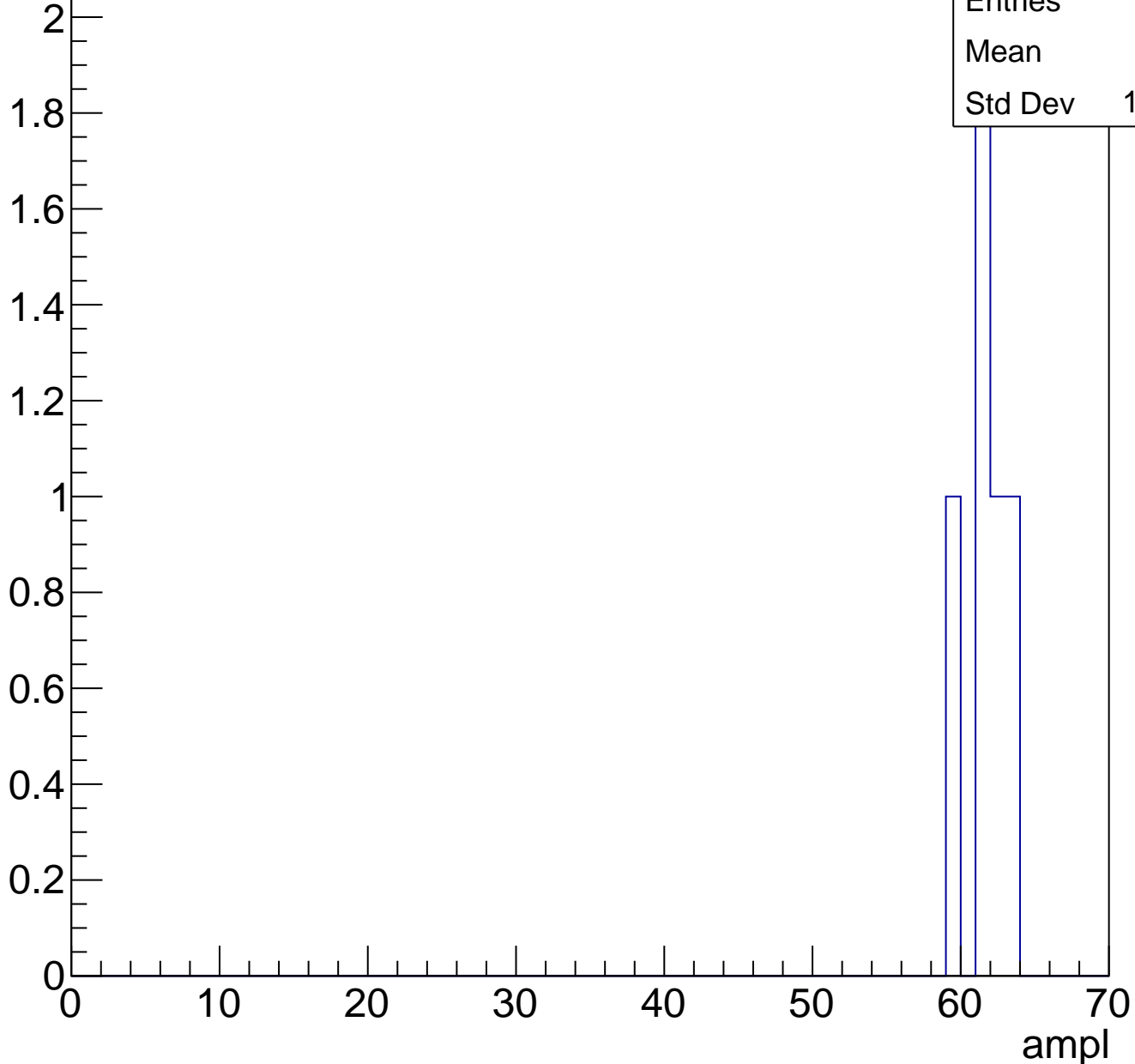
Entries	42
Mean	59.19
Std Dev	9.455



# B0L000S, U25-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	10
Std Dev	10

# B0L000S, U25-ch61, adc0

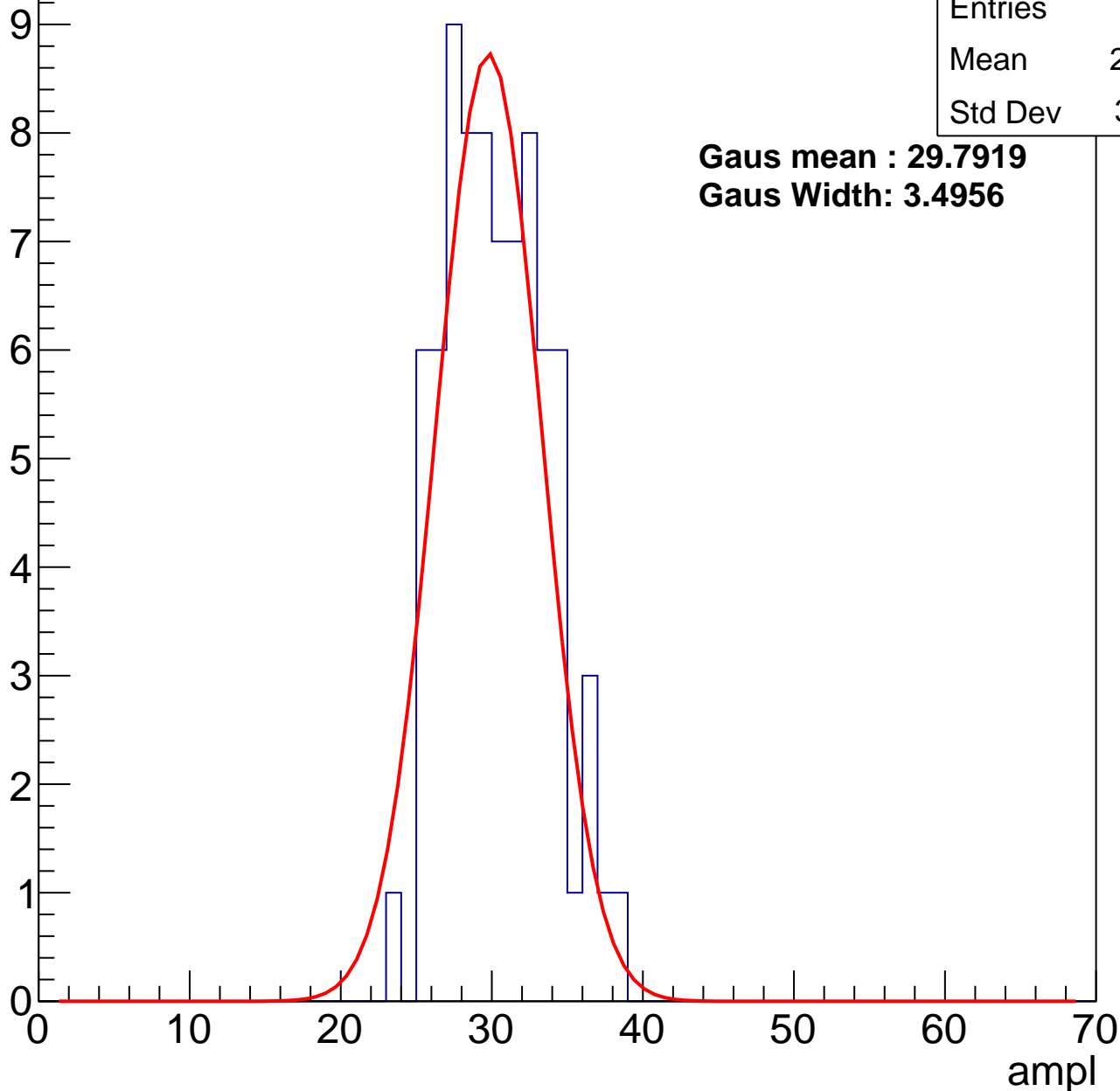
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	29.88
Std Dev	3.301

**Gaus mean : 29.7919**

**Gaus Width: 3.4956**



# B0L000S, U25-ch61, adc1

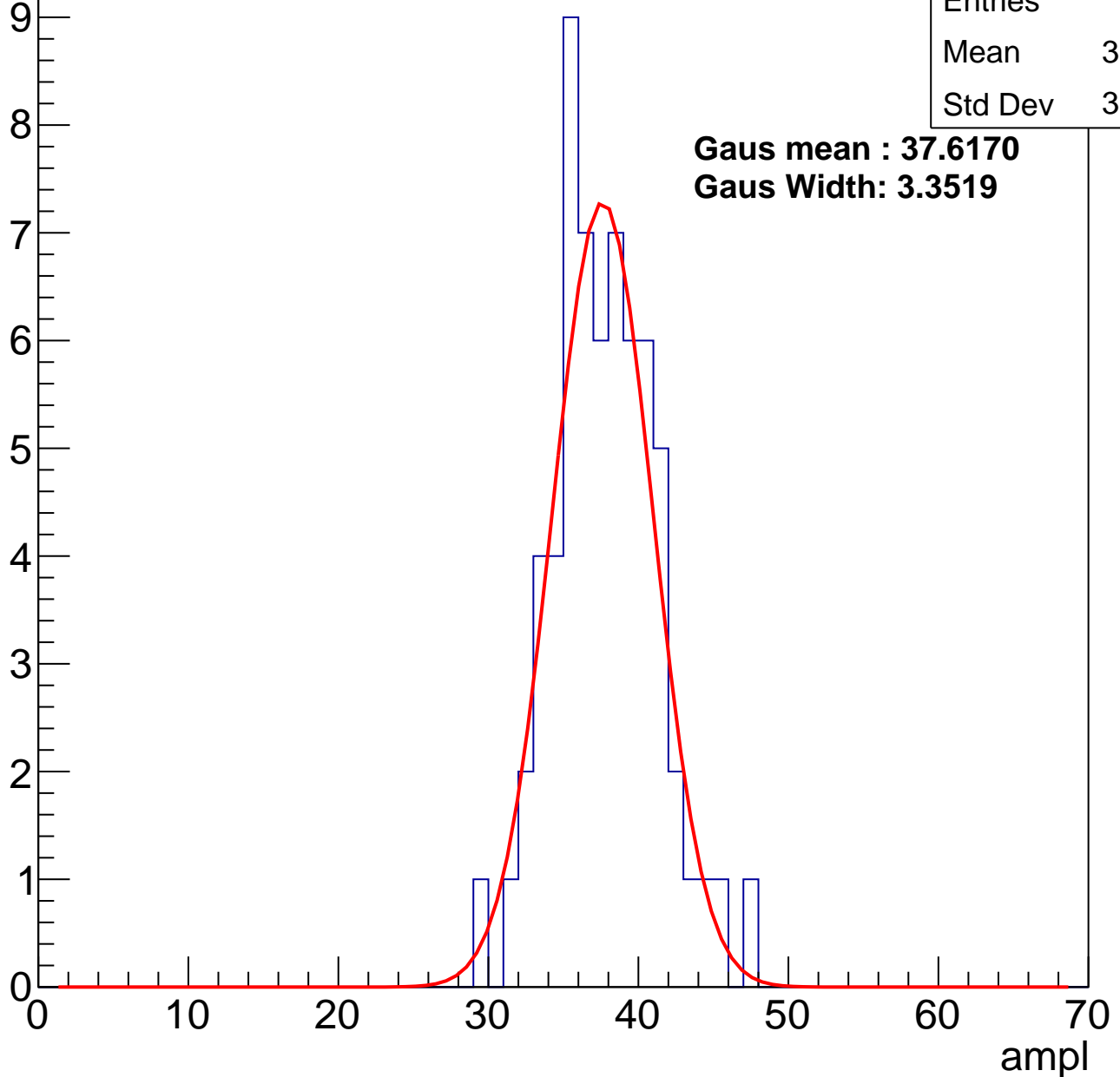
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	37.33
Std Dev	3.424

**Gaus mean : 37.6170**

**Gaus Width: 3.3519**



# B0L000S, U25-ch61, adc2

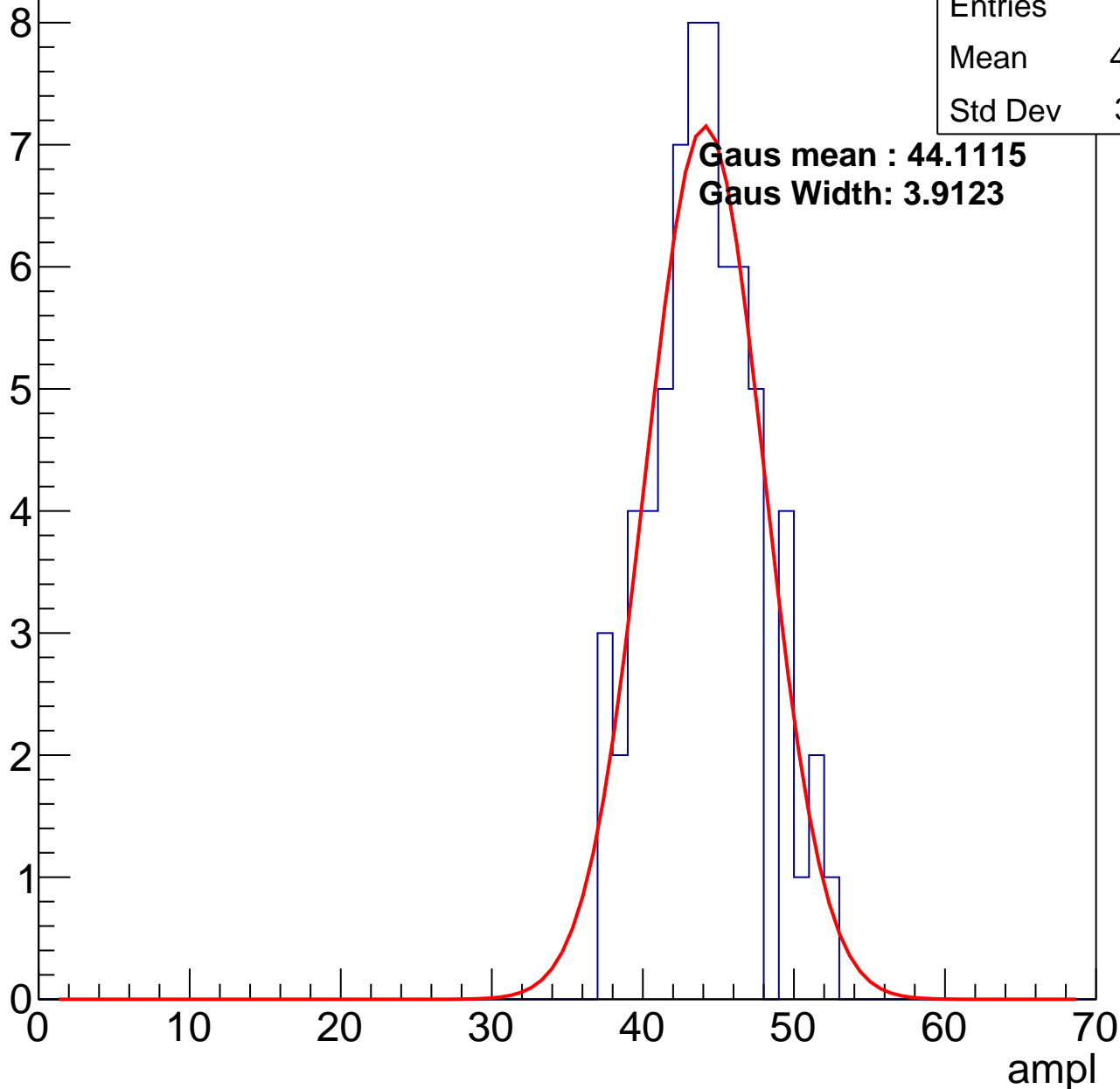
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	43.62
Std Dev	3.541

**Gaus mean : 44.1115**

**Gaus Width: 3.9123**

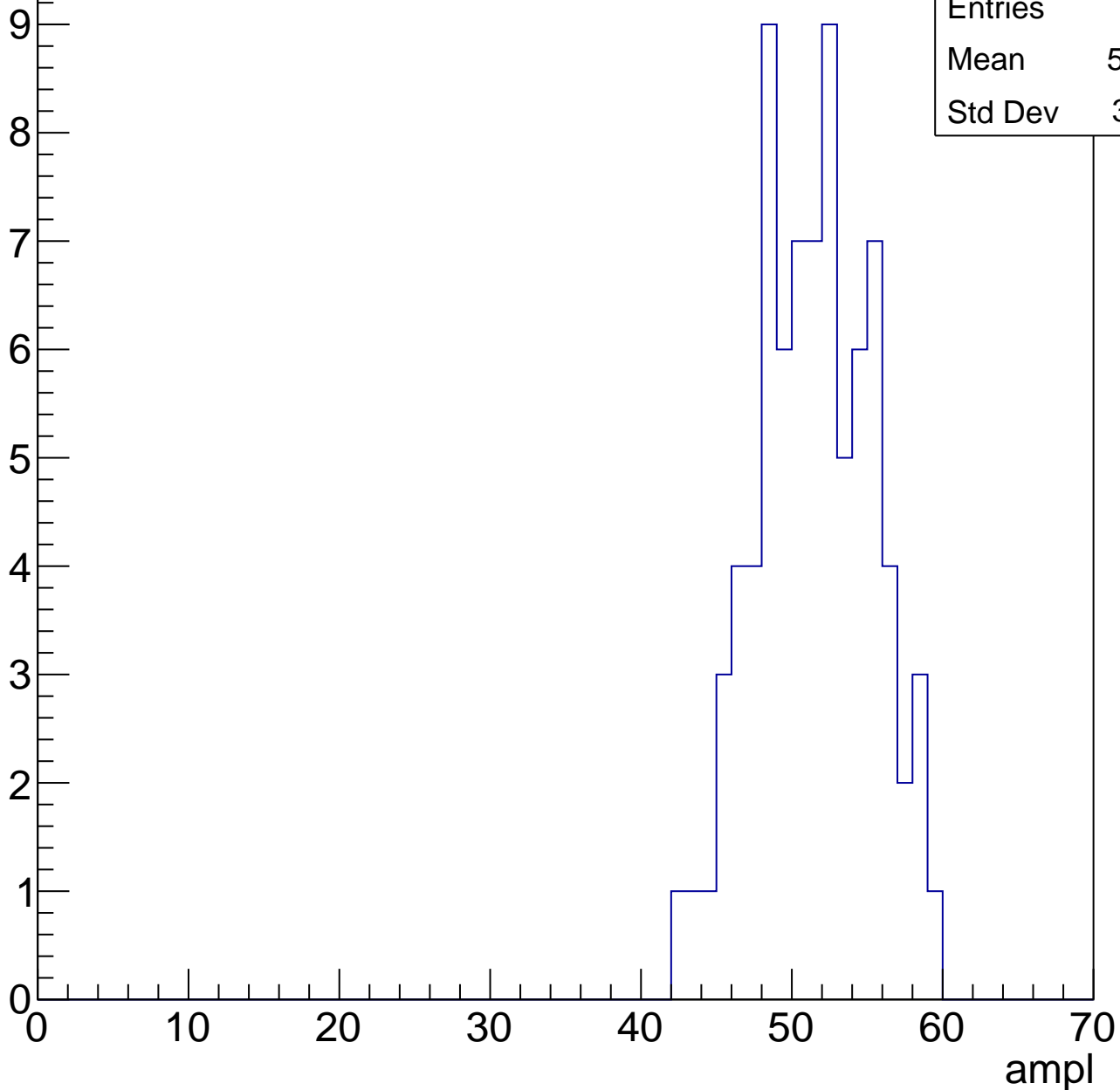


# B0L000S, U25-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	51.02
Std Dev	3.801

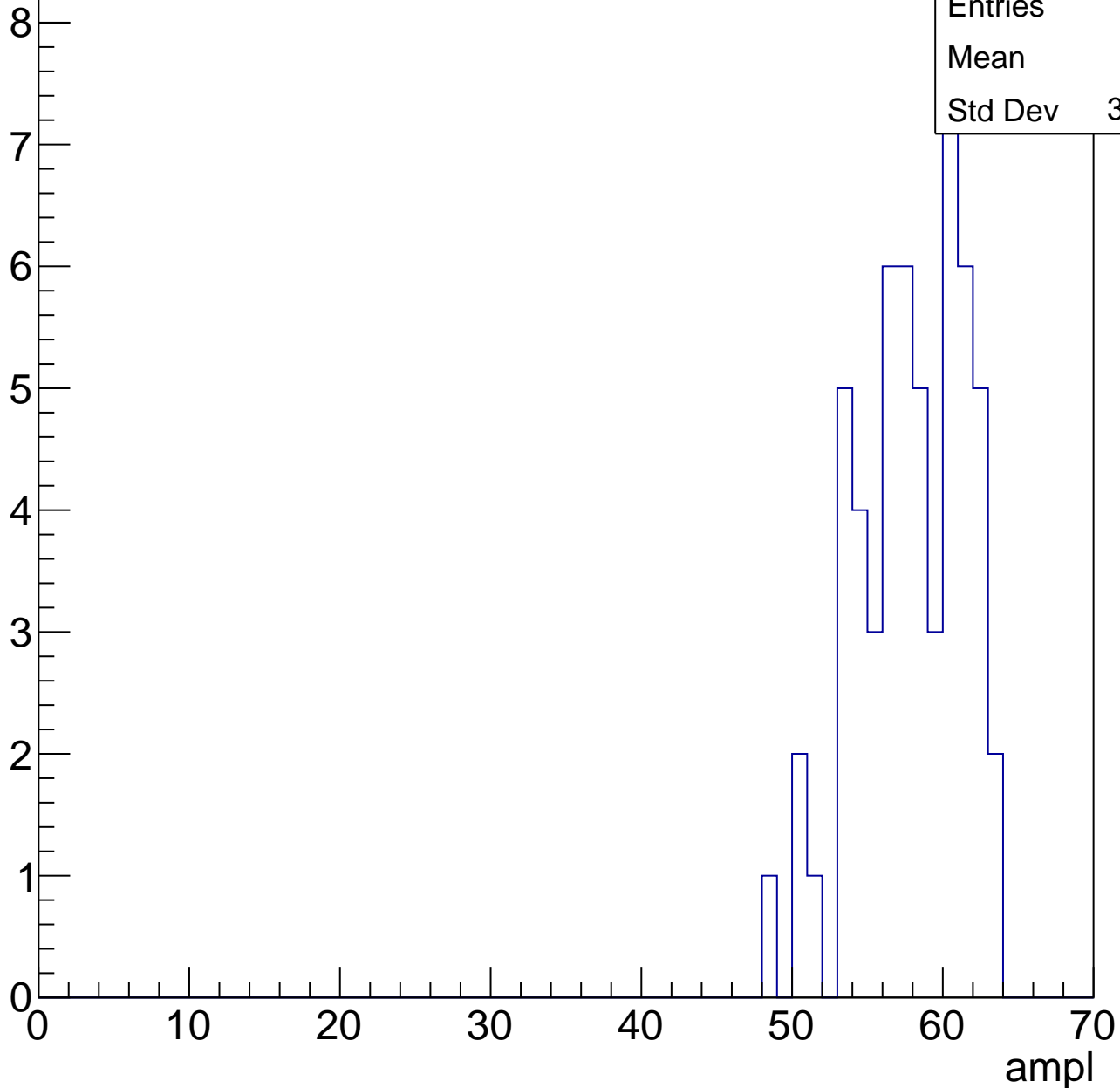


# B0L000S, U25-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	57.4
Std Dev	3.558

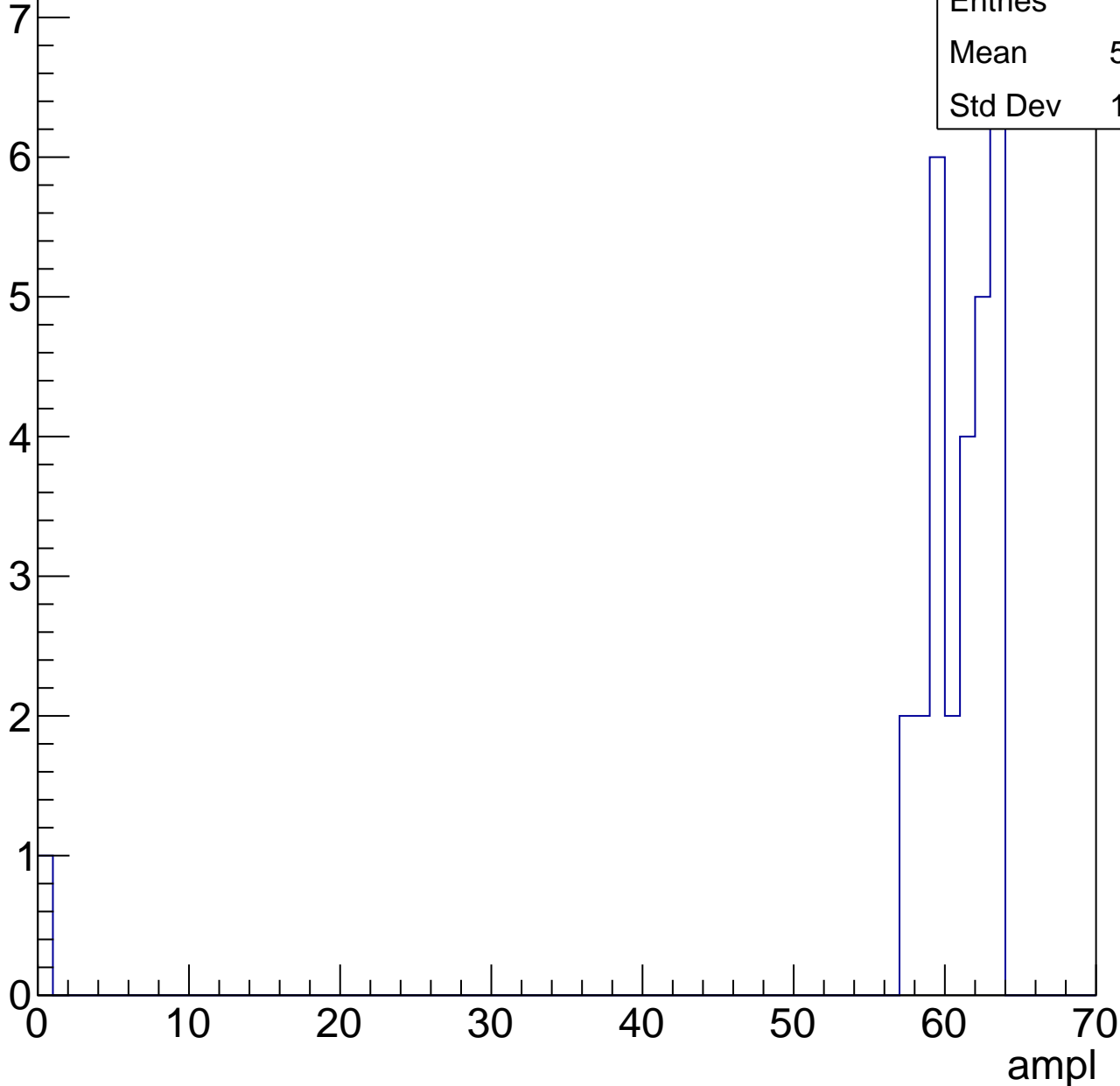


# B0L000S, U25-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	29
Mean	58.59
Std Dev	11.24



# B0L000S, U25-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch62, adc0

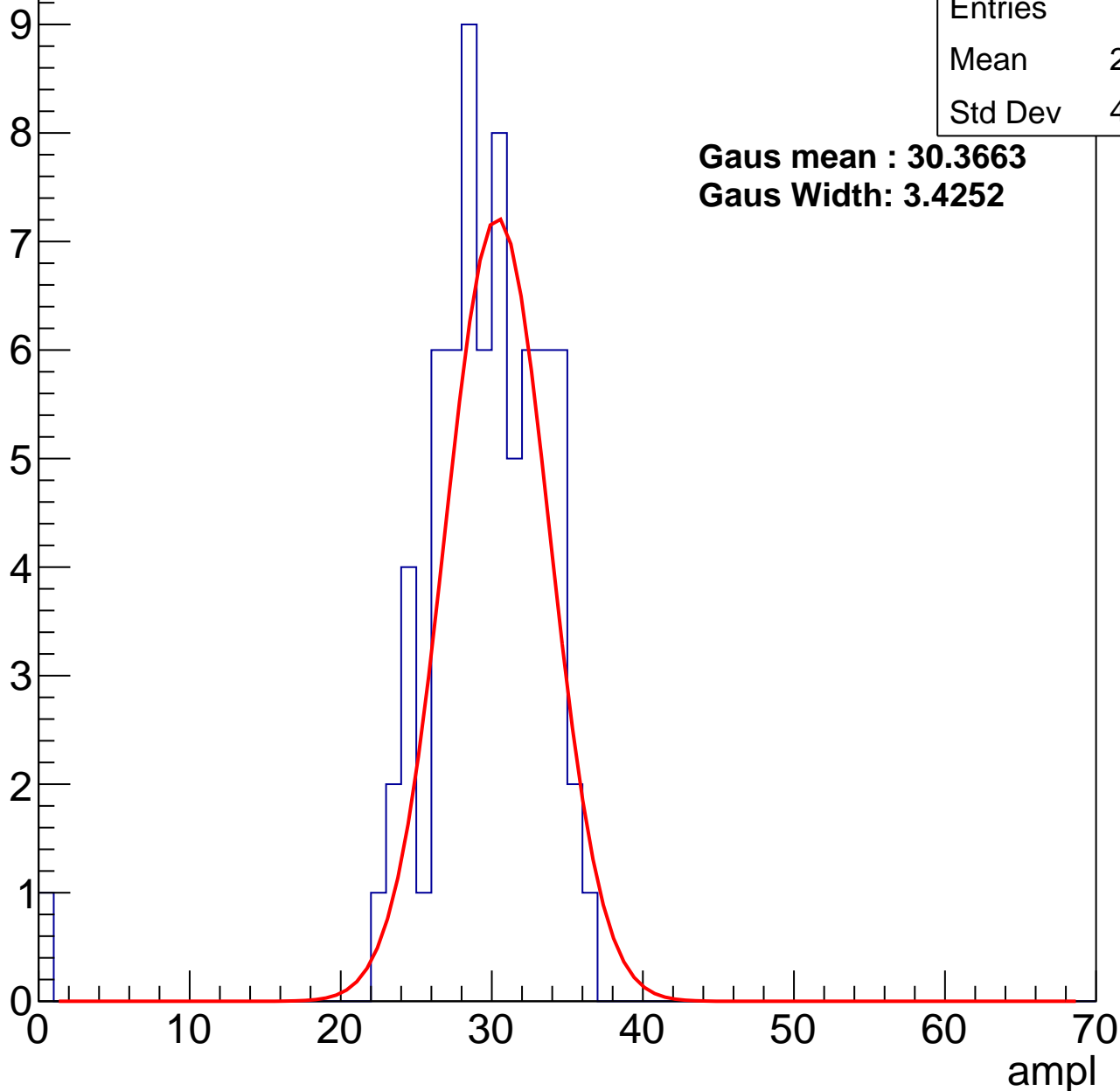
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	28.97
Std Dev	4.802

**Gaus mean : 30.3663**

**Gaus Width: 3.4252**



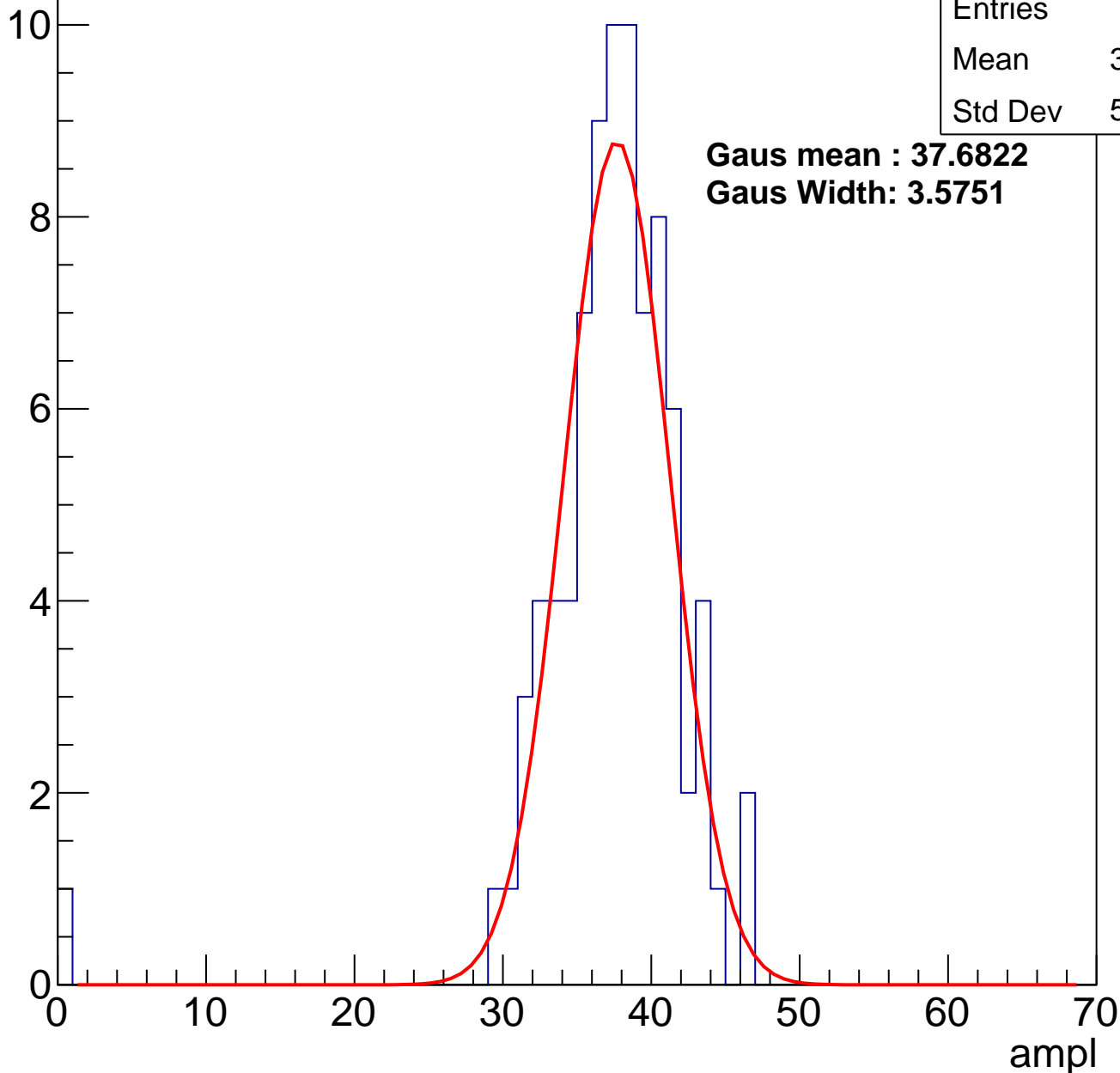
# B0L000S, U25-ch62, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	84
Mean	36.88
Std Dev	5.386

**Gaus mean : 37.6822**  
**Gaus Width: 3.5751**

Entry



# B0L000S, U25-ch62, adc2

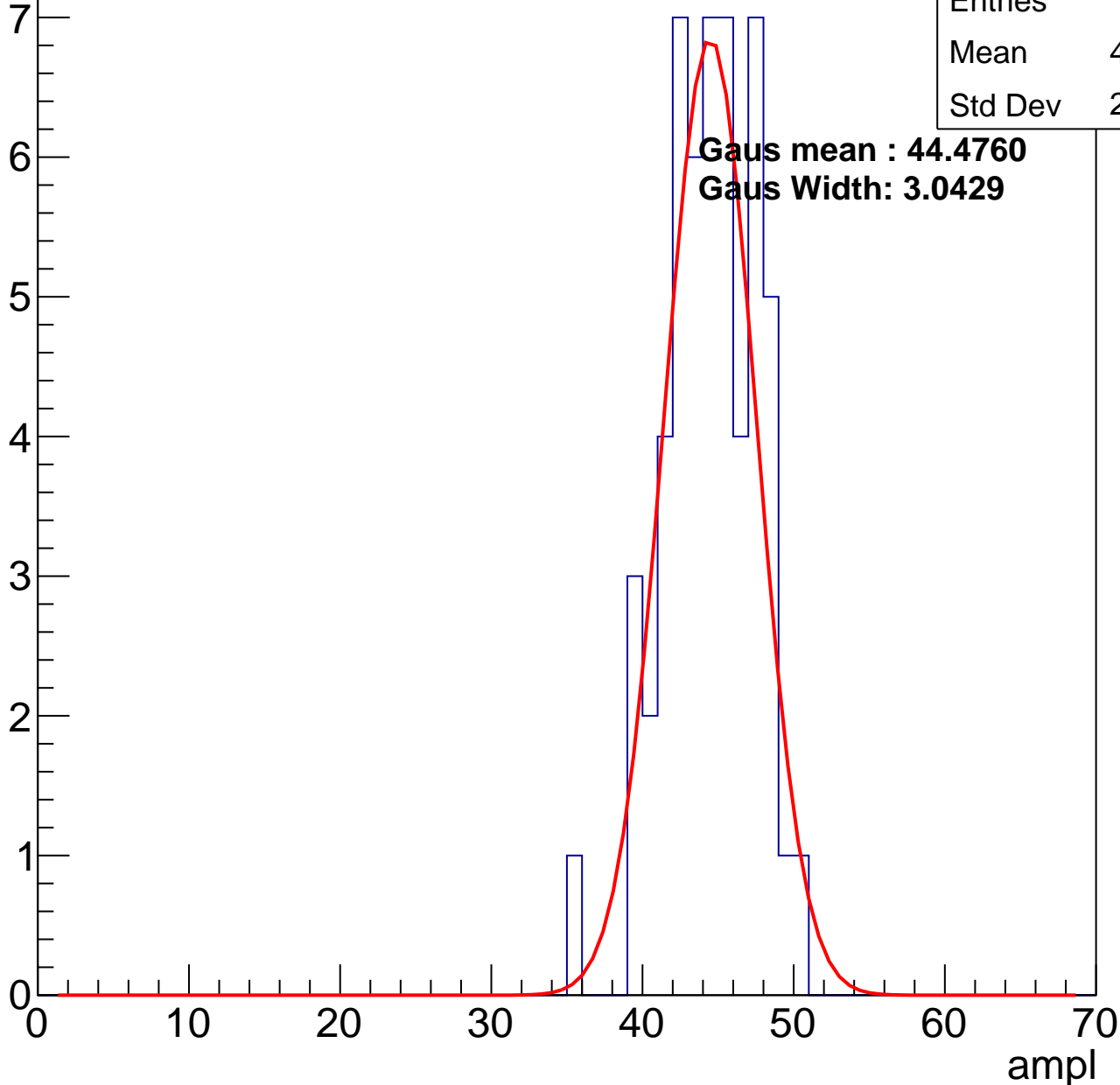
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	44.05
Std Dev	2.969

**Gaus mean : 44.4760**

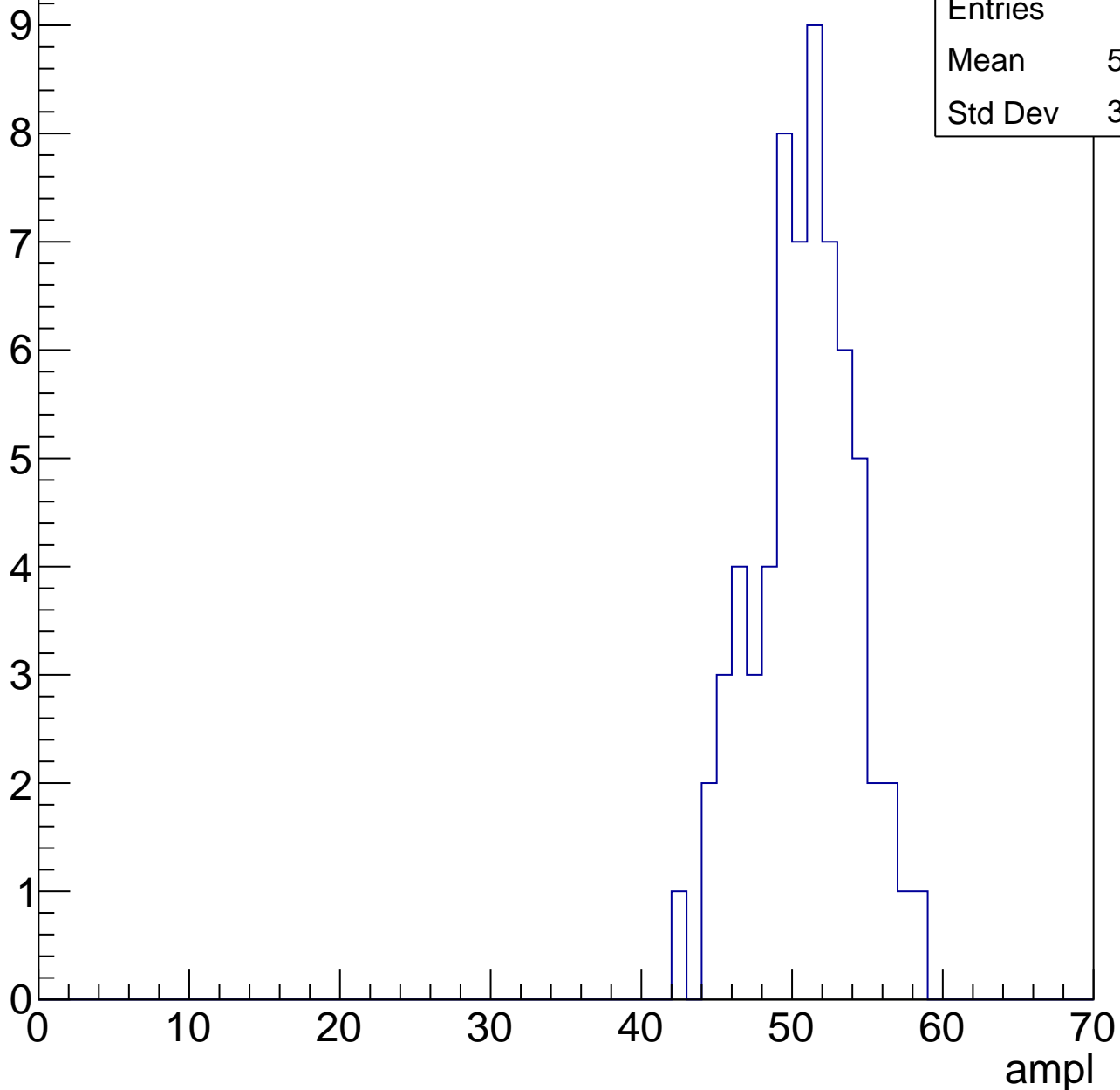
**Gaus Width: 3.0429**



# B0L000S, U25-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



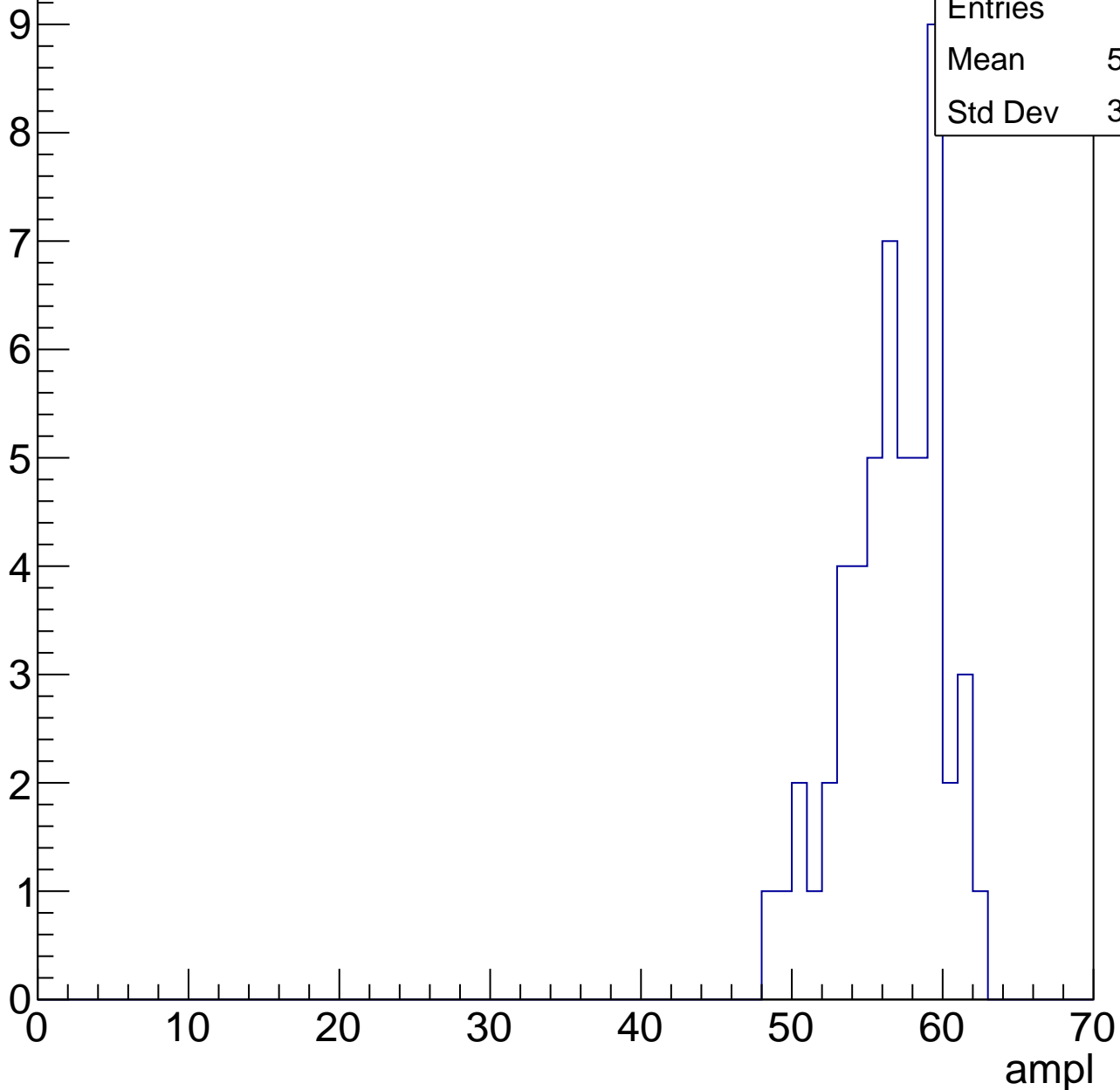
Entries	65
Mean	50.34
Std Dev	3.339

# B0L000S, U25-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56.12
Std Dev	3.244

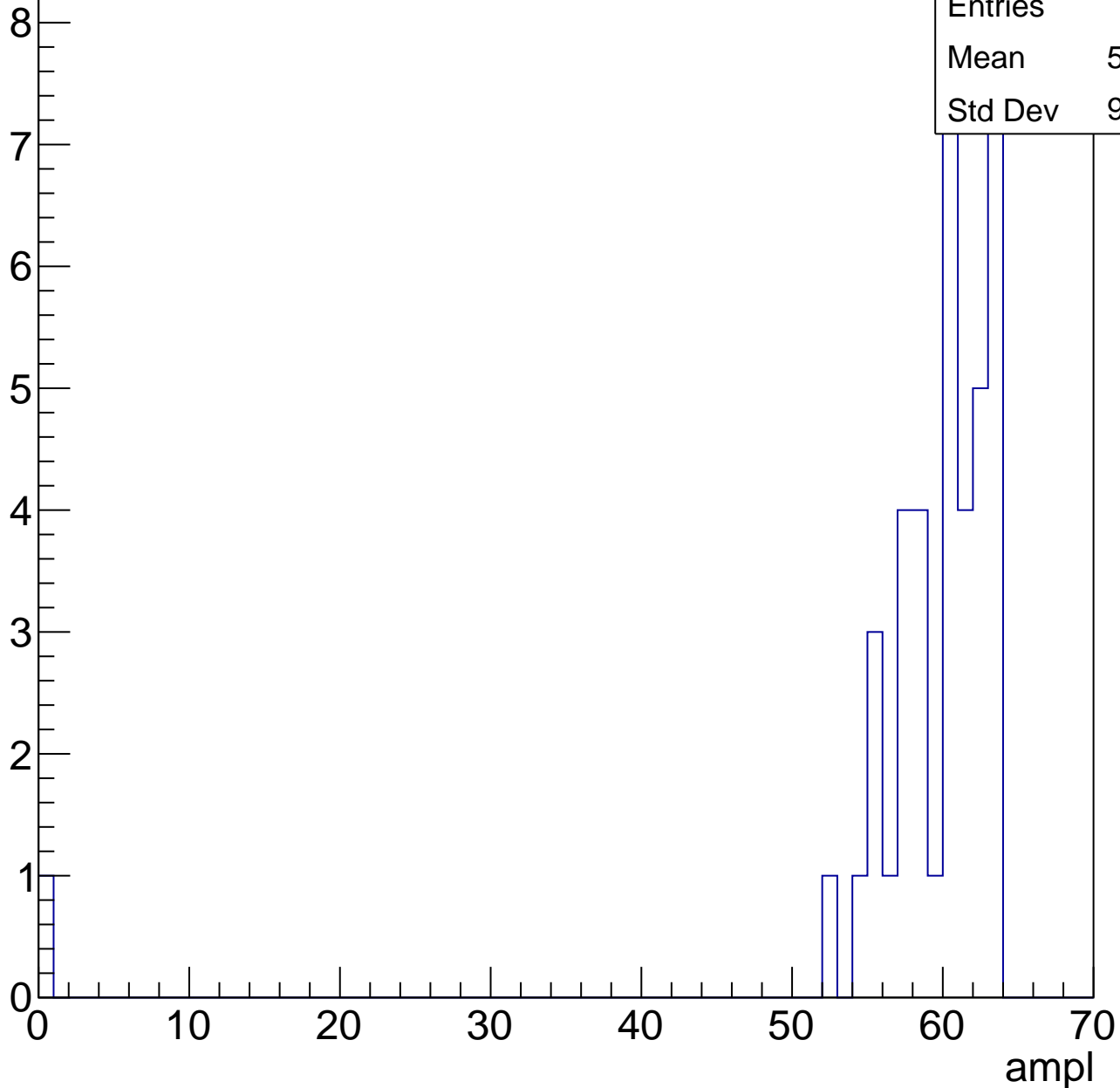


# B0L000S, U25-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.15
Std Dev	9.626



# B0L000S, U25-ch62, adc6

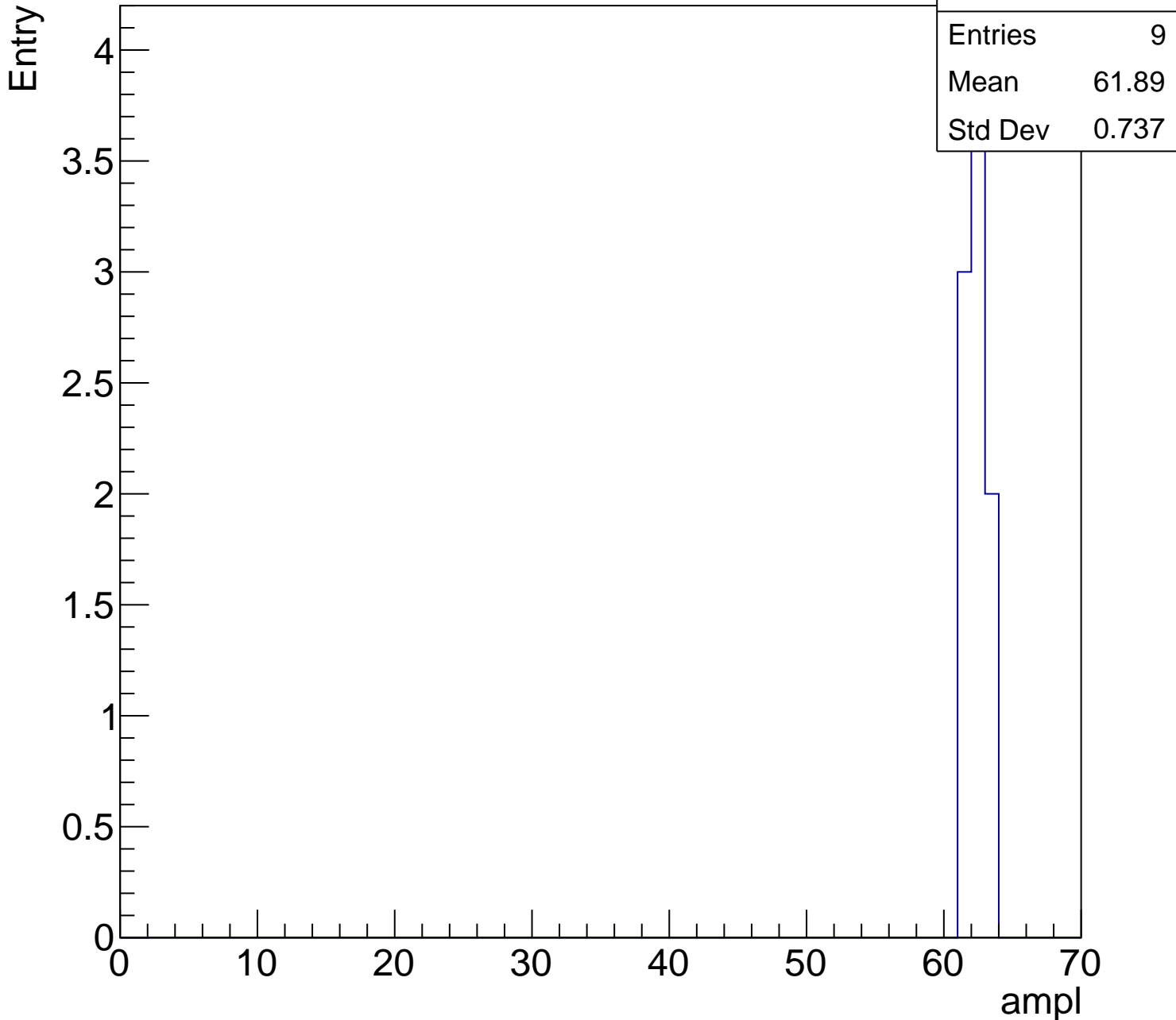
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	9
Mean	61.89
Std Dev	0.737

ampl





# B0L000S, U25-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch63, adc0

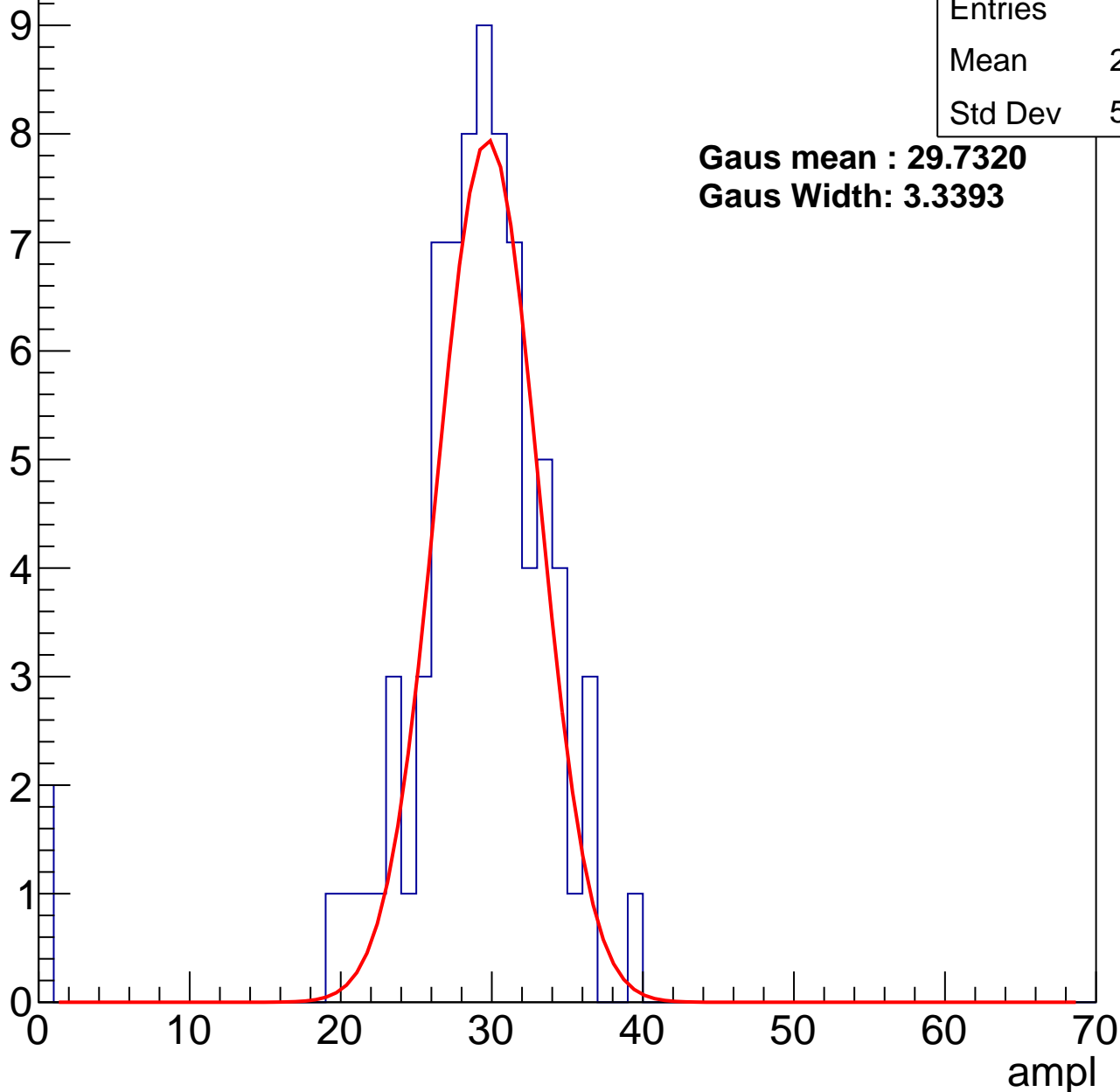
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	28.23
Std Dev	5.972

**Gaus mean : 29.7320**

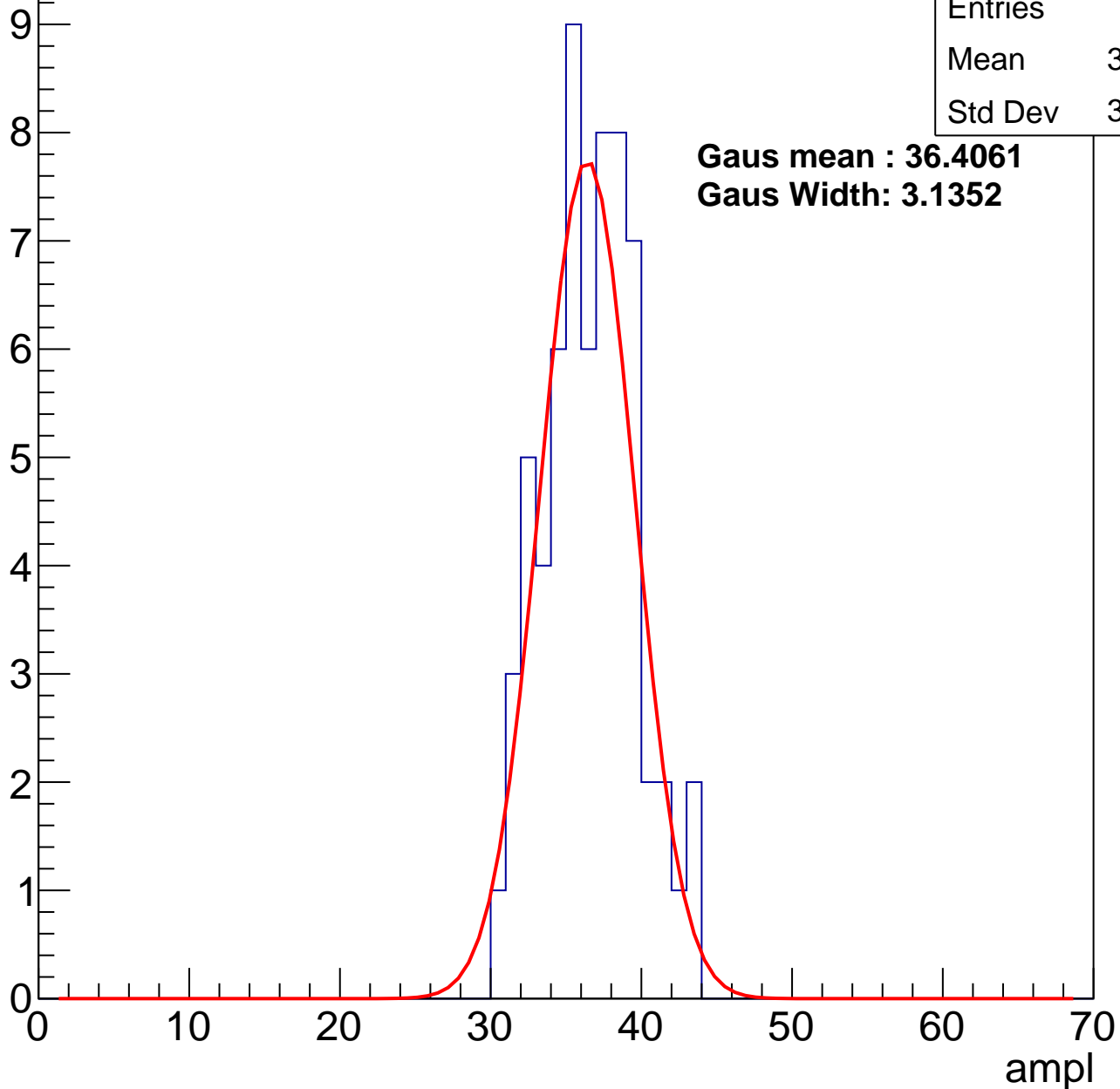
**Gaus Width: 3.3393**



# B0L000S, U25-ch63, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch63, adc2

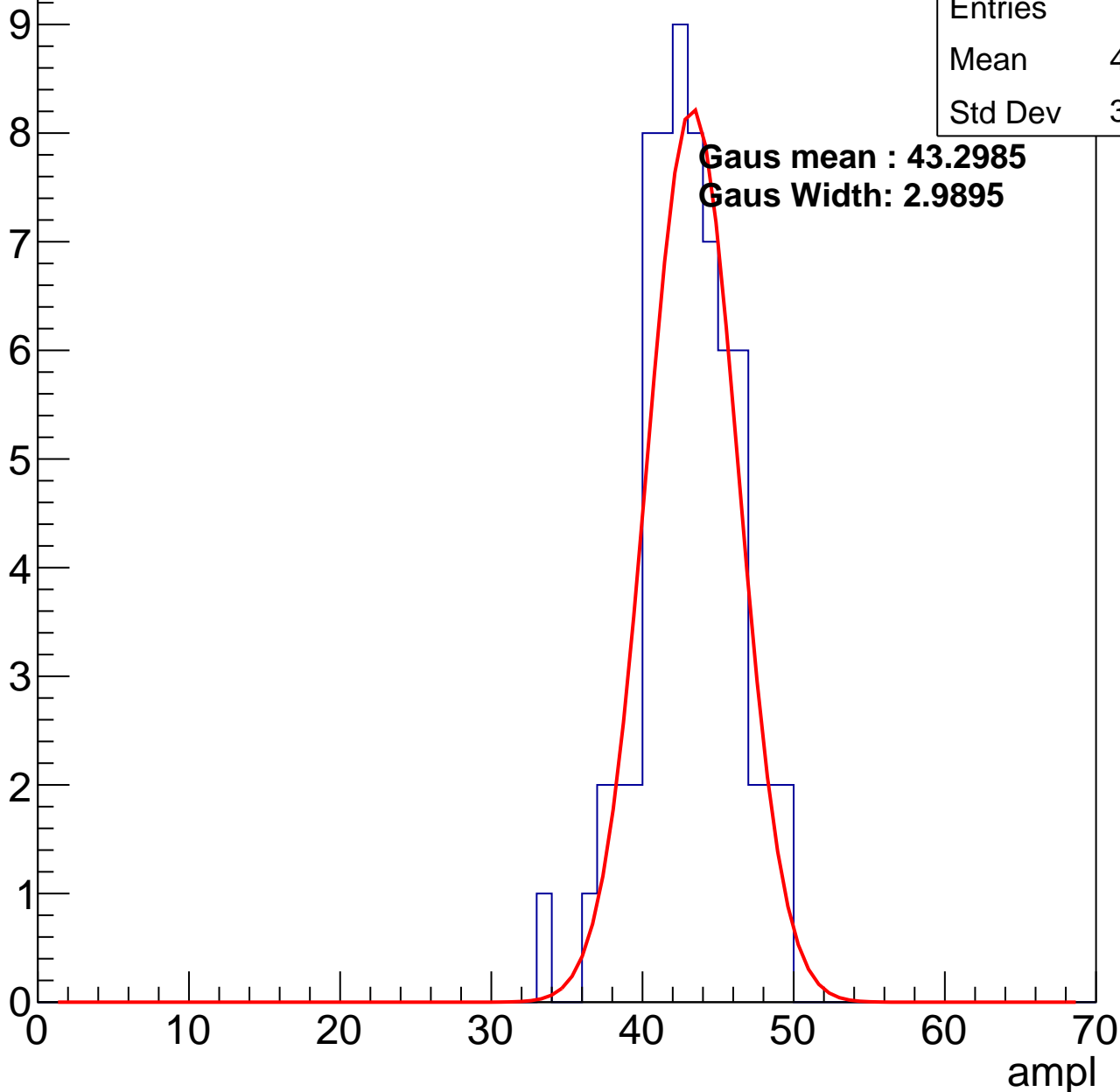
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	42.56
Std Dev	3.119

**Gaus mean : 43.2985**

**Gaus Width: 2.9895**

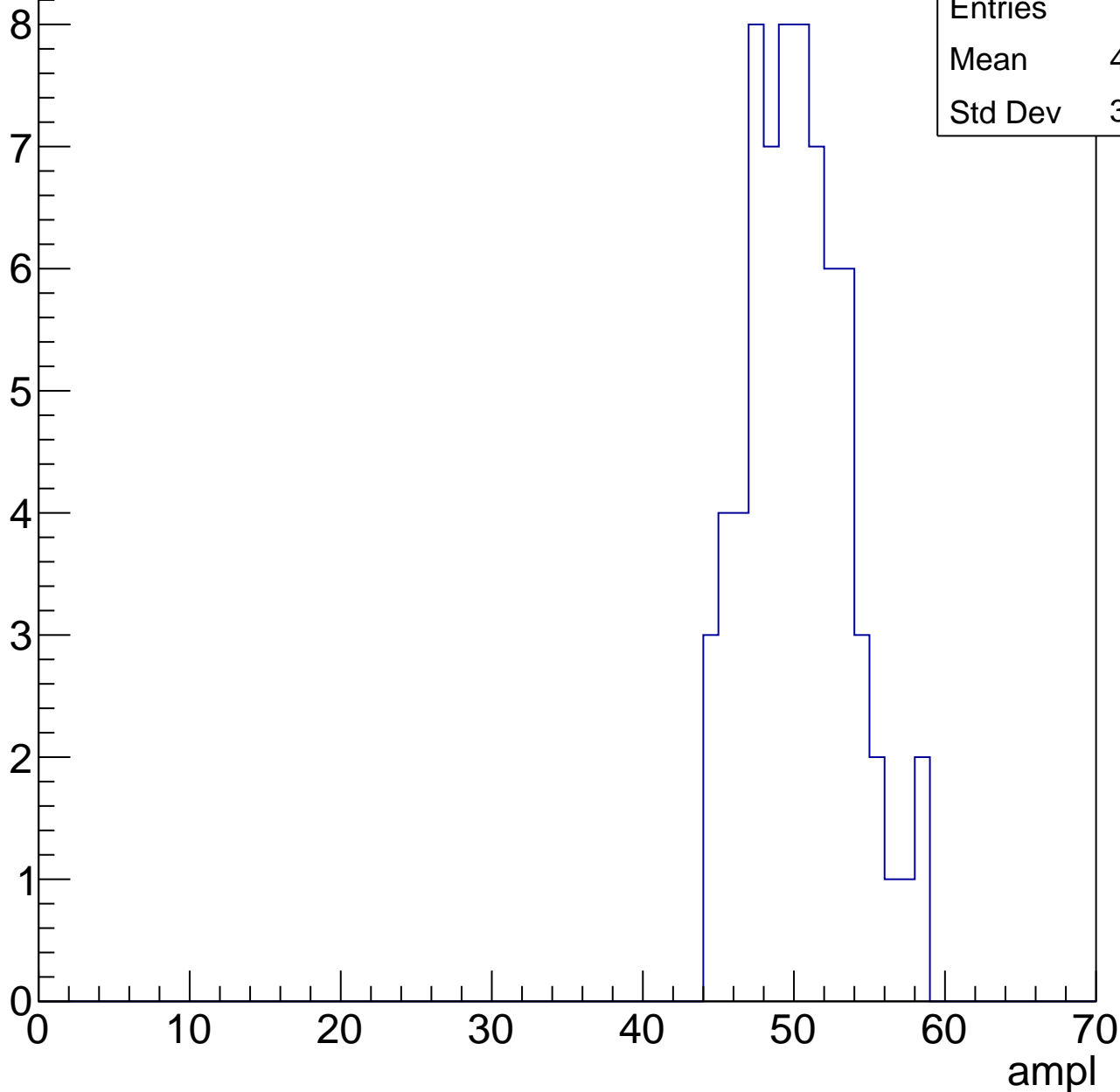


# B0L000S, U25-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	49.83
Std Dev	3.325

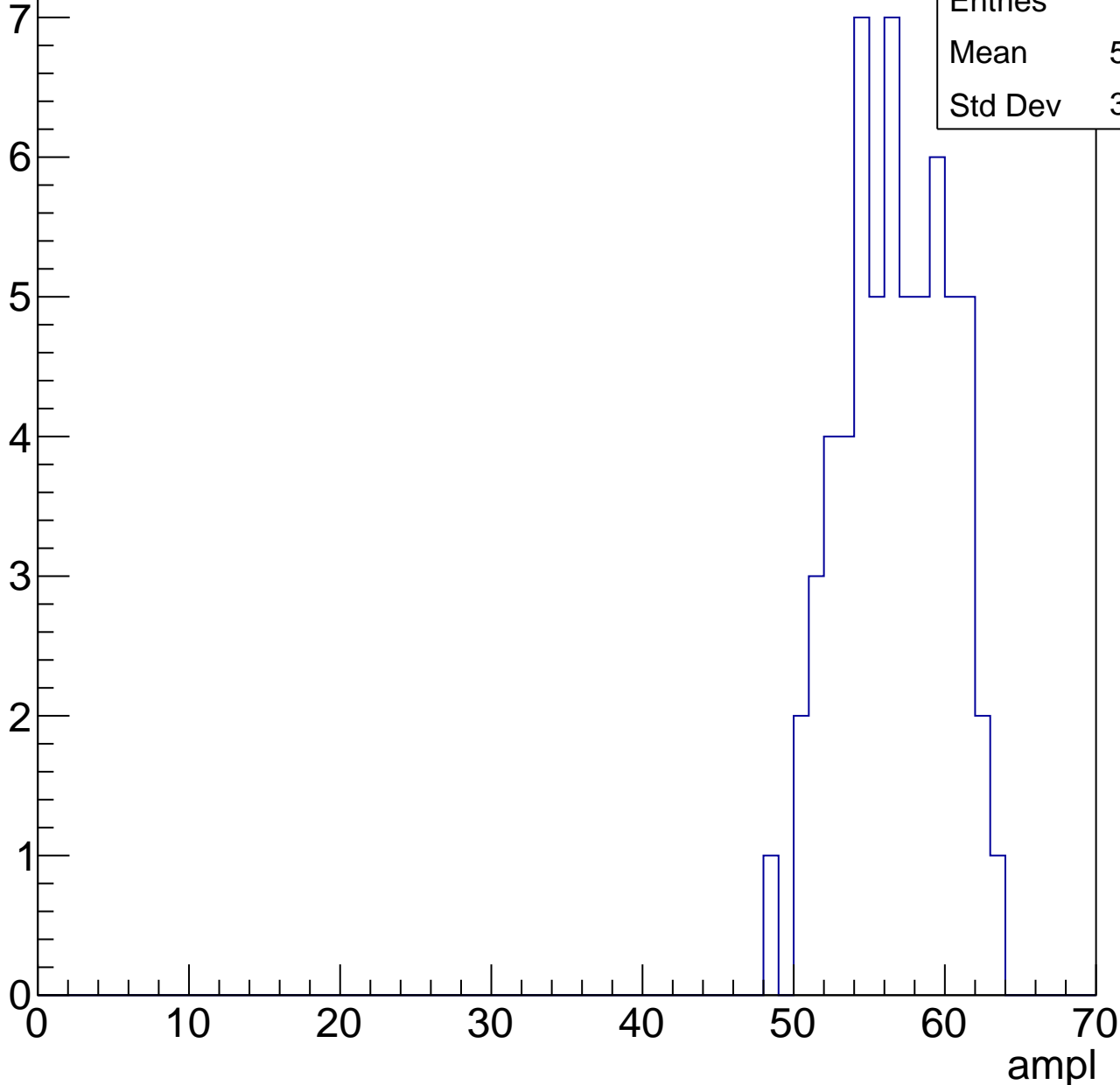


# B0L000S, U25-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	56.24
Std Dev	3.467

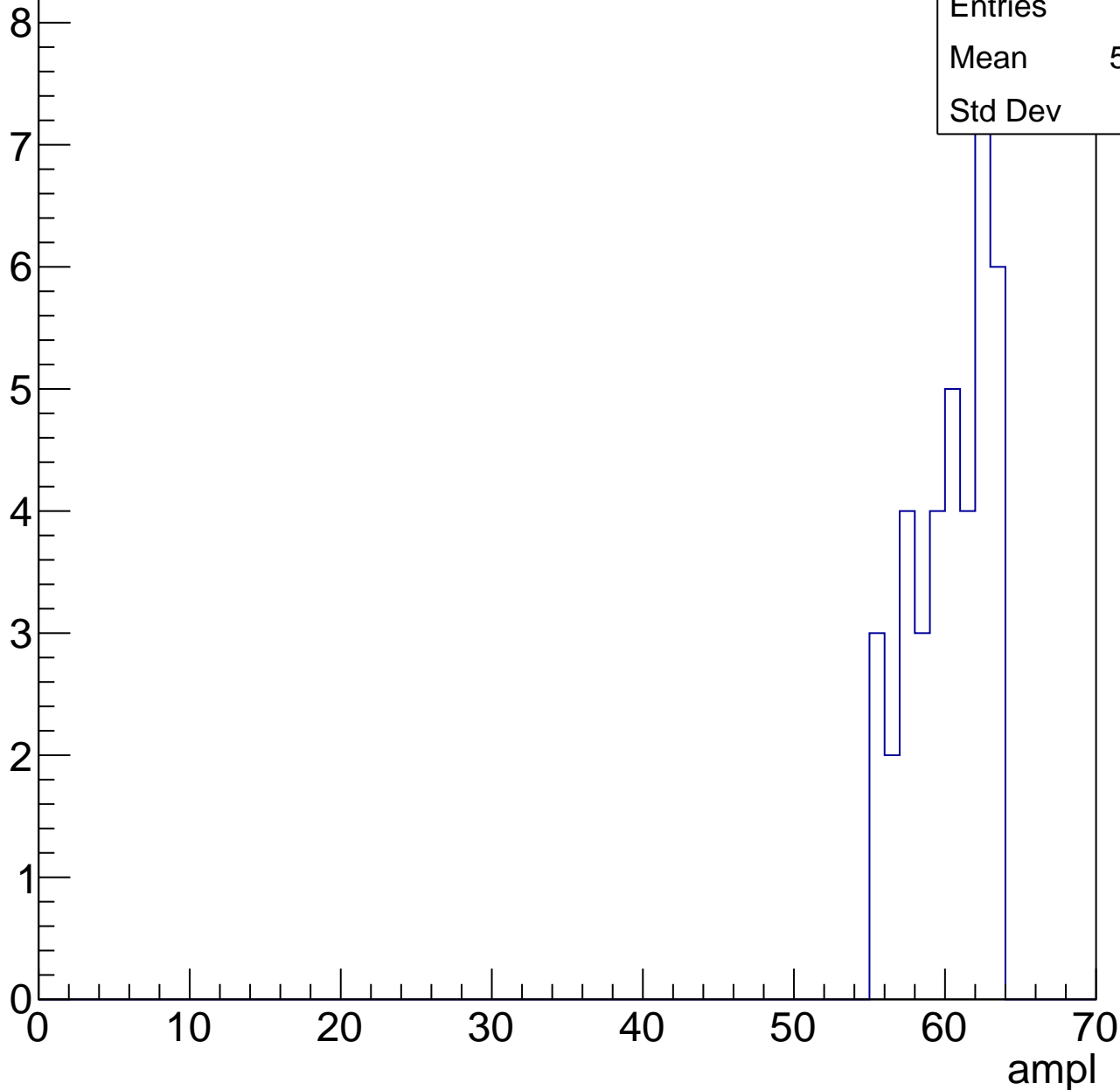


# B0L000S, U25-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

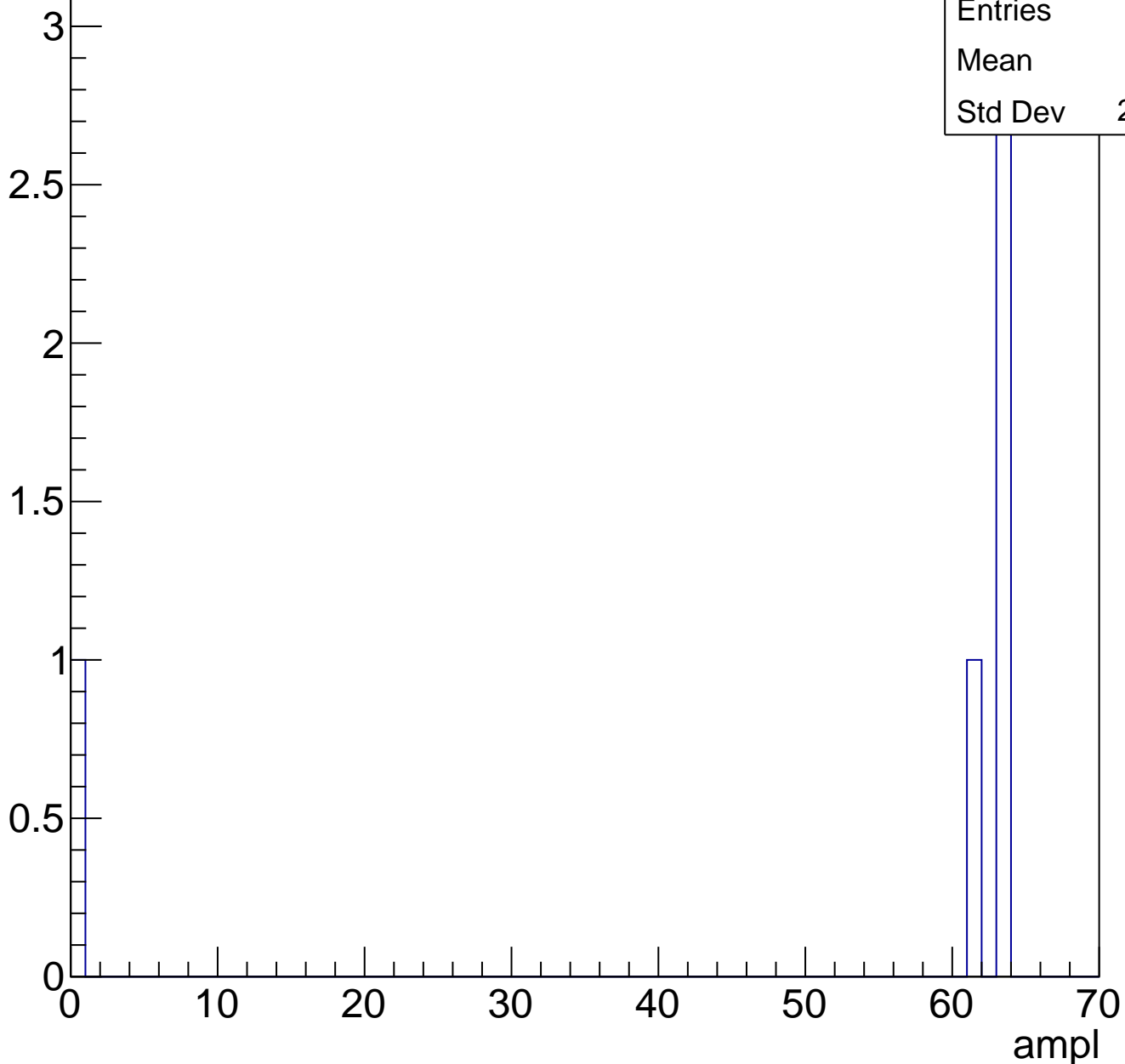
Entries	39
Mean	59.82
Std Dev	2.52



# B0L000S, U25-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch64, adc0

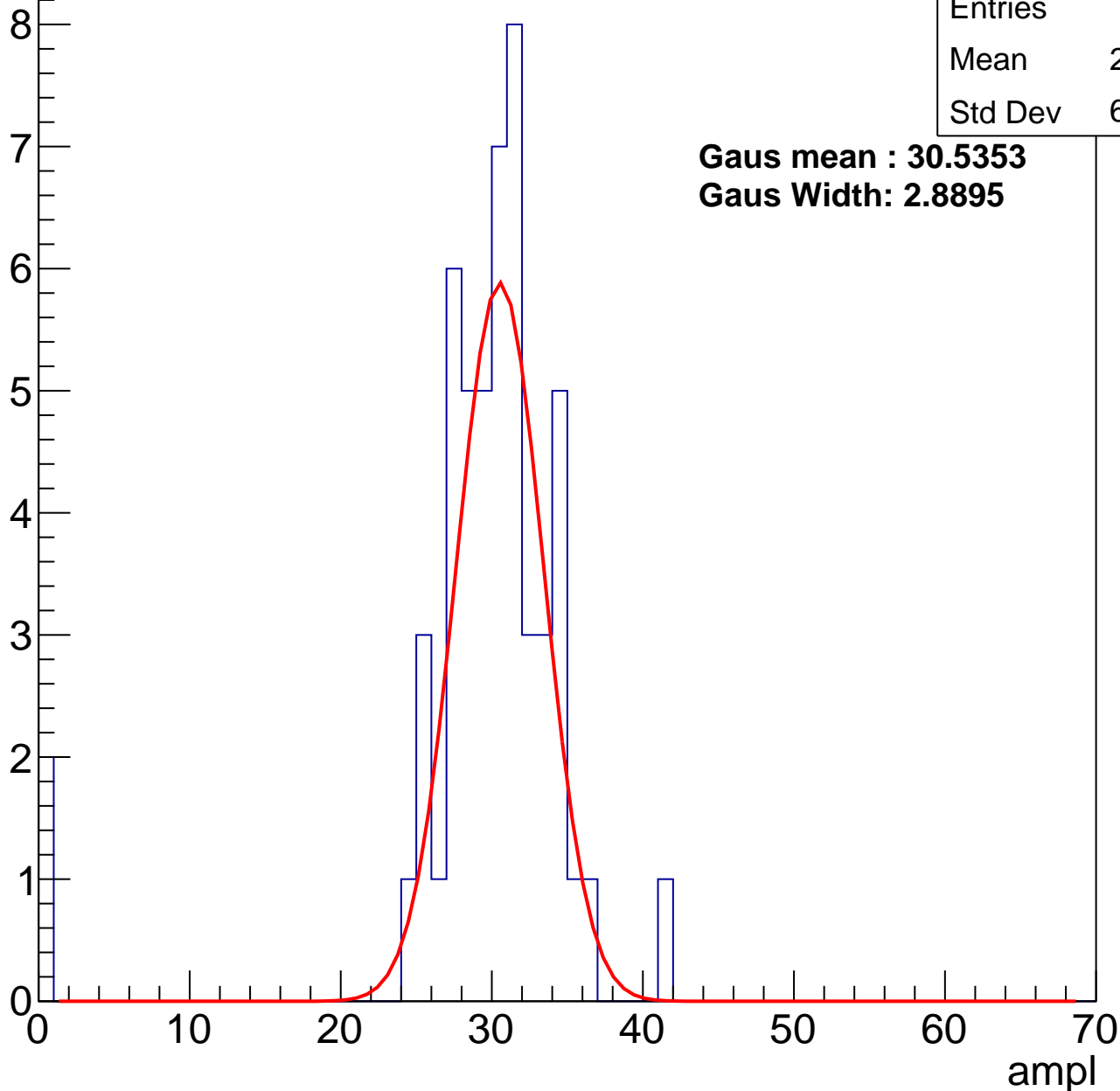
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	28.98
Std Dev	6.594

**Gaus mean : 30.5353**

**Gaus Width: 2.8895**



# B0L000S, U25-ch64, adc1

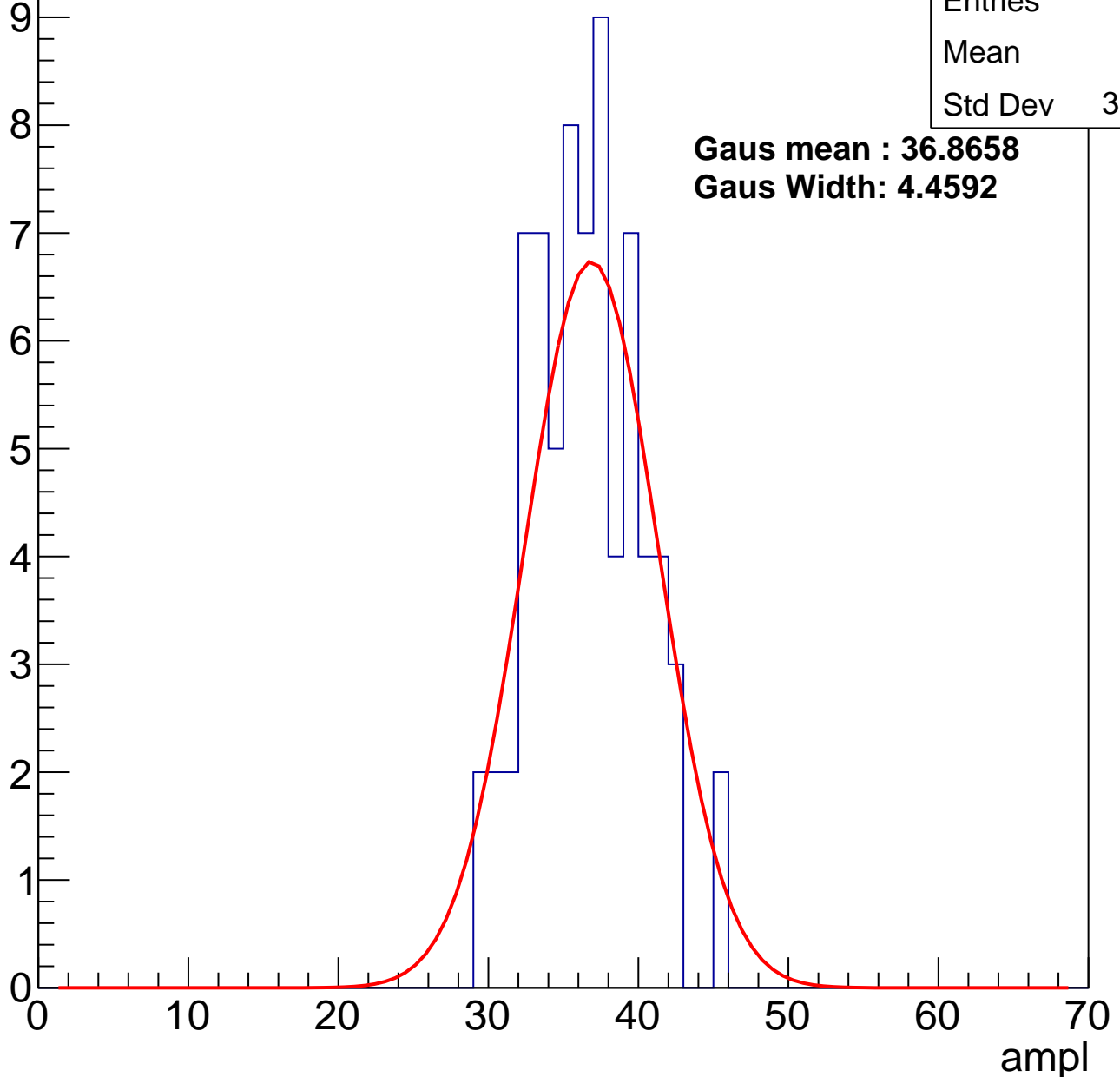
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.1
Std Dev	3.597

**Gaus mean : 36.8658**

**Gaus Width: 4.4592**



# B0L000S, U25-ch64, adc2

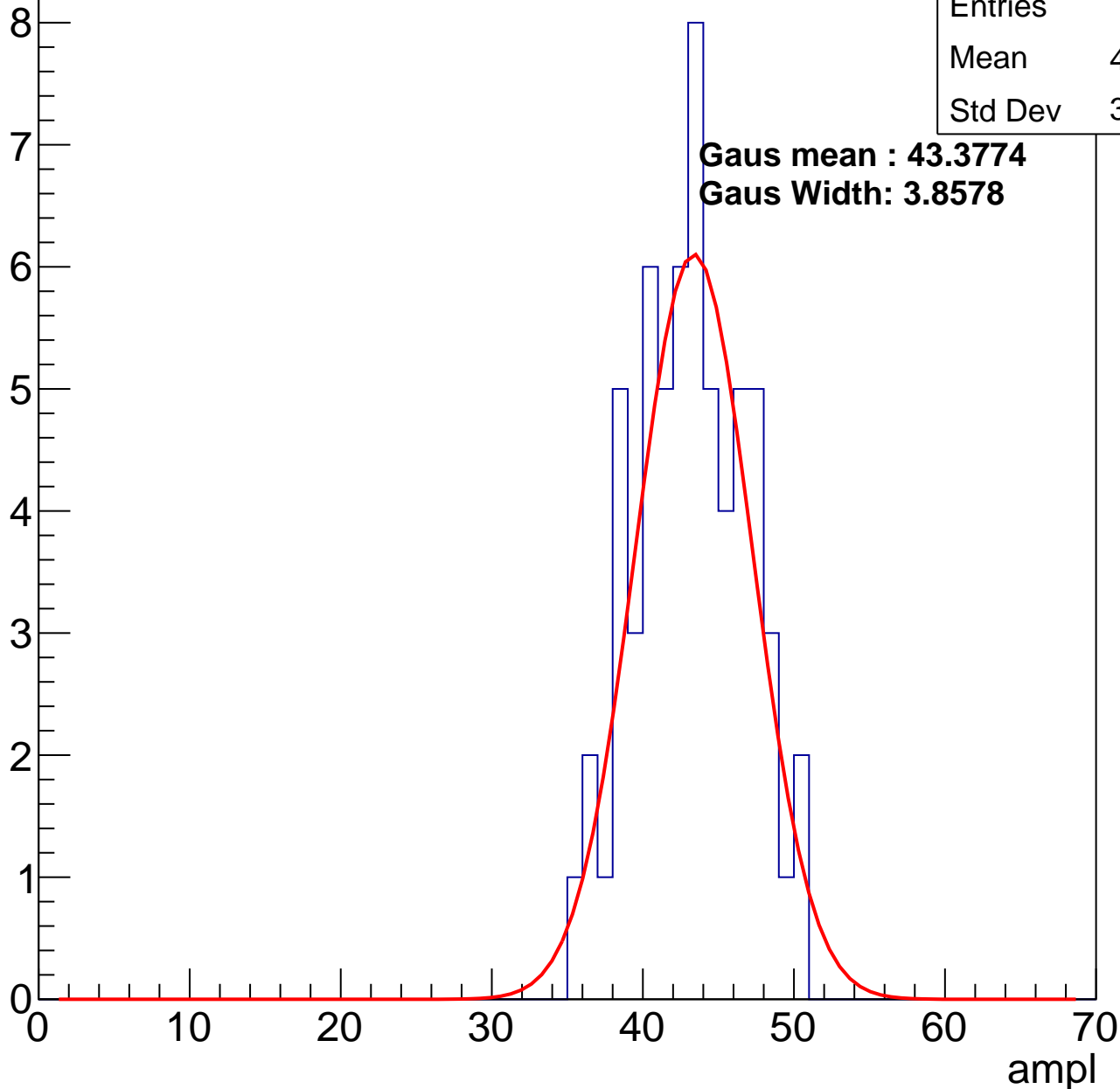
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.74
Std Dev	3.596

**Gaus mean : 43.3774**

**Gaus Width: 3.8578**

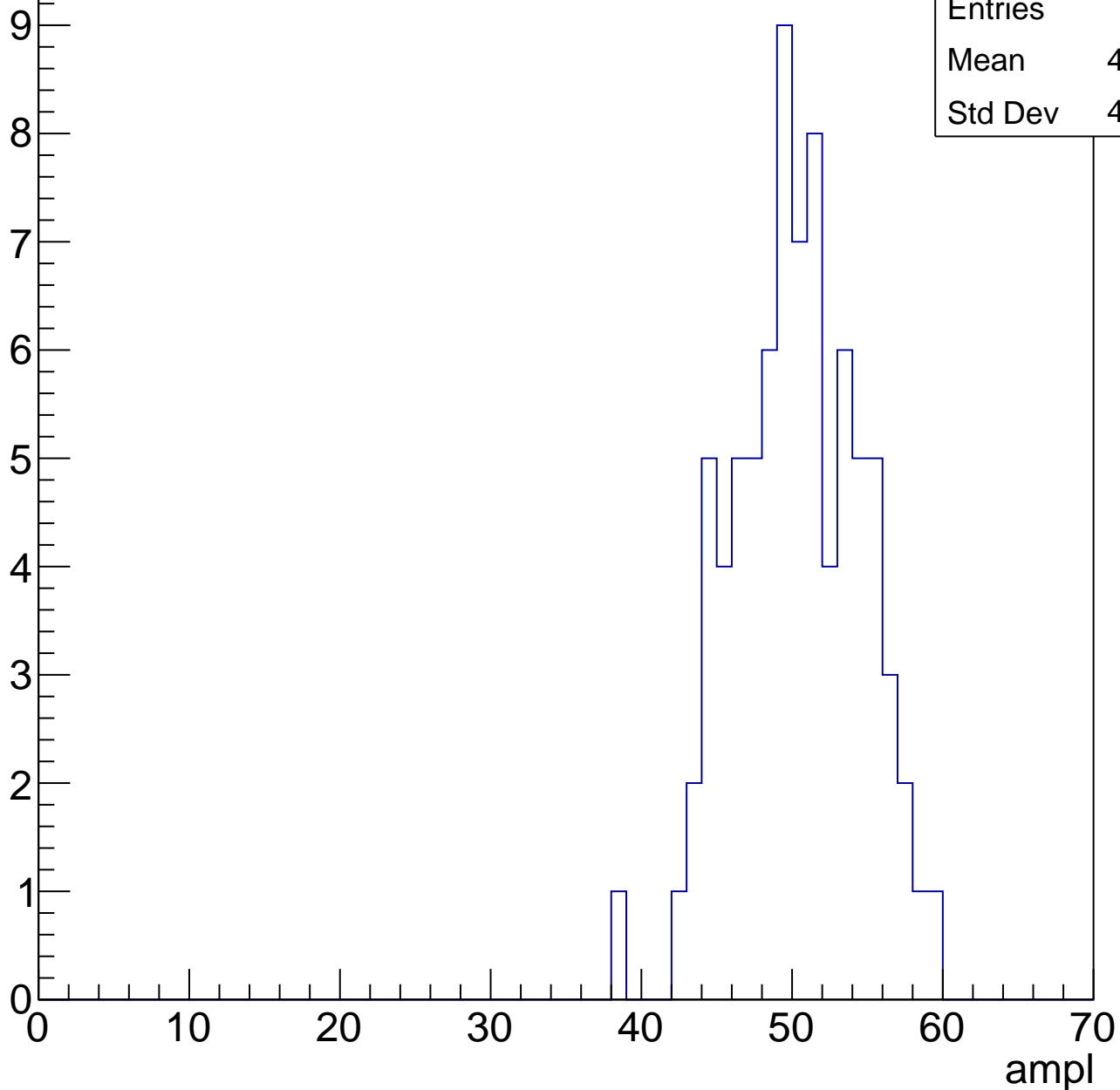


# B0L000S, U25-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

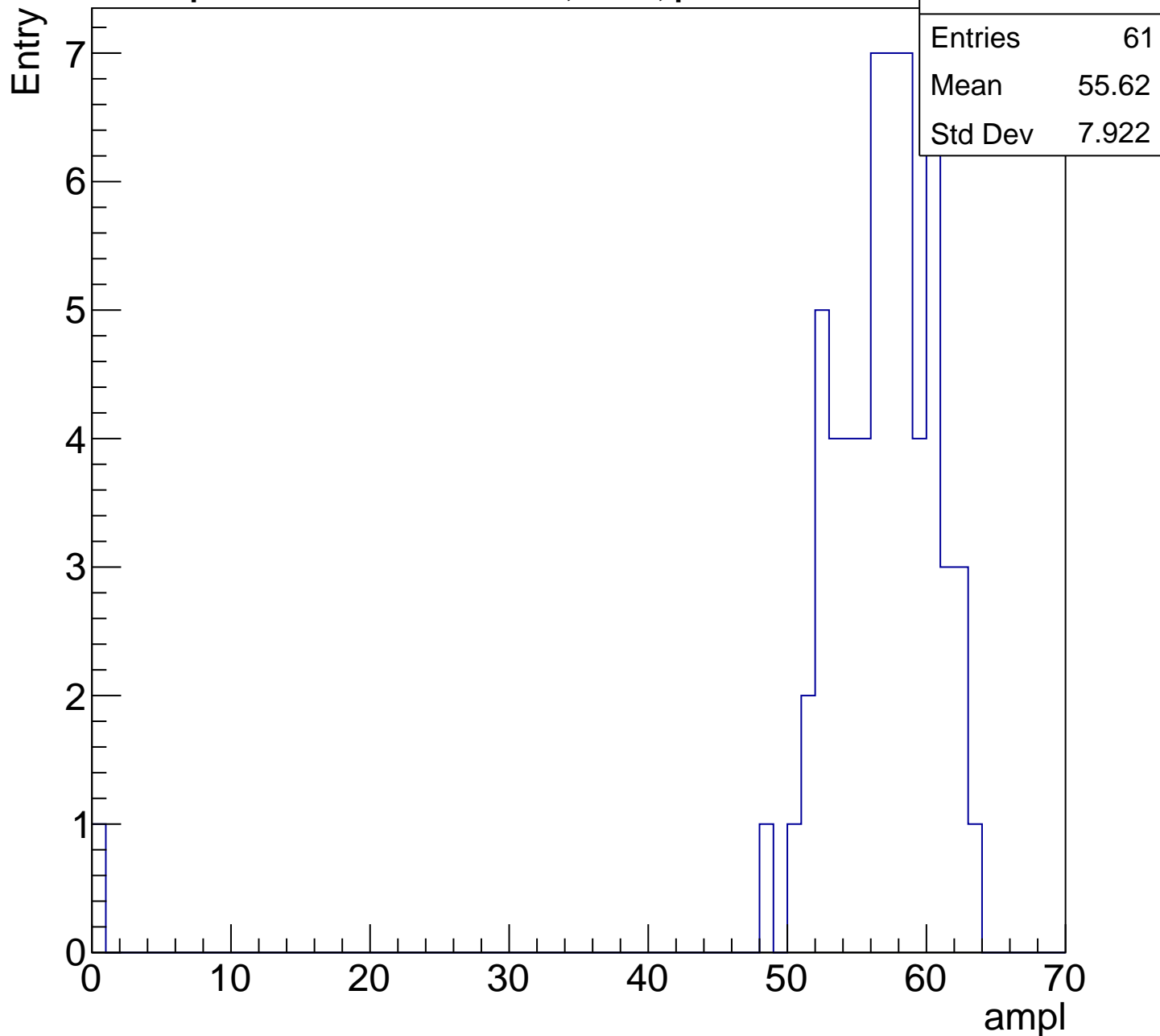
Entry

Entries	80
Mean	49.85
Std Dev	4.148



# B0L000S, U25-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1



# B0L000S, U25-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 34

Mean 60.24

Std Dev 2.624

ampl

0

10

20

30

40

50

60

70

# B0L000S, U25-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	3
Mean	63
Std Dev	0

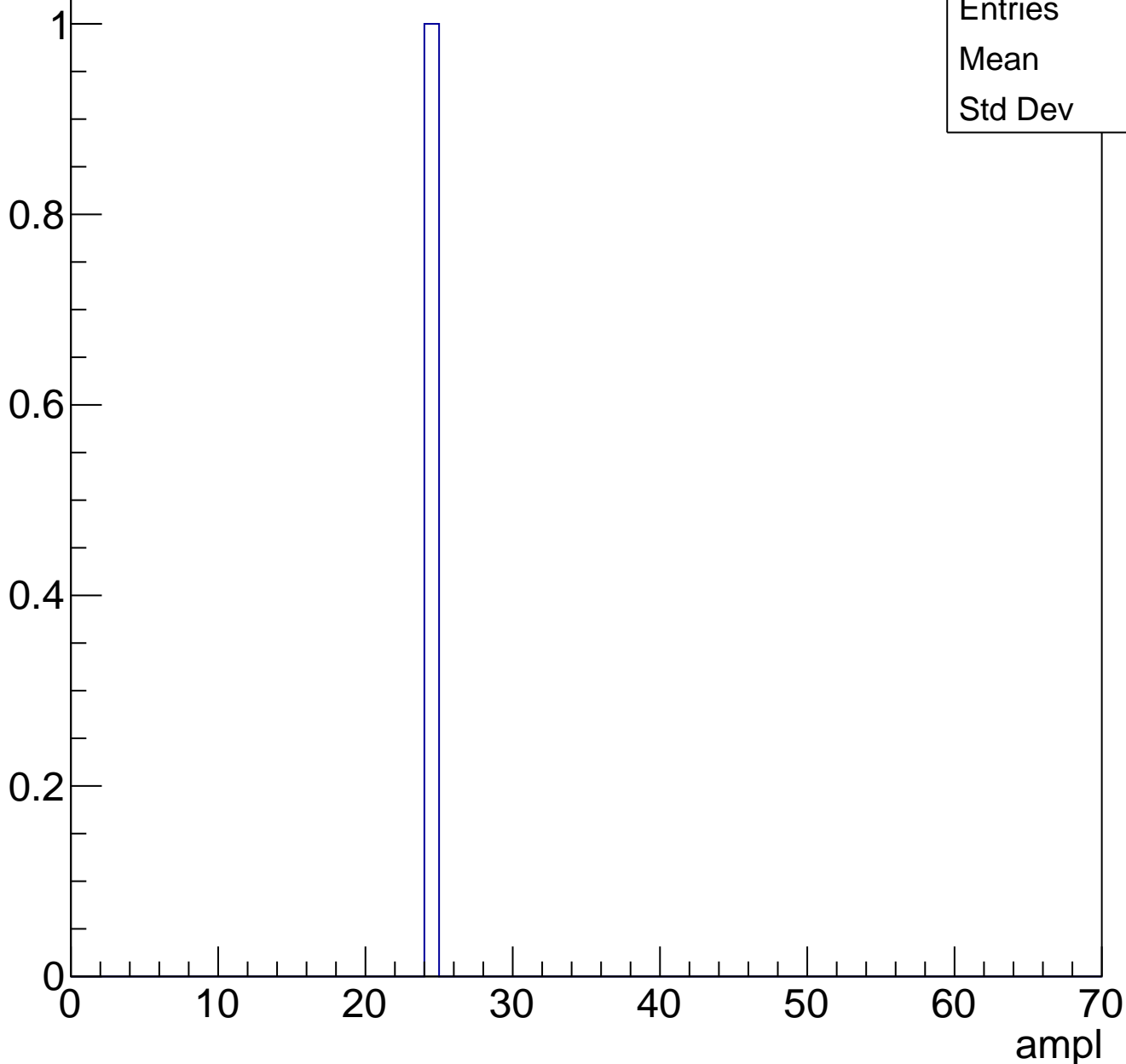
ampl



# B0L000S, U25-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	24
Std Dev	0

# B0L000S, U25-ch65, adc0

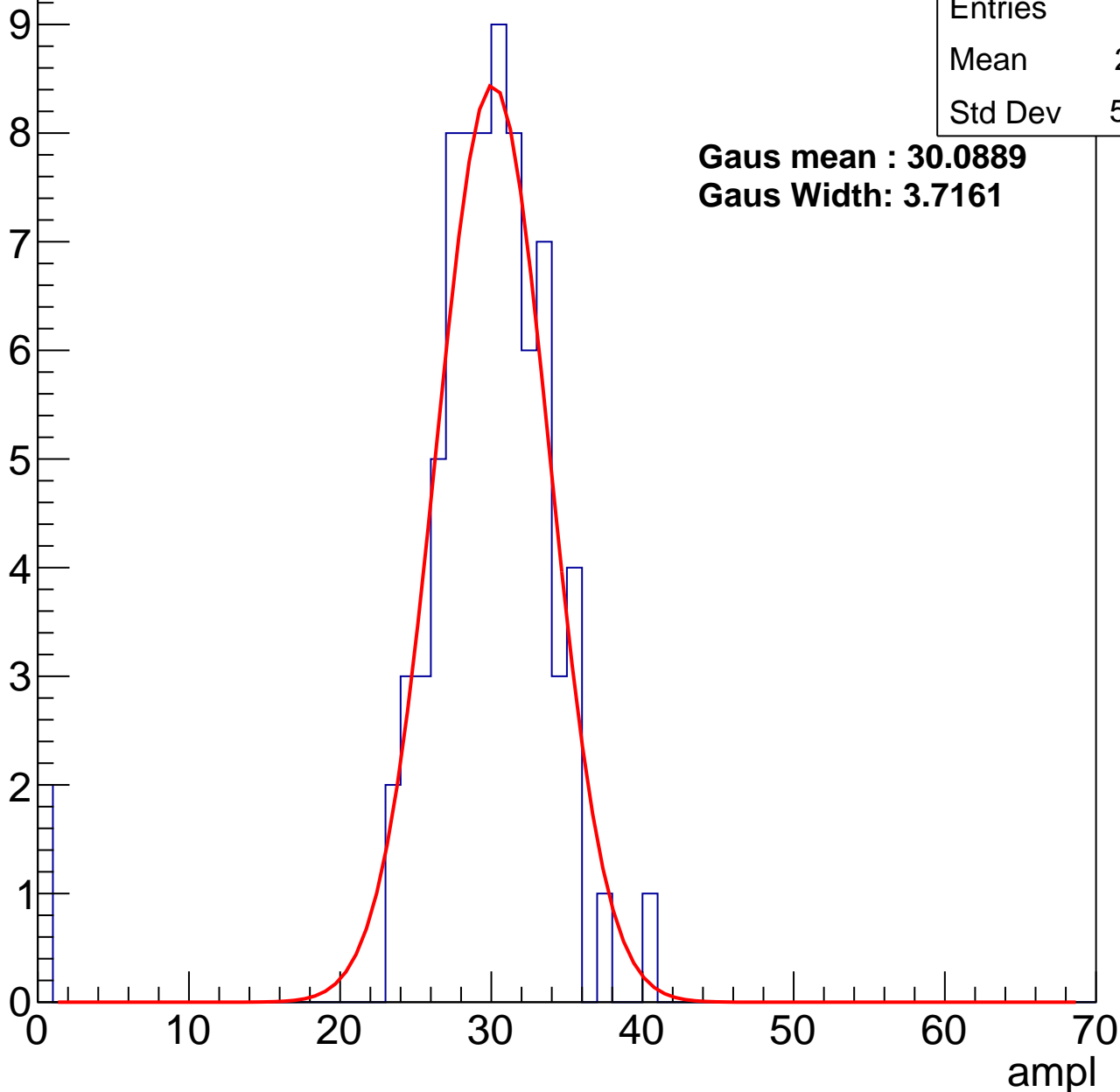
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	28.91
Std Dev	5.743

**Gaus mean : 30.0889**

**Gaus Width: 3.7161**



# B0L000S, U25-ch65, adc1

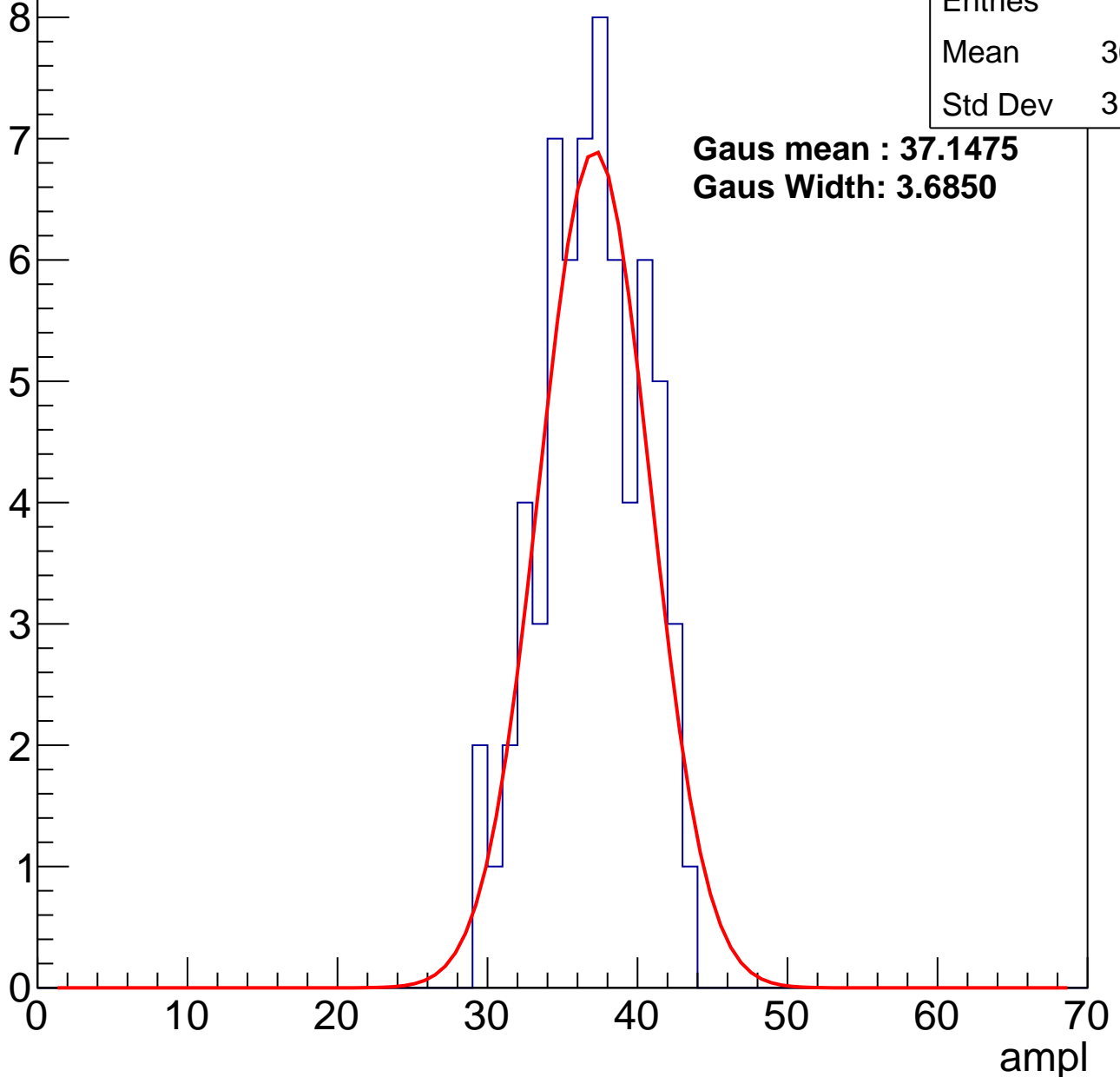
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.48
Std Dev	3.375

**Gaus mean : 37.1475**

**Gaus Width: 3.6850**



# B0L000S, U25-ch65, adc2

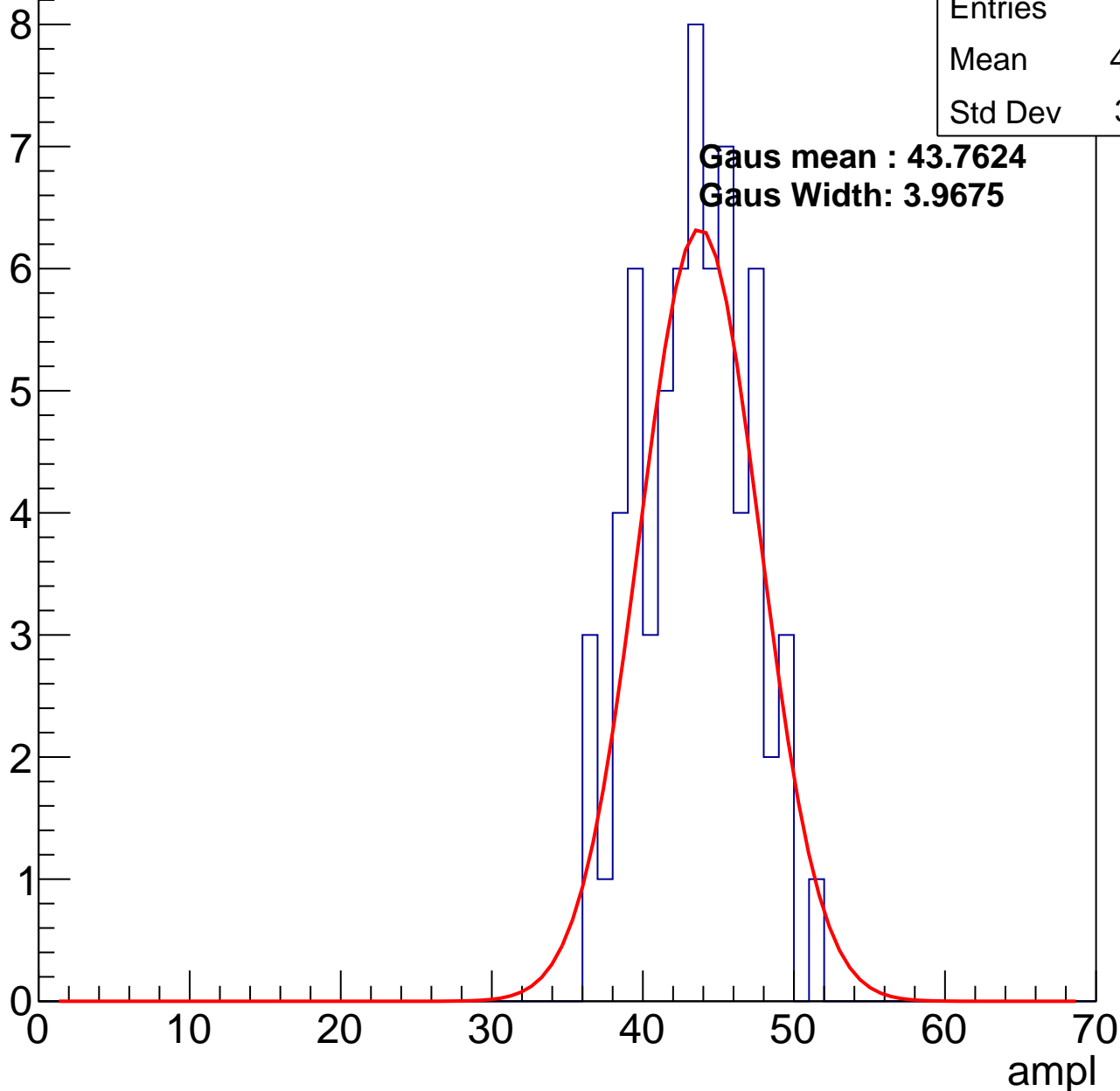
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	42.94
Std Dev	3.551

**Gaus mean : 43.7624**

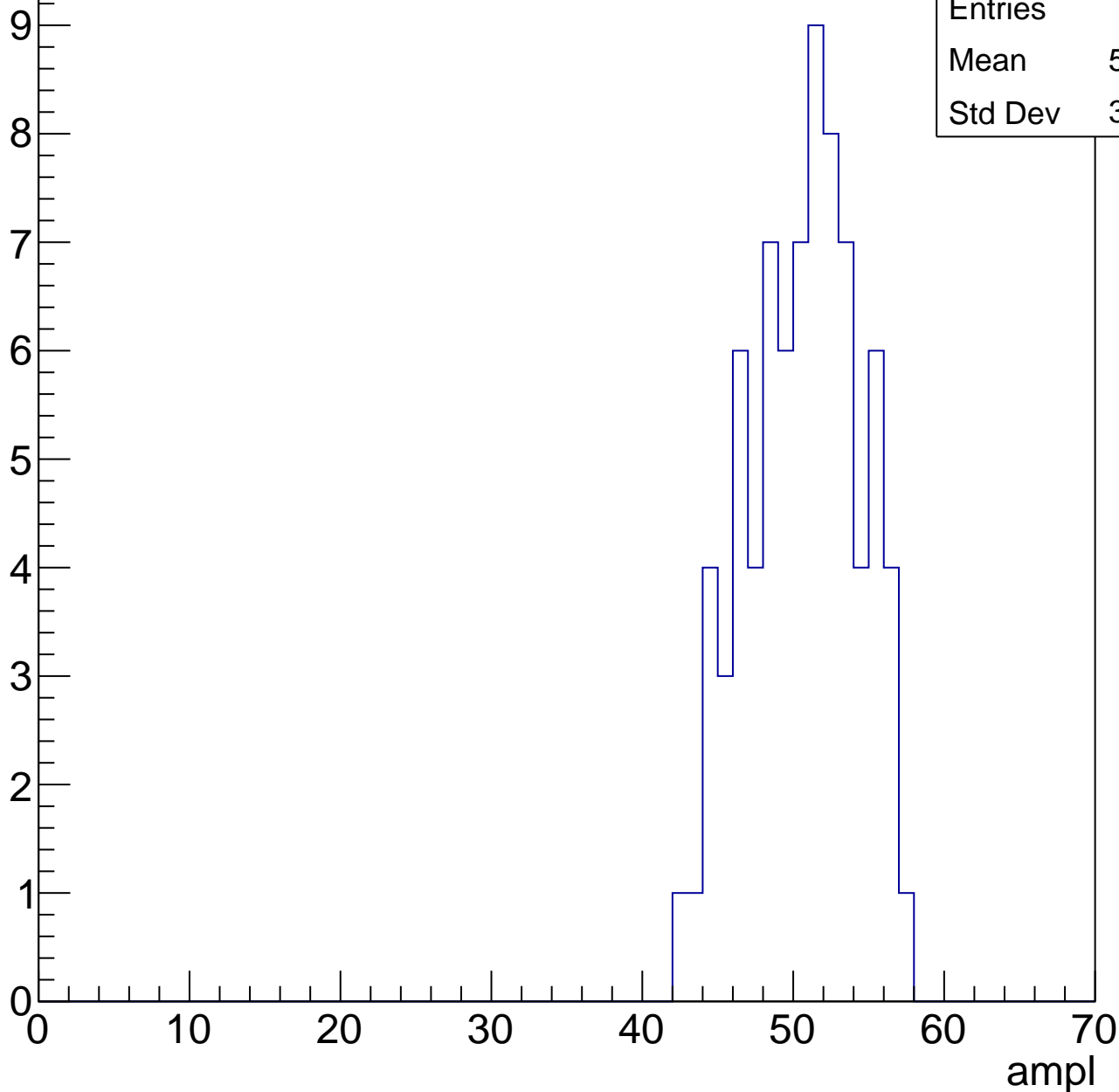
**Gaus Width: 3.9675**



# B0L000S, U25-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

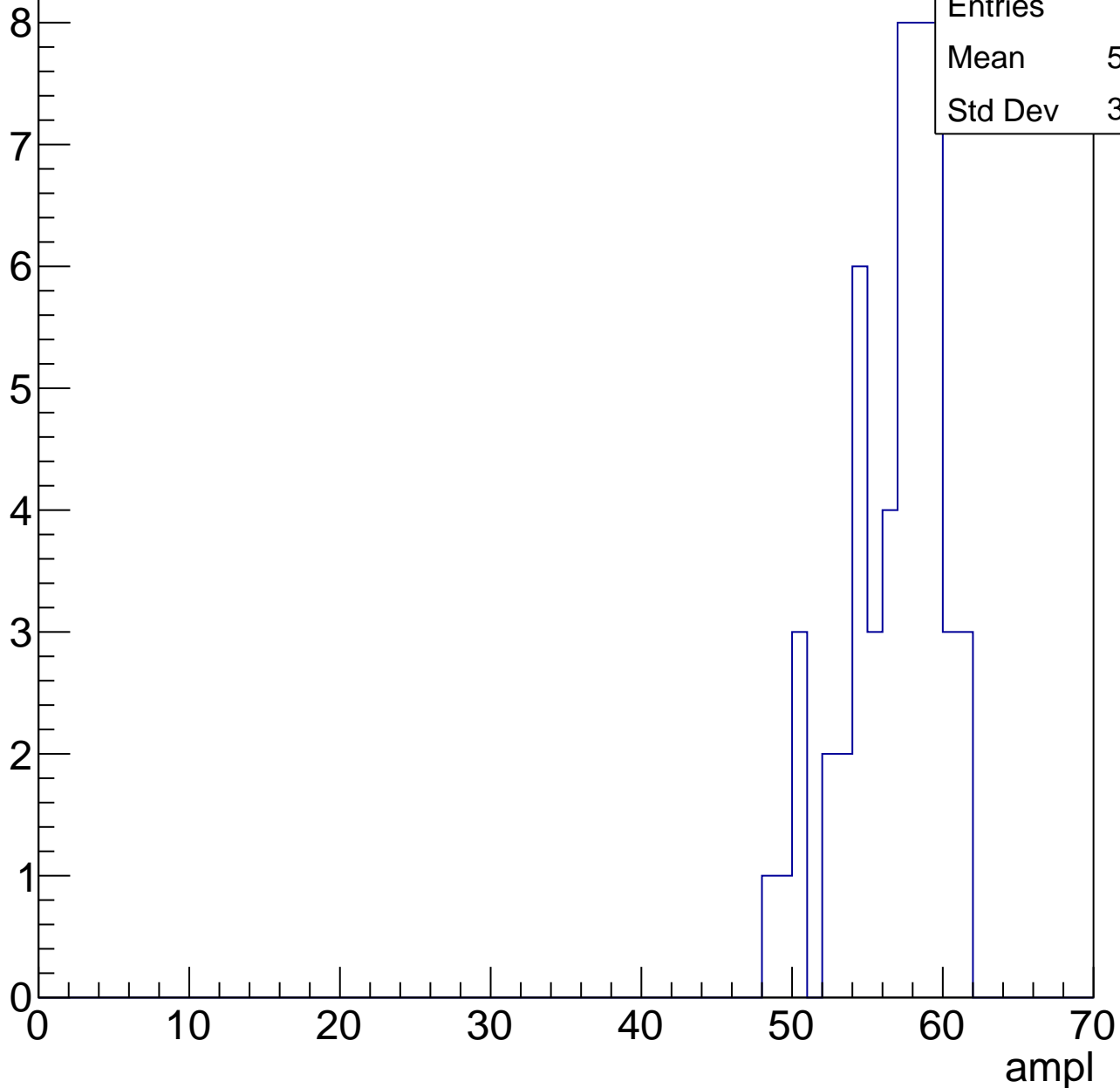


# B0L000S, U25-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56.25
Std Dev	3.192

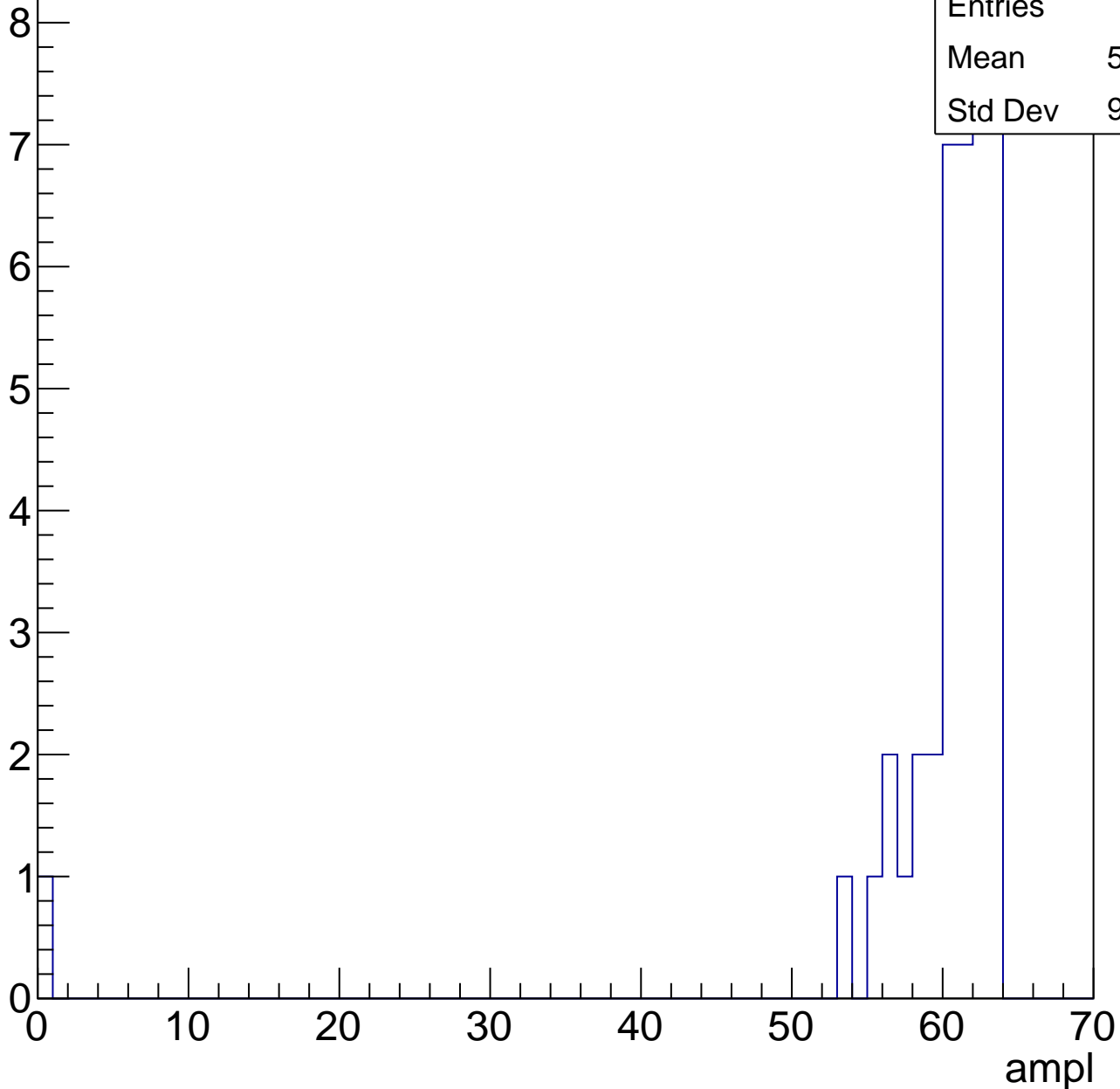


# B0L000S, U25-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.95
Std Dev	9.736



# B0L000S, U25-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch66, adc0

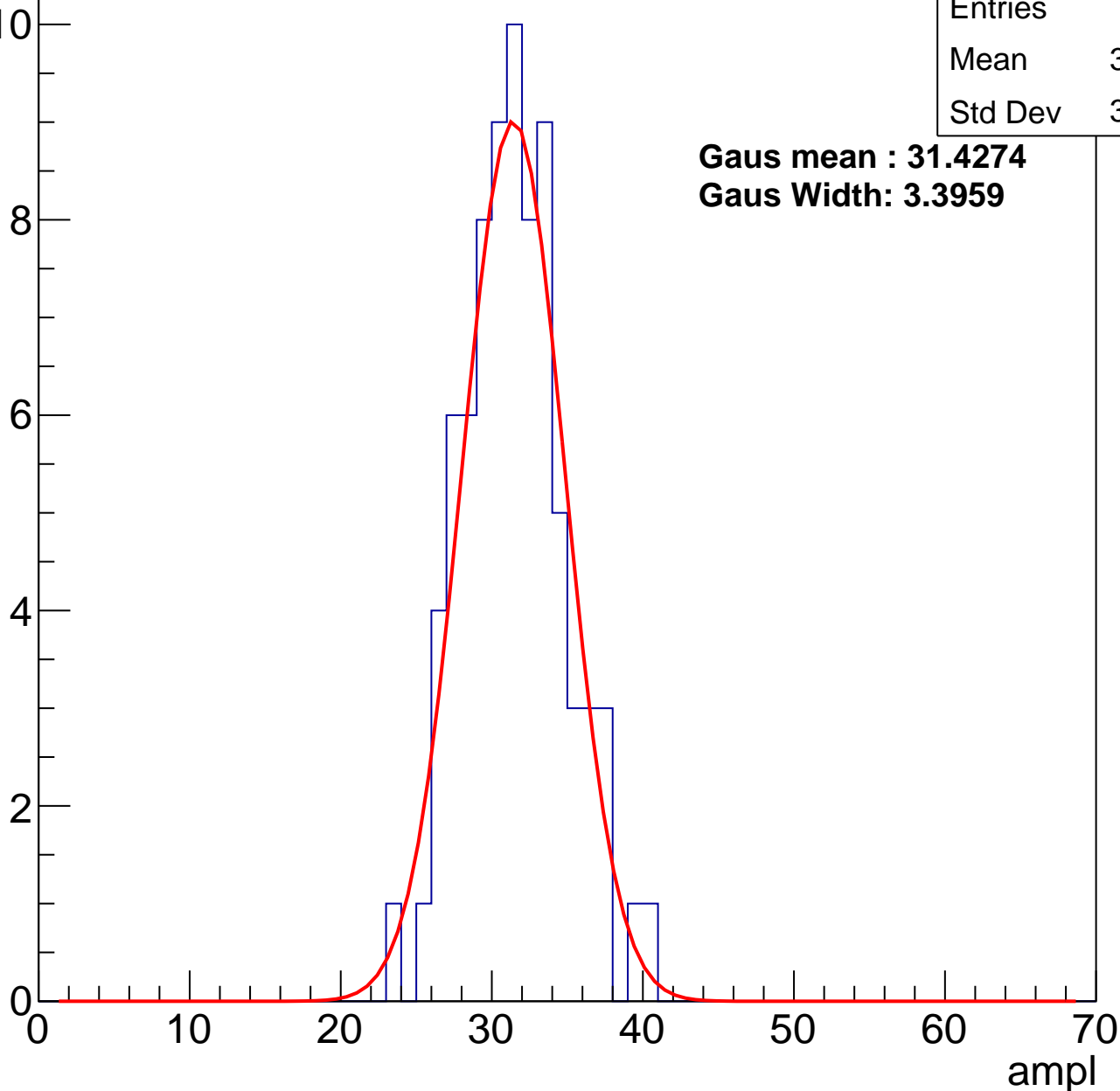
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	31.03
Std Dev	3.313

**Gaus mean : 31.4274**

**Gaus Width: 3.3959**



# B0L000S, U25-ch66, adc1

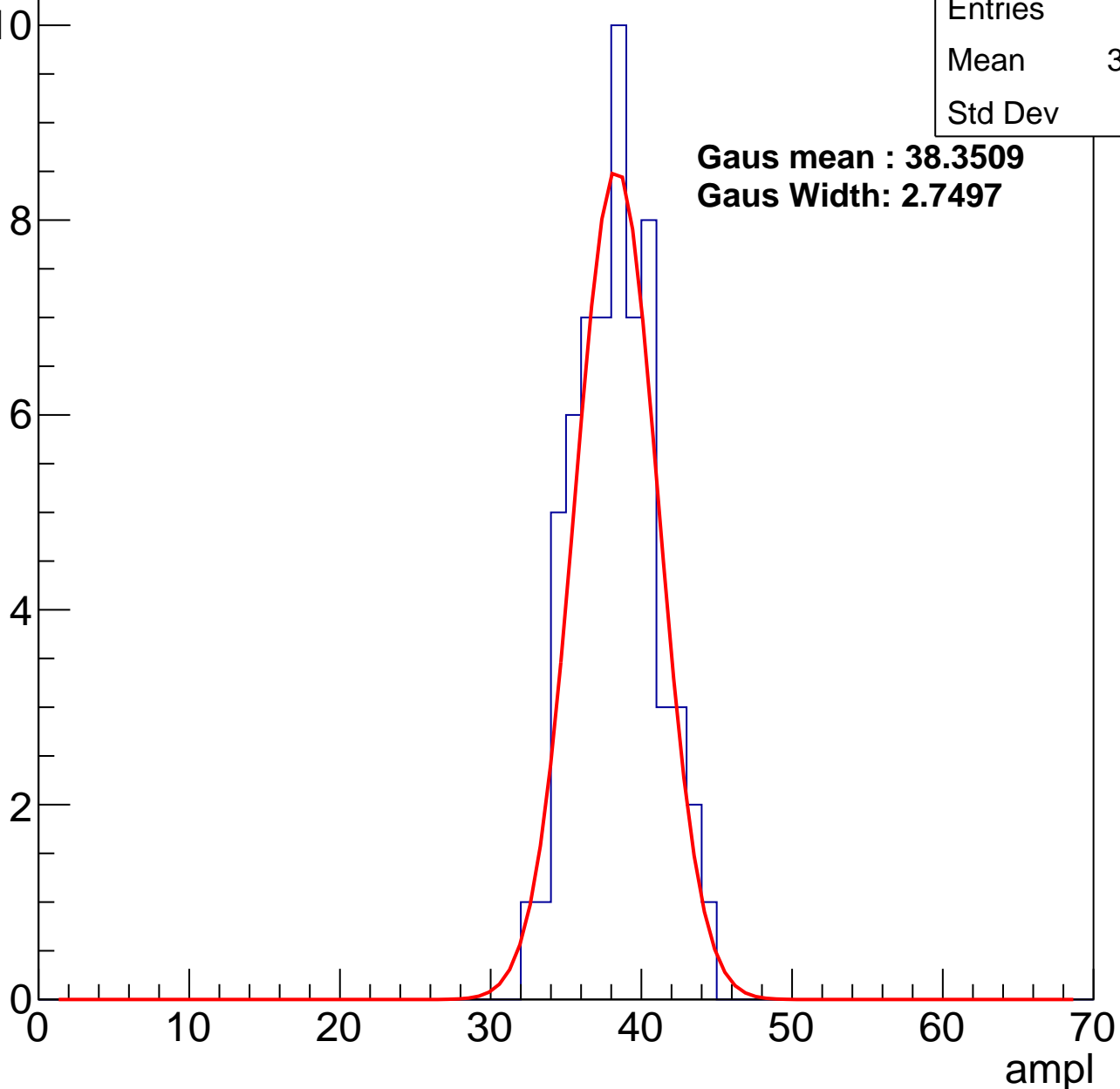
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	37.84
Std Dev	2.65

**Gaus mean : 38.3509**

**Gaus Width: 2.7497**



# B0L000S, U25-ch66, adc2

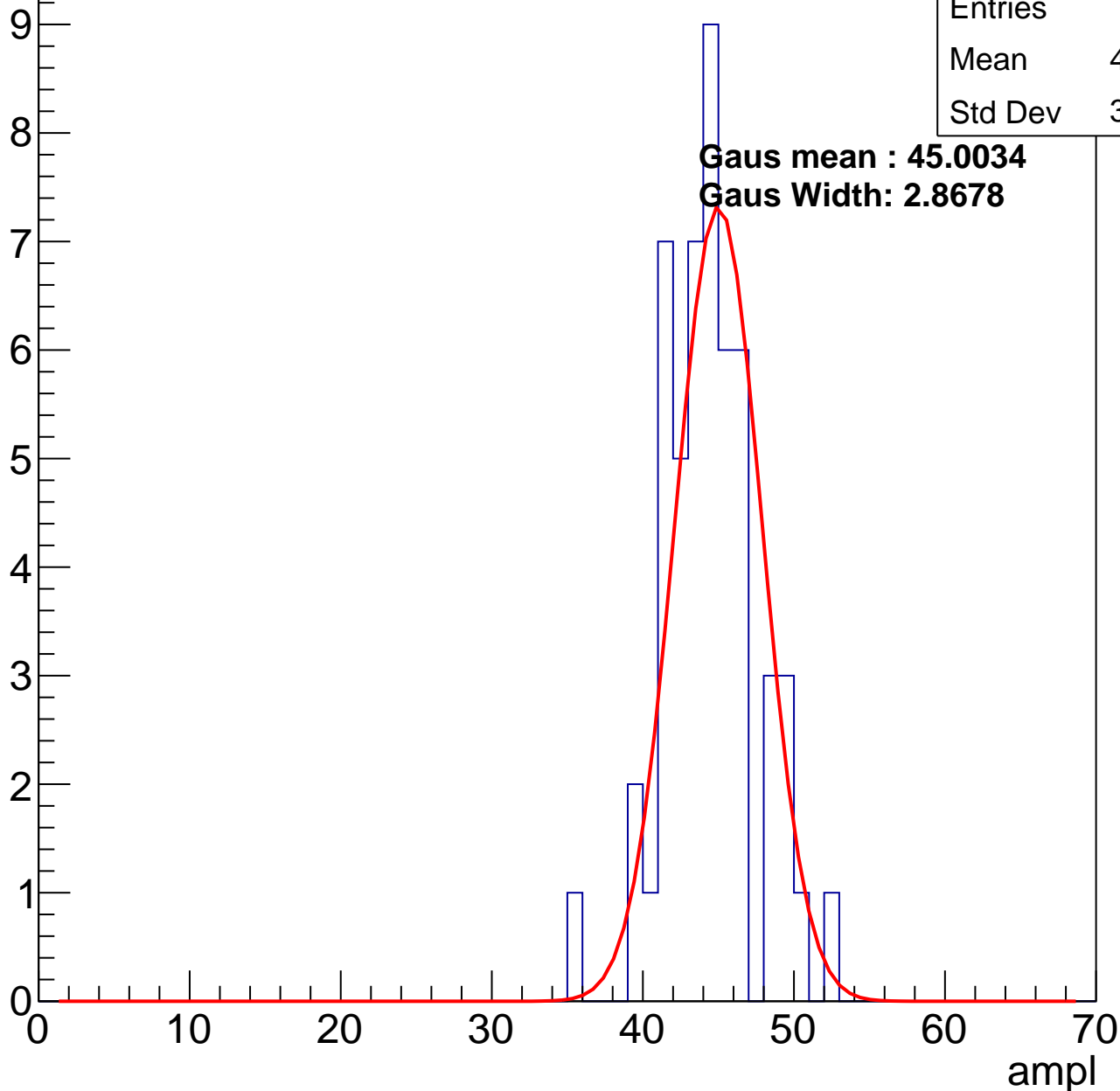
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	43.96
Std Dev	3.069

**Gaus mean : 45.0034**

**Gaus Width: 2.8678**



# B0L000S, U25-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	49.84
Std Dev	3.39

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

70

2

1

10

7

5

3

2

1

# B0L000S, U25-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	58
Mean	56.29
Std Dev	2.792

Entry

10

8

6

4

2

0

0

10

20

30

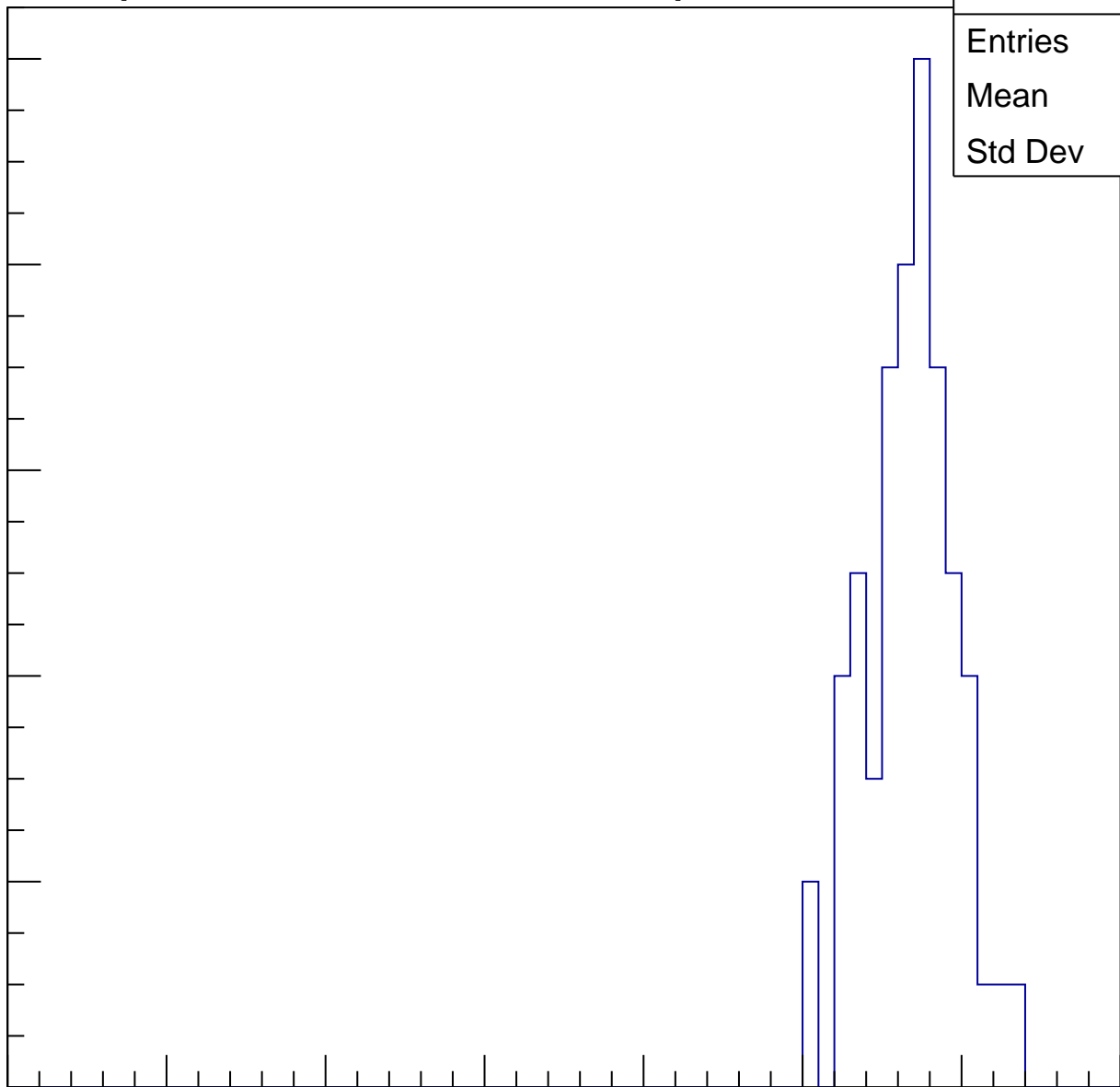
40

50

60

70

ampl

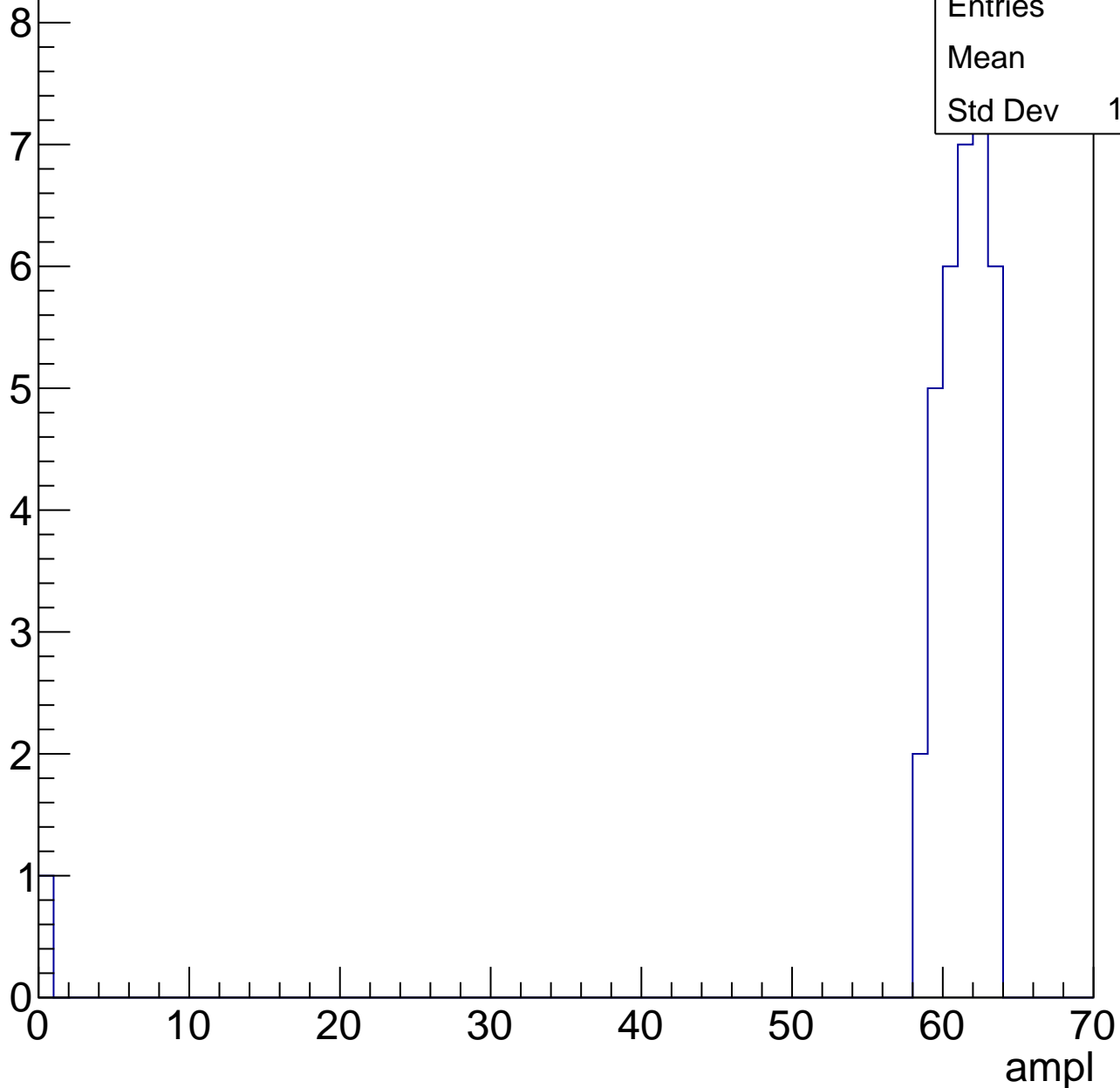


# B0L000S, U25-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	59.2
Std Dev	10.26



# B0L000S, U25-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	7.667
Std Dev	10.84

# B0L000S, U25-ch67, adc0

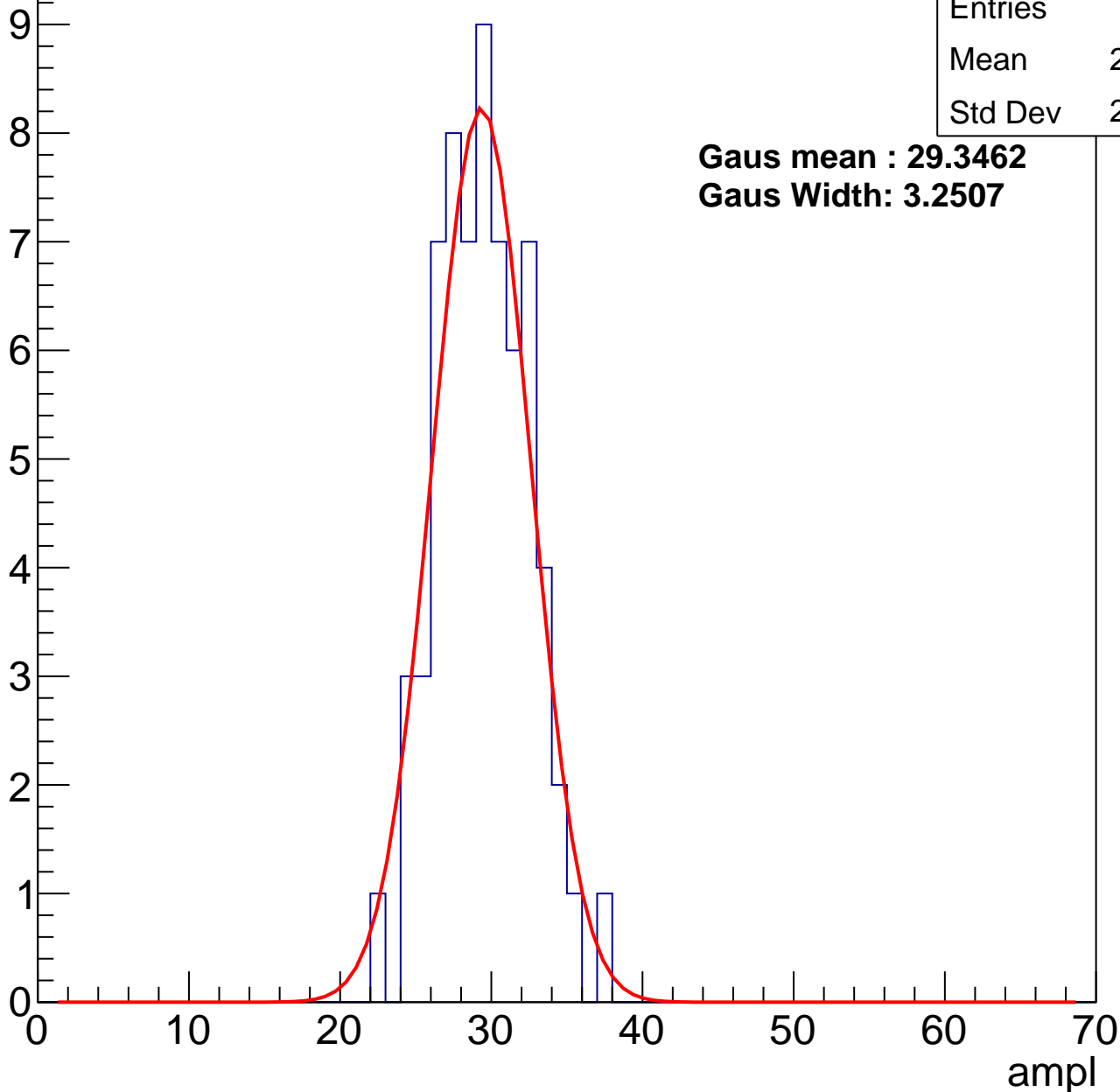
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	29.03
Std Dev	2.969

**Gaus mean : 29.3462**

**Gaus Width: 3.2507**



# B0L000S, U25-ch67, adc1

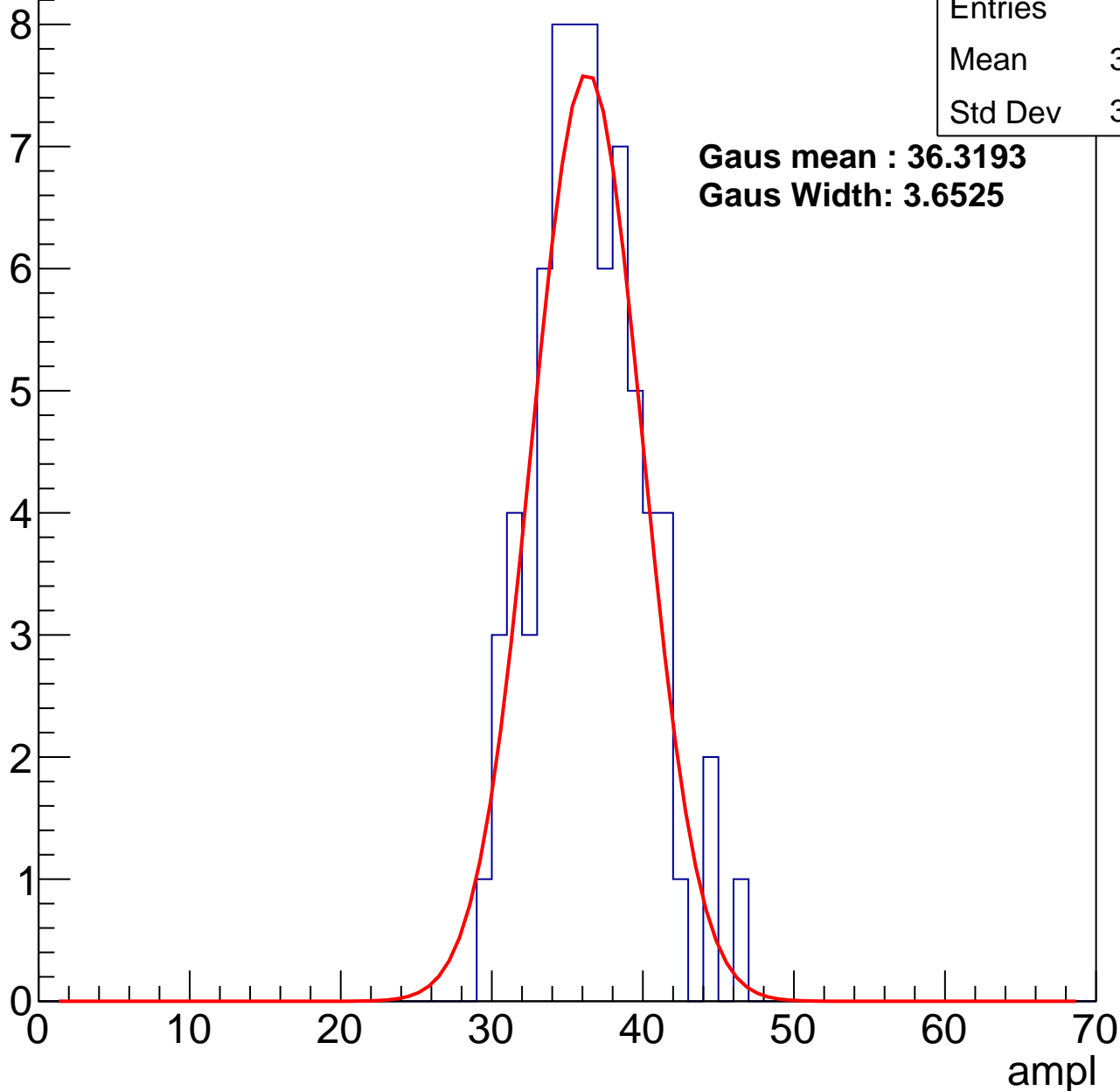
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	36.06
Std Dev	3.572

**Gaus mean : 36.3193**

**Gaus Width: 3.6525**



# B0L000S, U25-ch67, adc2

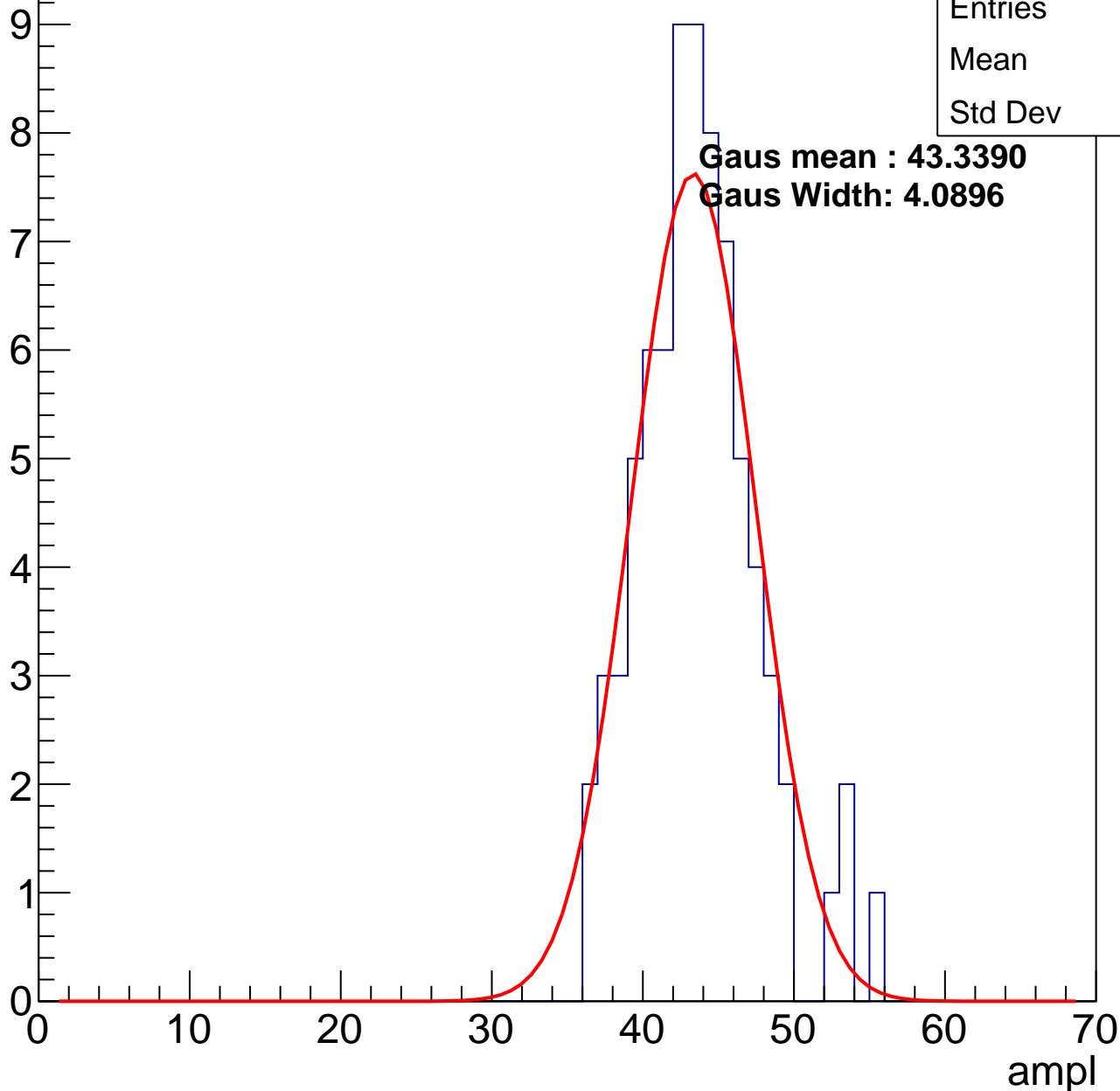
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	43.2
Std Dev	3.91

**Gaus mean : 43.3390**

**Gaus Width: 4.0896**

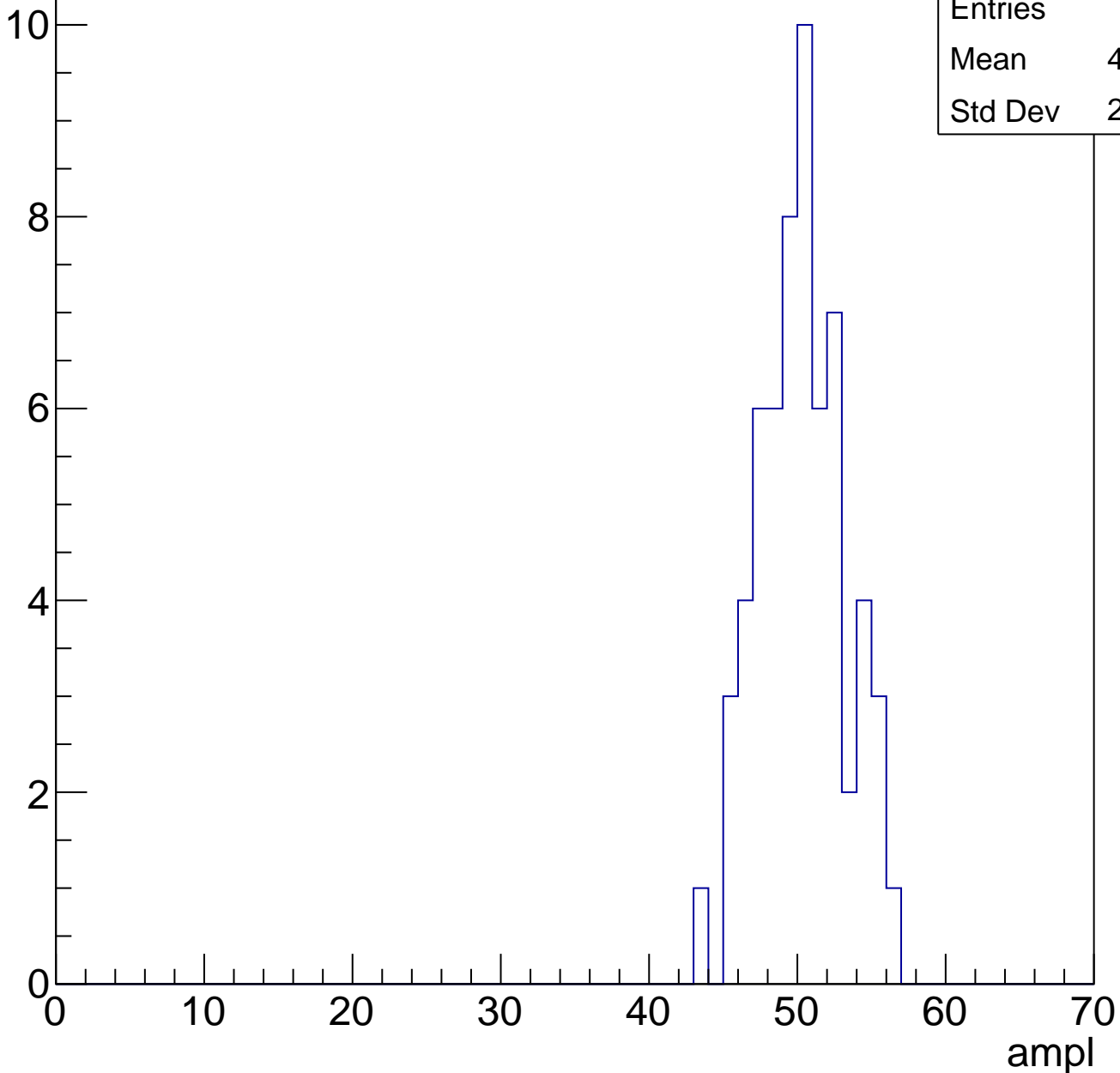


# B0L000S, U25-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	49.79
Std Dev	2.858



# B0L000S, U25-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	56
Mean	55.79
Std Dev	2.932

Entry

10

8

6

4

2

0

0

10

20

30

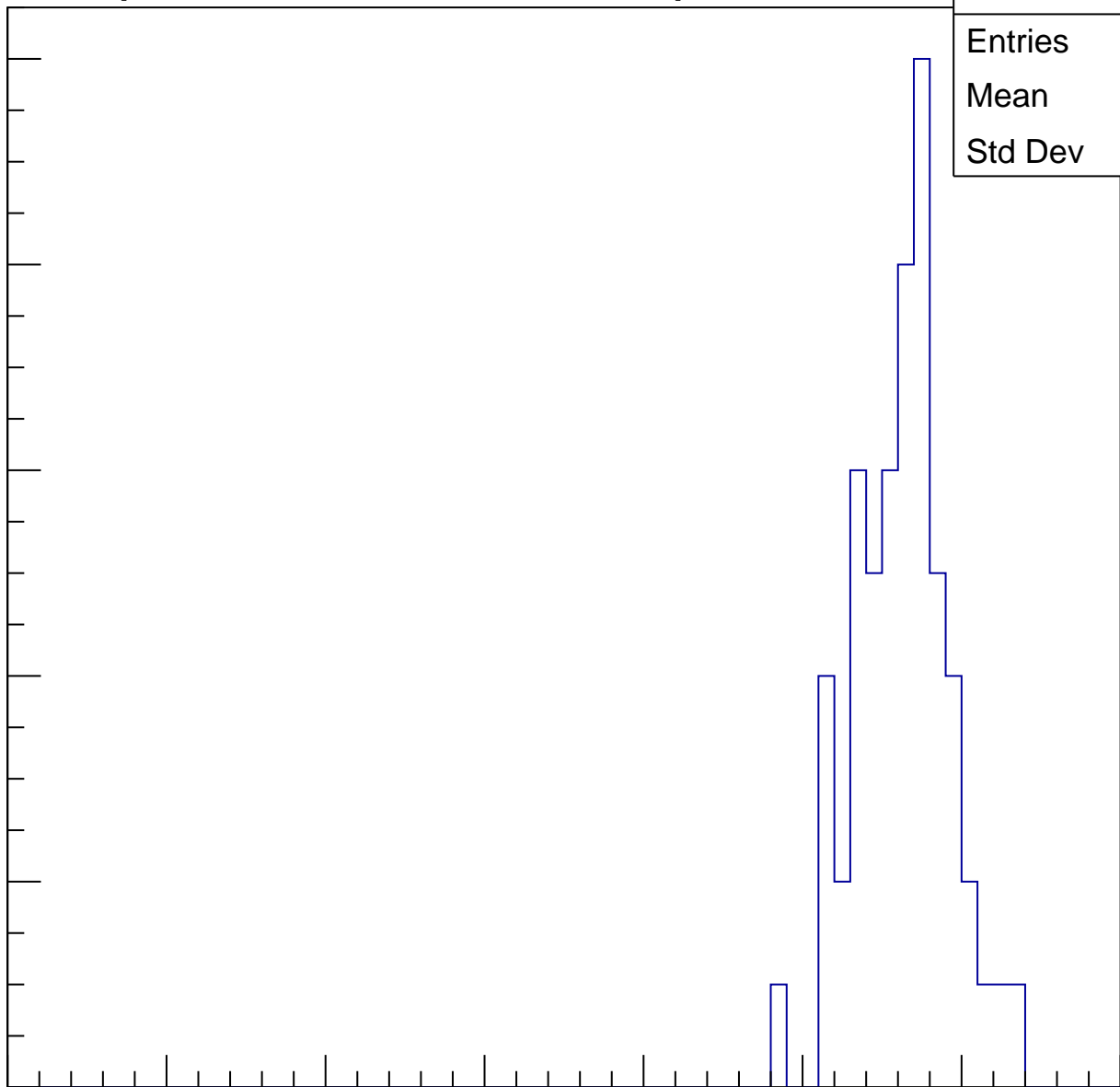
40

50

60

70

ampl



# B0L000S, U25-ch67, adc5

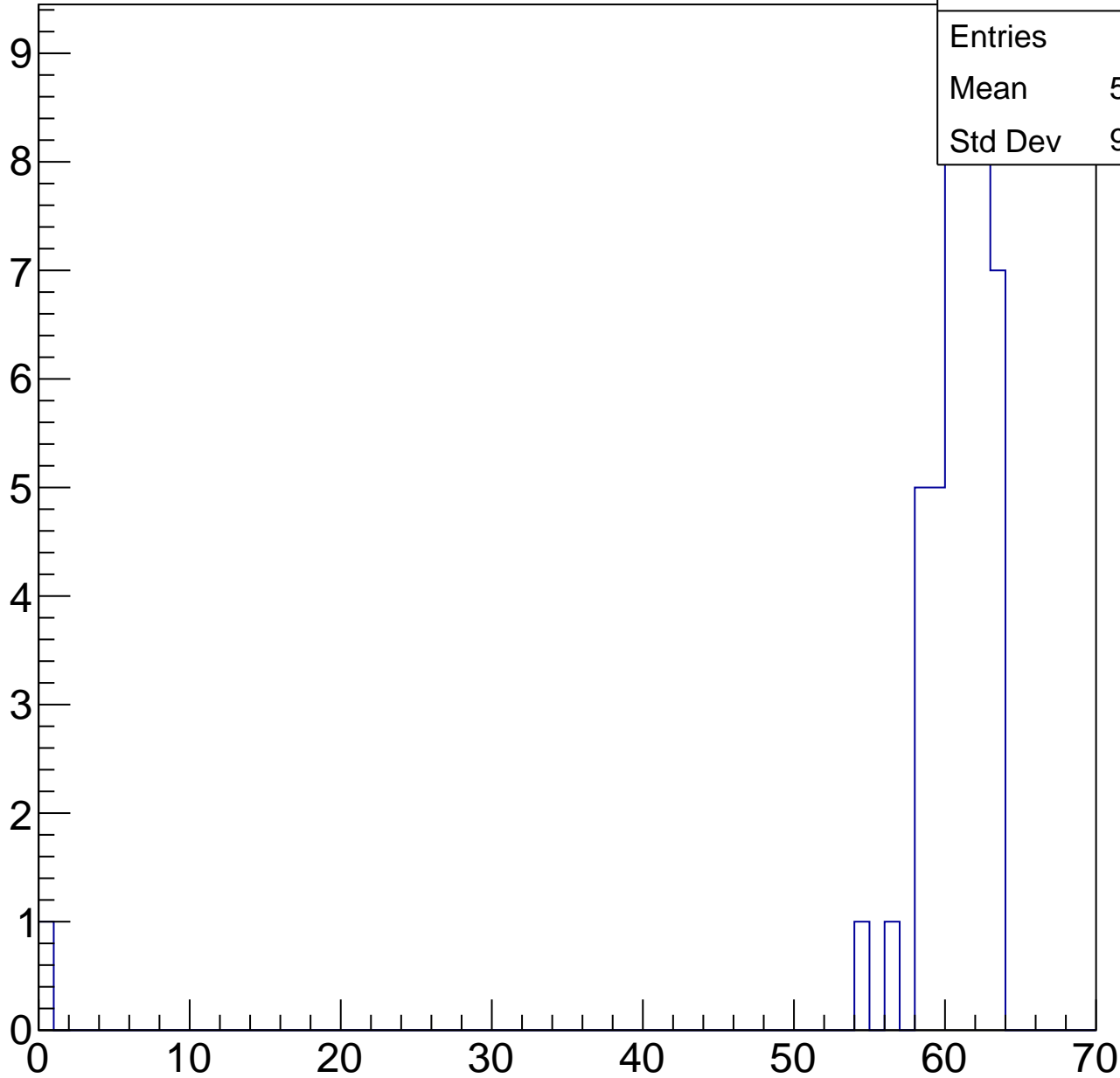
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	45
Mean	59.13
Std Dev	9.125

ampl



# B0L000S, U25-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

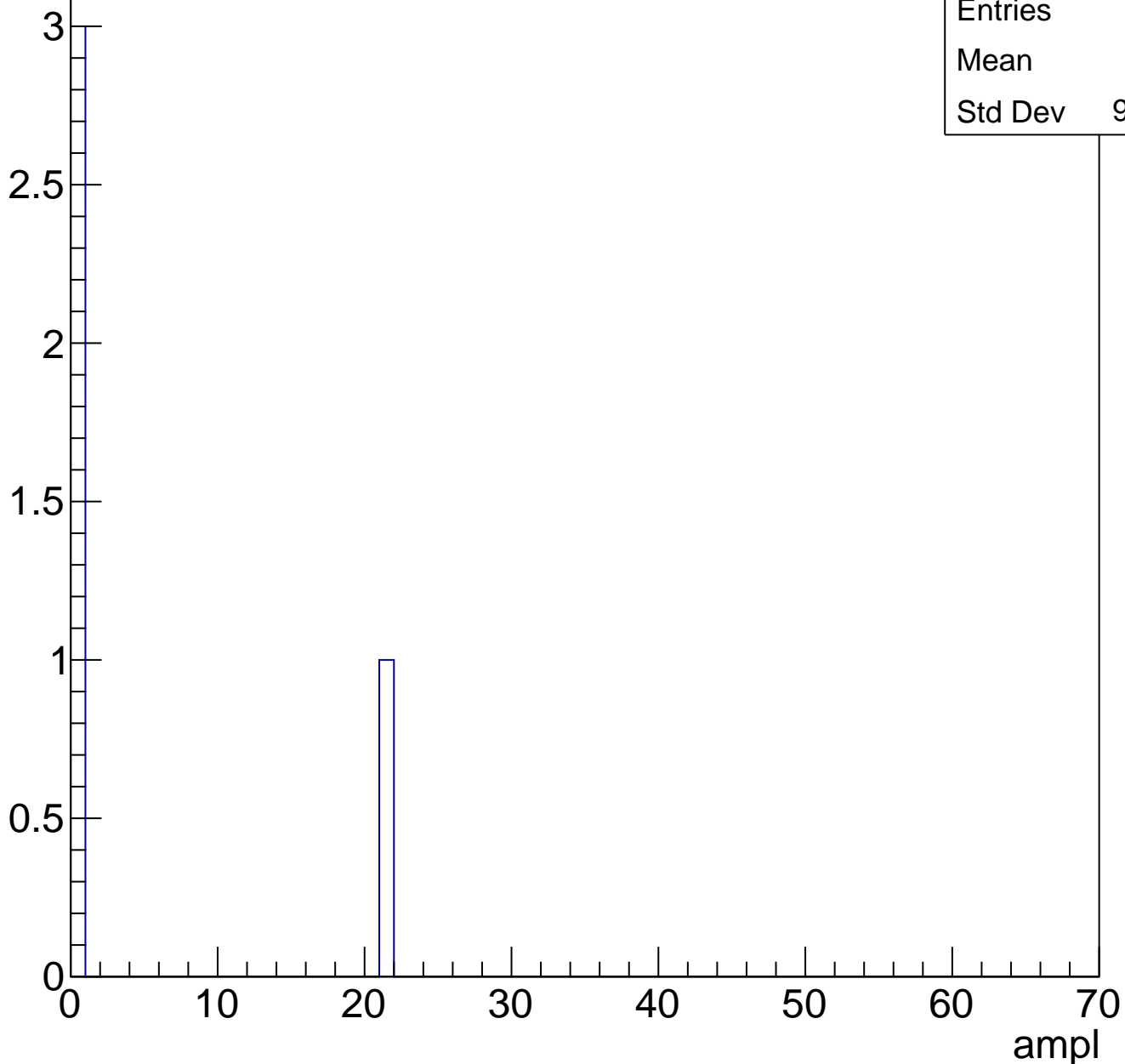




# B0L000S, U25-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	4
Mean	5.25
Std Dev	9.093

# B0L000S, U25-ch68, adc0

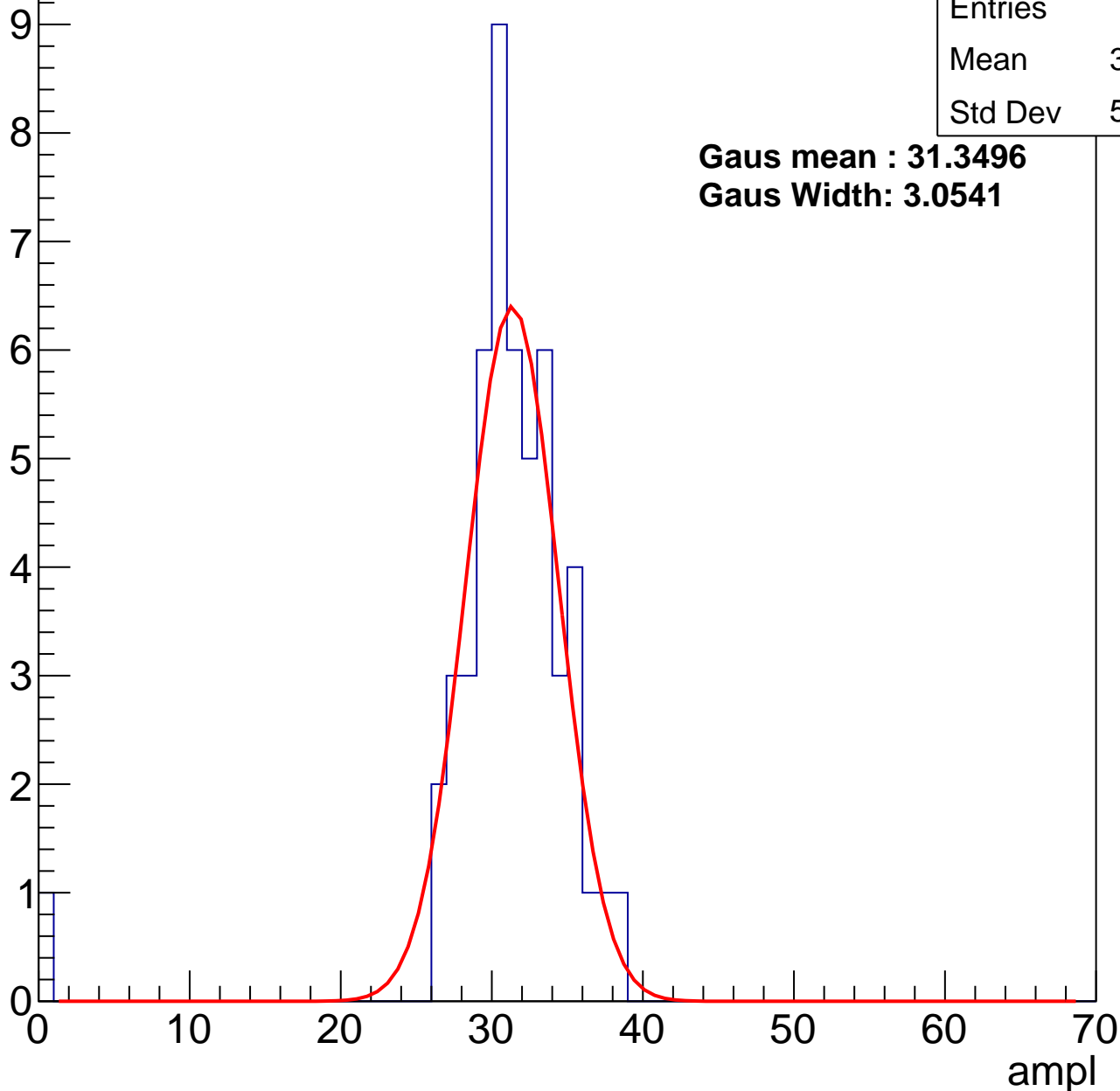
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	30.55
Std Dev	5.123

**Gaus mean : 31.3496**

**Gaus Width: 3.0541**



# B0L000S, U25-ch68, adc1

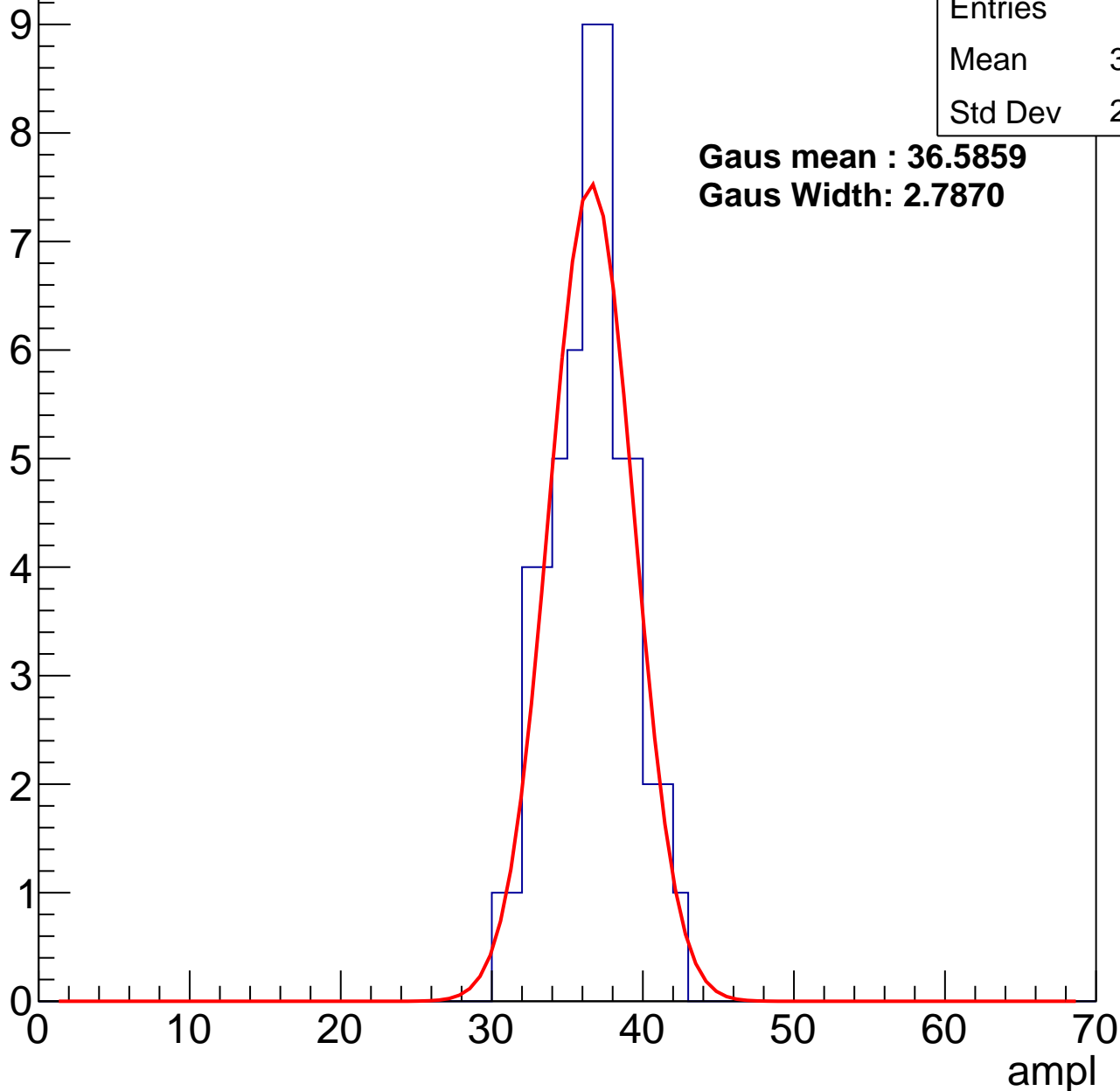
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	36.06
Std Dev	2.649

**Gaus mean : 36.5859**

**Gaus Width: 2.7870**



# B0L000S, U25-ch68, adc2

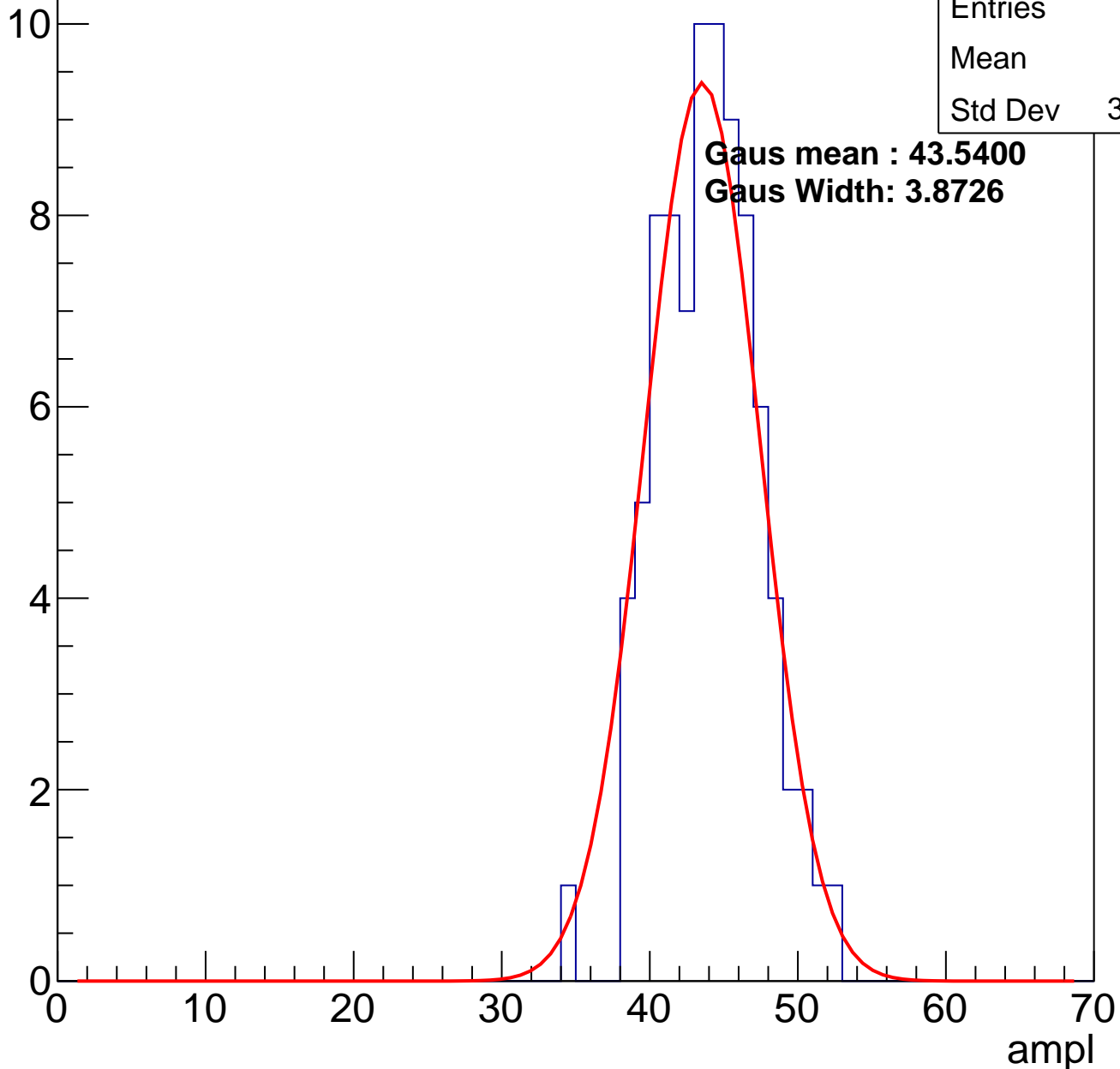
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	86
Mean	43.5
Std Dev	3.375

**Gaus mean : 43.5400**

**Gaus Width: 3.8726**

Entry

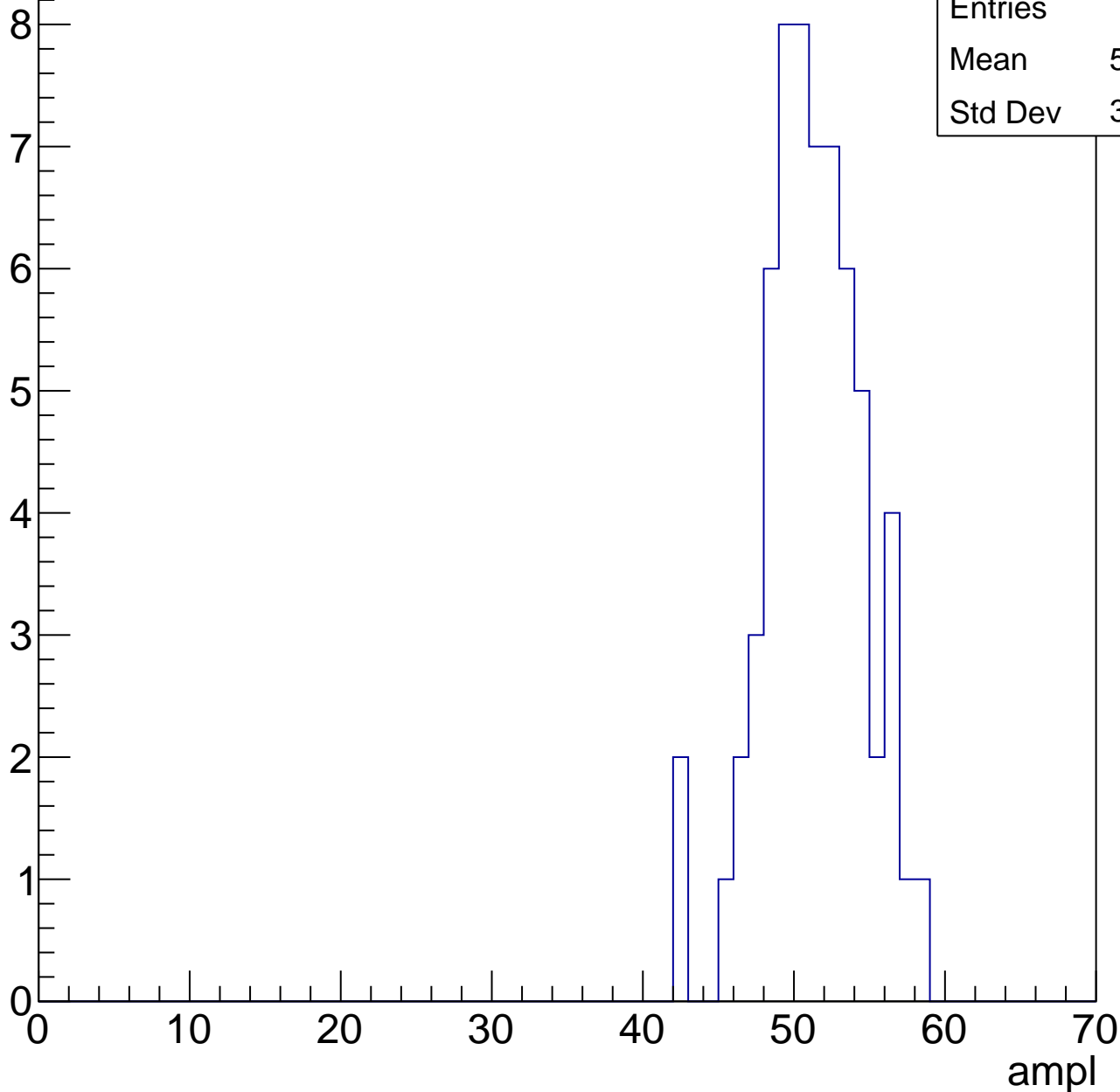


# B0L000S, U25-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

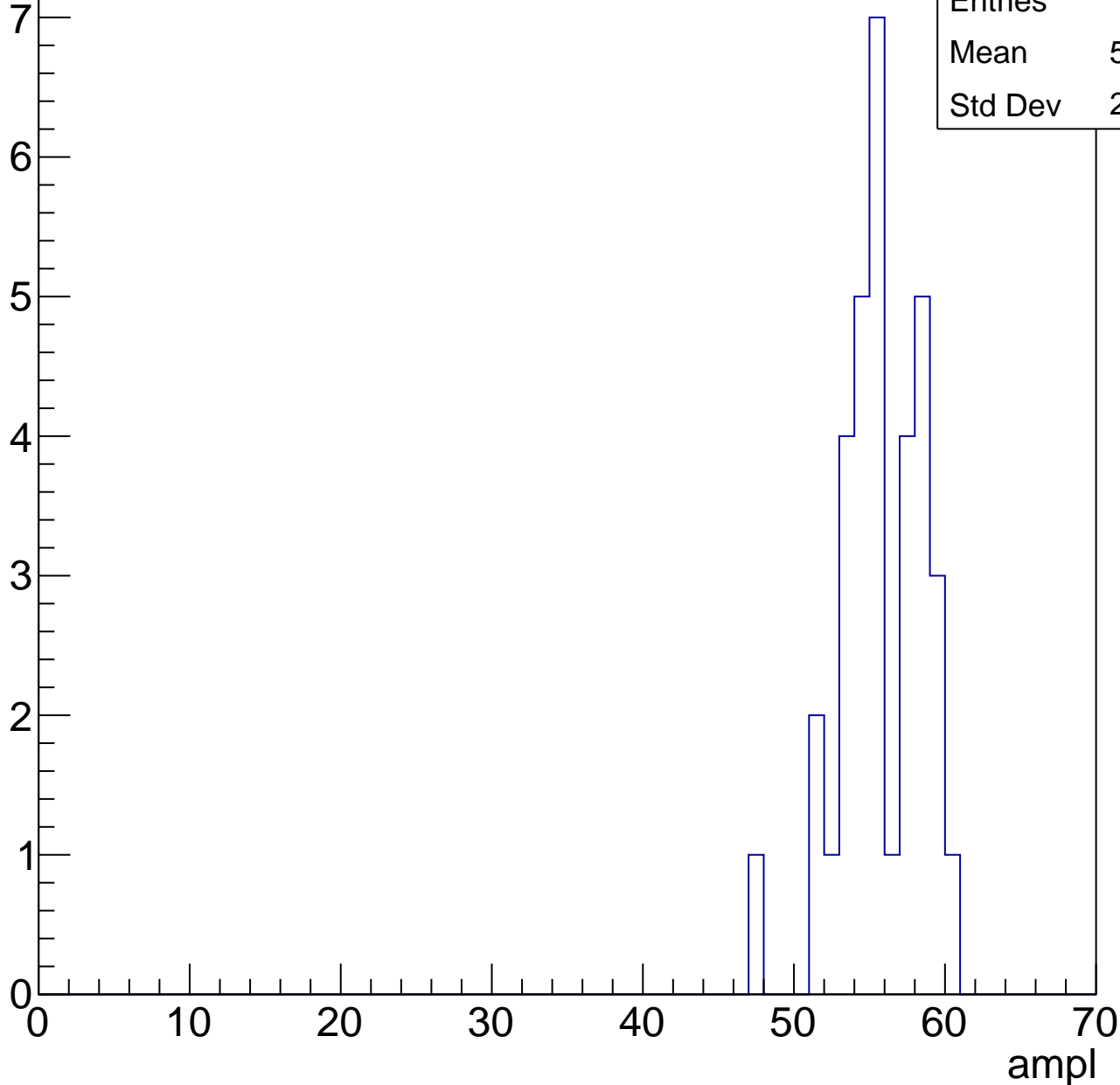
Entries	63
Mean	50.79
Std Dev	3.286



# B0L000S, U25-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

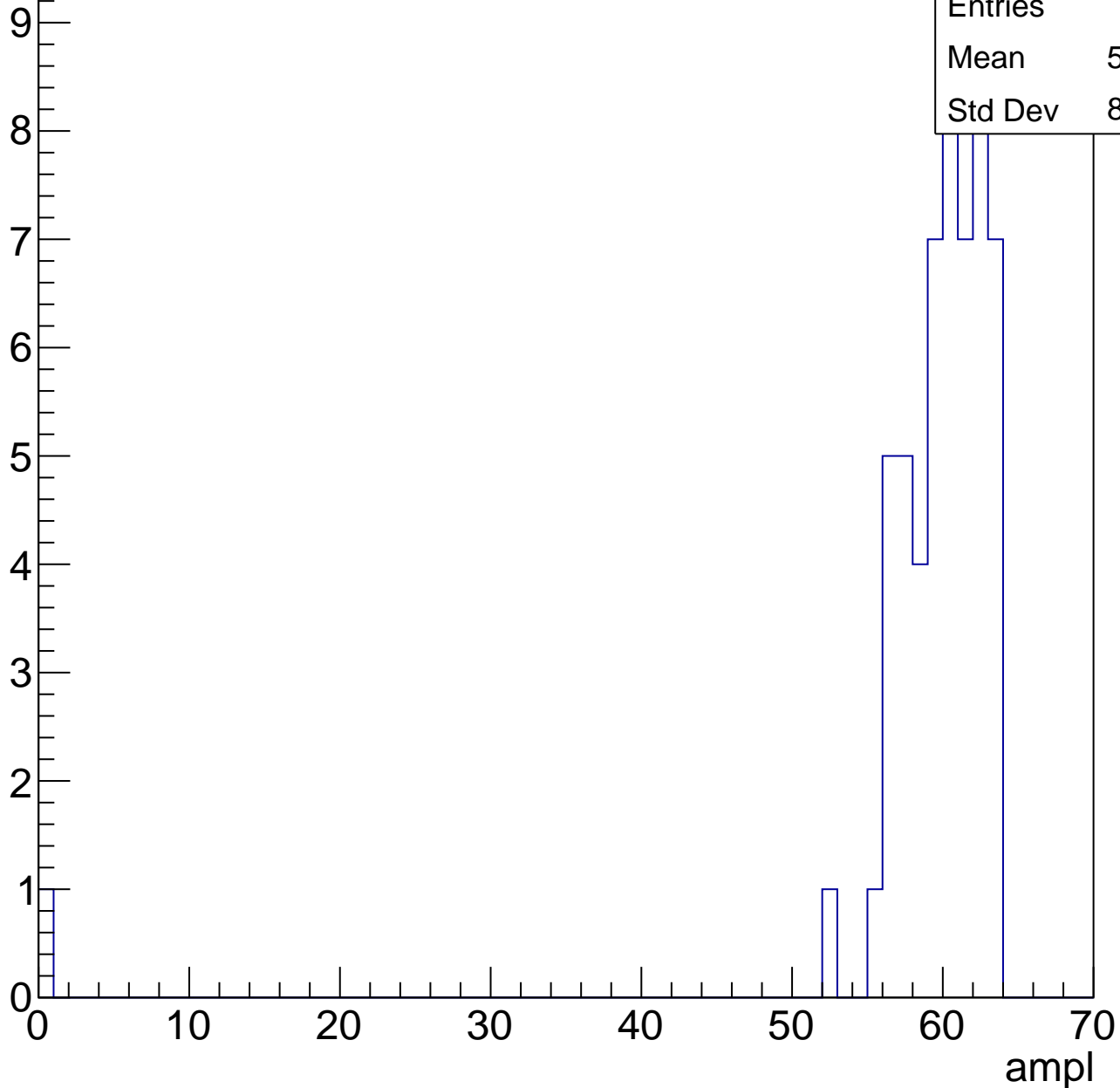
Entry



# B0L000S, U25-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

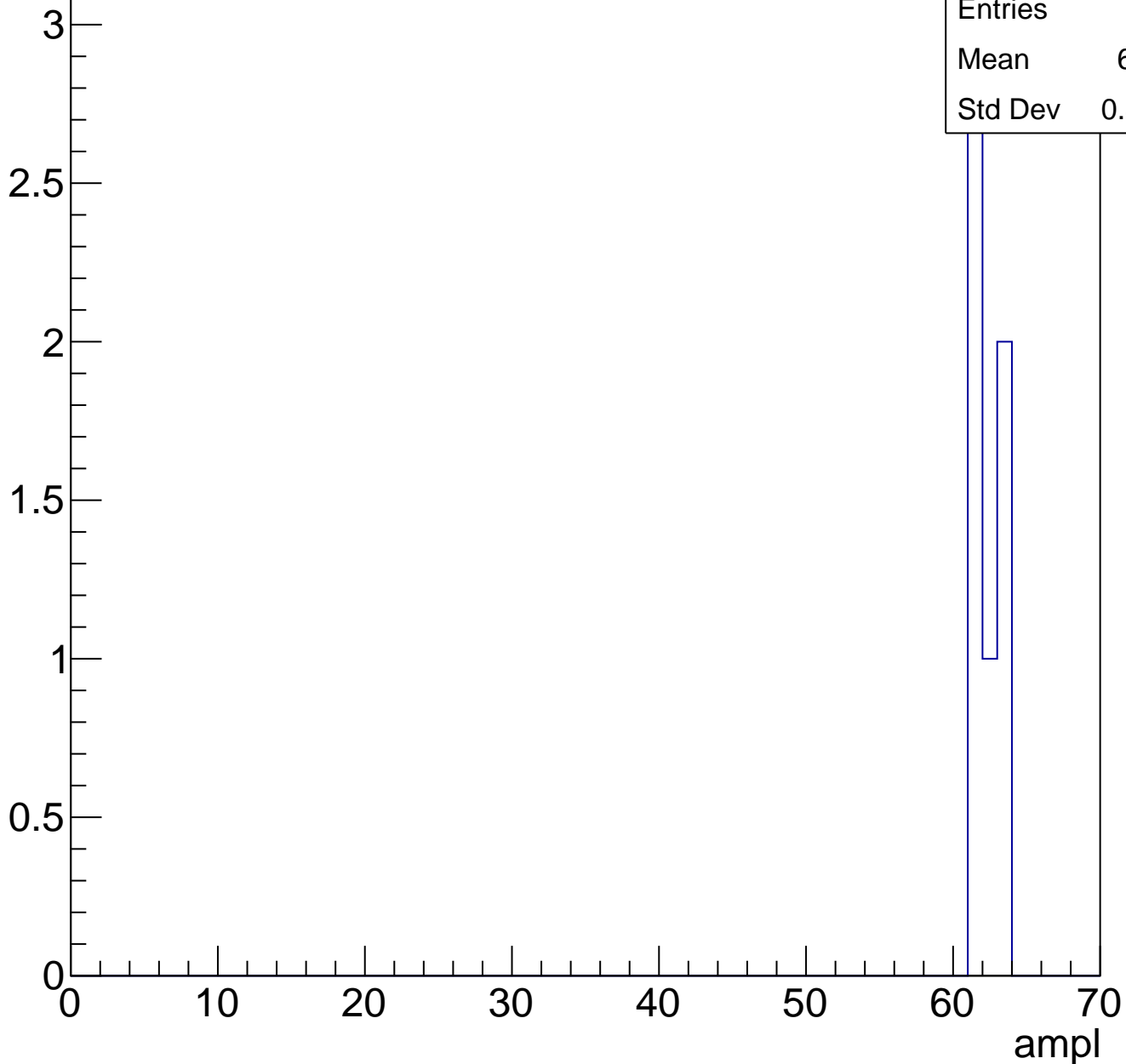
Entry



# B0L000S, U25-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

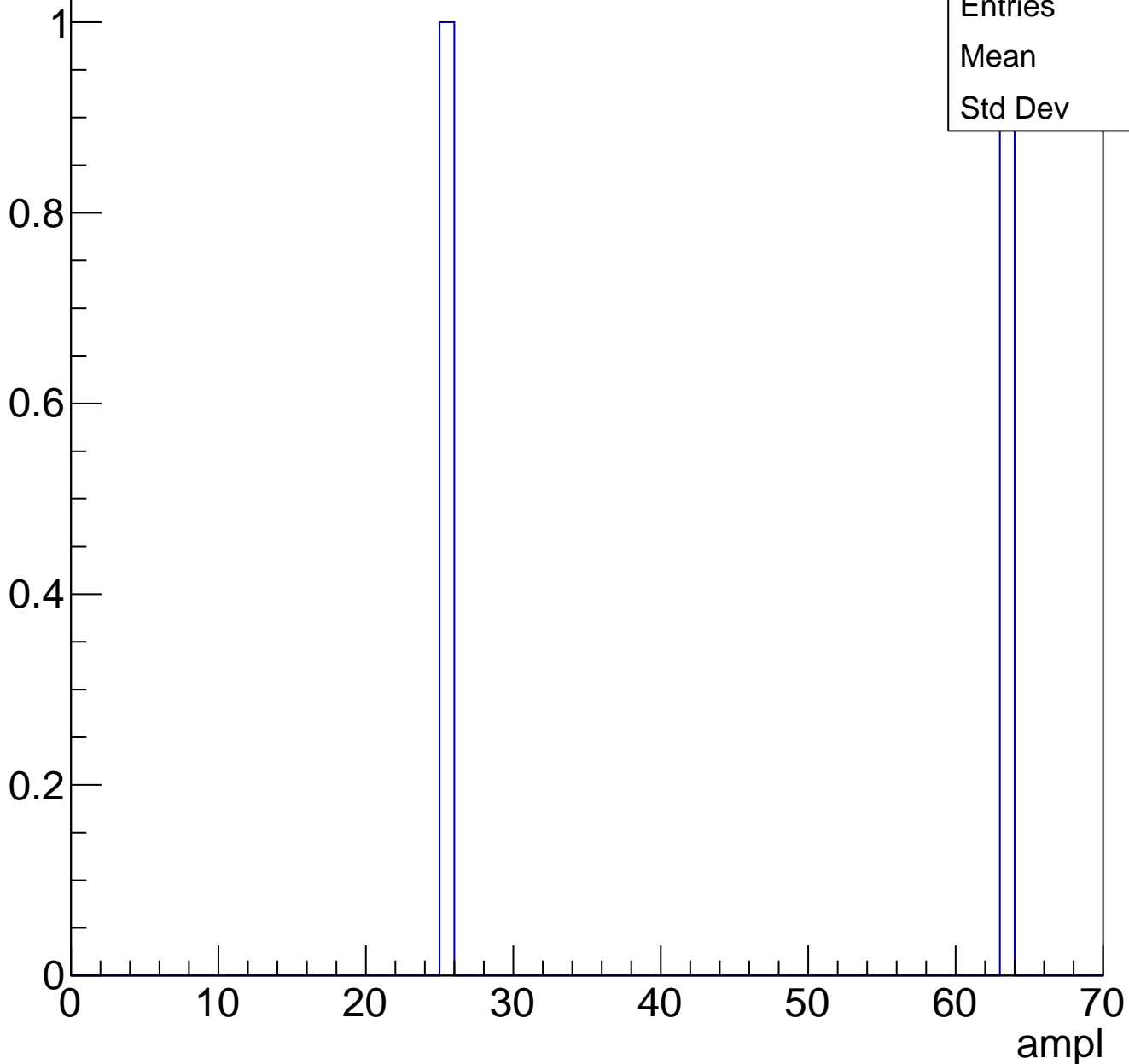




# B0L000S, U25-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch69, adc0

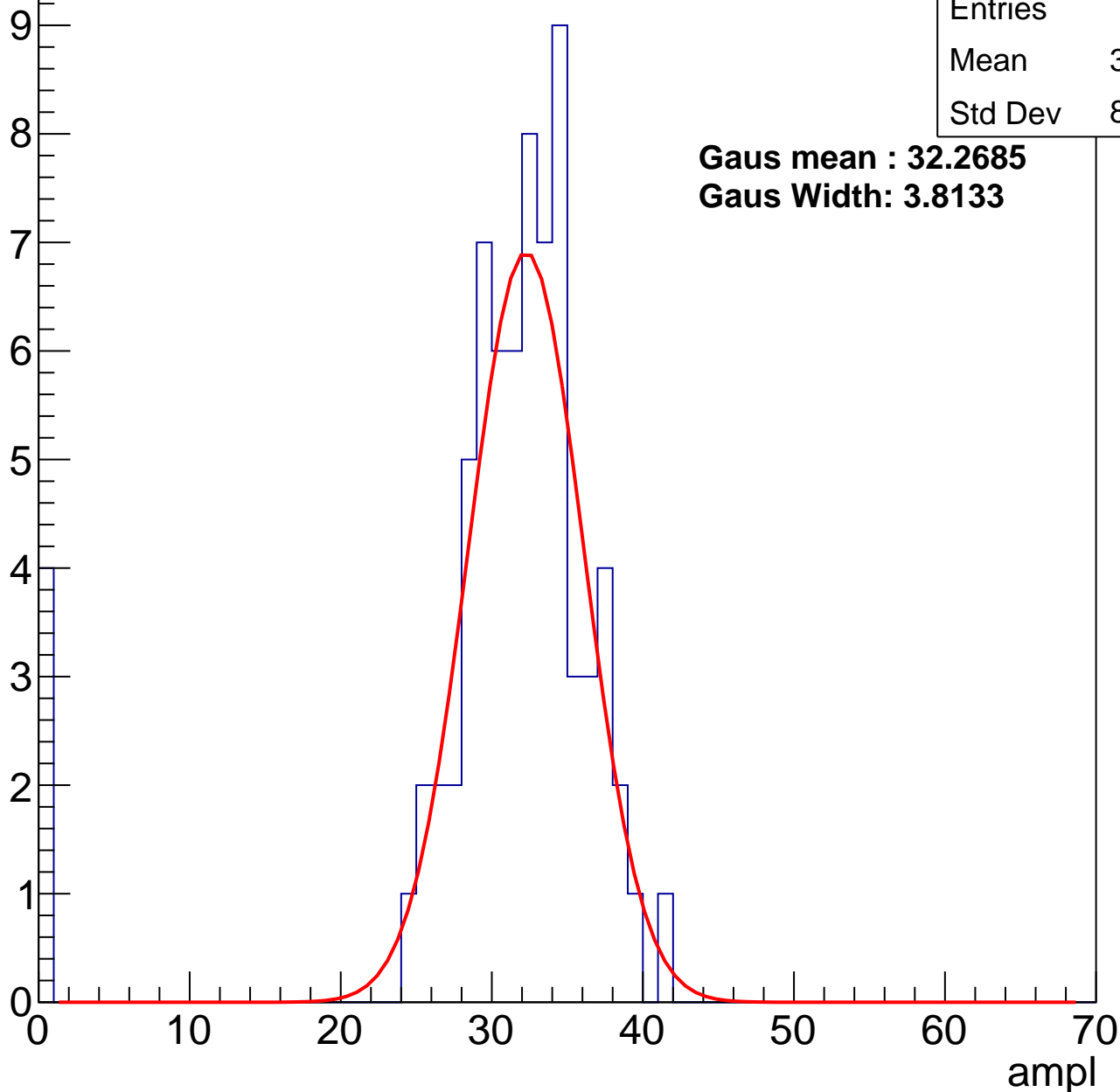
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	30.12
Std Dev	8.037

**Gaus mean : 32.2685**

**Gaus Width: 3.8133**



# B0L000S, U25-ch69, adc1

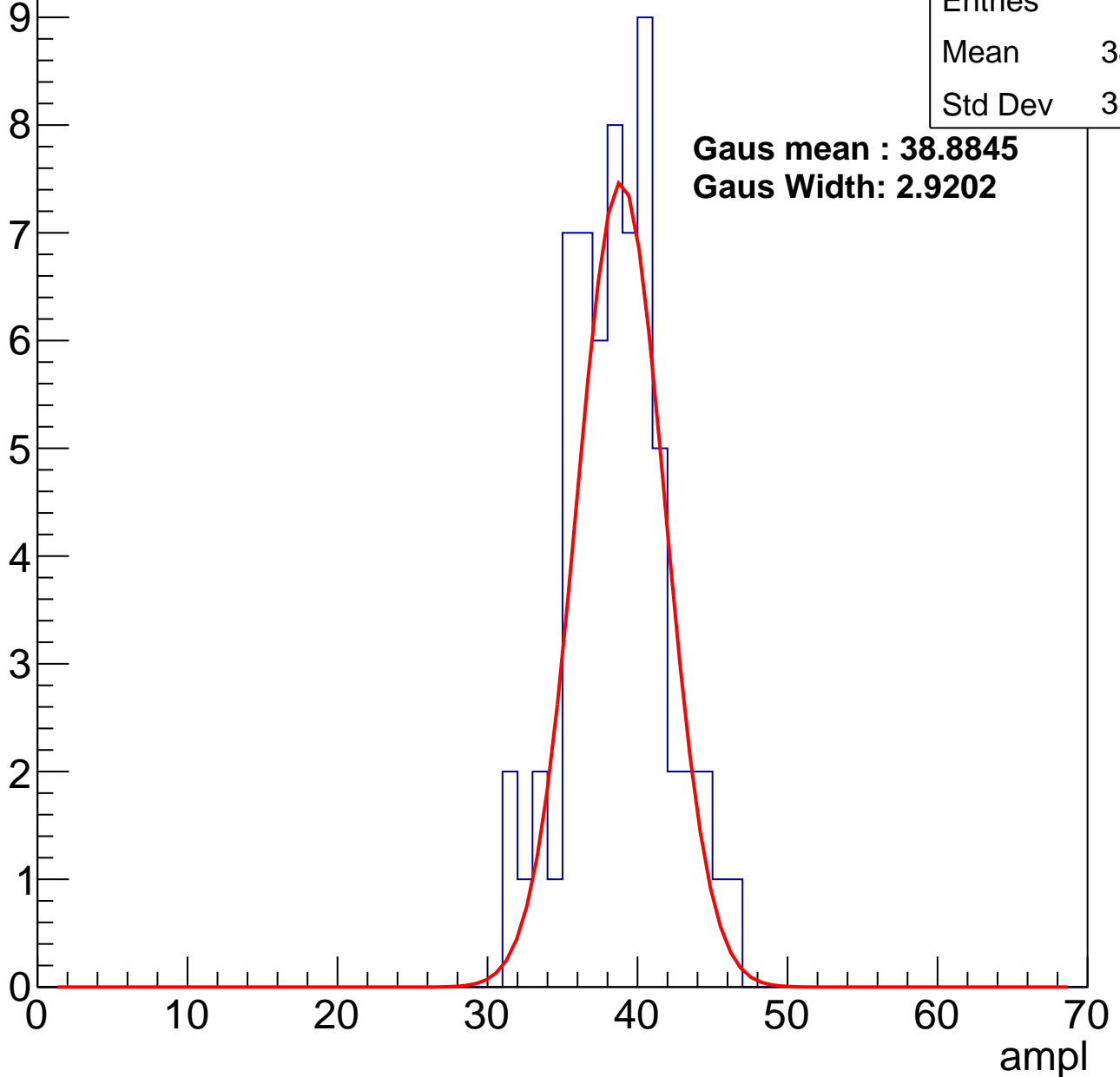
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	38.16
Std Dev	3.213

**Gaus mean : 38.8845**

**Gaus Width: 2.9202**



# B0L000S, U25-ch69, adc2

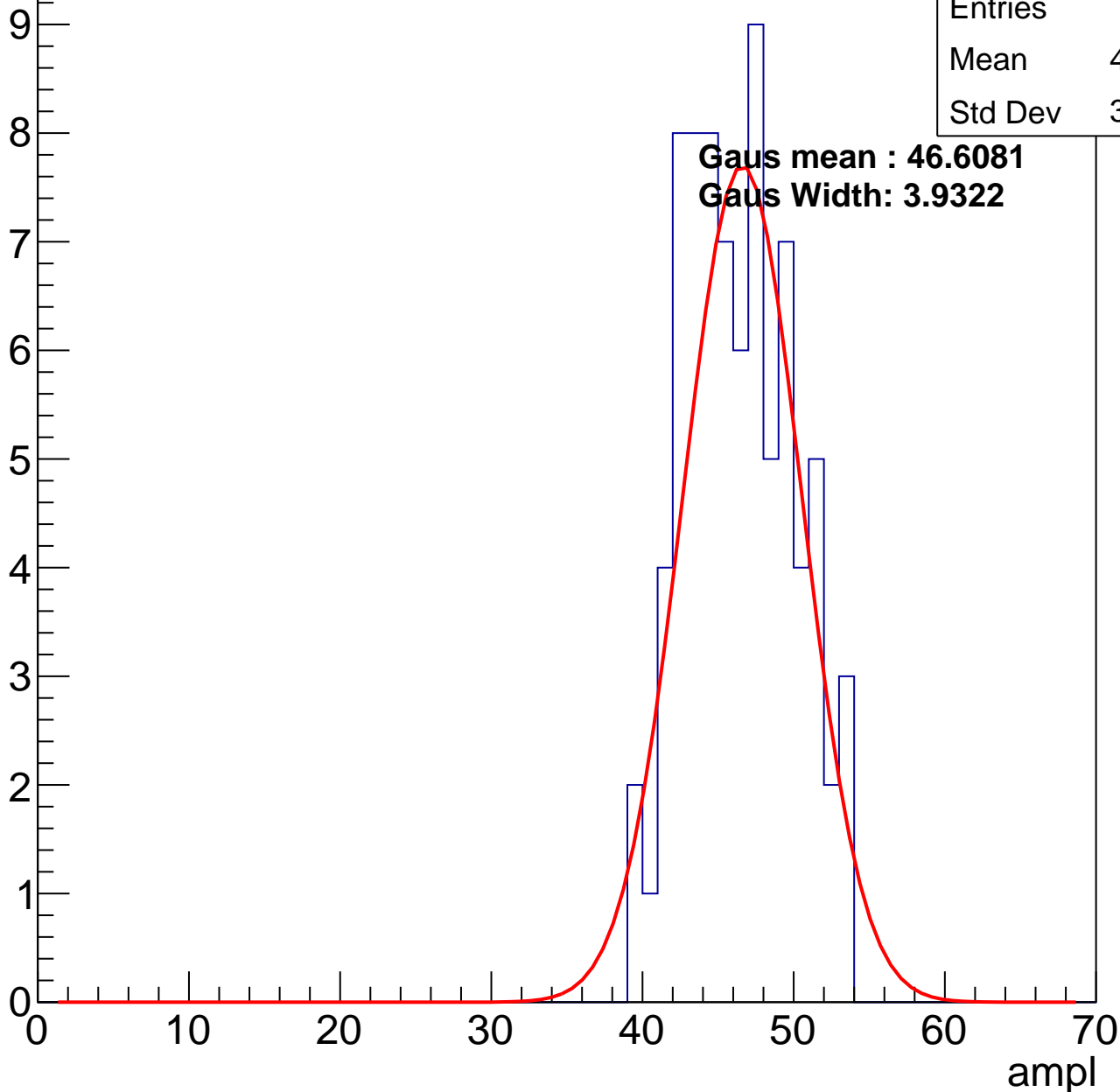
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	45.94
Std Dev	3.509

**Gaus mean : 46.6081**

**Gaus Width: 3.9322**

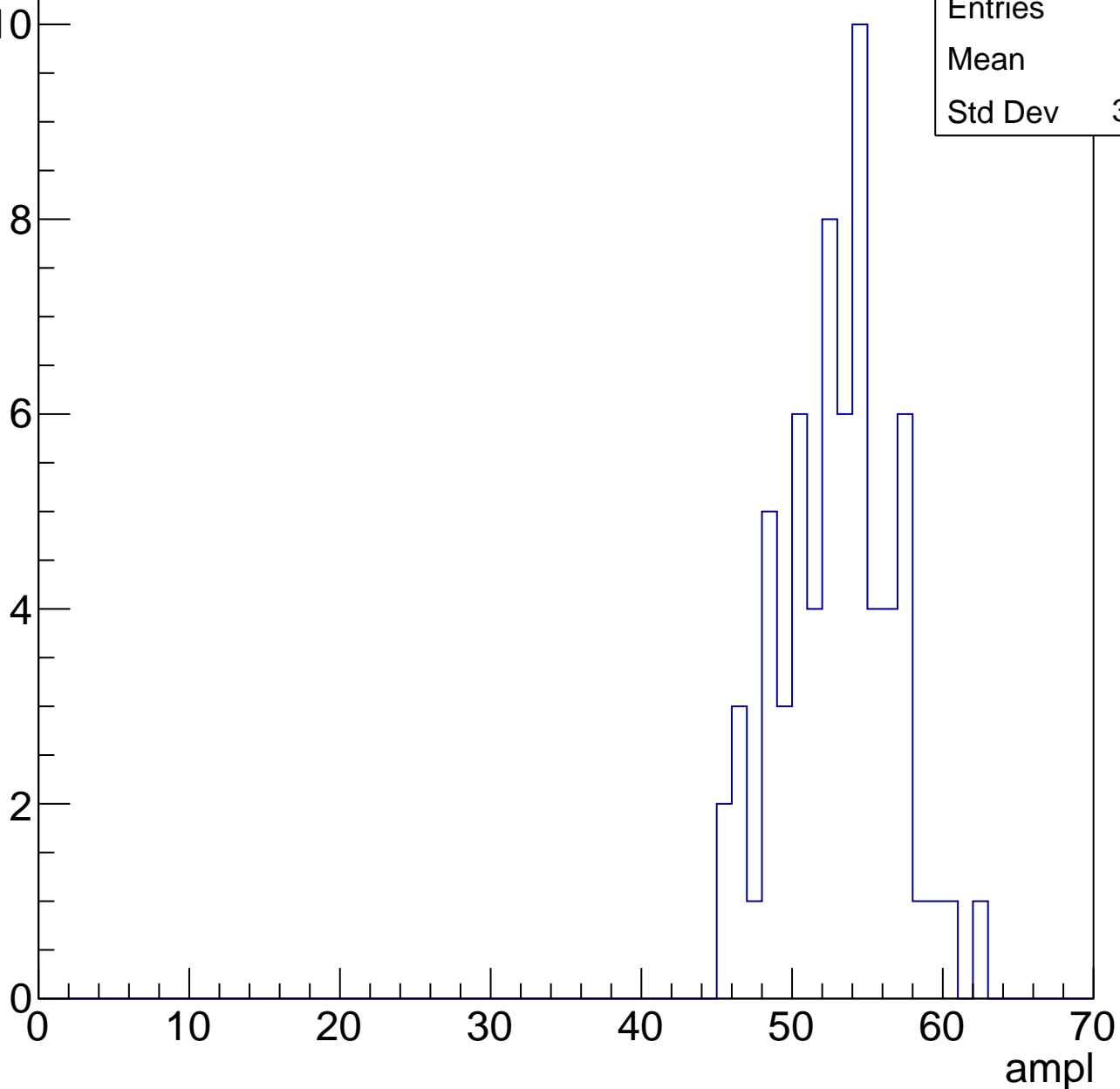


# B0L000S, U25-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	52.5
Std Dev	3.681

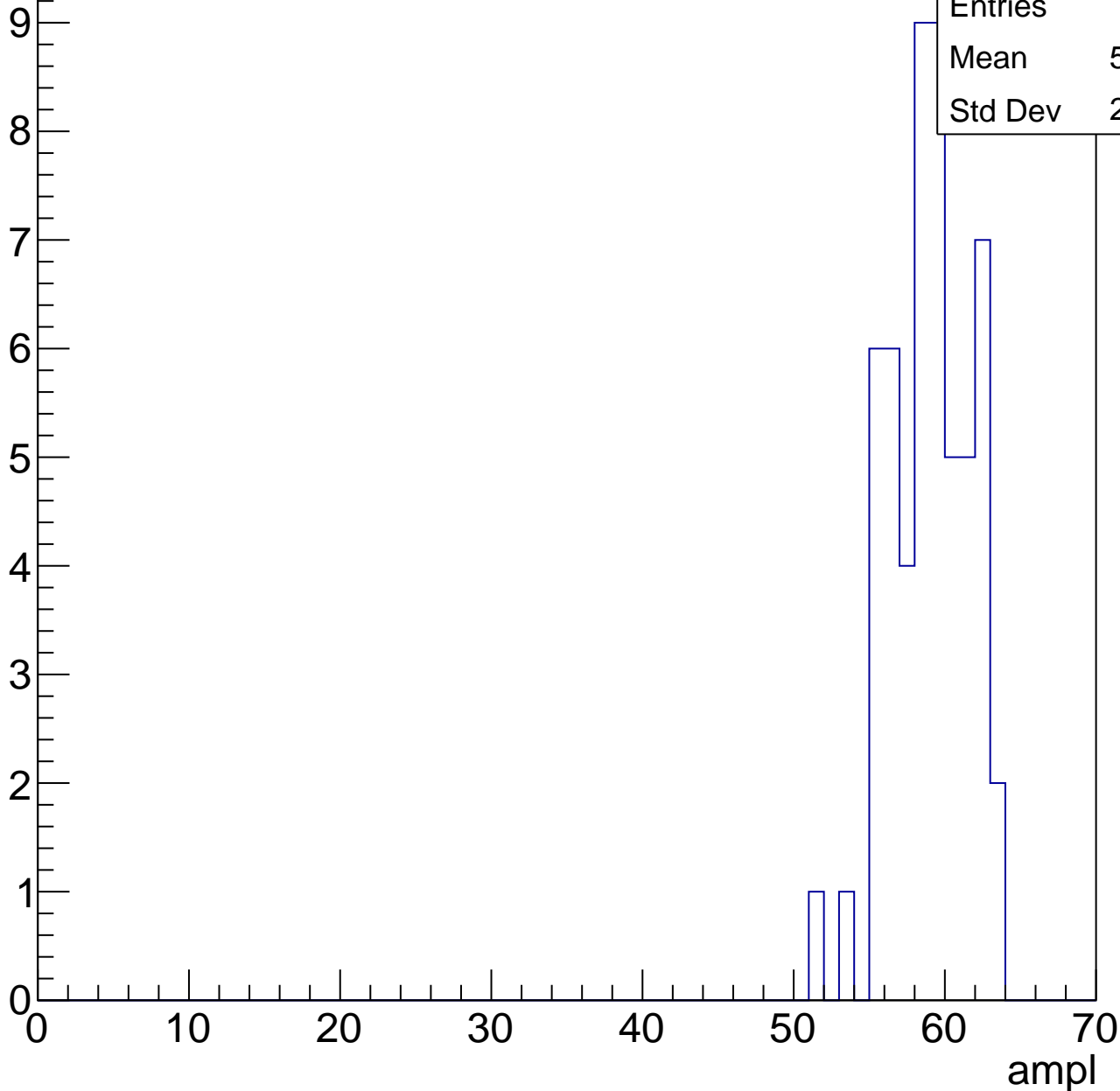


# B0L000S, U25-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	58.47
Std Dev	2.628

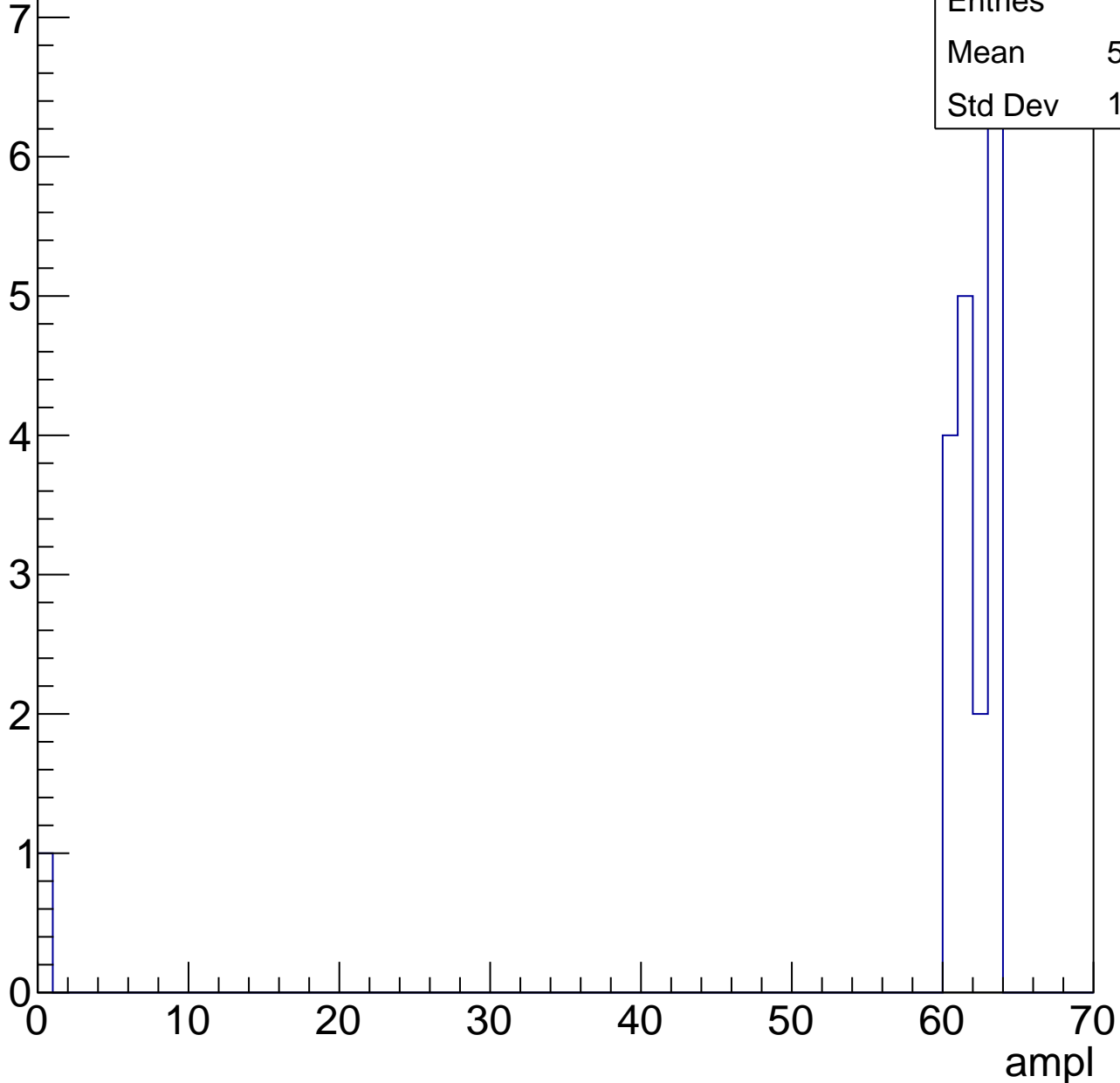


# B0L000S, U25-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	19
Mean	58.42
Std Dev	13.82



# B0L000S, U25-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch70, adc0

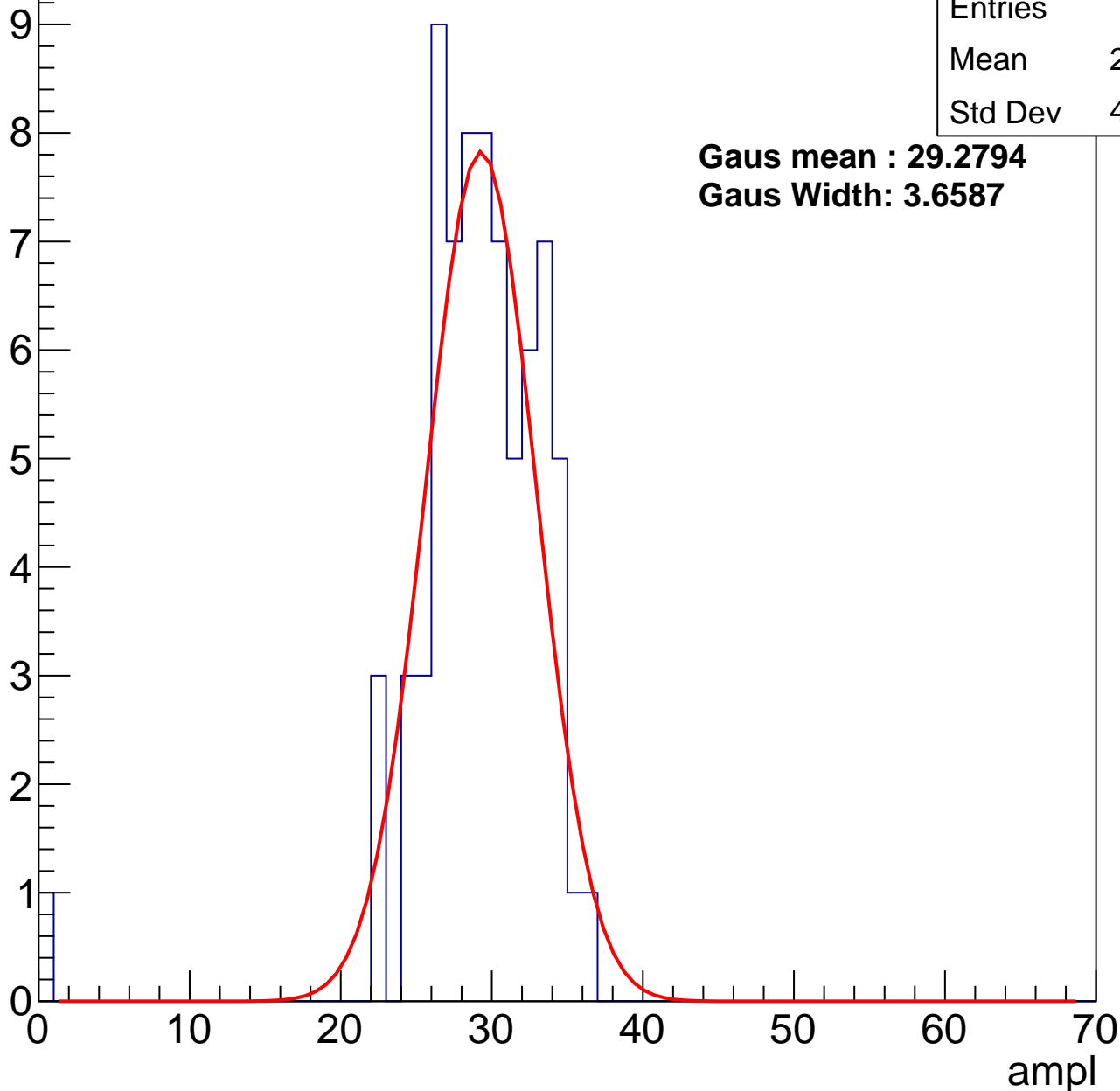
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	28.66
Std Dev	4.683

**Gaus mean : 29.2794**

**Gaus Width: 3.6587**



# B0L000S, U25-ch70, adc1

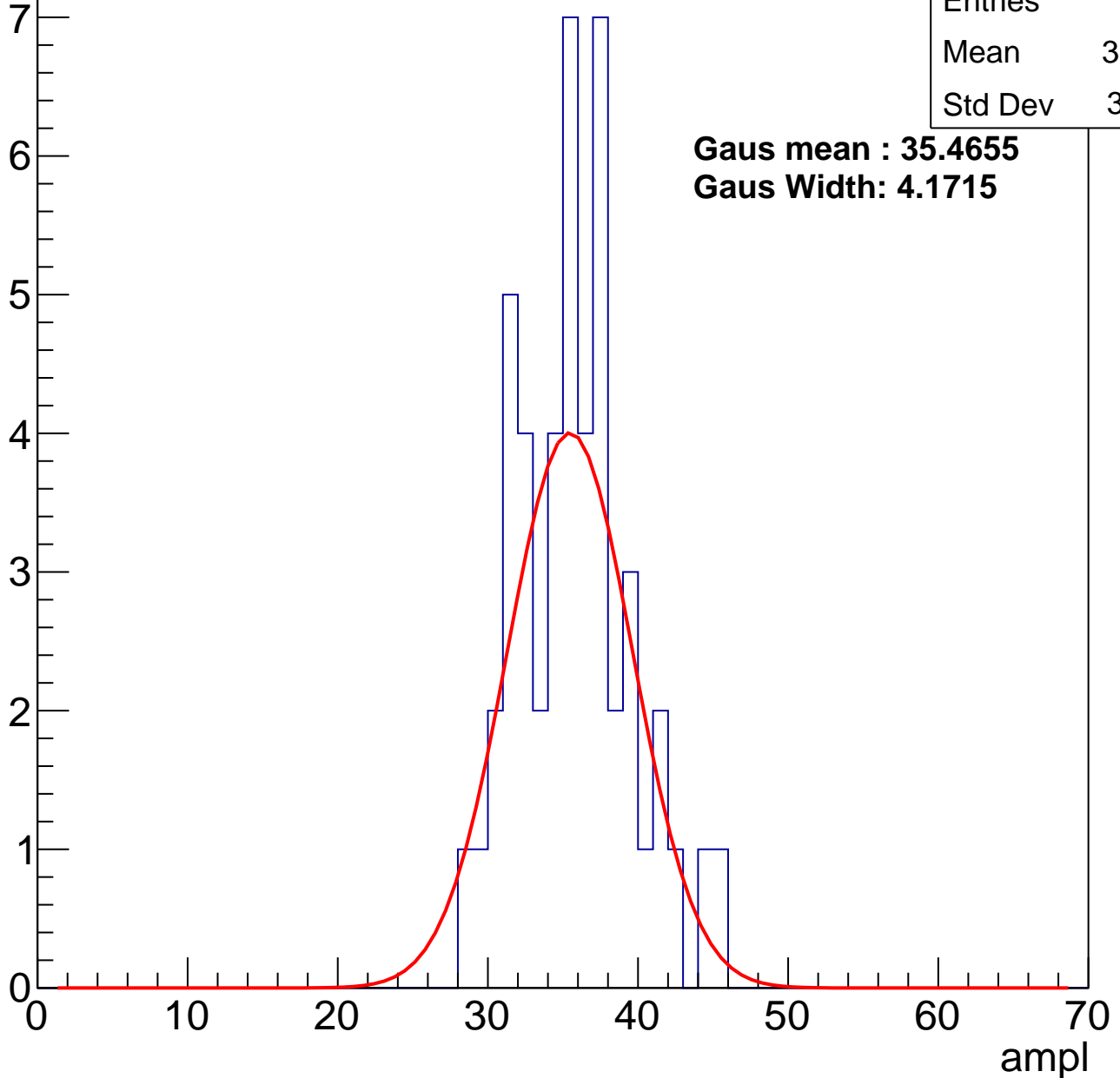
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	35.33
Std Dev	3.771

**Gaus mean : 35.4655**

**Gaus Width: 4.1715**



# B0L000S, U25-ch70, adc2

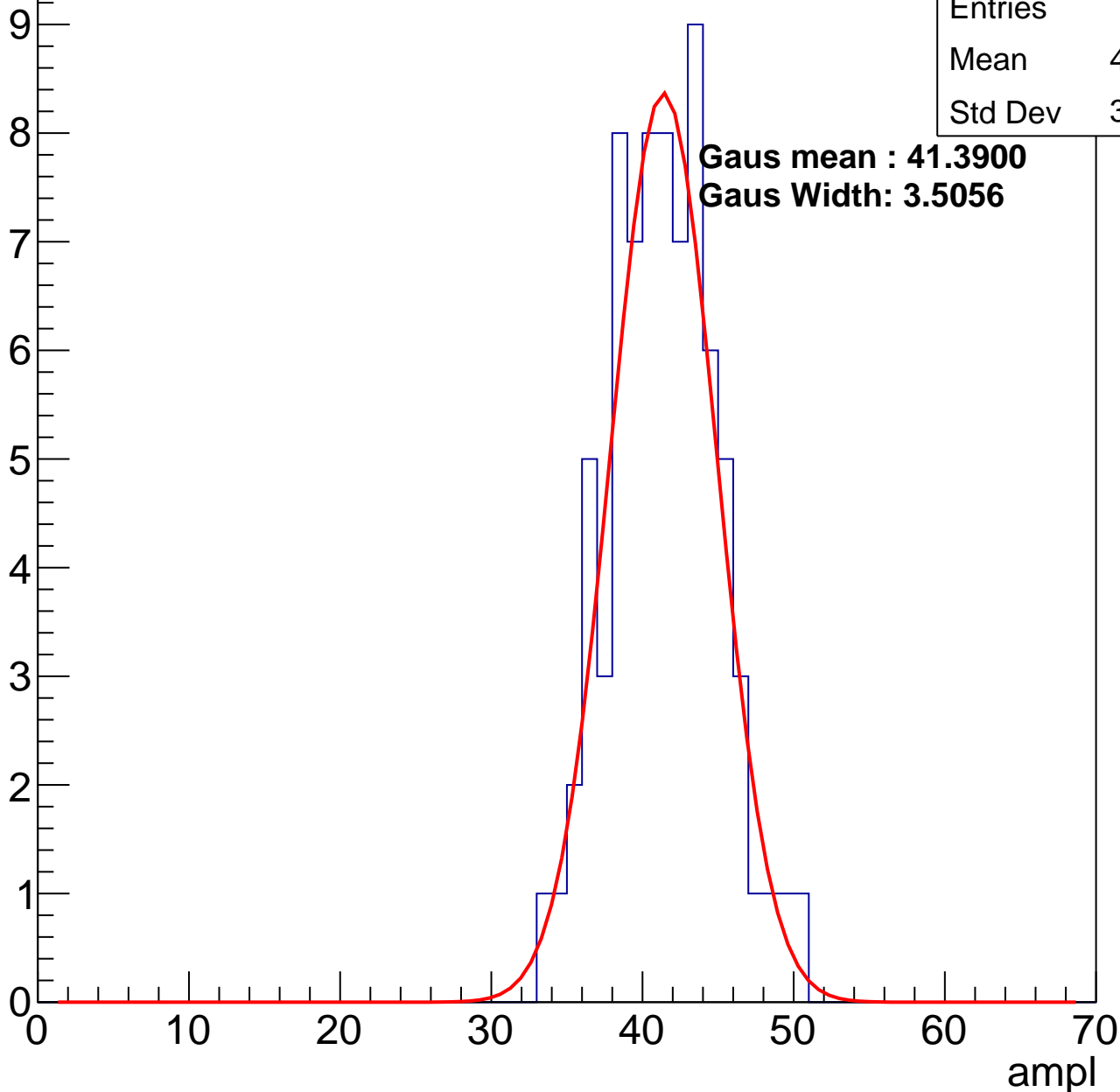
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	40.97
Std Dev	3.509

**Gaus mean : 41.3900**

**Gaus Width: 3.5056**

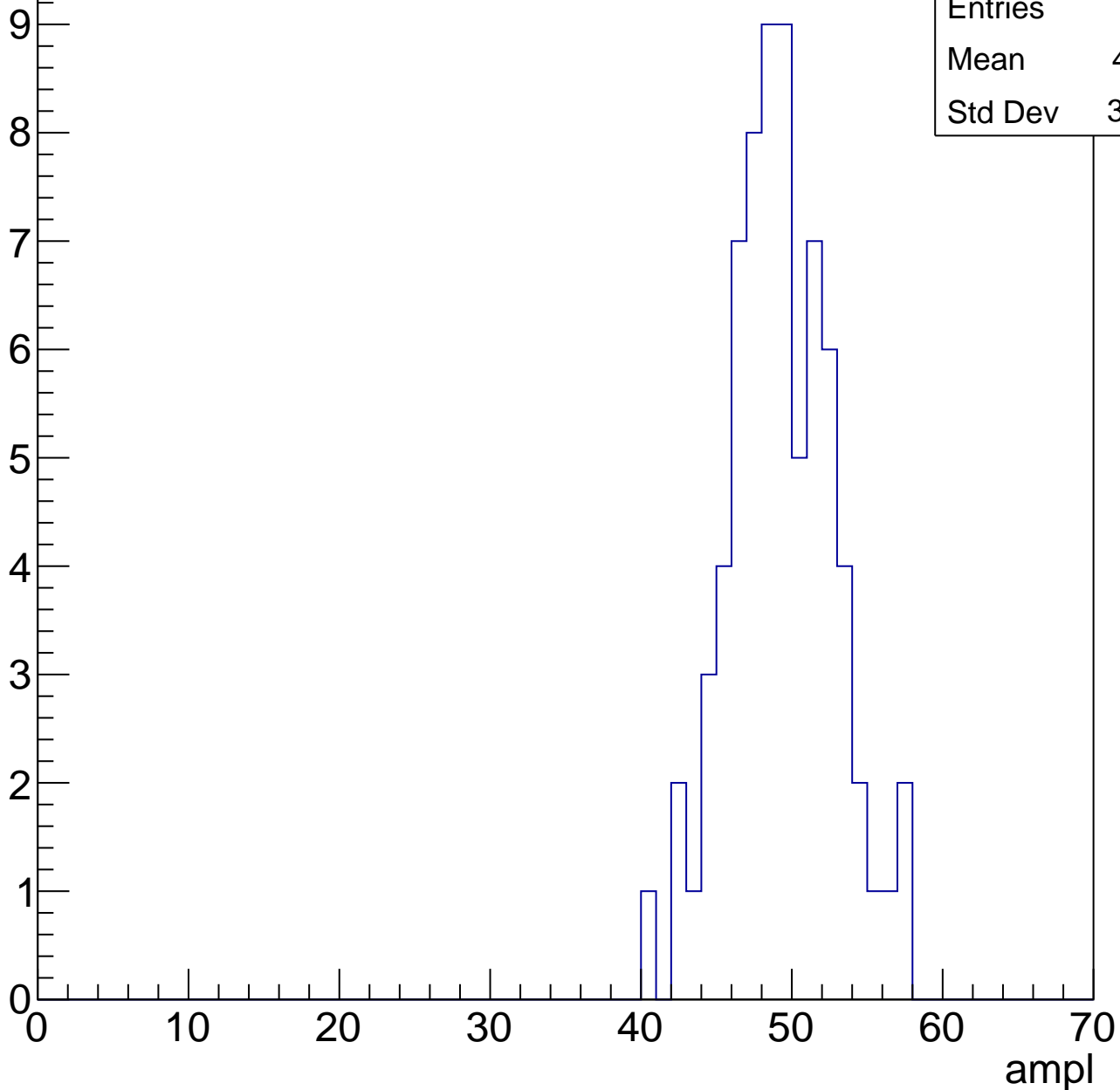


# B0L000S, U25-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.81
Std Dev	3.475

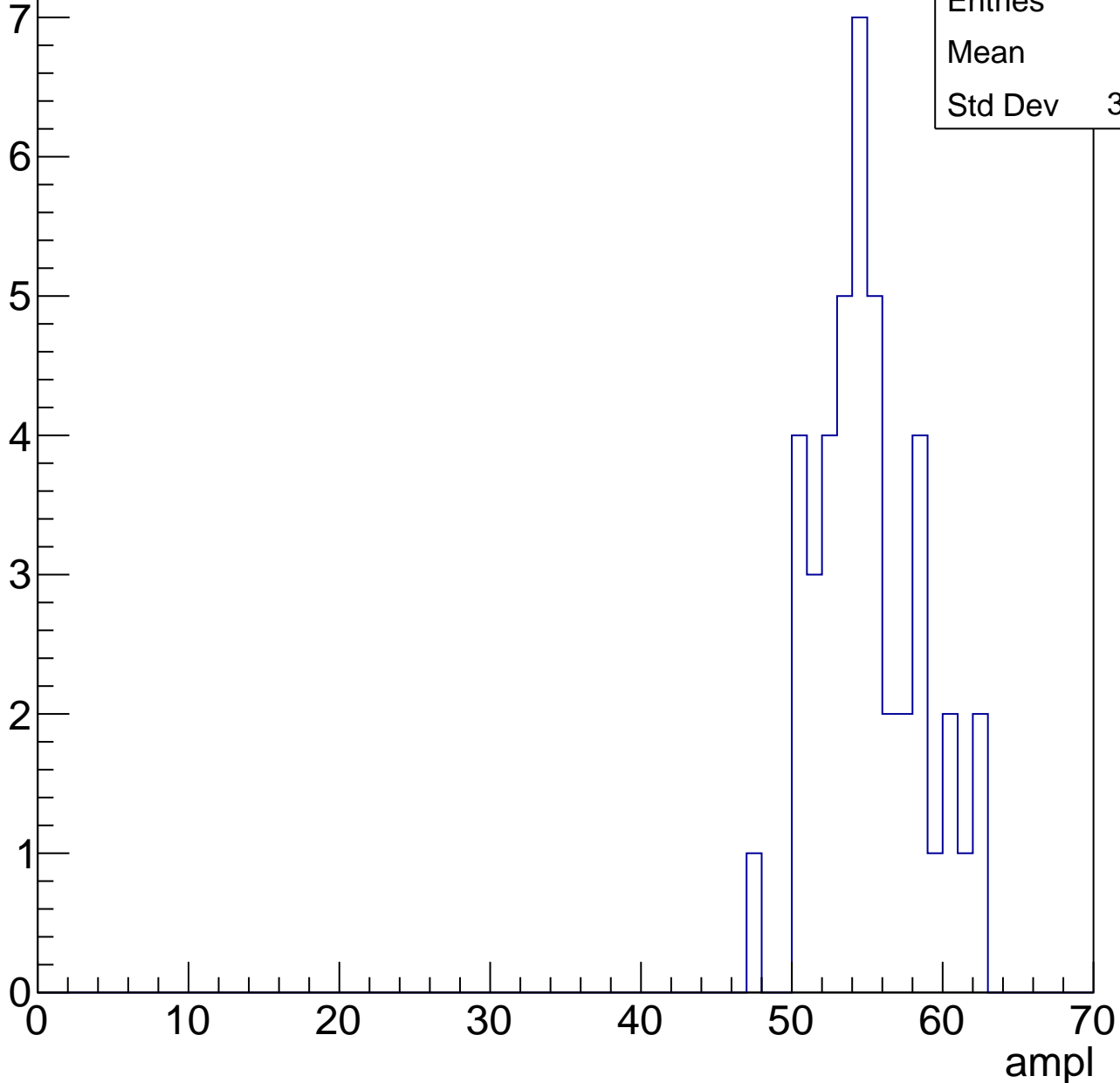


# B0L000S, U25-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	54.6
Std Dev	3.458

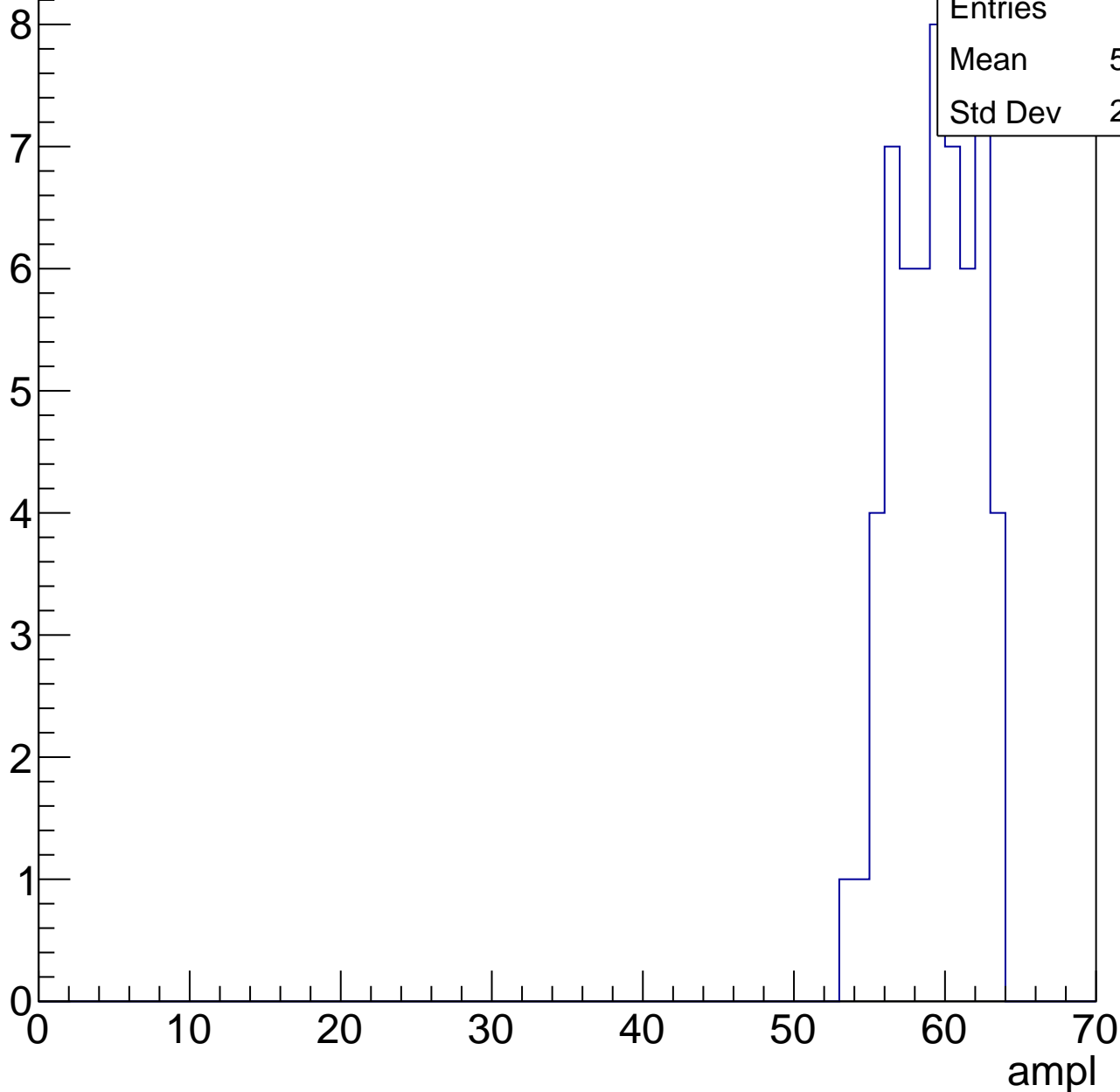


# B0L000S, U25-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	58.88
Std Dev	2.574

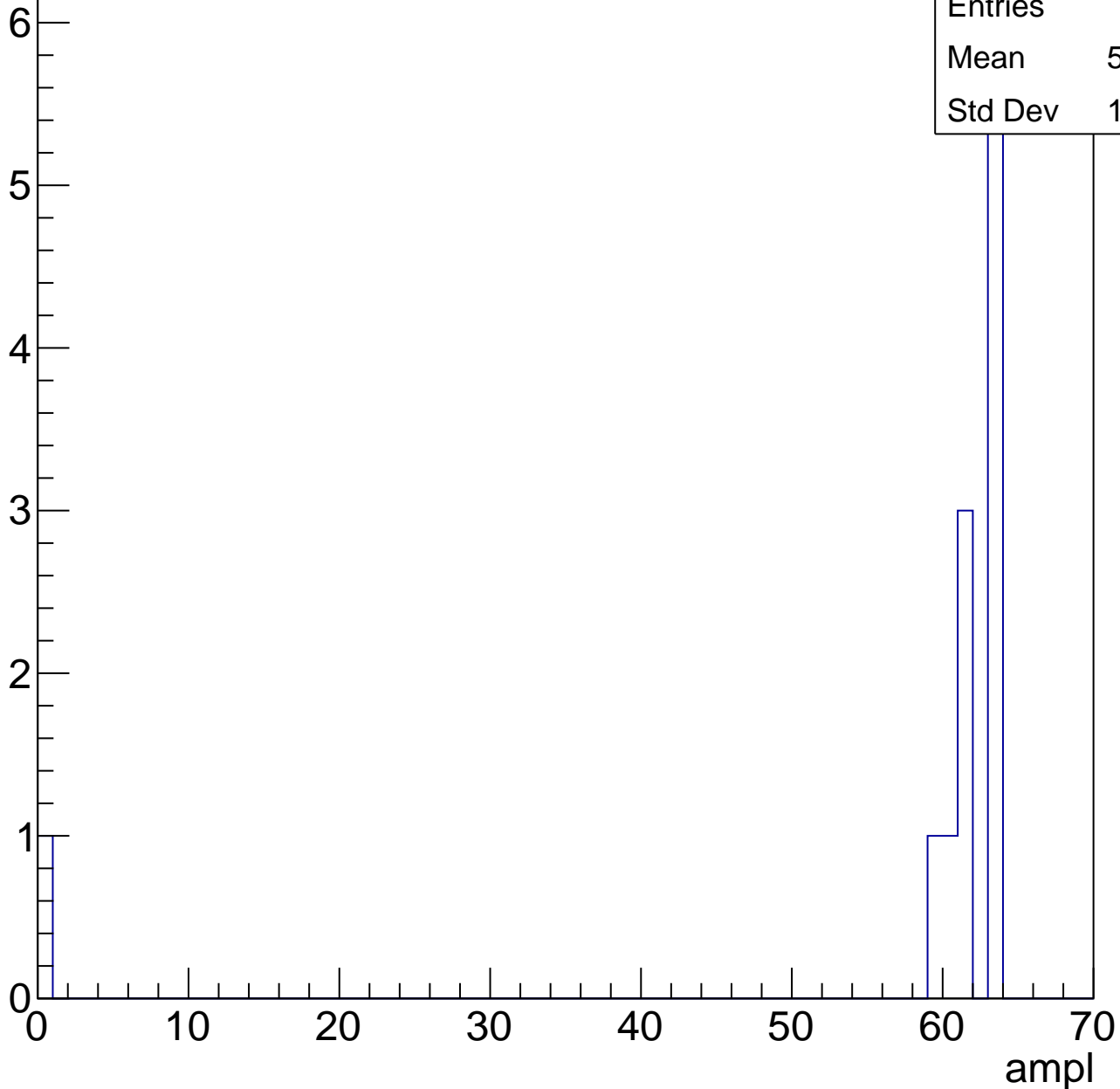


# B0L000S, U25-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.67
Std Dev	17.14





# B0L000S, U25-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch71, adc0

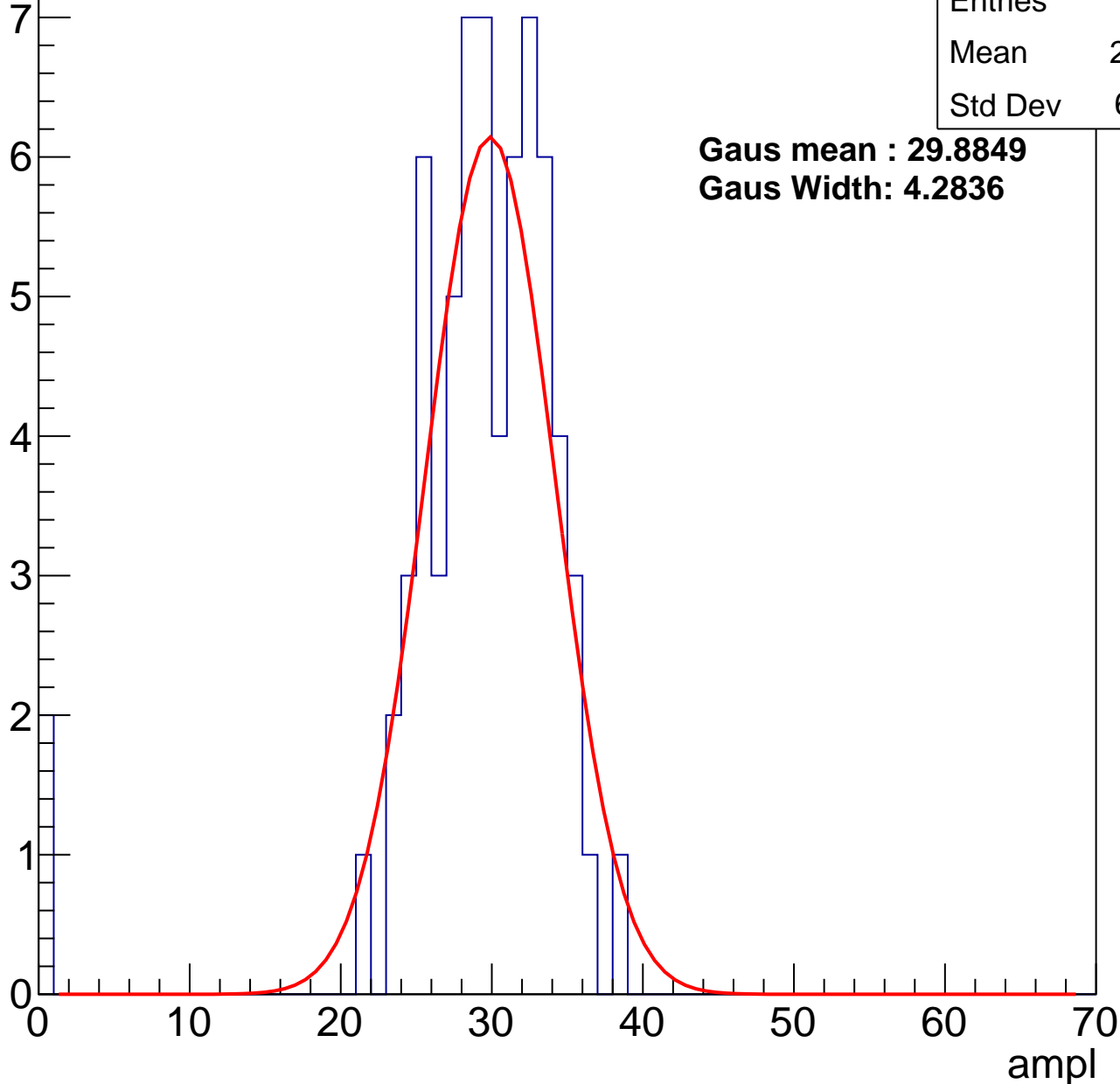
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	28.59
Std Dev	6.131

**Gaus mean : 29.8849**

**Gaus Width: 4.2836**



# B0L000S, U25-ch71, adc1

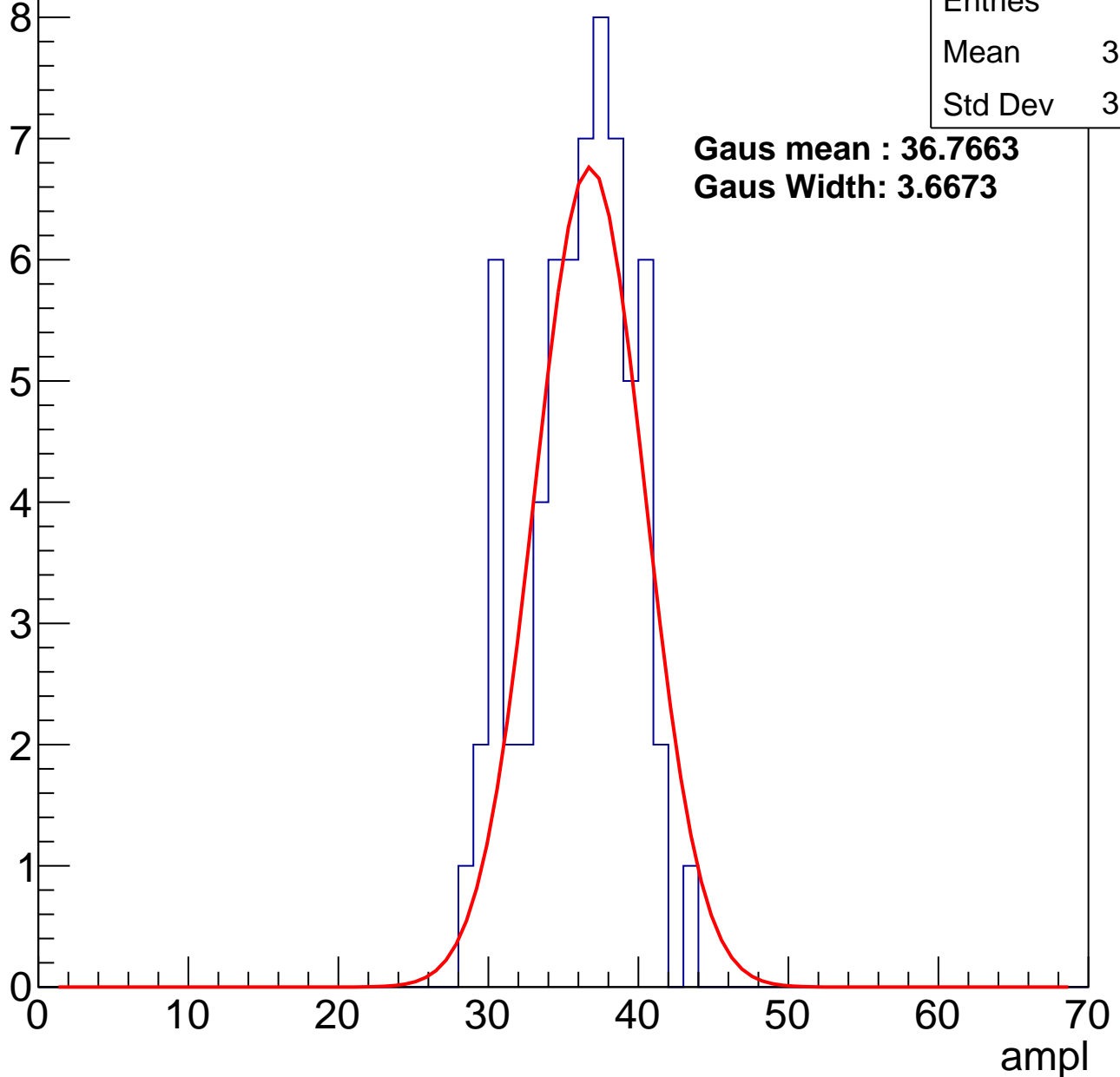
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.57
Std Dev	3.486

**Gaus mean : 36.7663**

**Gaus Width: 3.6673**



# B0L000S, U25-ch71, adc2

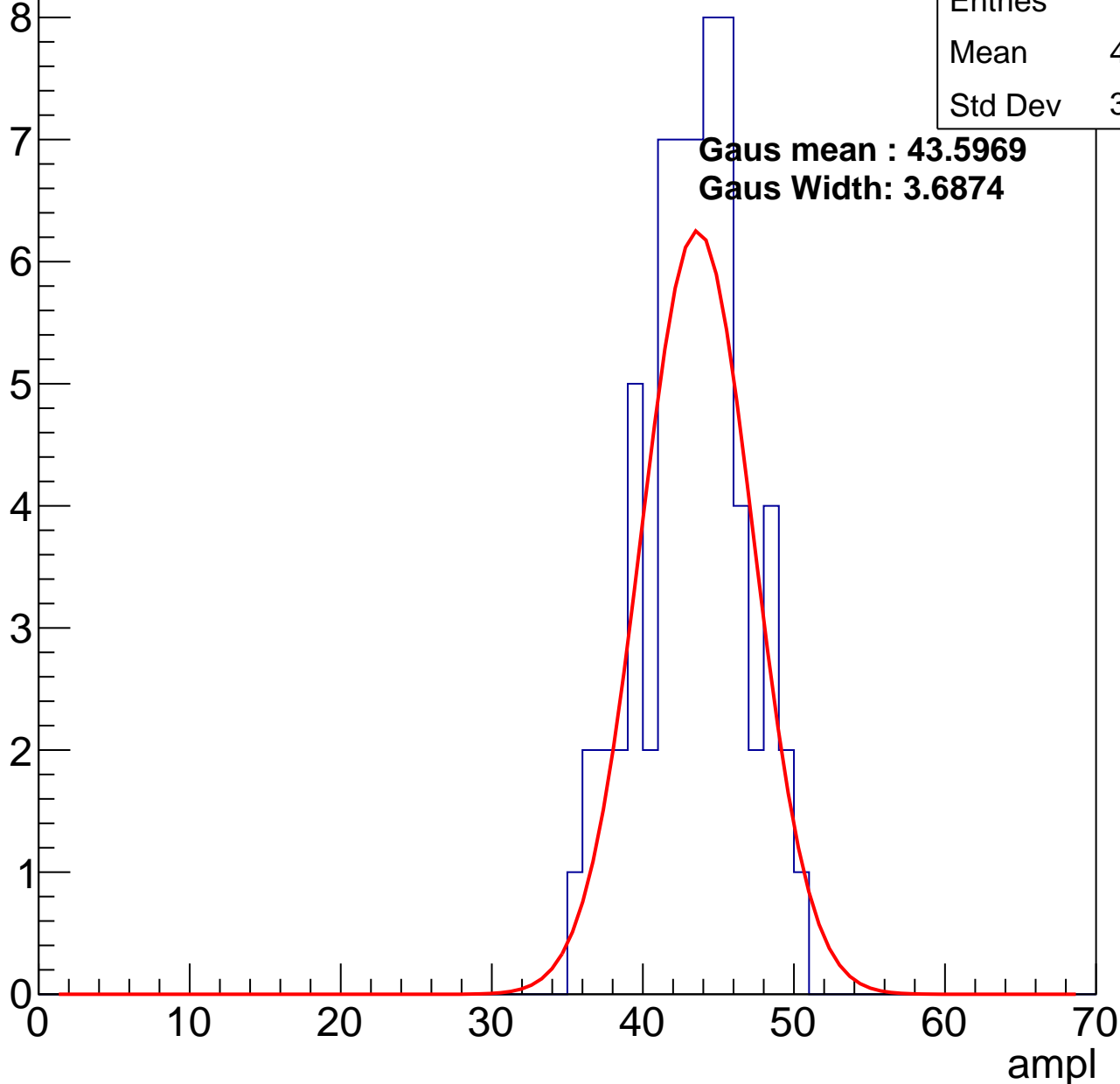
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	42.88
Std Dev	3.412

**Gaus mean : 43.5969**

**Gaus Width: 3.6874**

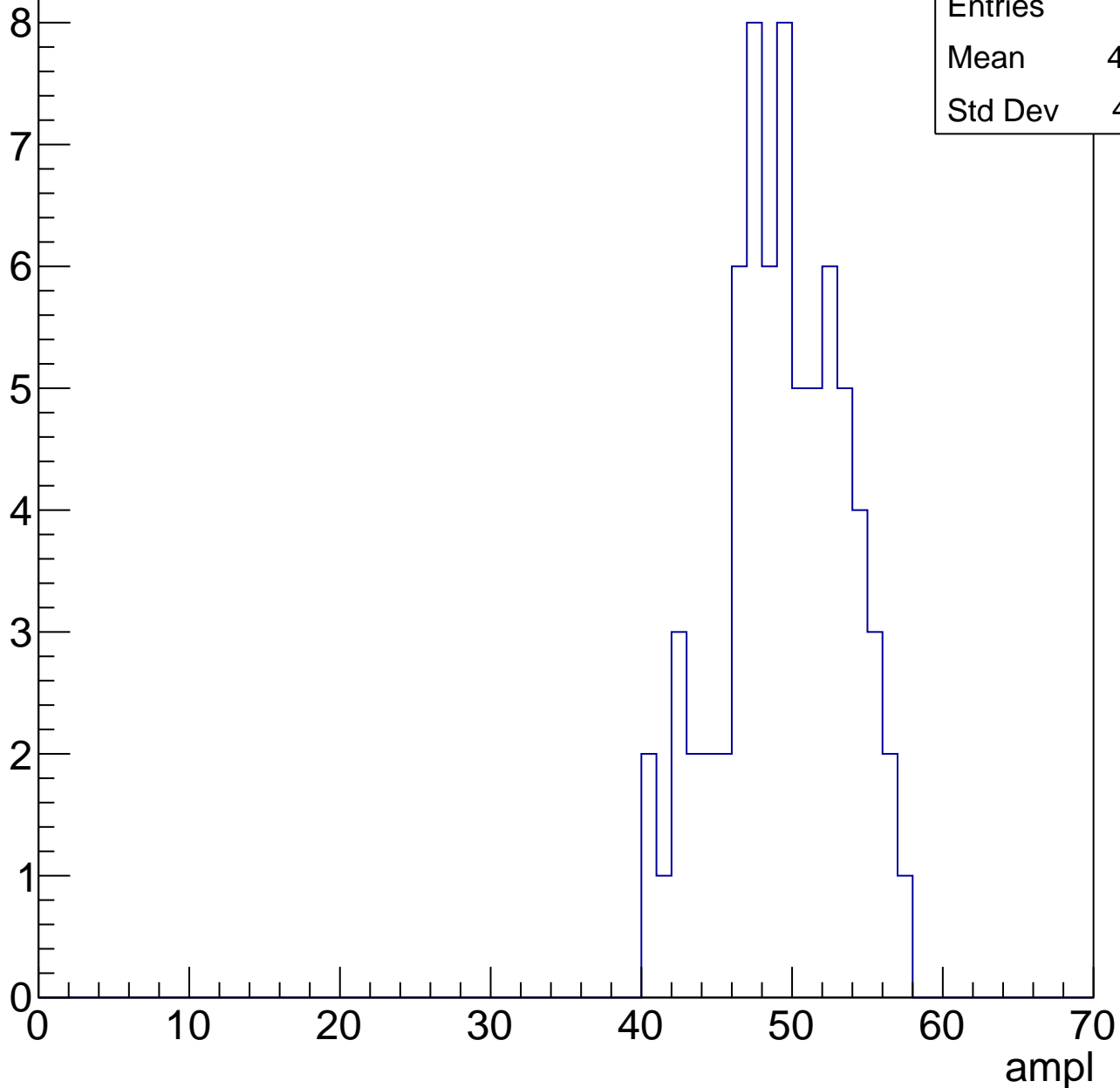


# B0L000S, U25-ch71, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	48.94
Std Dev	4.021

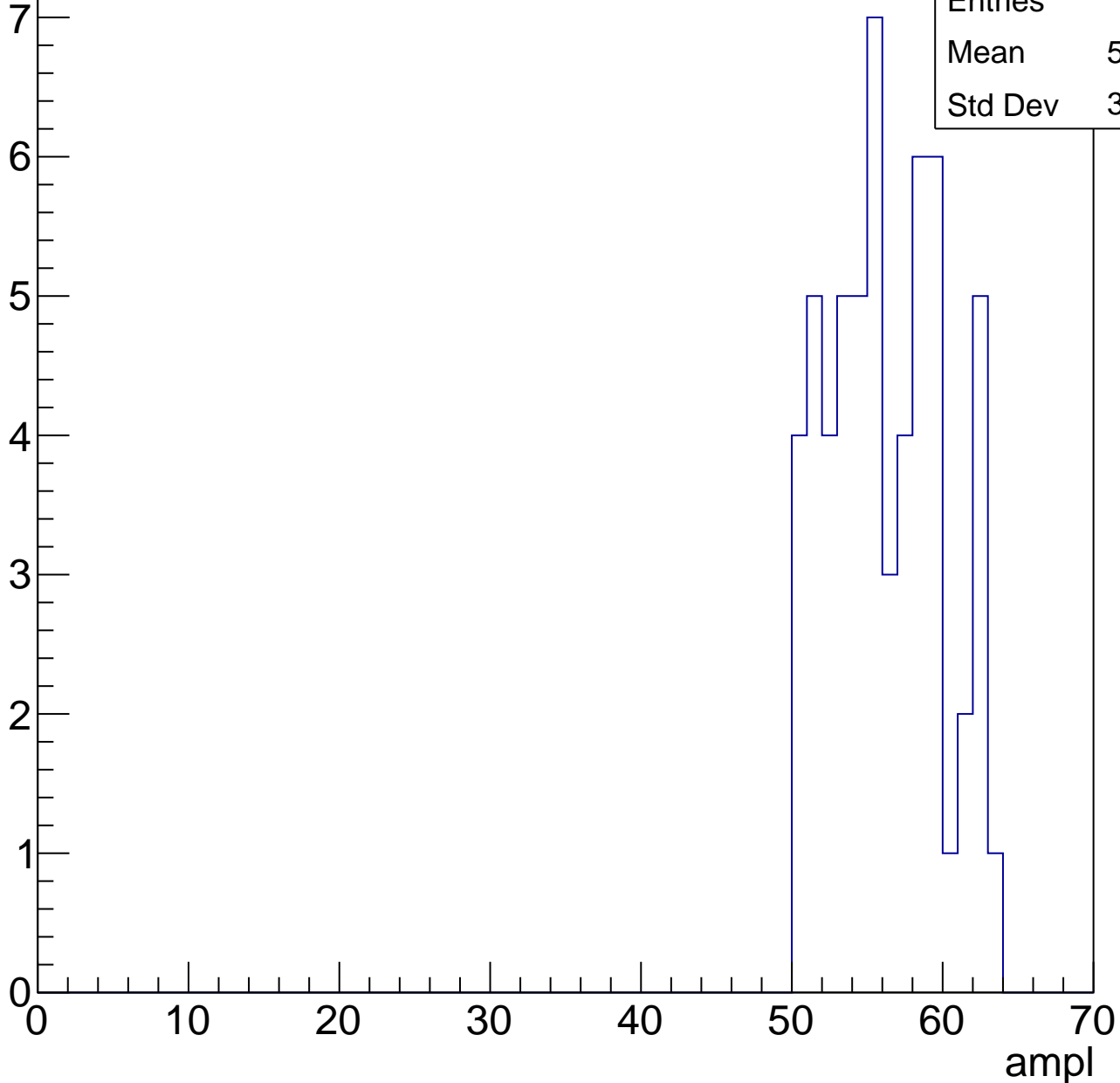


# B0L000S, U25-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	55.79
Std Dev	3.666

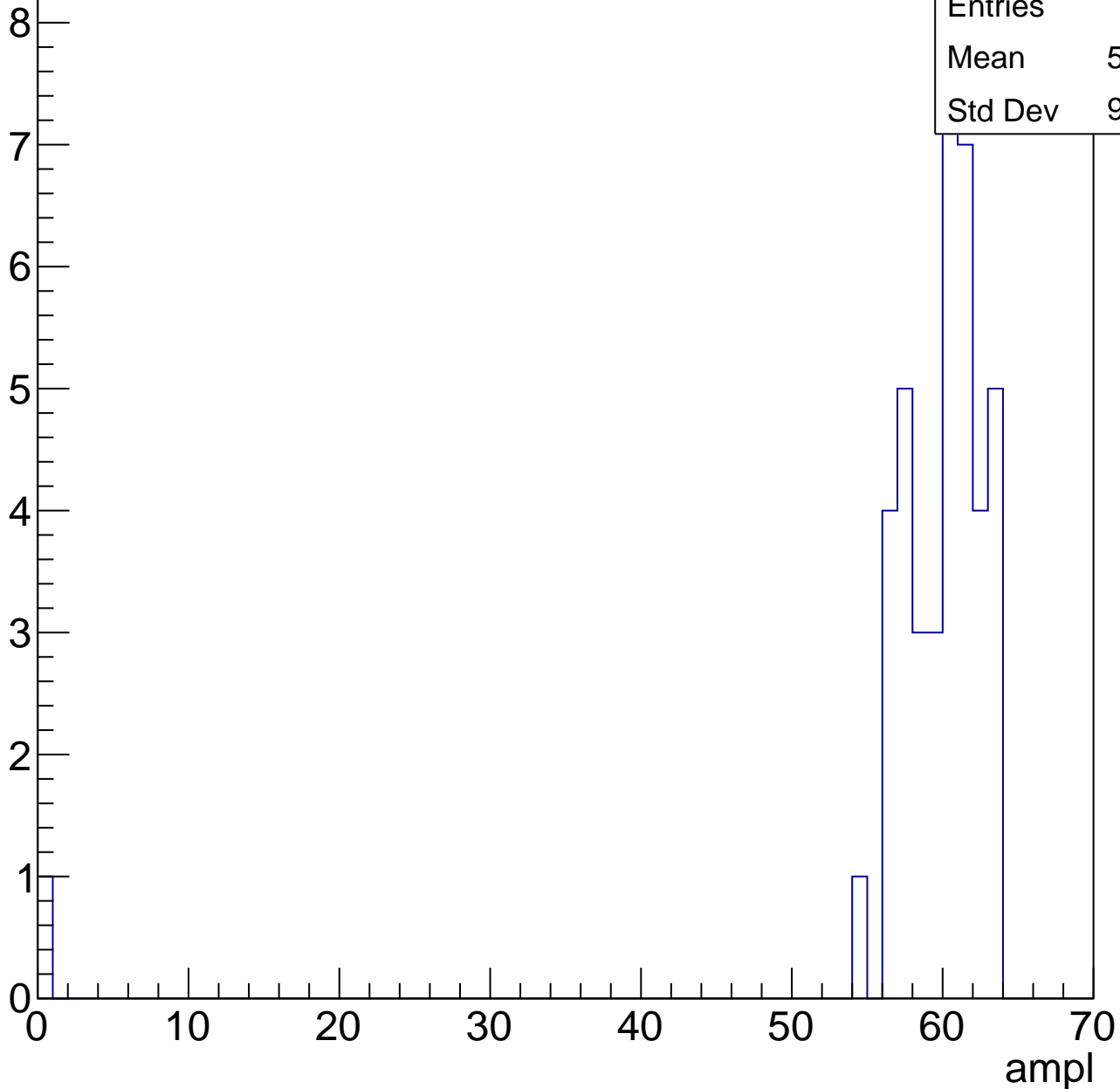


# B0L000S, U25-ch71, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

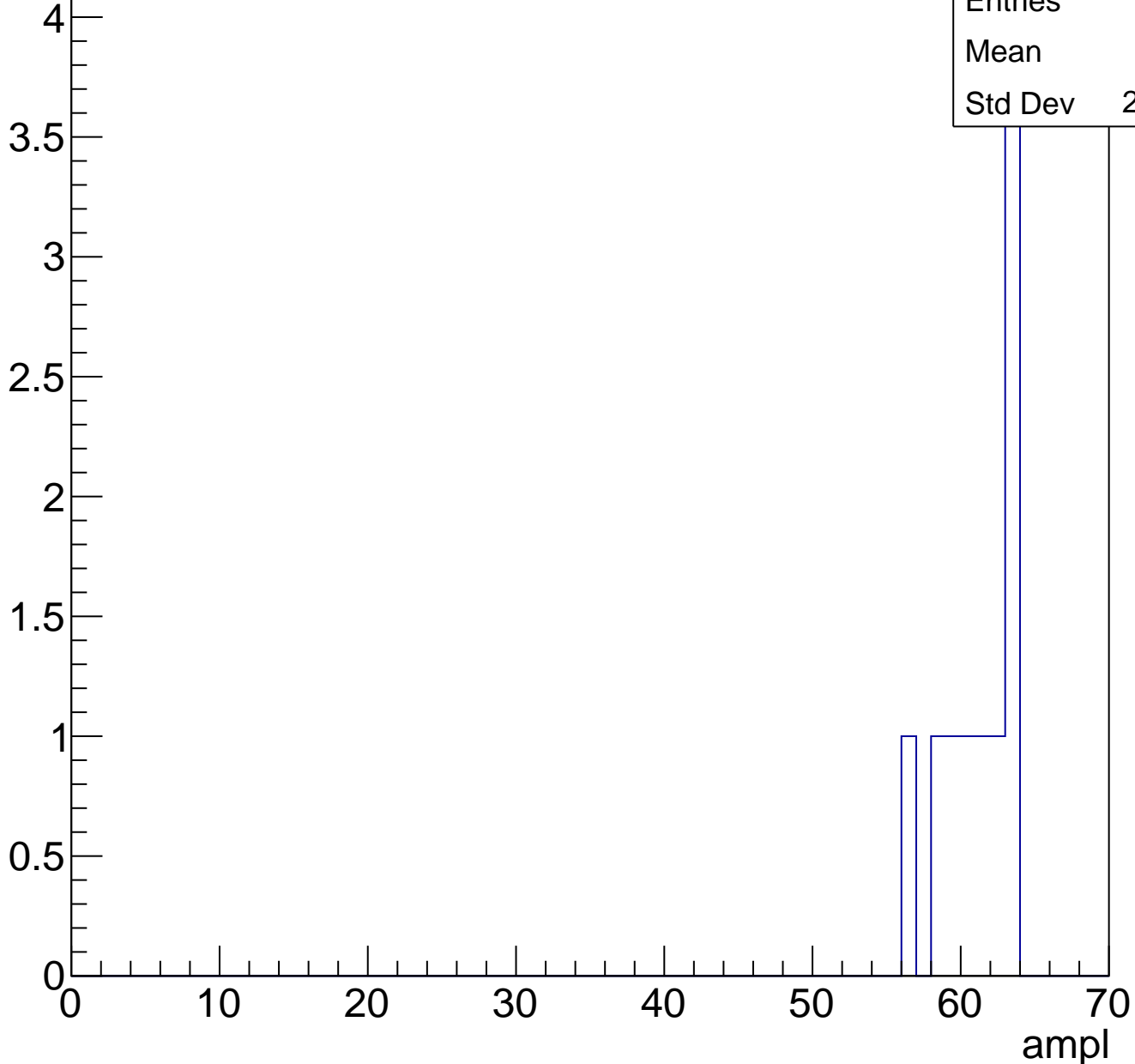
Entries	41
Mean	58.15
Std Dev	9.483



# B0L000S, U25-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	10
Mean	60.8
Std Dev	2.358



# B0L000S, U25-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U25-ch72, adc0

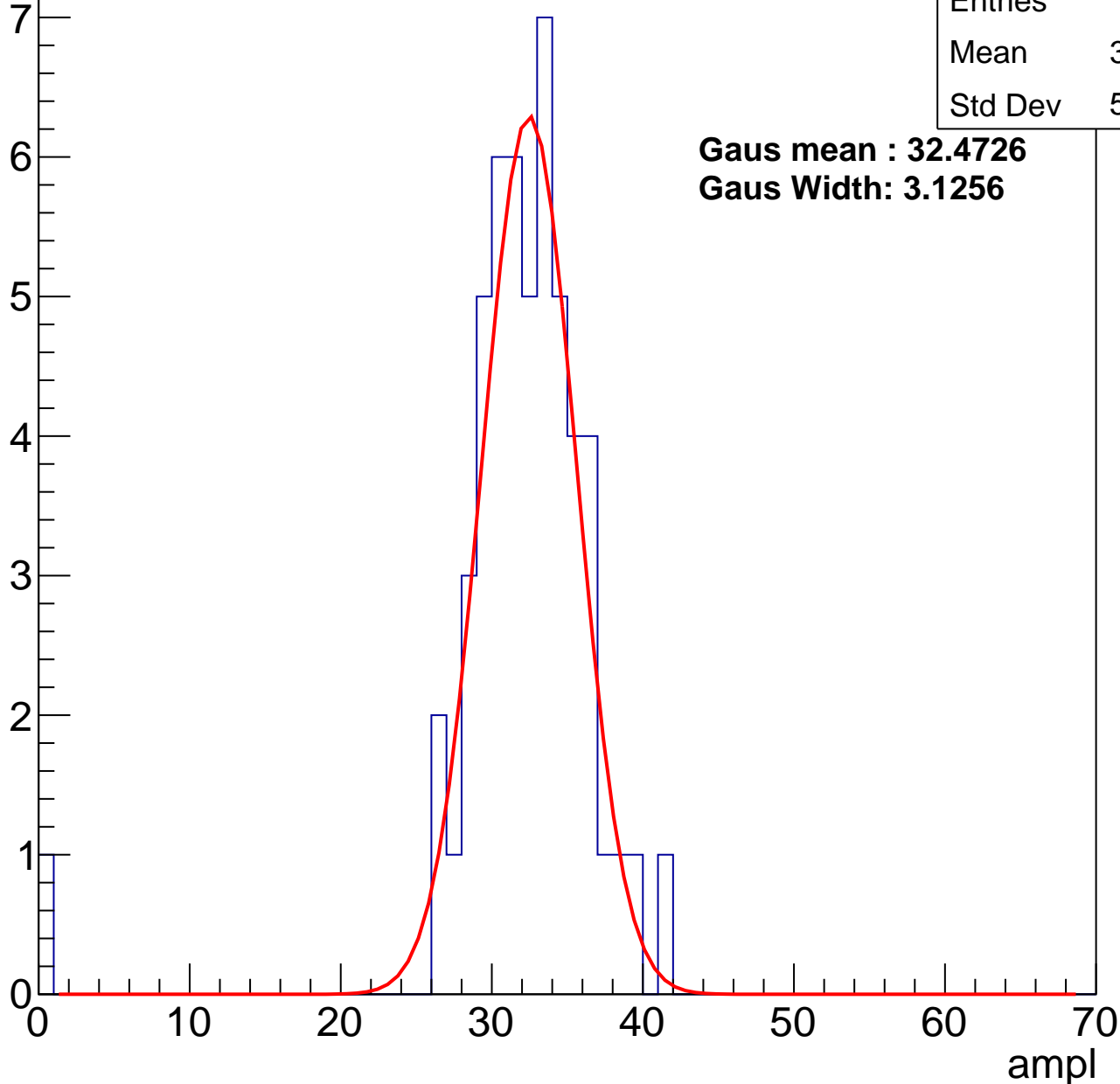
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	31.58
Std Dev	5.413

**Gaus mean : 32.4726**

**Gaus Width: 3.1256**



# B0L000S, U25-ch72, adc1

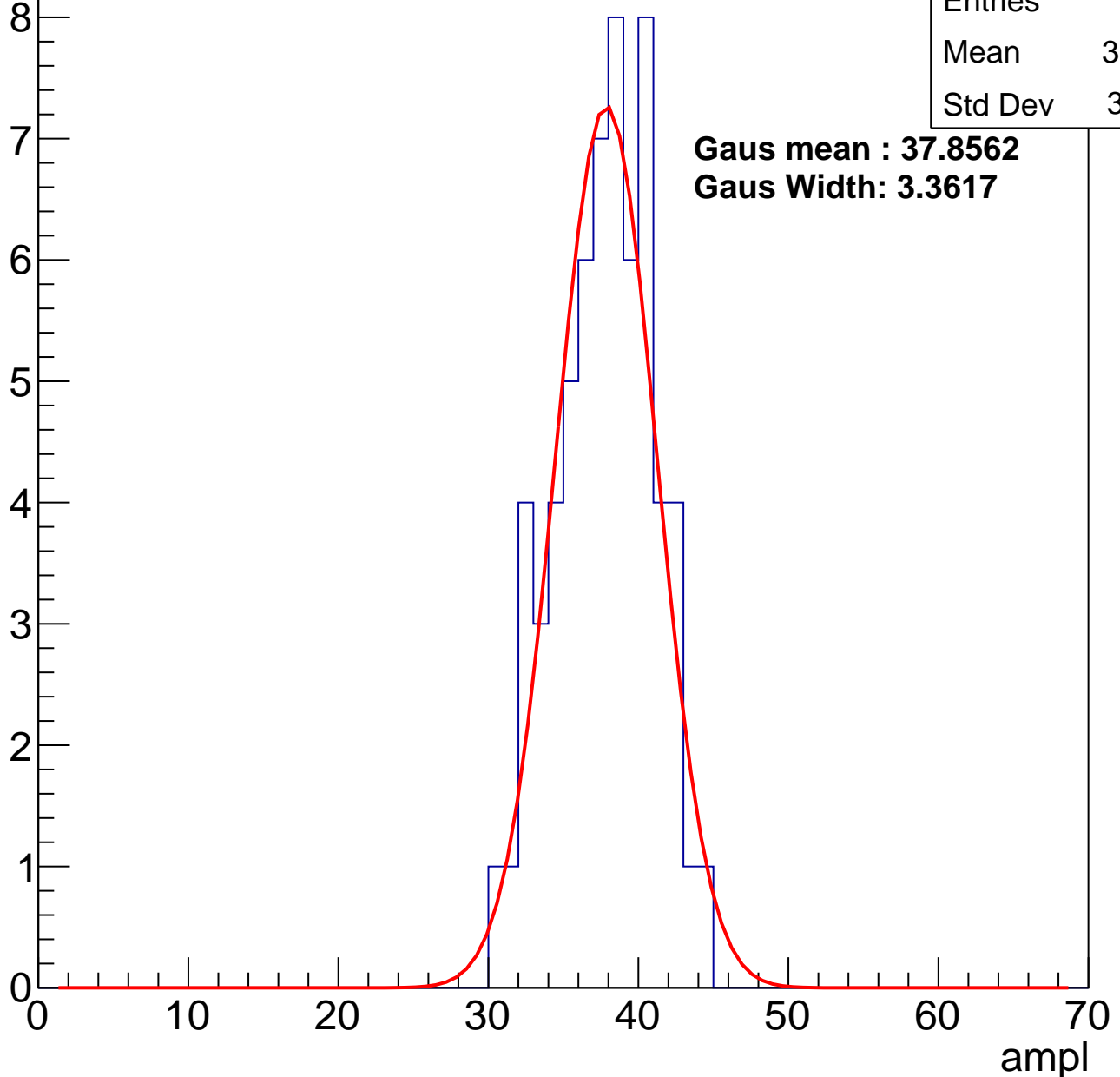
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	37.32
Std Dev	3.191

**Gaus mean : 37.8562**

**Gaus Width: 3.3617**



# B0L000S, U25-ch72, adc2

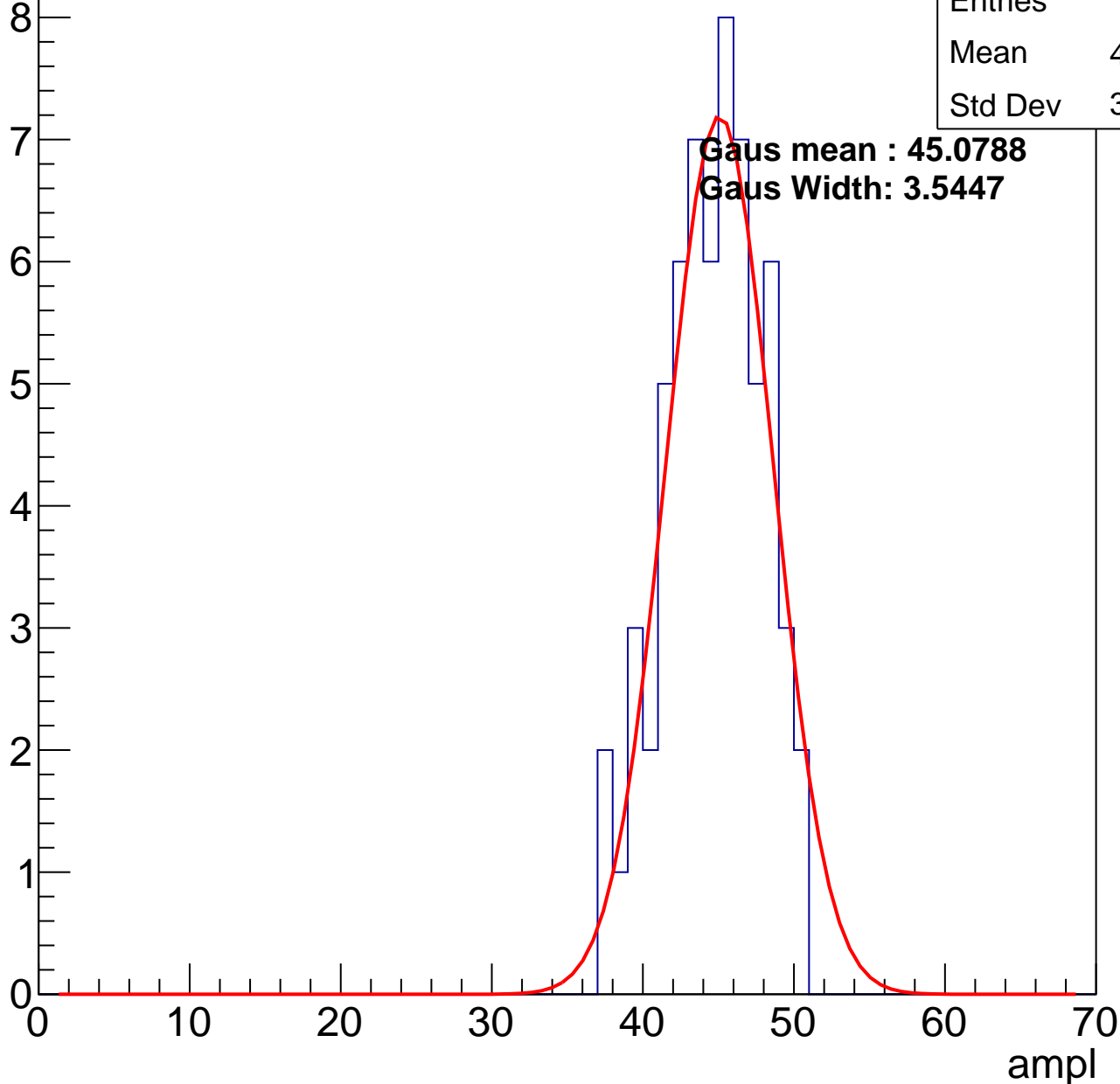
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	44.17
Std Dev	3.185

**Gaus mean : 45.0788**

**Gaus Width: 3.5447**

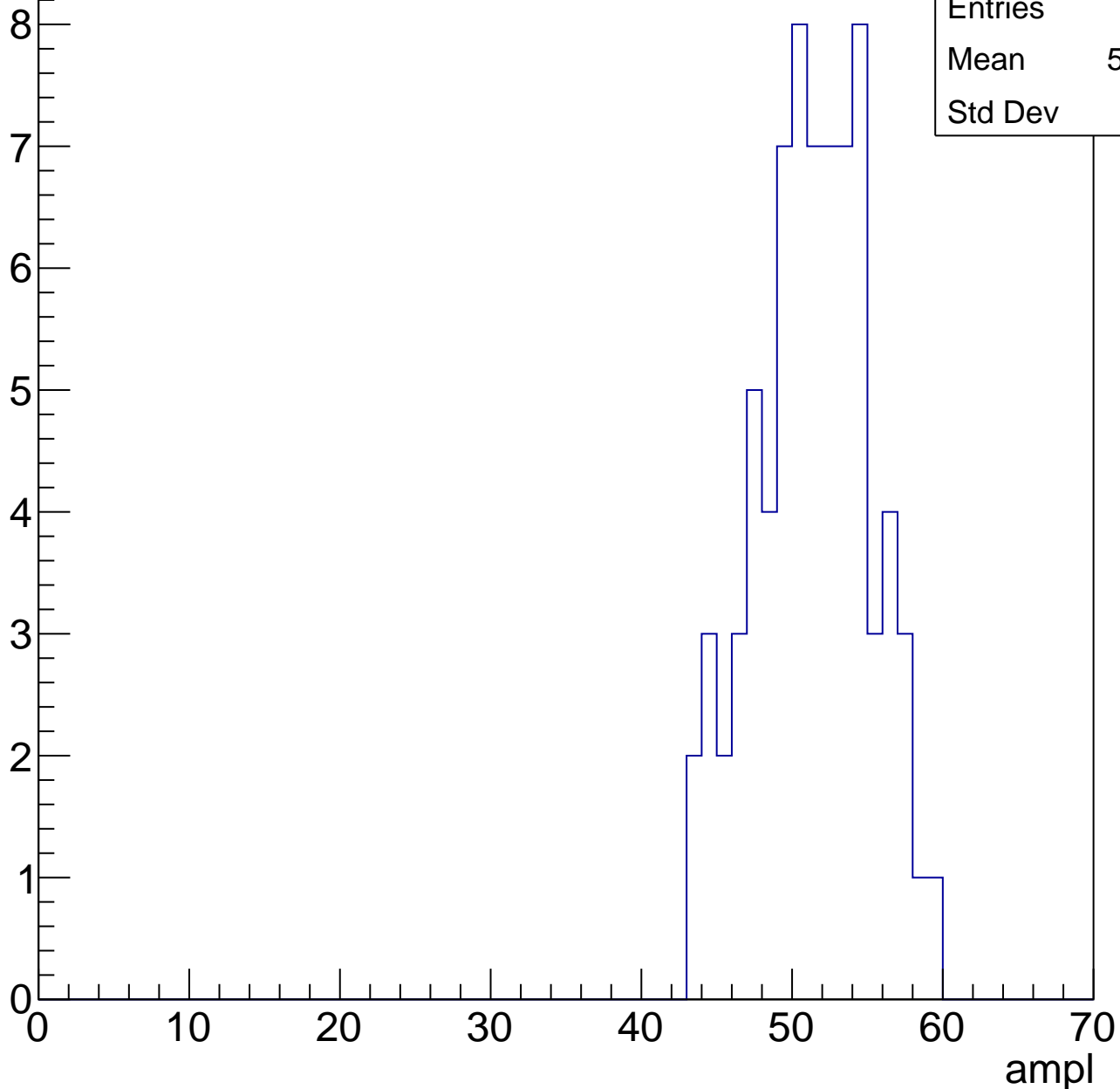


# B0L000S, U25-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	50.89
Std Dev	3.74

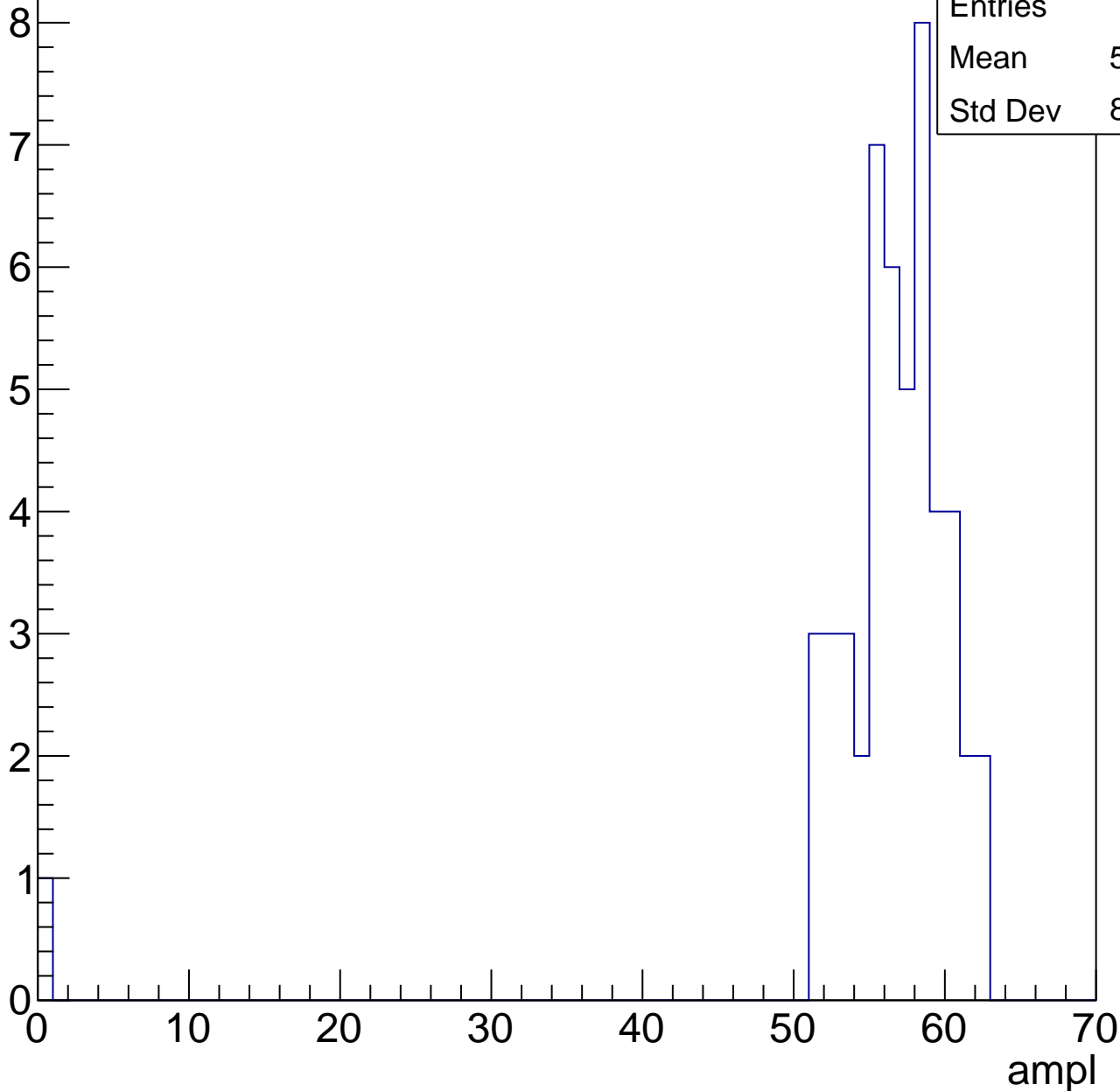


# B0L000S, U25-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

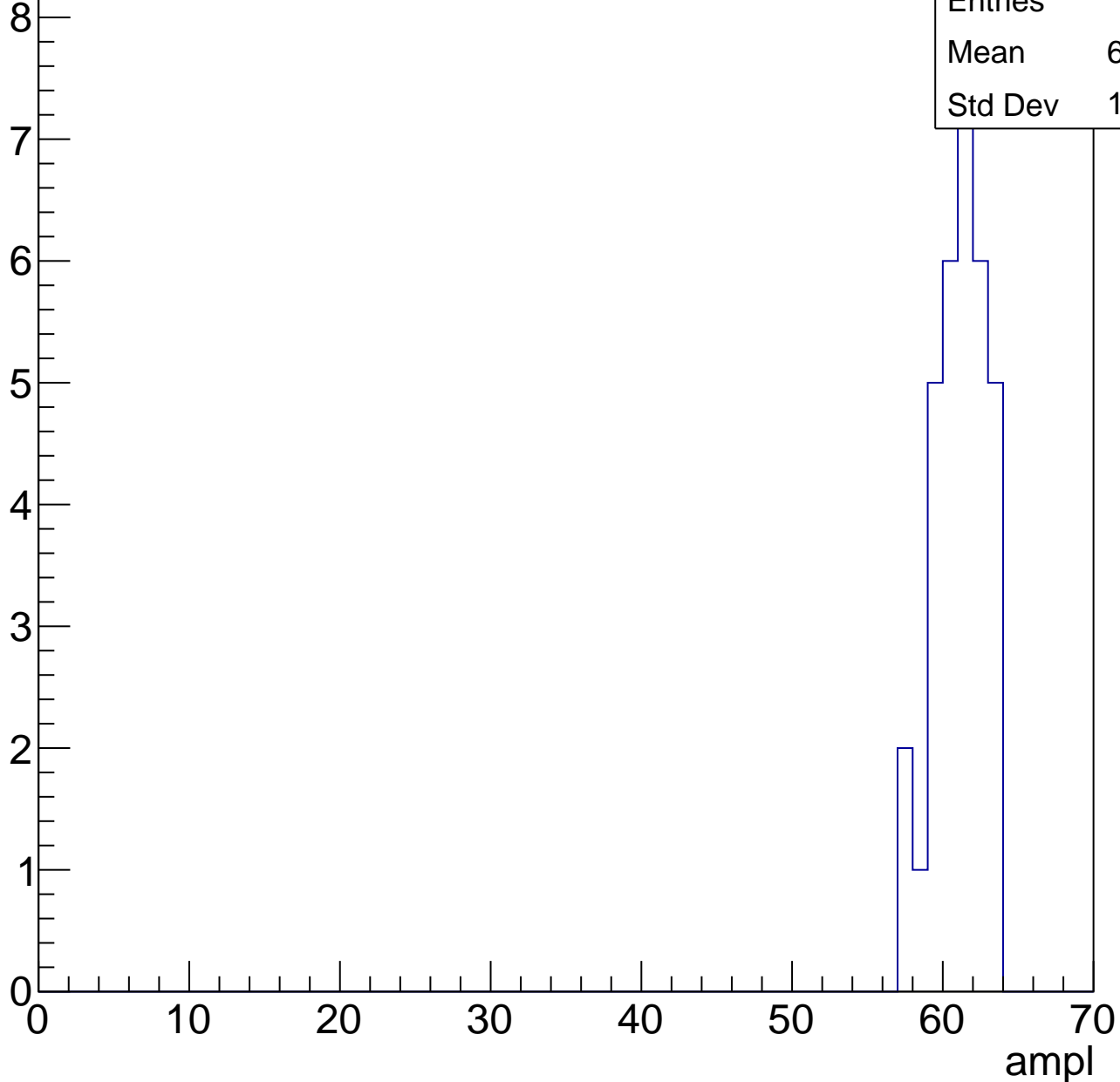
Entries	50
Mean	55.36
Std Dev	8.414



# B0L000S, U25-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

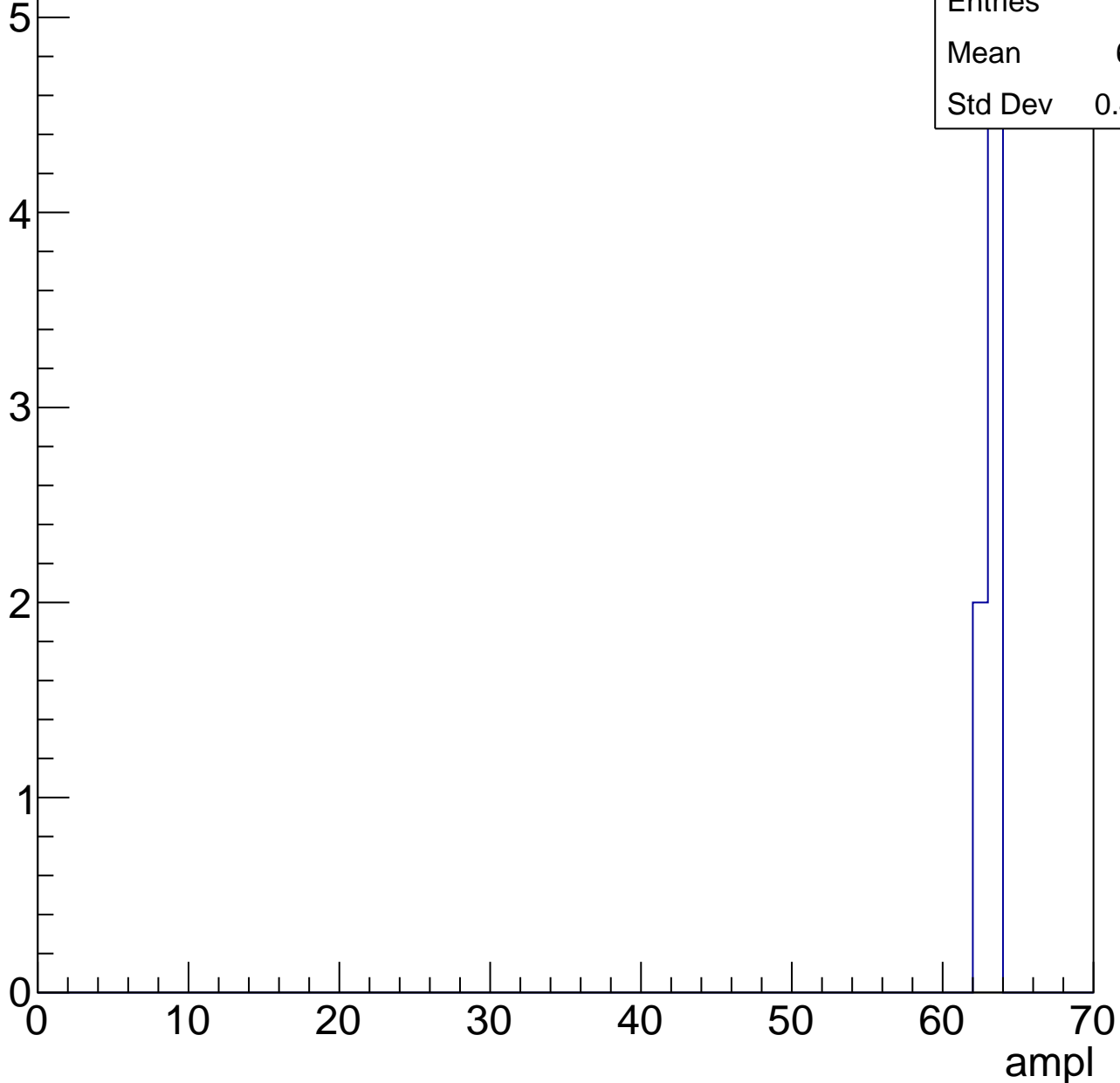


# B0L000S, U25-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	7
Mean	62.71
Std Dev	0.4518





# B0L000S, U25-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch73, adc0

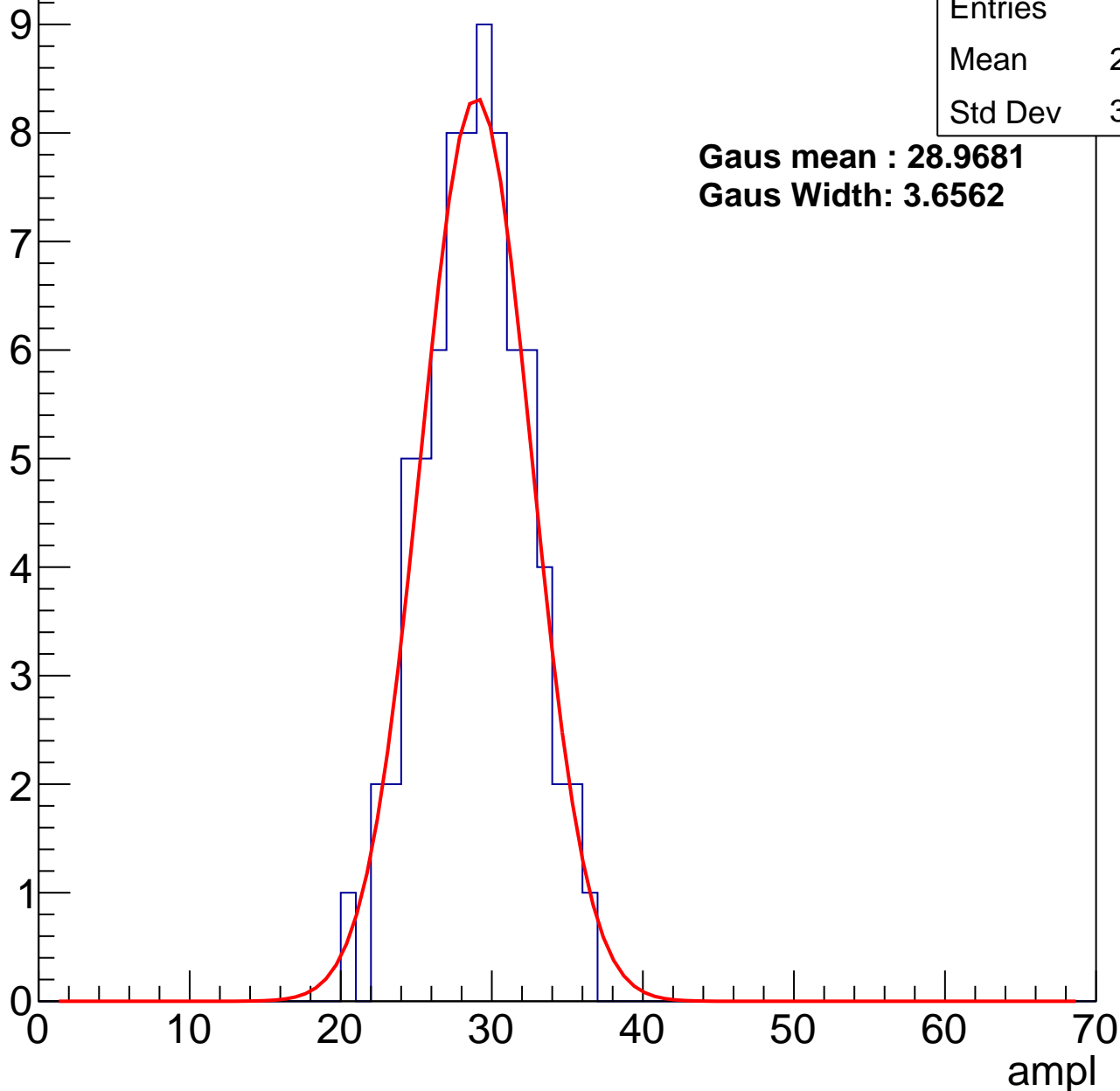
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	28.48
Std Dev	3.368

**Gaus mean : 28.9681**

**Gaus Width: 3.6562**



# B0L000S, U25-ch73, adc1

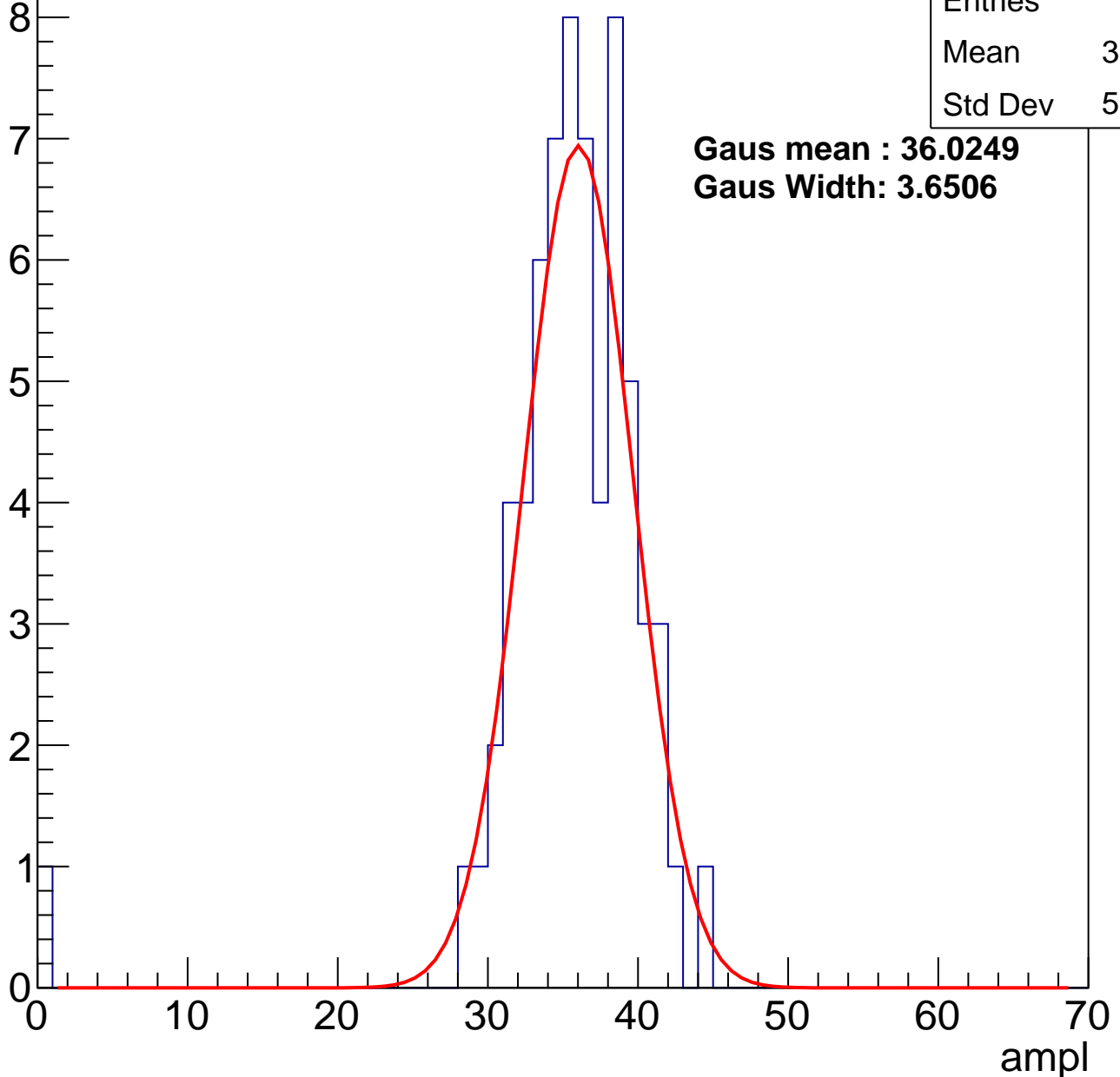
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	35.05
Std Dev	5.476

**Gaus mean : 36.0249**

**Gaus Width: 3.6506**



# B0L000S, U25-ch73, adc2

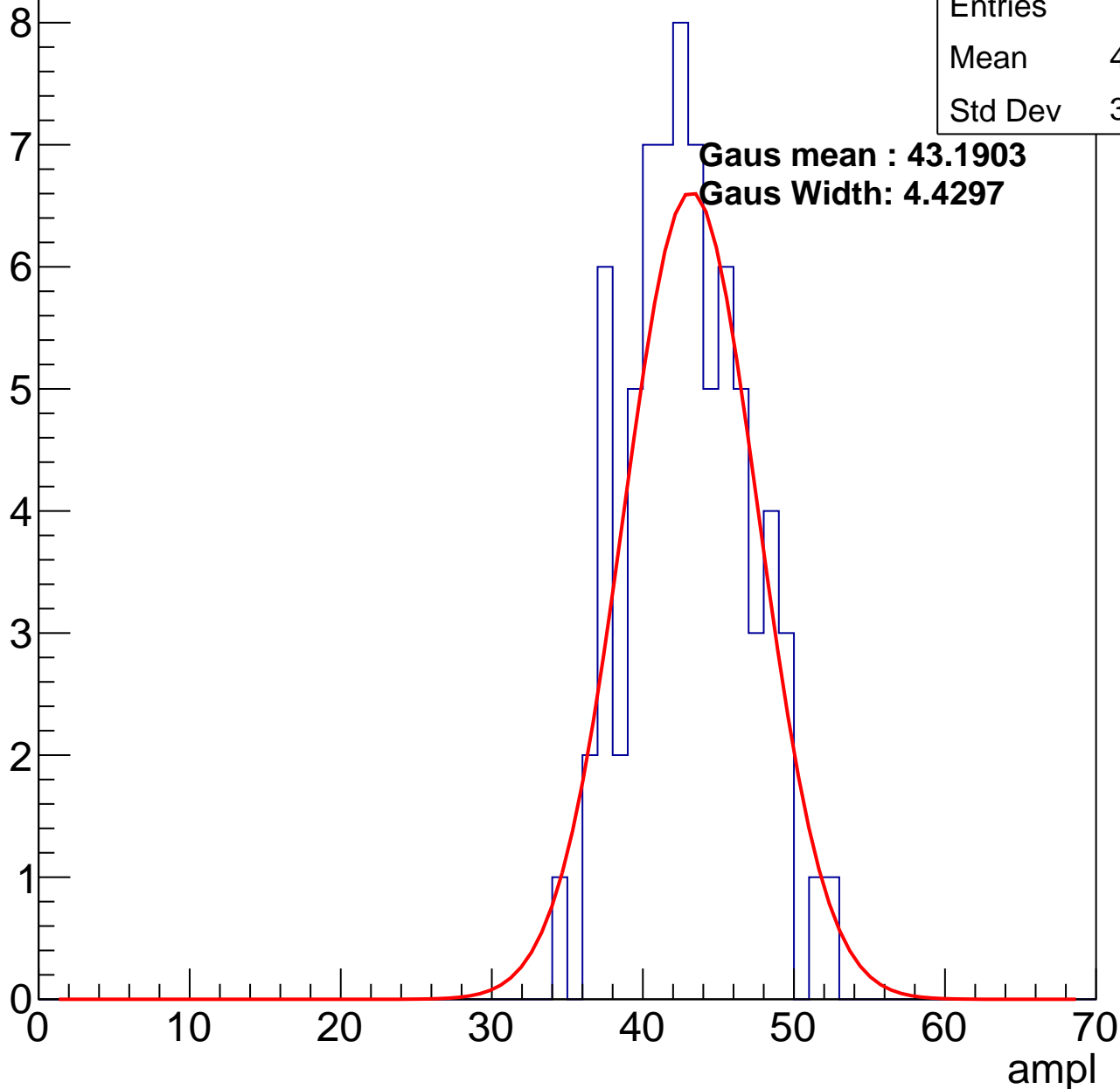
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	42.55
Std Dev	3.854

**Gaus mean : 43.1903**

**Gaus Width: 4.4297**



# B0L000S, U25-ch73, adc3

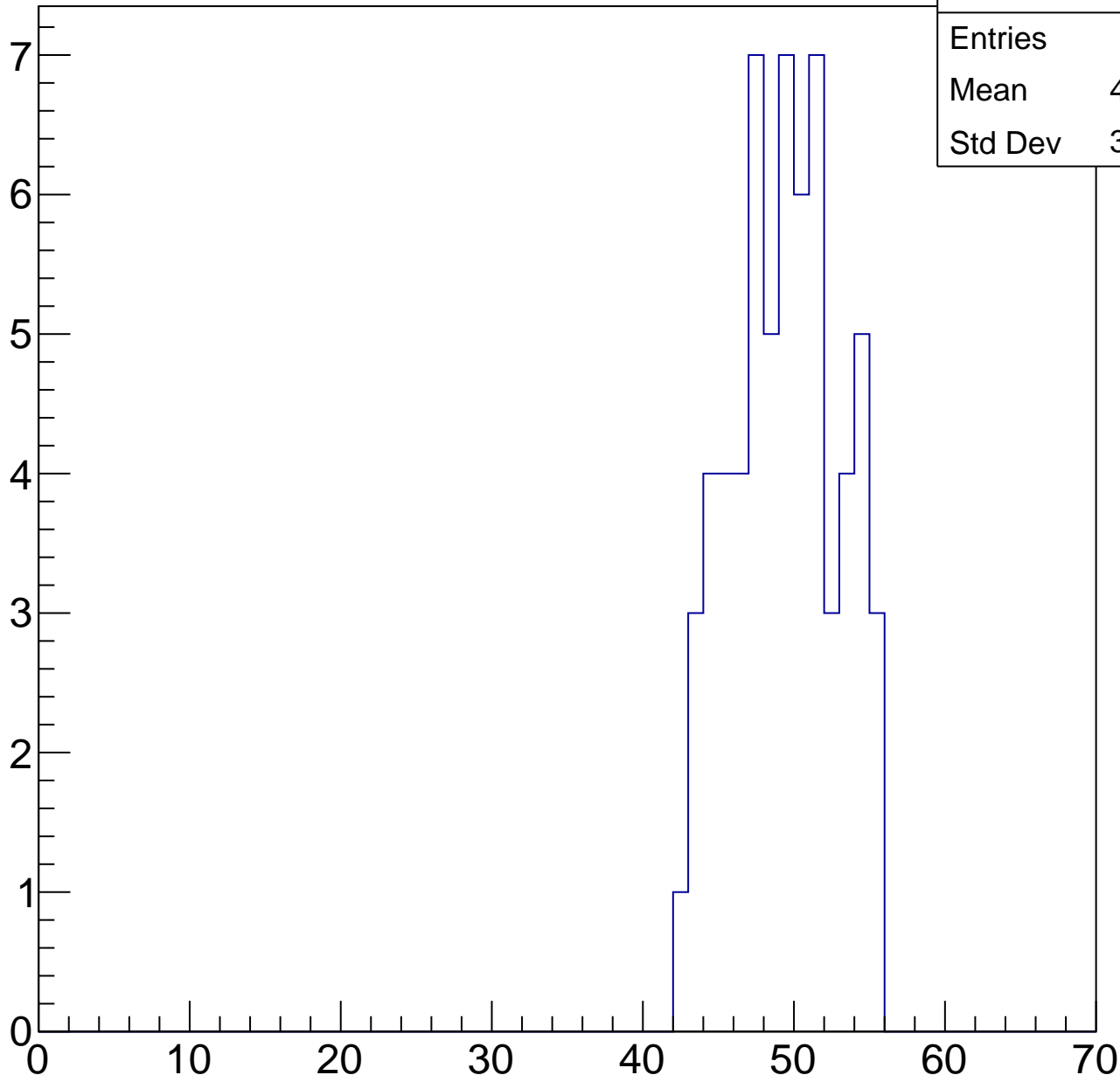
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	48.94
Std Dev	3.445

ampl

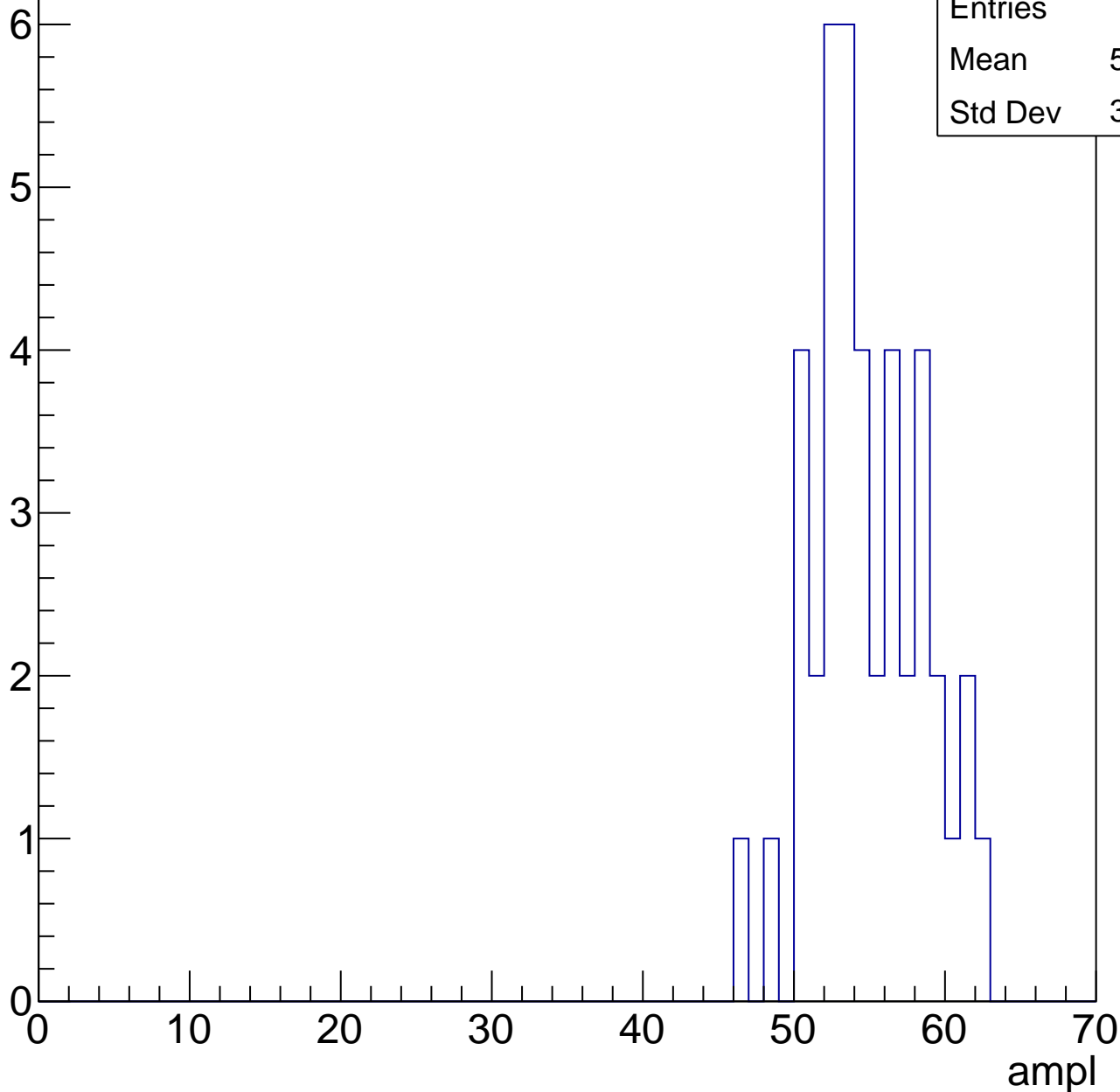


# B0L000S, U25-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

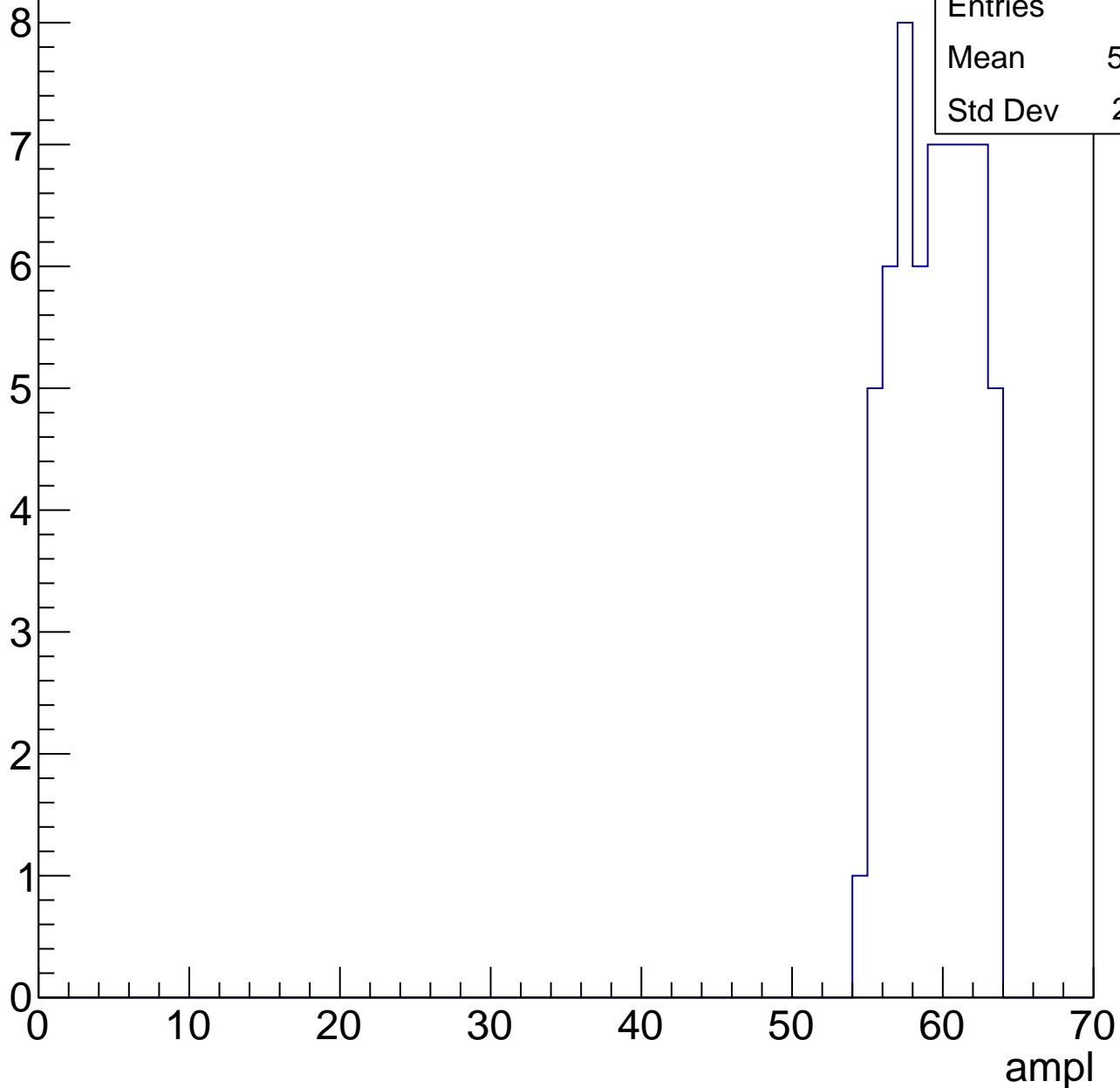
Entries	42
Mean	54.38
Std Dev	3.632



# B0L000S, U25-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

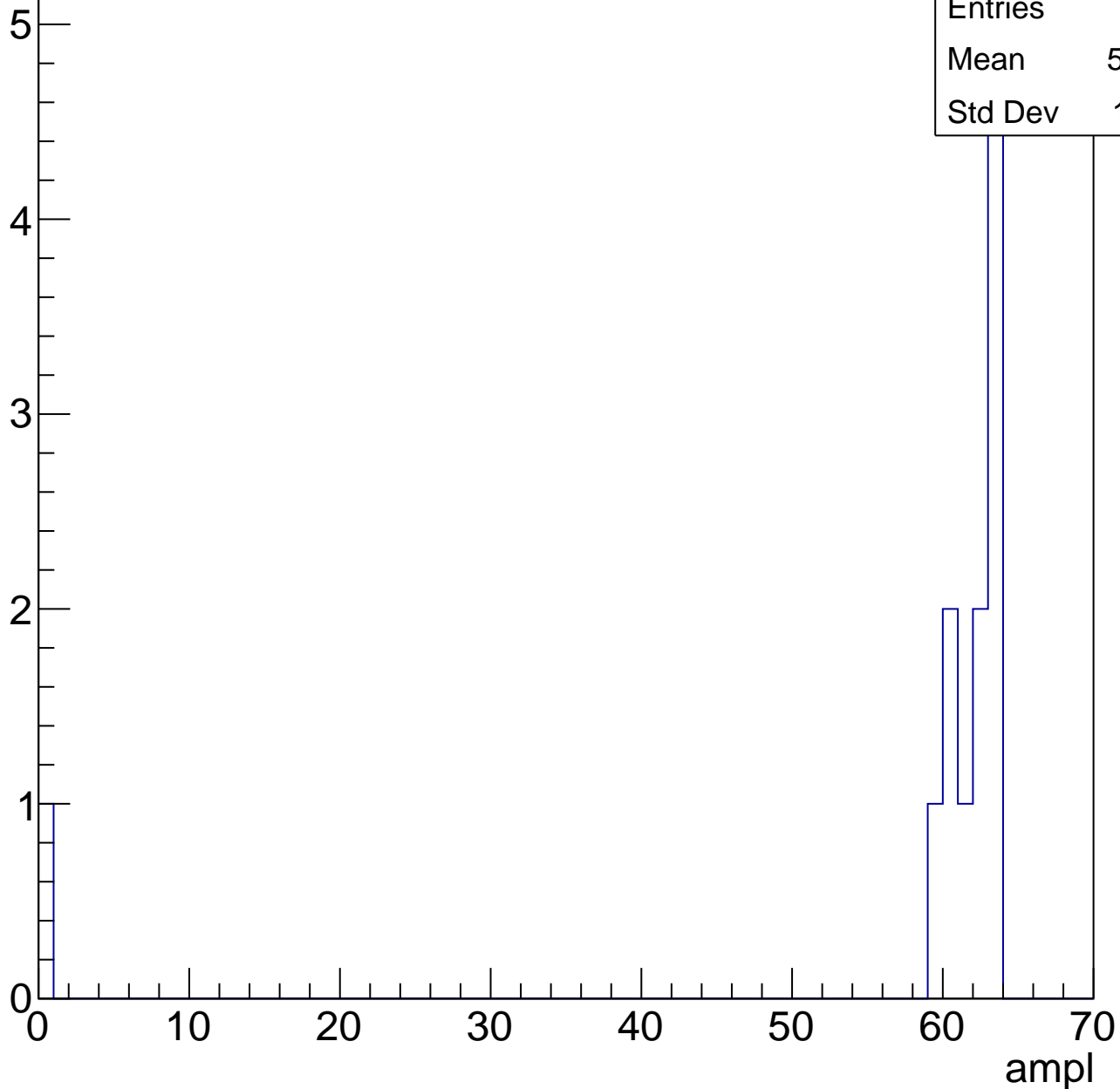


# B0L000S, U25-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	56.58
Std Dev	17.11





# B0L000S, U25-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch74, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	82
Mean	28.94
Std Dev	3.713

**Gaus mean : 29.0169**

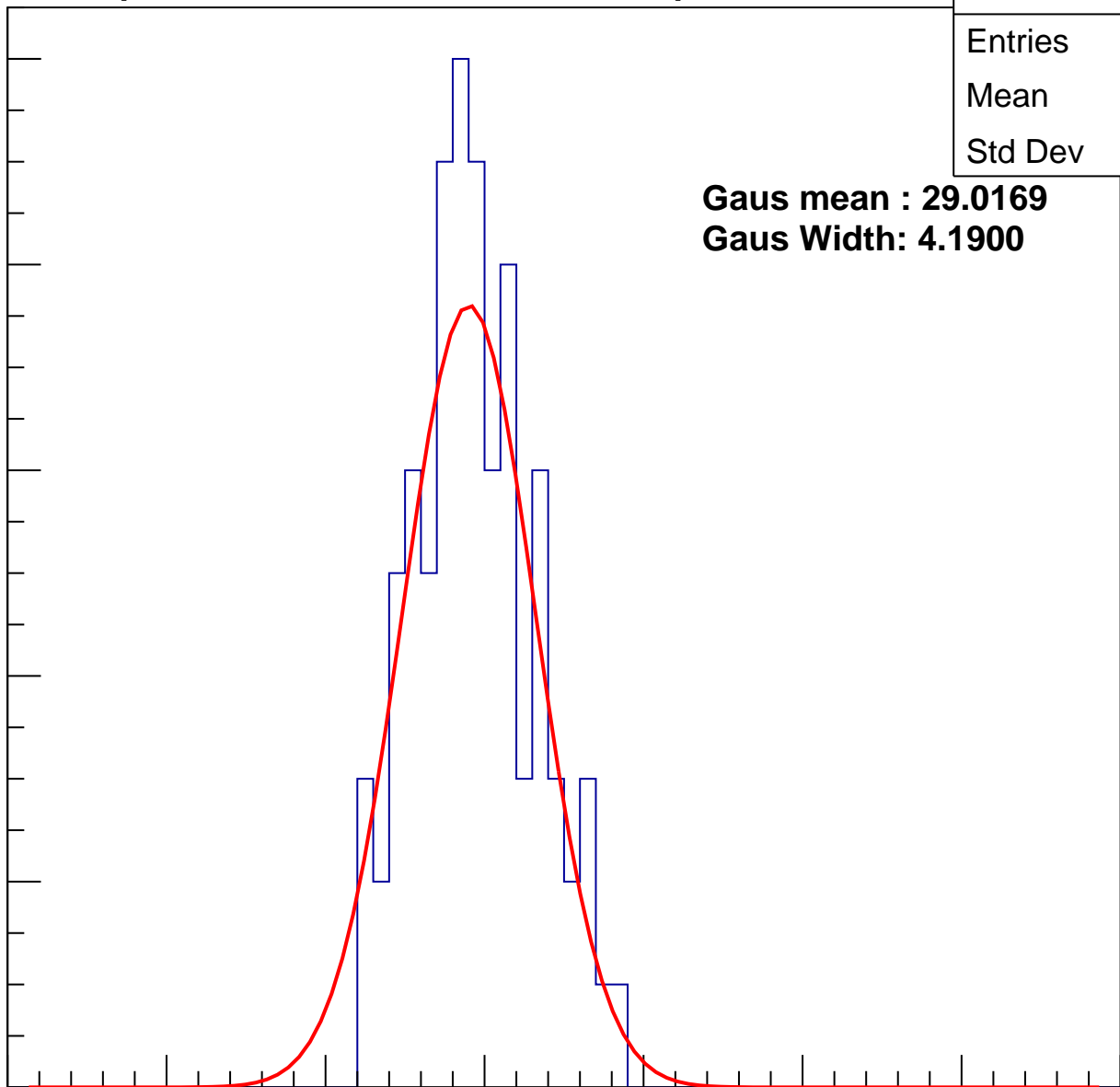
**Gaus Width: 4.1900**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch74, adc1

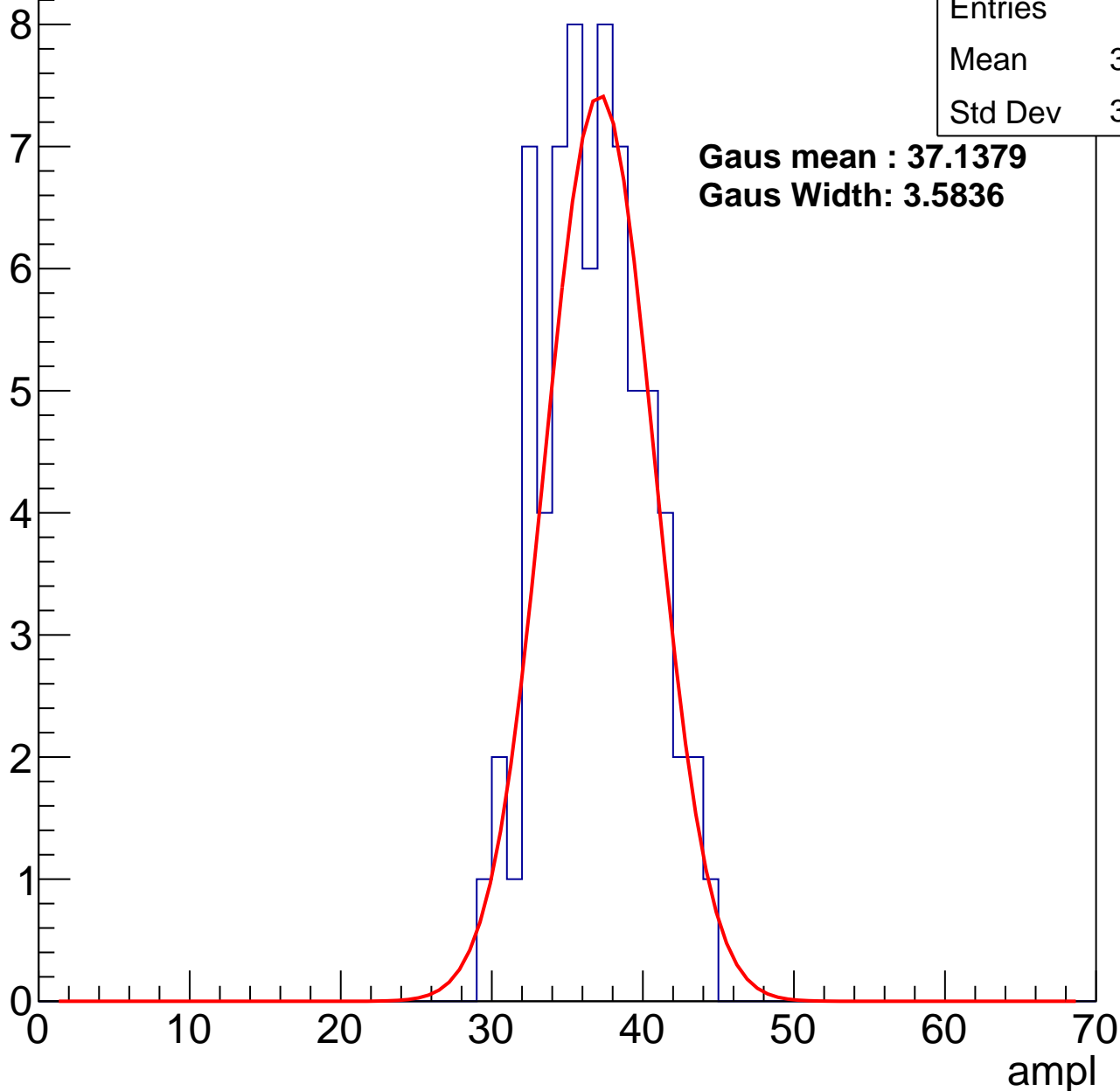
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	36.36
Std Dev	3.414

**Gaus mean : 37.1379**

**Gaus Width: 3.5836**



# B0L000S, U25-ch74, adc2

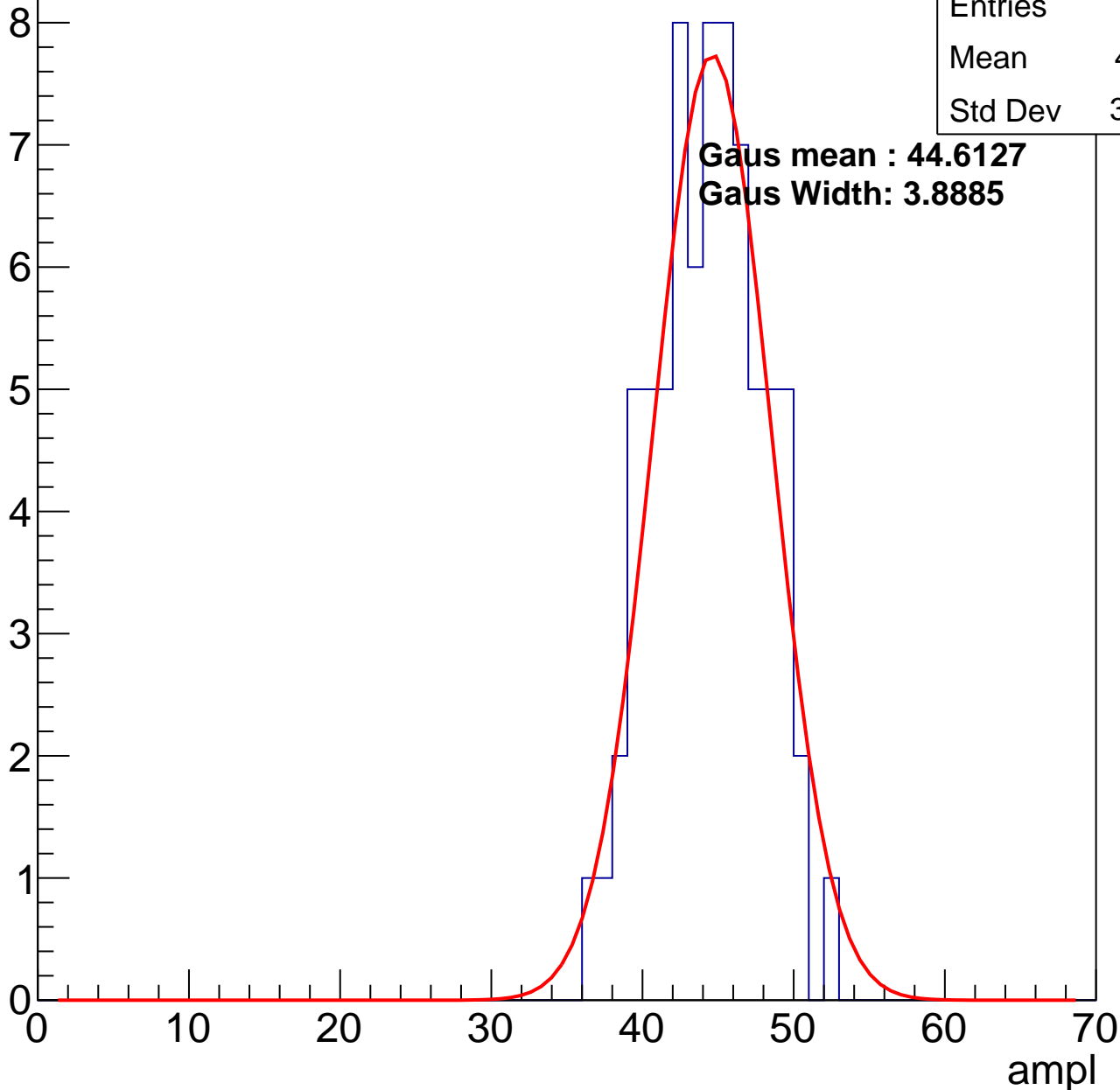
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	43.91
Std Dev	3.476

**Gaus mean : 44.6127**

**Gaus Width: 3.8885**

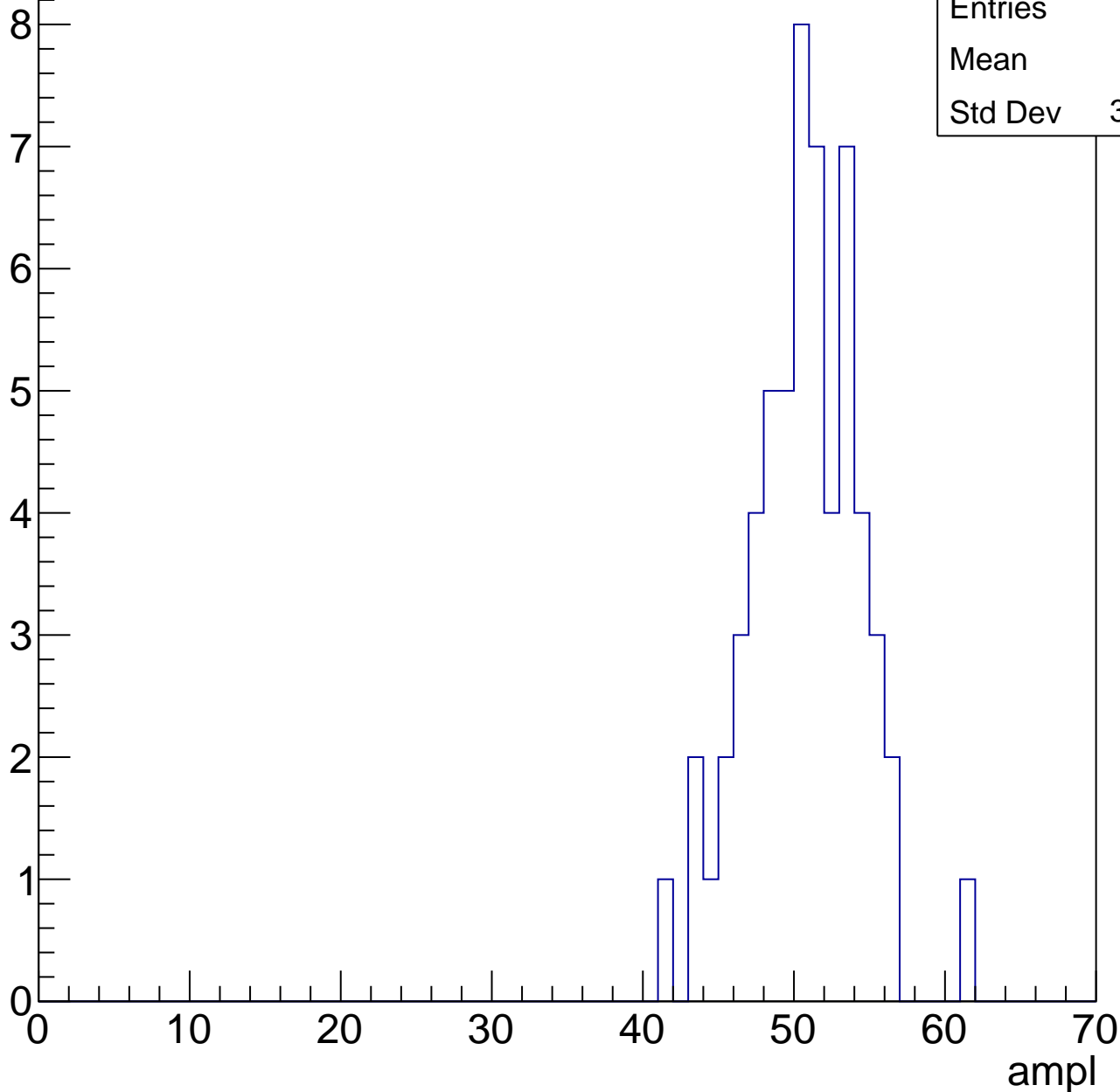


# B0L000S, U25-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	50.2
Std Dev	3.658

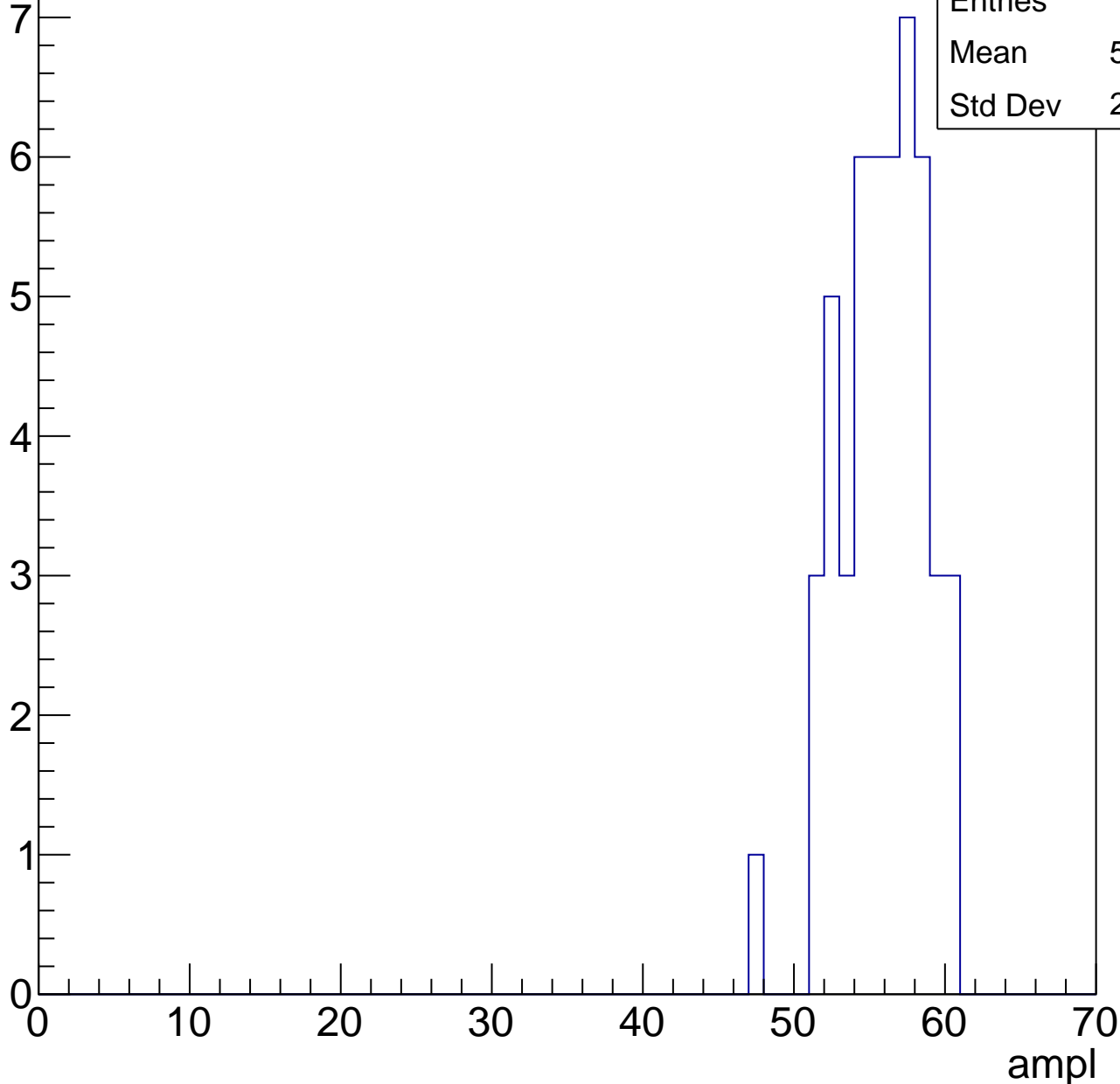


# B0L000S, U25-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

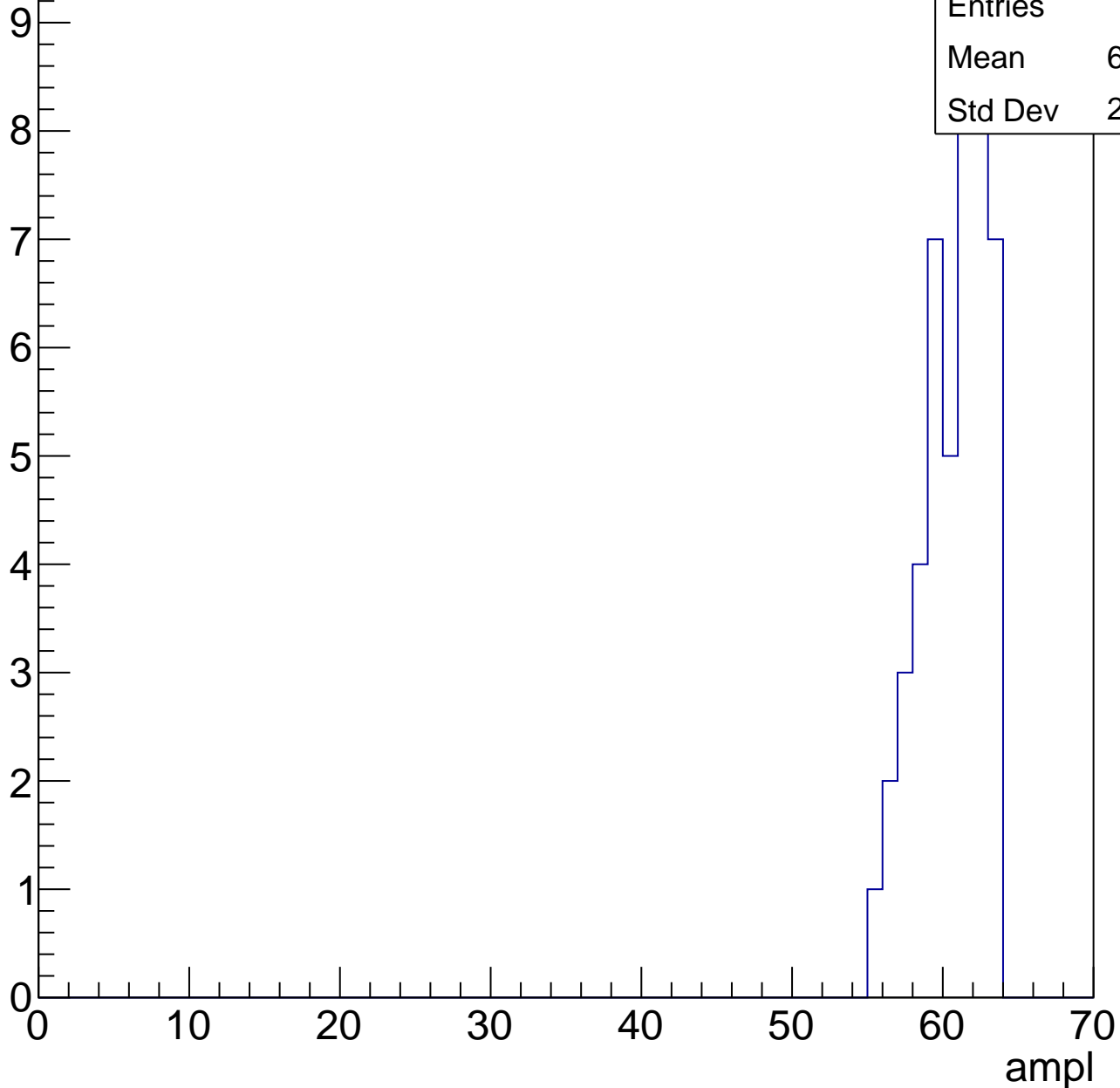
Entries	49
Mean	55.37
Std Dev	2.783



# B0L000S, U25-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

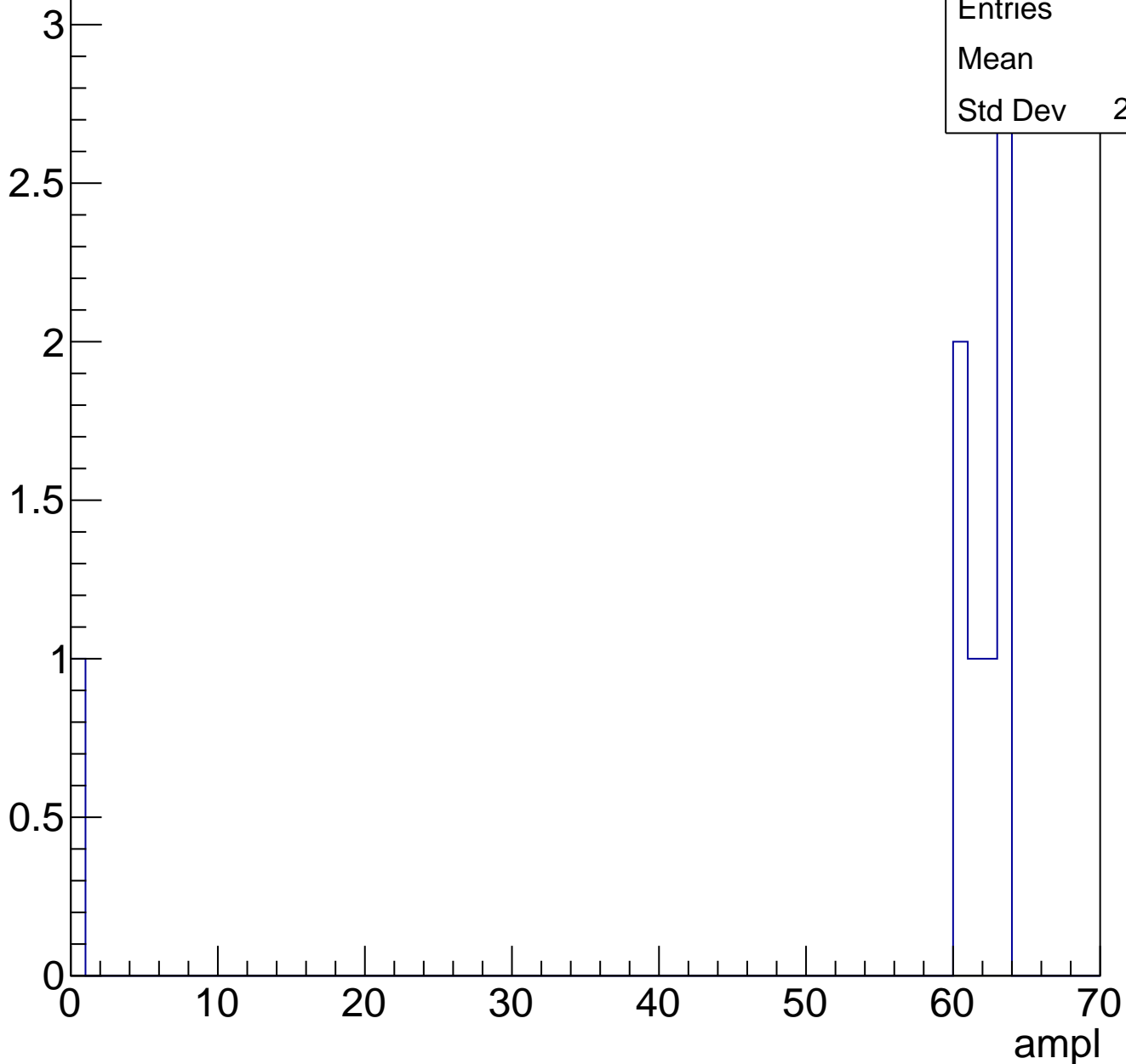
Entry



# B0L000S, U25-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch75, adc0

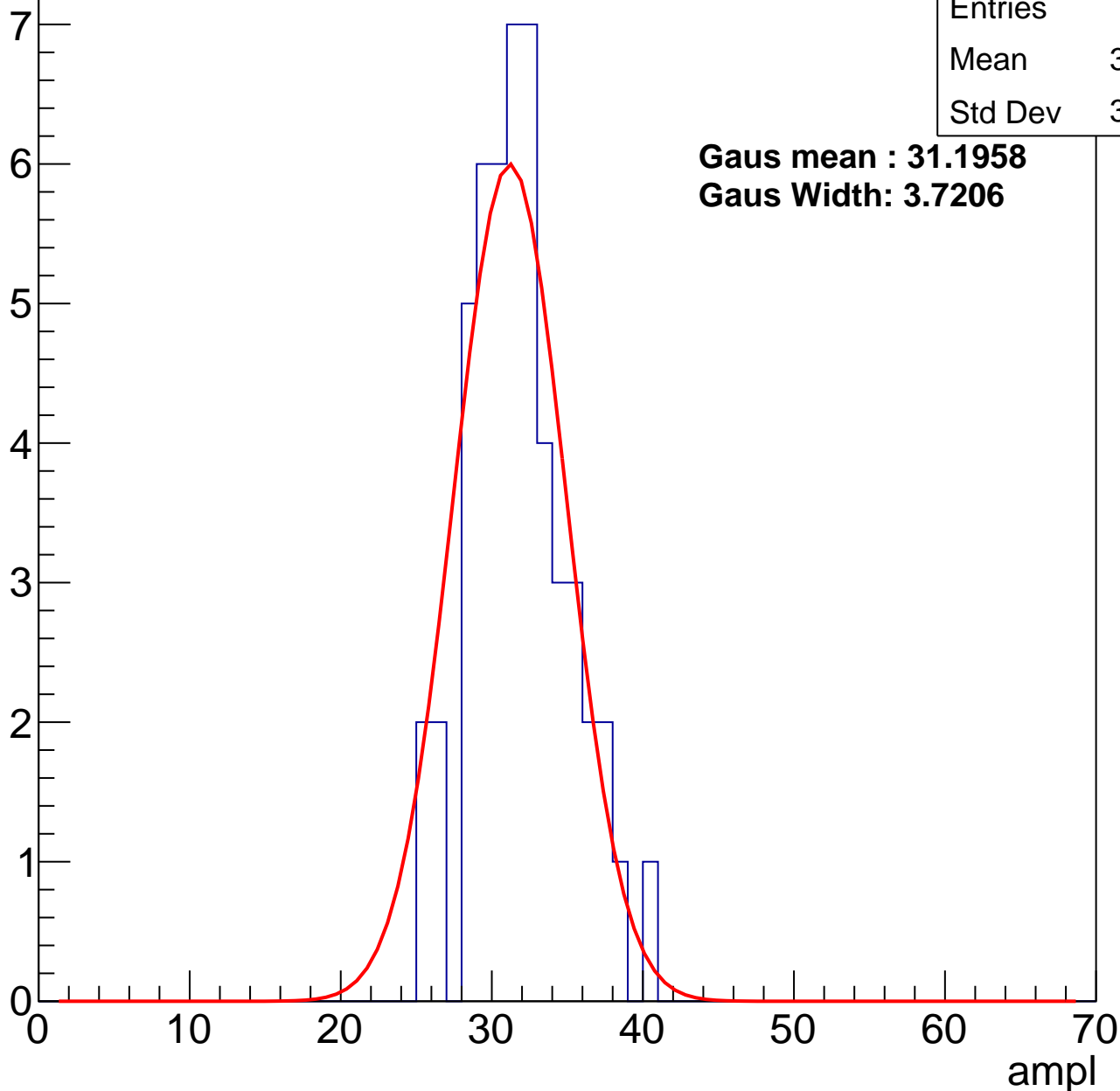
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	31.37
Std Dev	3.254

**Gaus mean : 31.1958**

**Gaus Width: 3.7206**



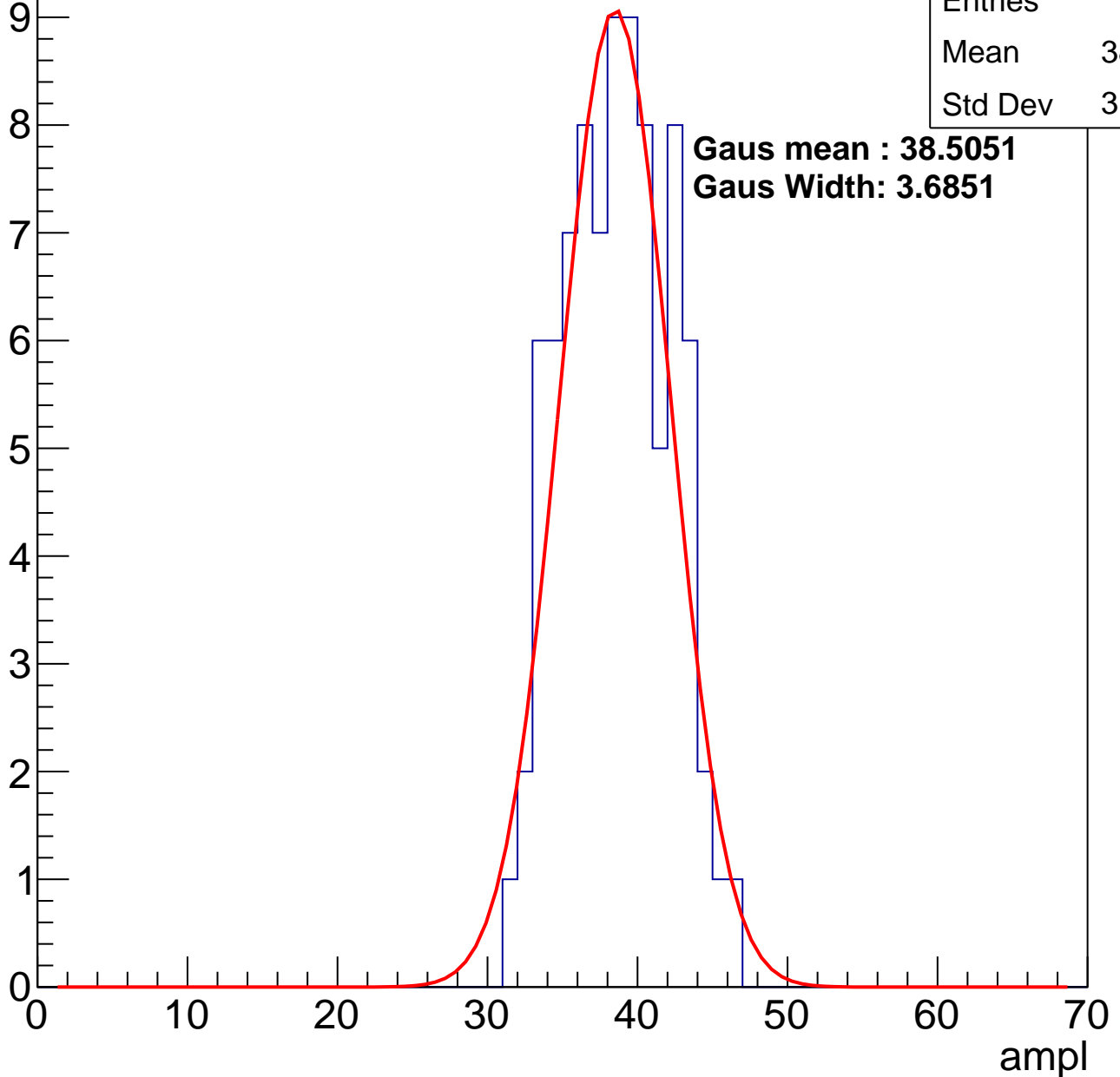
# B0L000S, U25-ch75, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	38.14
Std Dev	3.438

**Gaus mean : 38.5051**  
**Gaus Width: 3.6851**



# B0L000S, U25-ch75, adc2

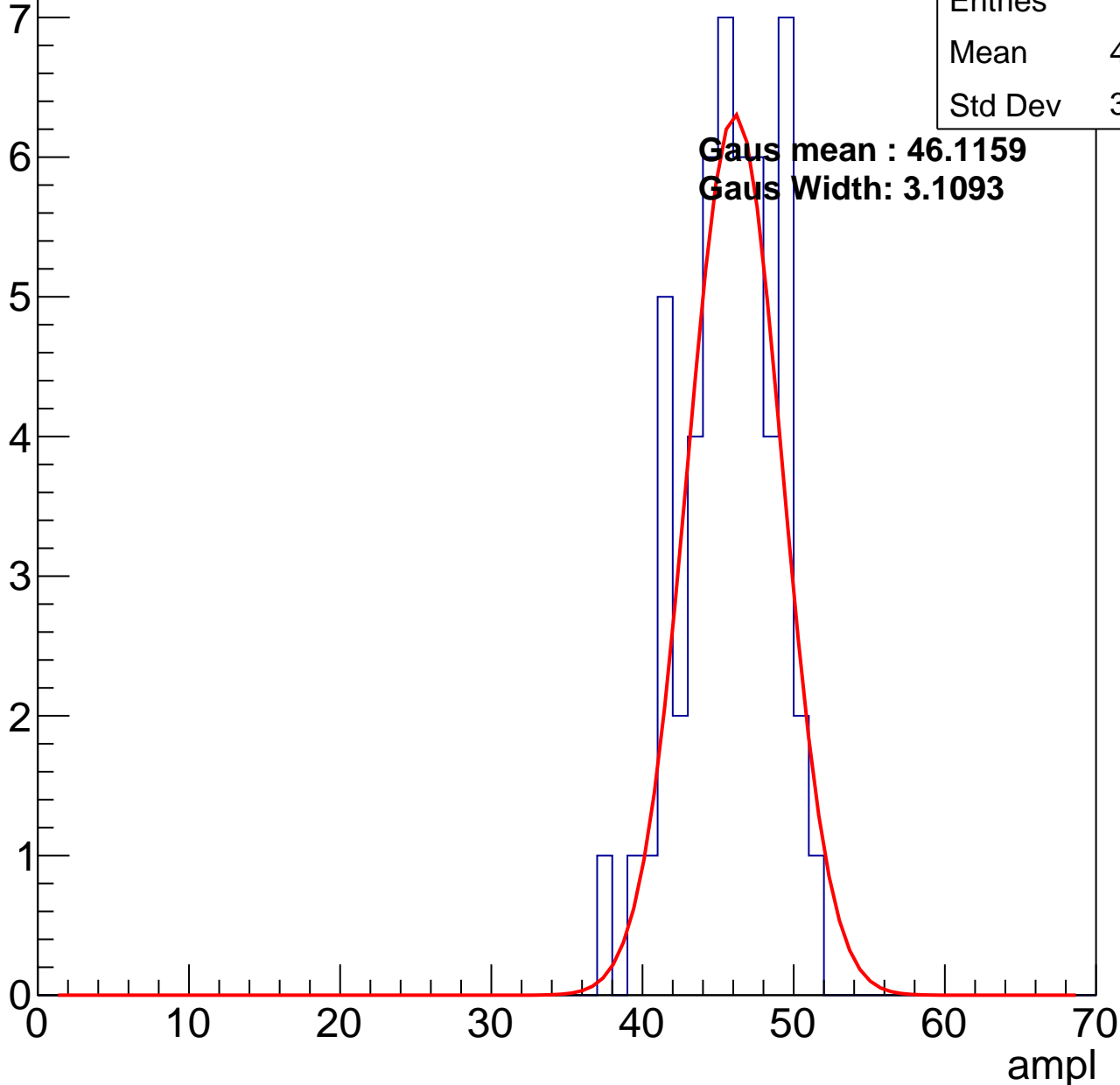
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	45.28
Std Dev	3.086

Gaus mean : 46.1159

Gaus Width: 3.1093

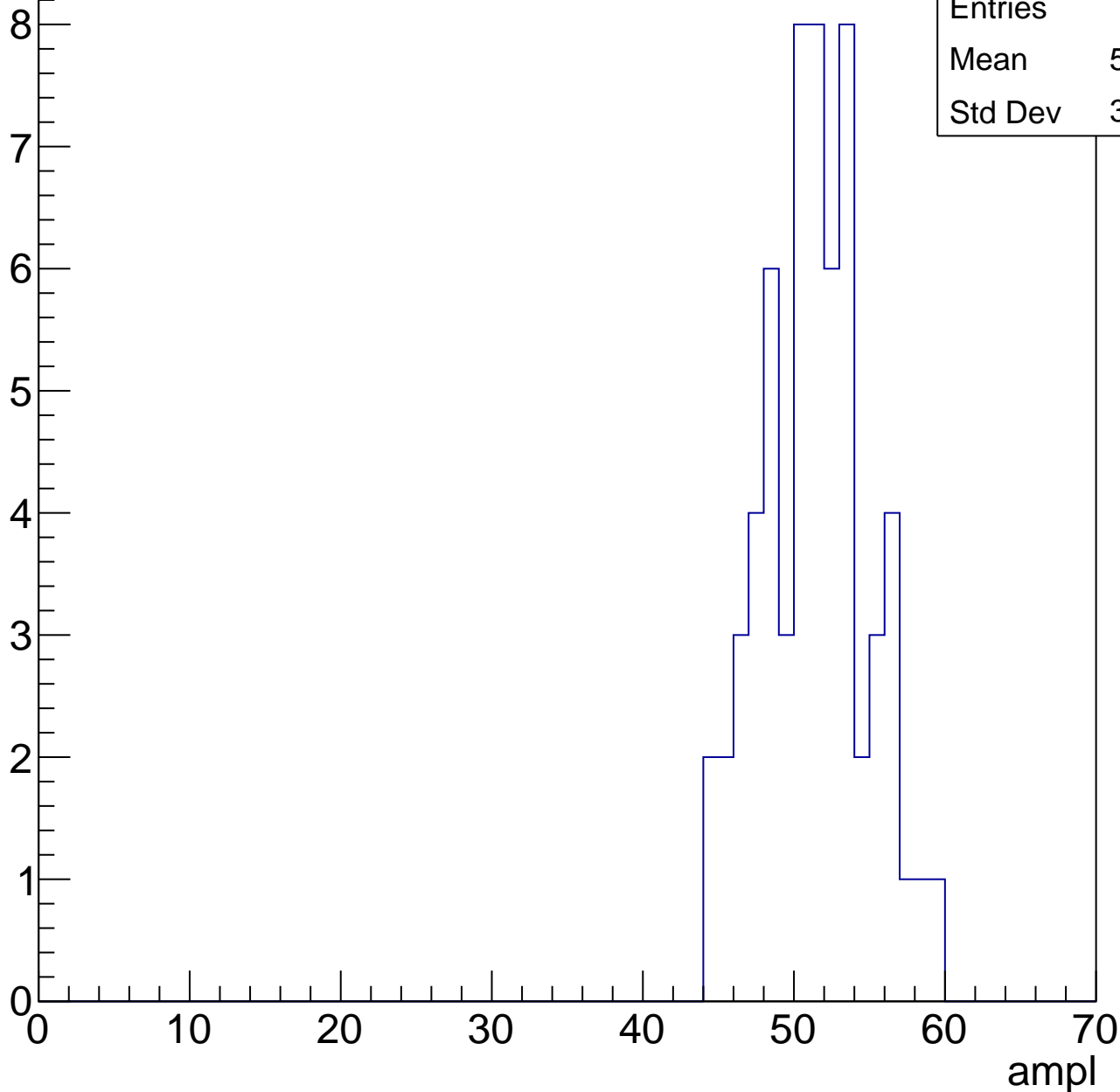


# B0L000S, U25-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	50.87
Std Dev	3.443

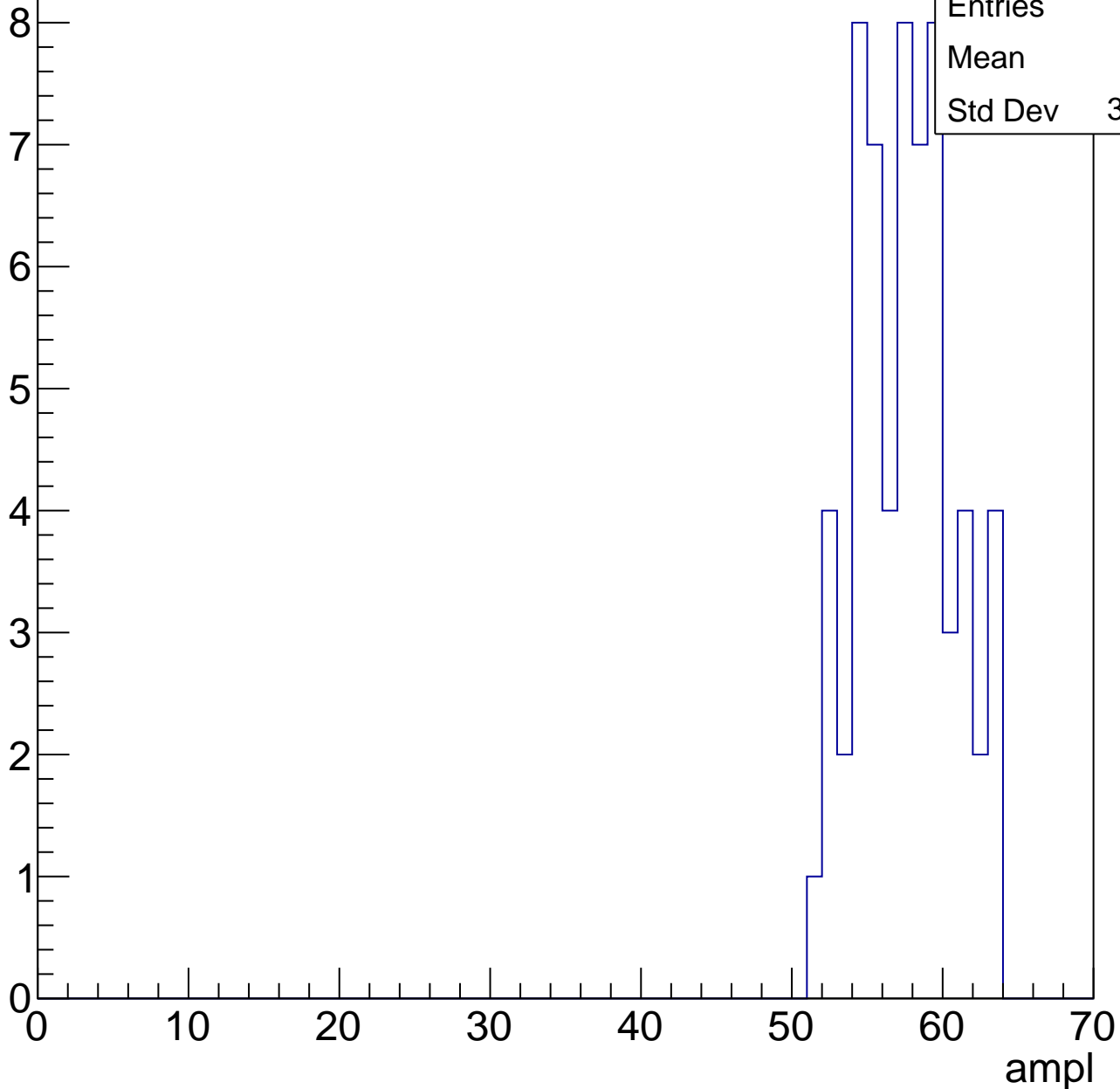


# B0L000S, U25-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

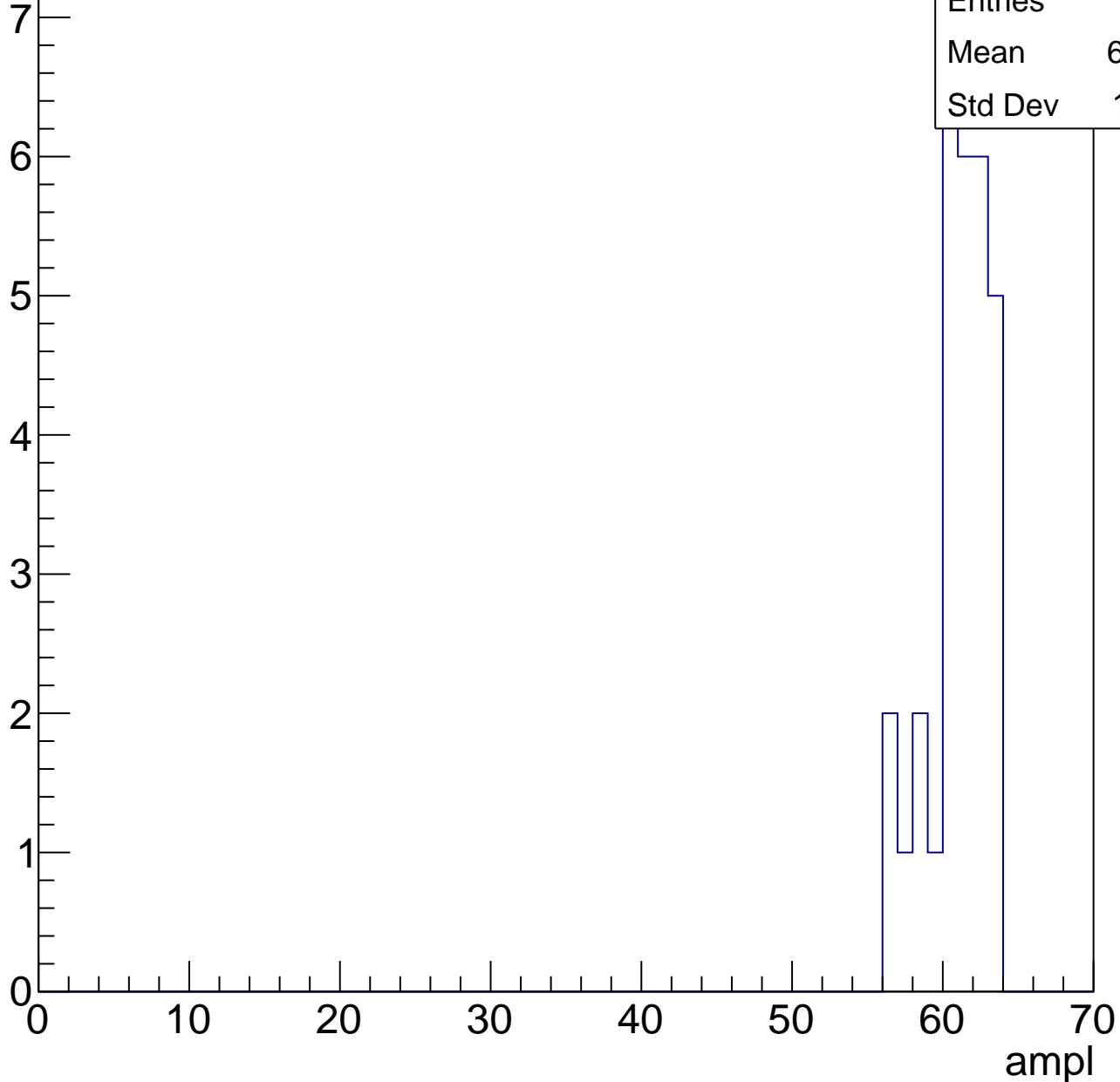
Entries	62
Mean	57.1
Std Dev	3.099



# B0L000S, U25-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

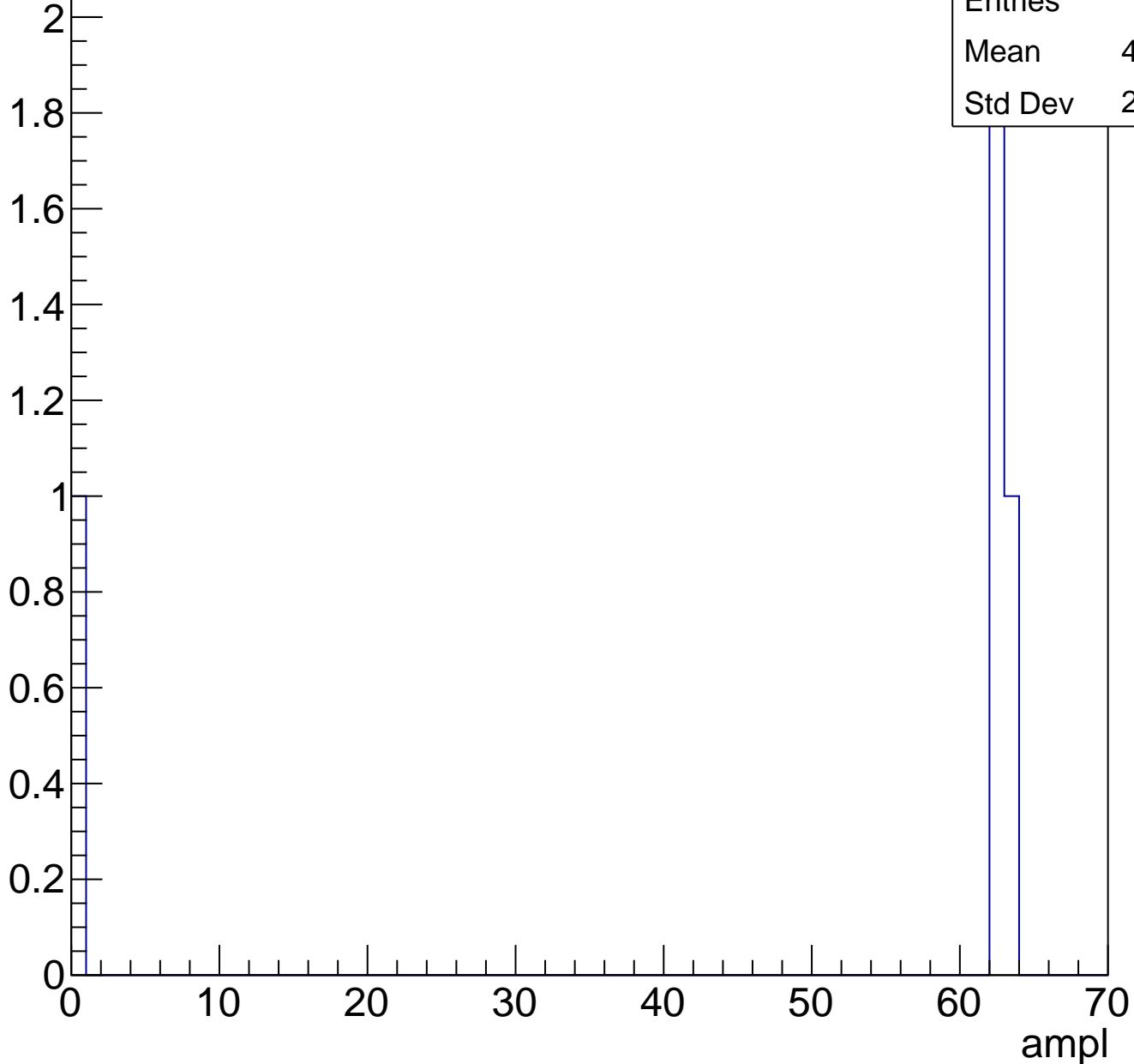
Entry



# B0L000S, U25-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch76, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	29.67
Std Dev	6.027

**Gaus mean : 31.1715**

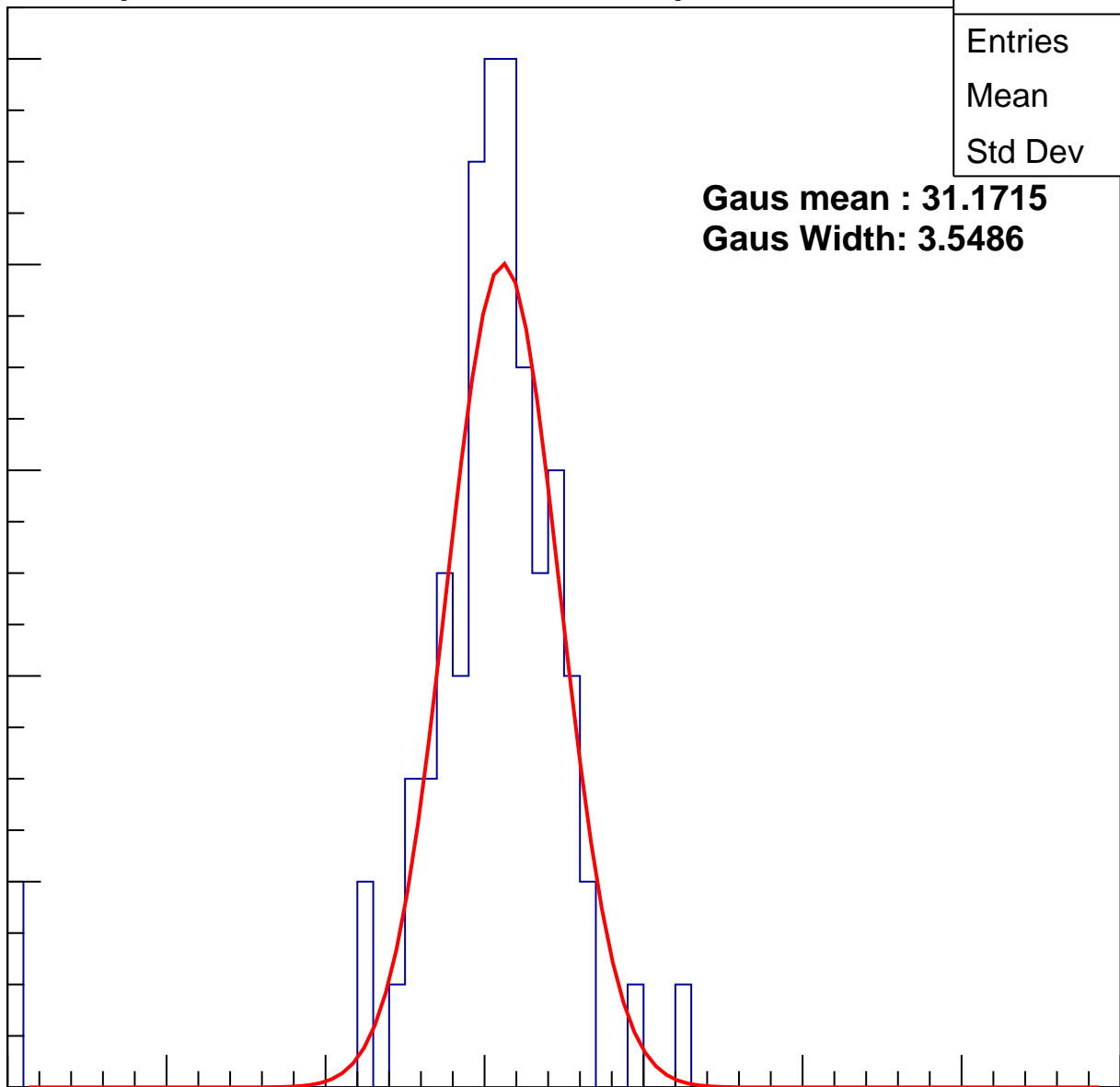
**Gaus Width: 3.5486**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

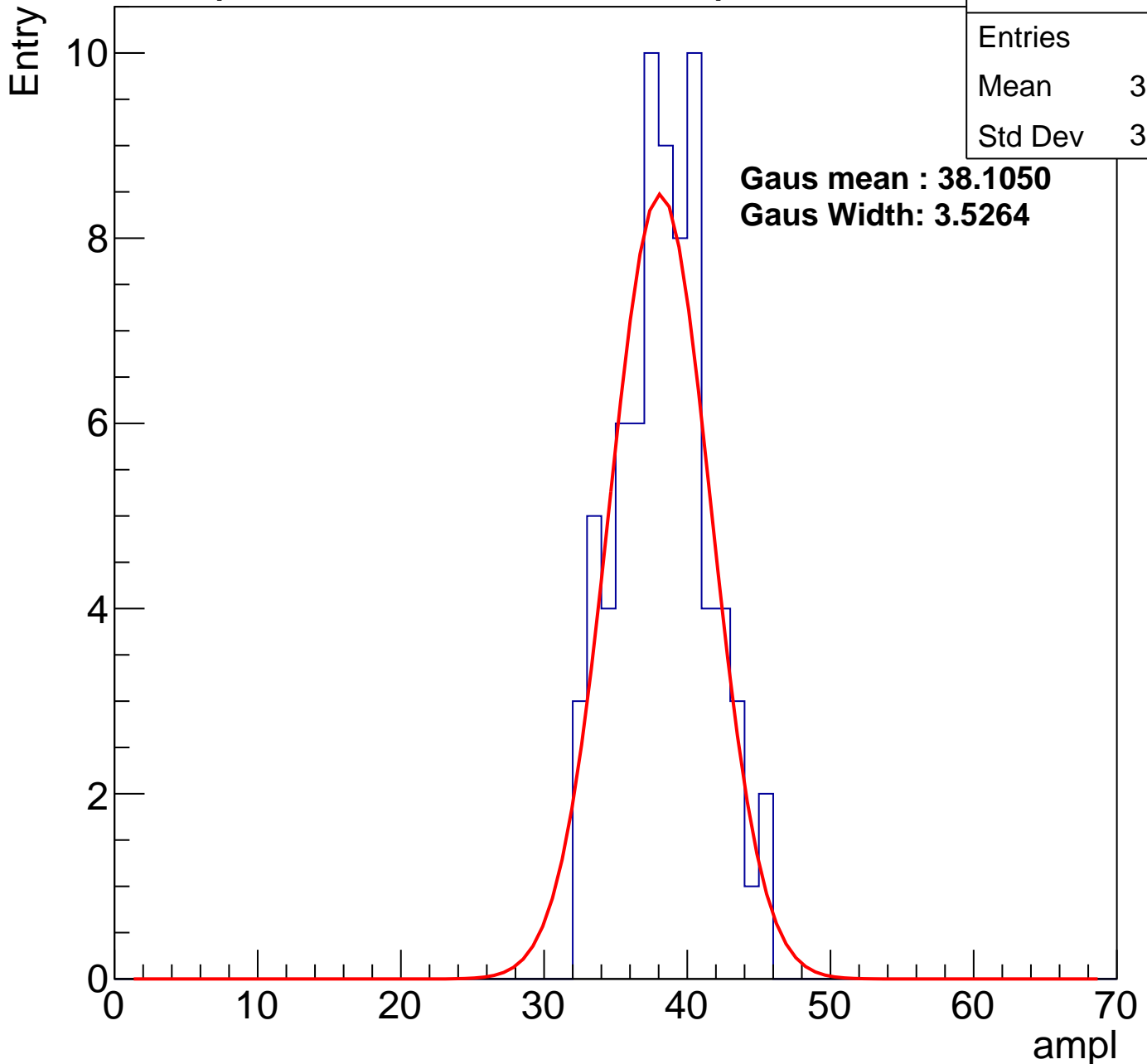


# B0L000S, U25-ch76, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	37.89
Std Dev	3.144

**Gaus mean : 38.1050**  
**Gaus Width: 3.5264**



# B0L000S, U25-ch76, adc2

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	63
Mean	44.76
Std Dev	3.308

**Gaus mean : 45.3329**

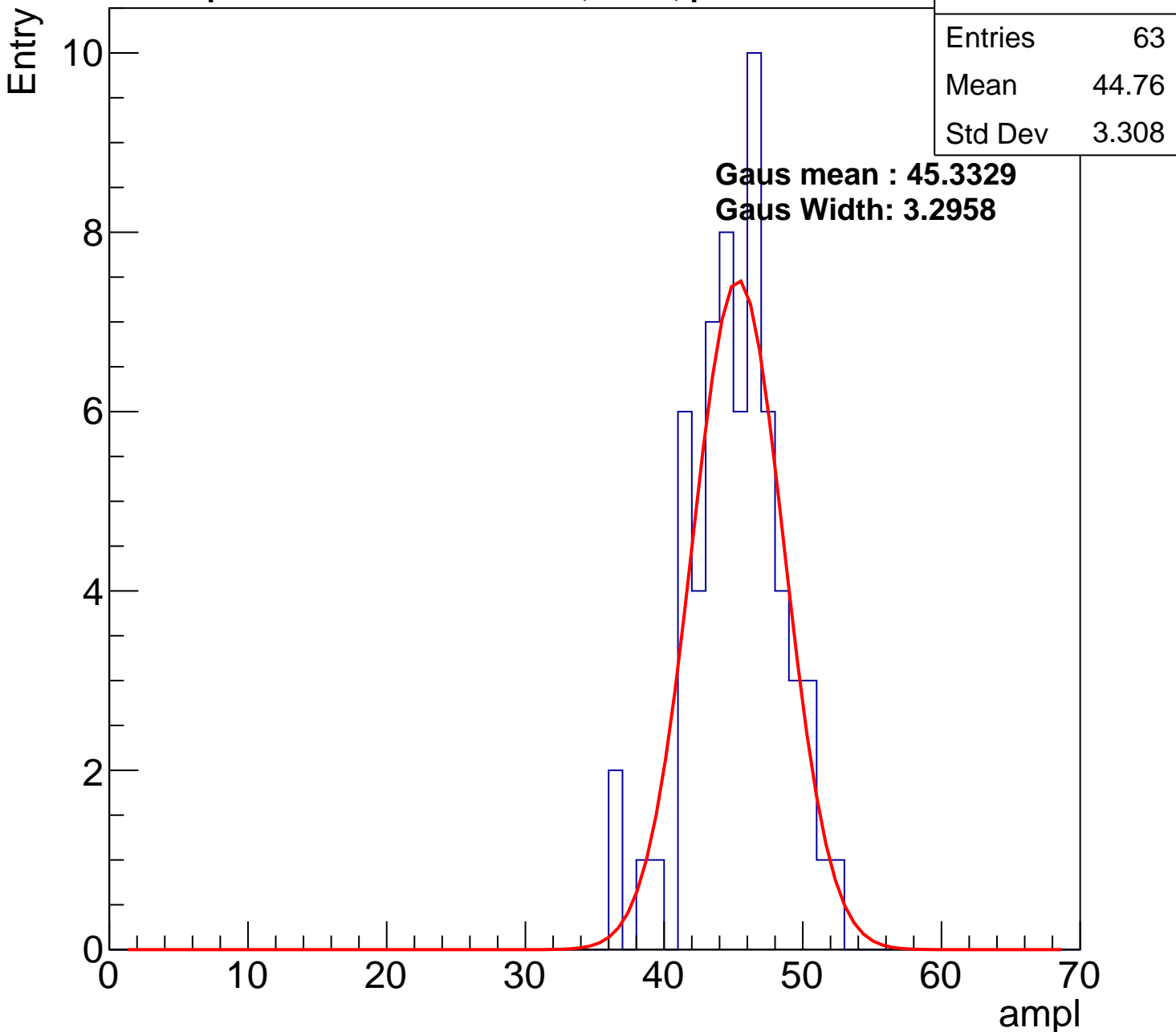
**Gaus Width: 3.2958**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

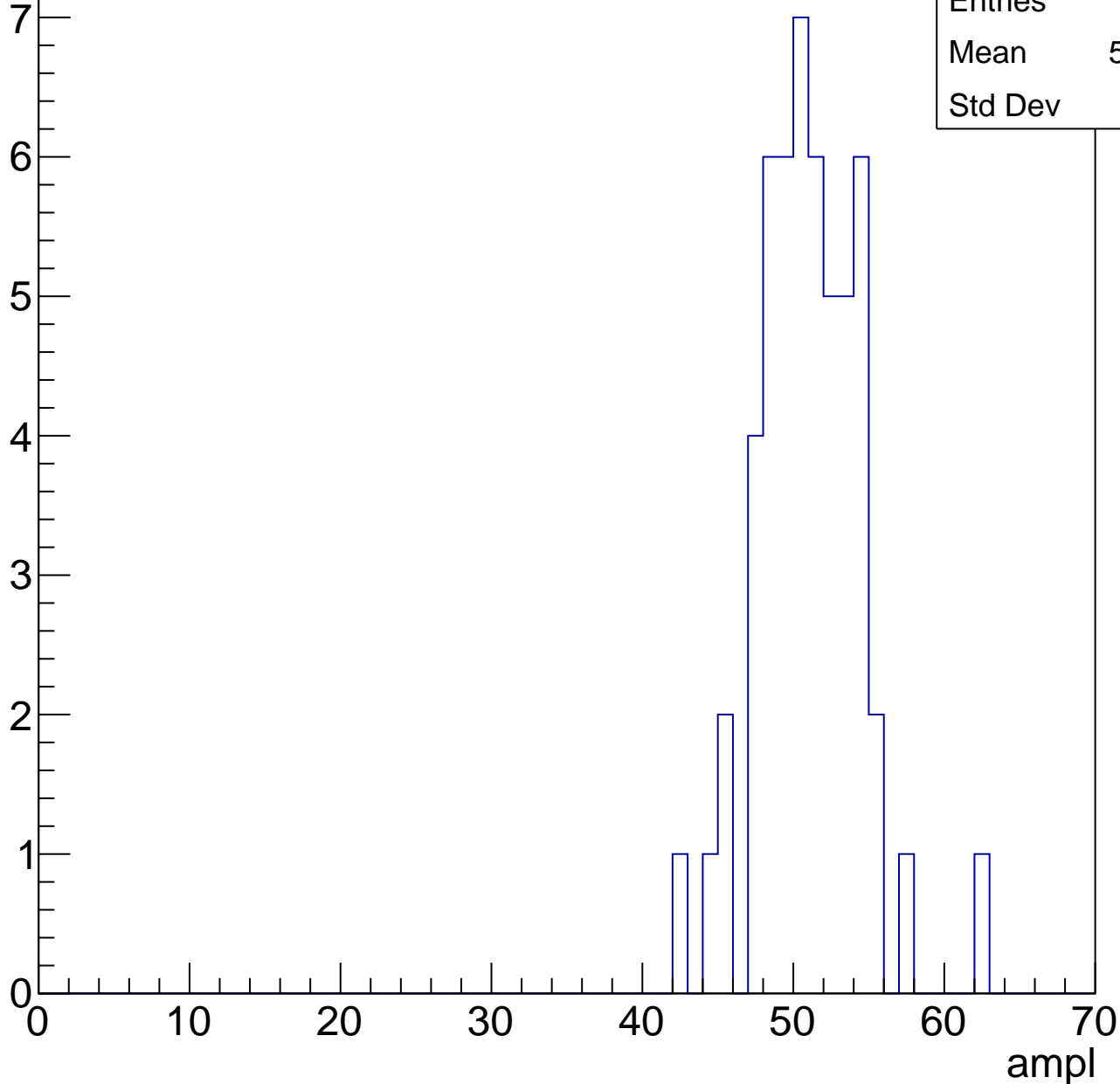


# B0L000S, U25-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

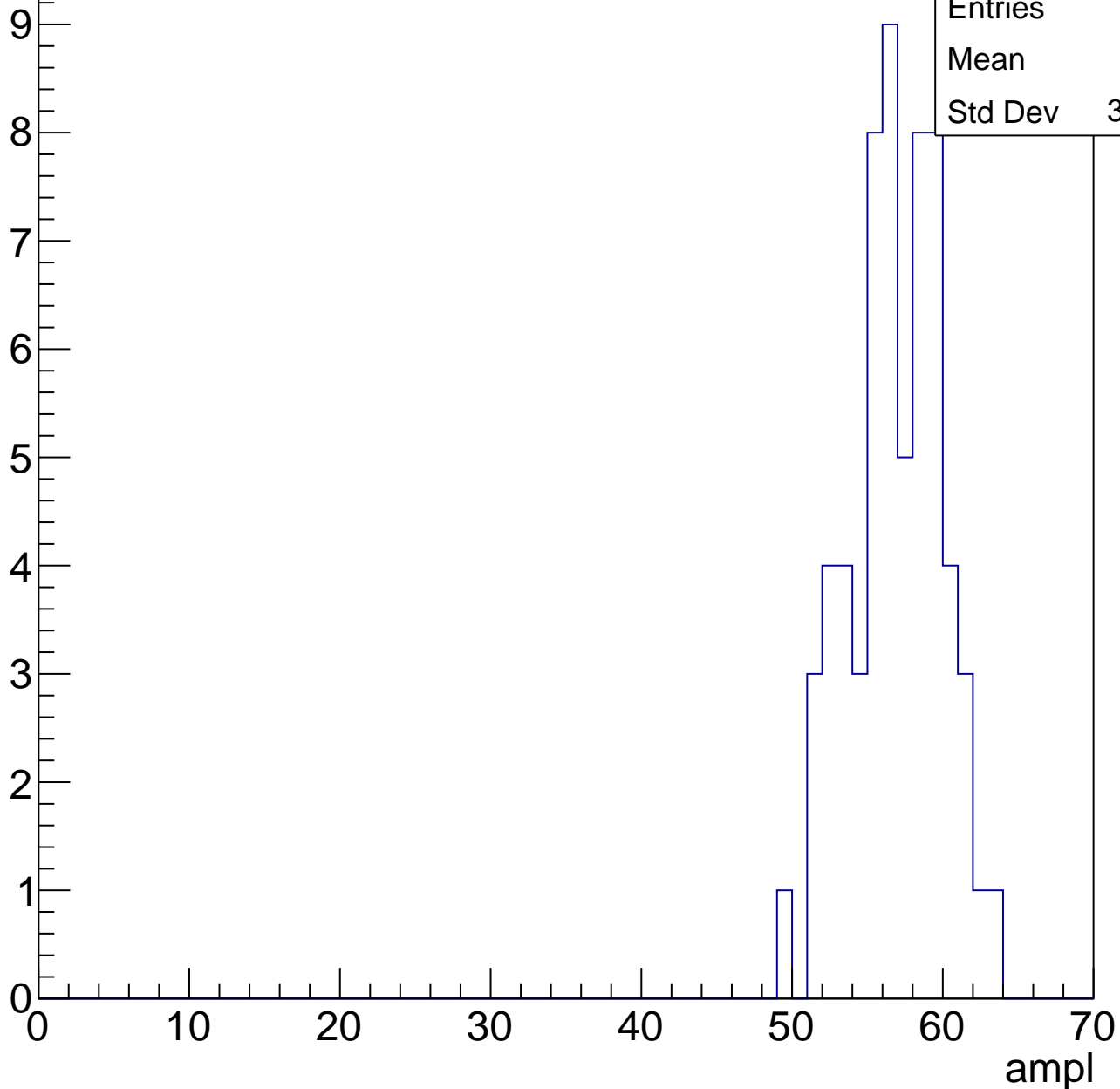
Entries	53
Mean	50.57
Std Dev	3.39



# B0L000S, U25-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



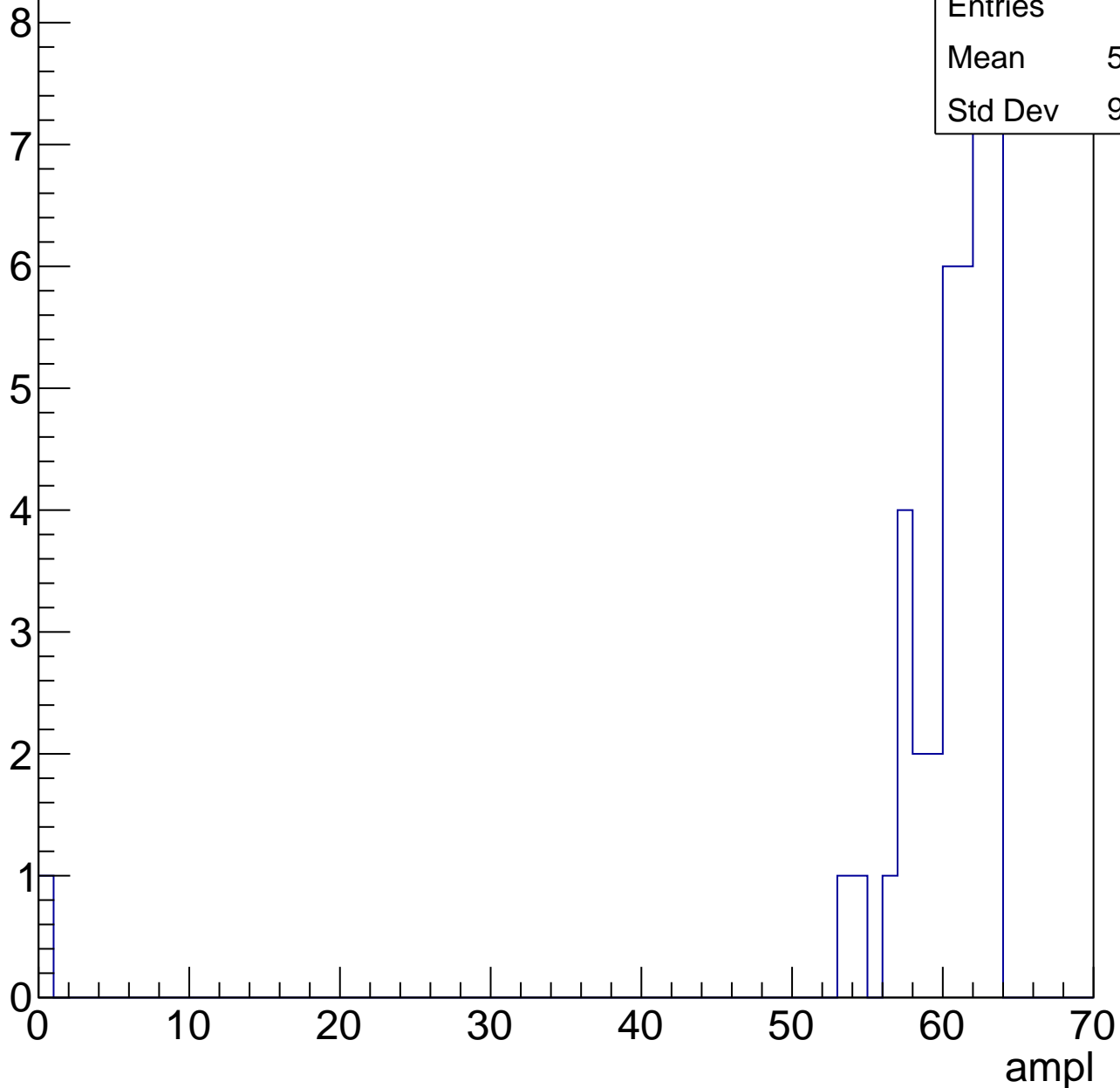
Entries	62
Mean	56.4
Std Dev	3.024

# B0L000S, U25-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	58.77
Std Dev	9.743



# B0L000S, U25-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch77, adc0

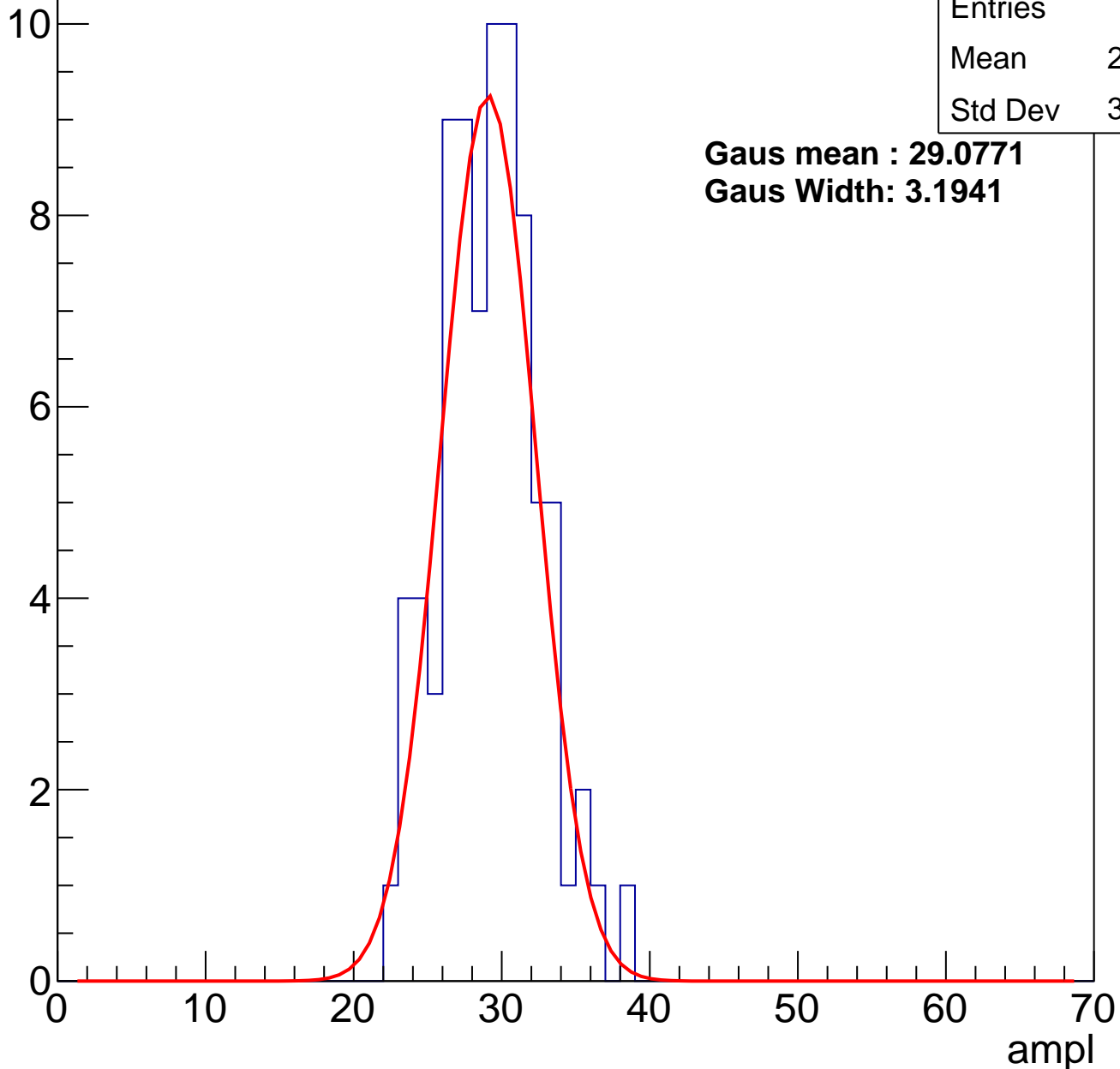
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	80
Mean	28.74
Std Dev	3.266

**Gaus mean : 29.0771**

**Gaus Width: 3.1941**

Entry



# B0L000S, U25-ch77, adc1

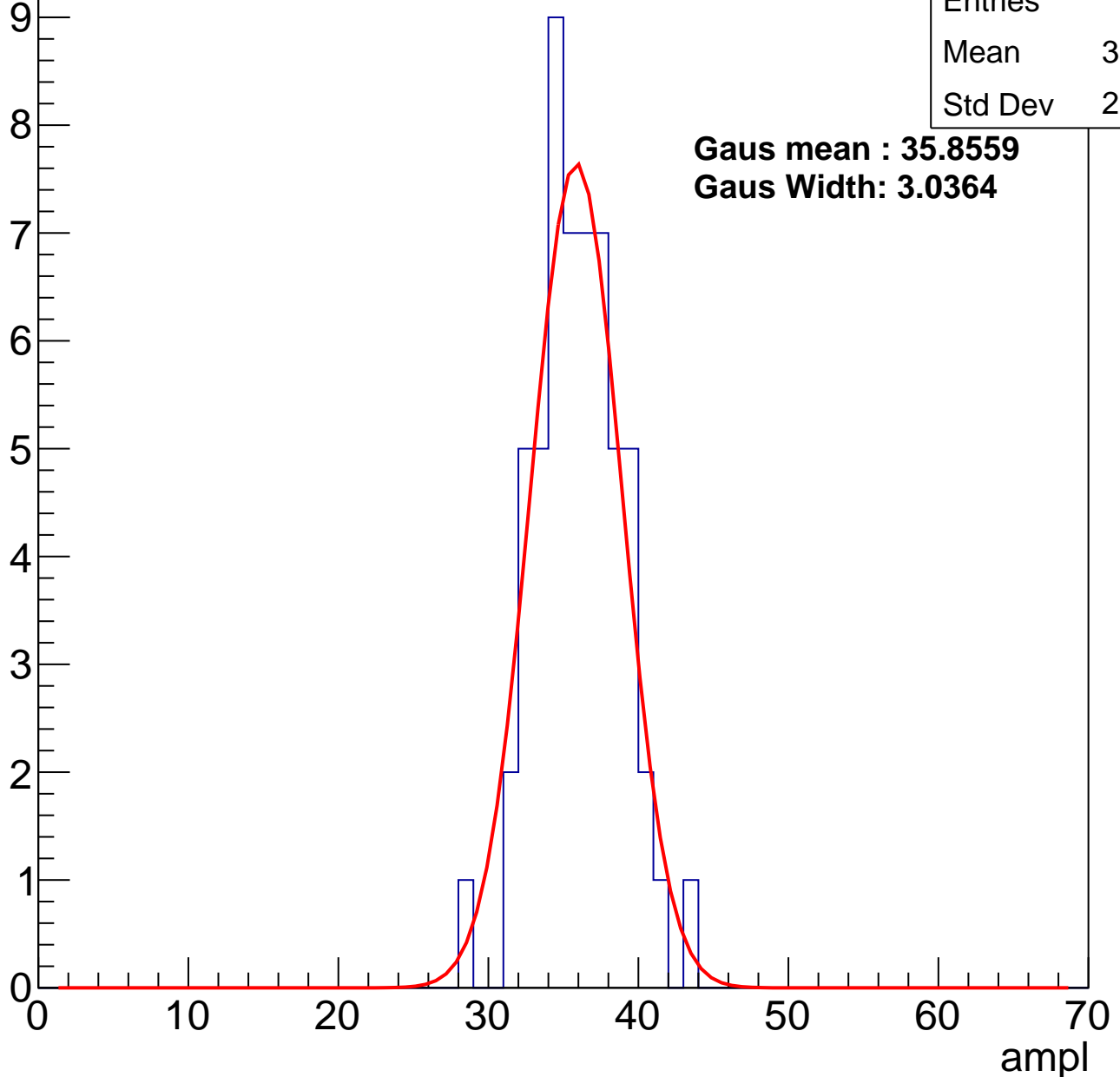
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	35.54
Std Dev	2.804

**Gaus mean : 35.8559**

**Gaus Width: 3.0364**



# B0L000S, U25-ch77, adc2

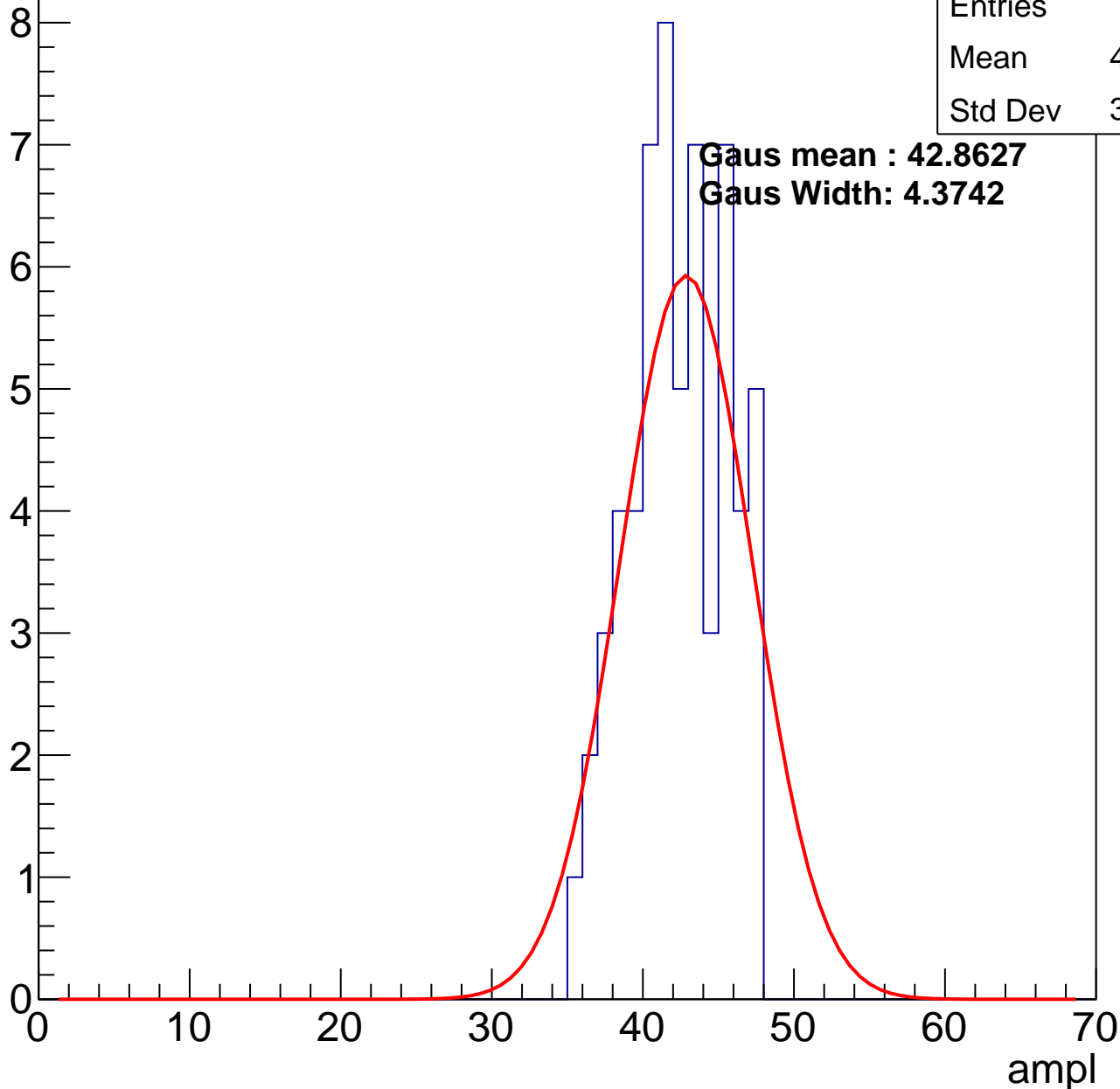
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.85
Std Dev	3.167

**Gaus mean : 42.8627**

**Gaus Width: 4.3742**

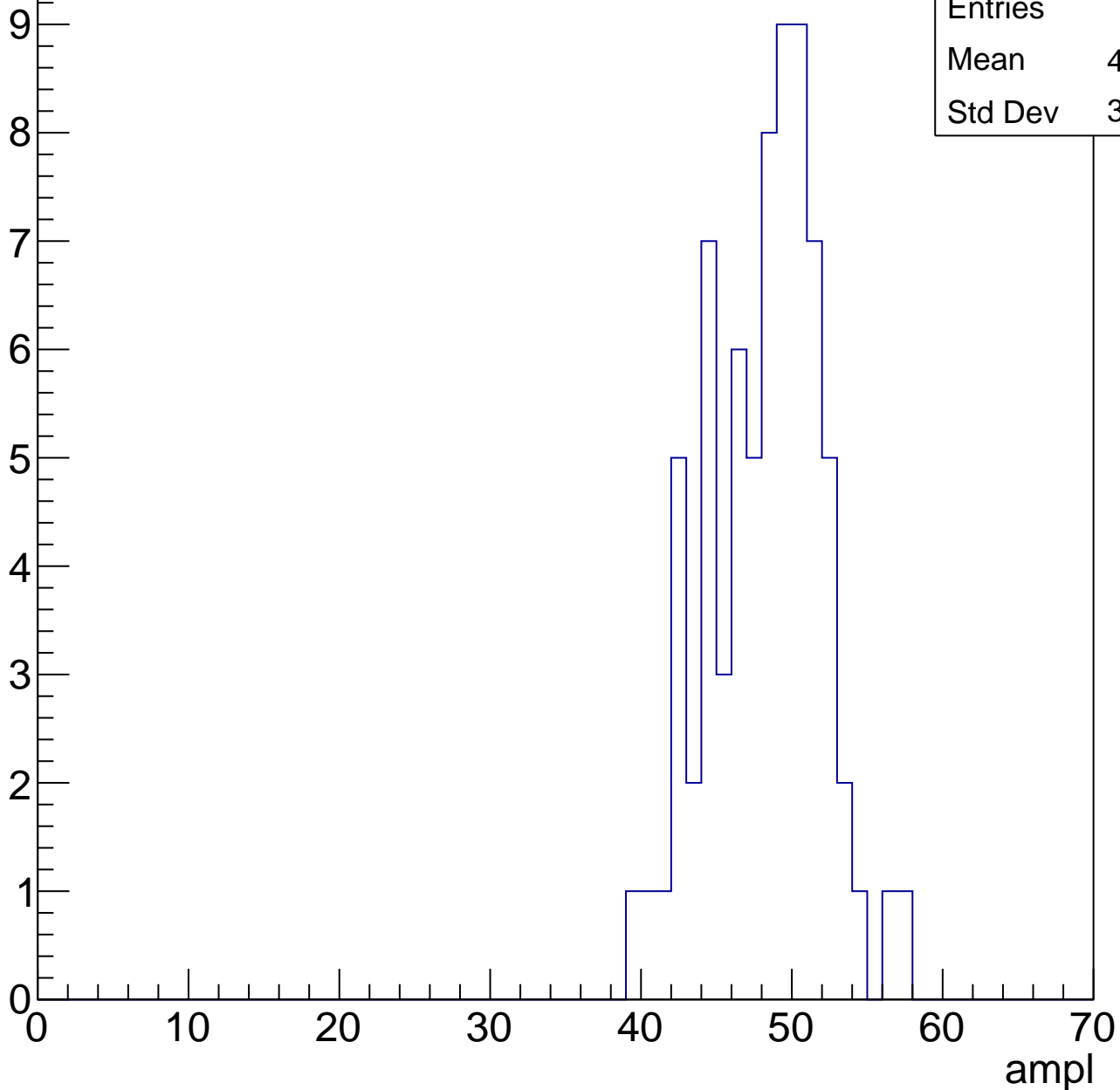


# B0L000S, U25-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

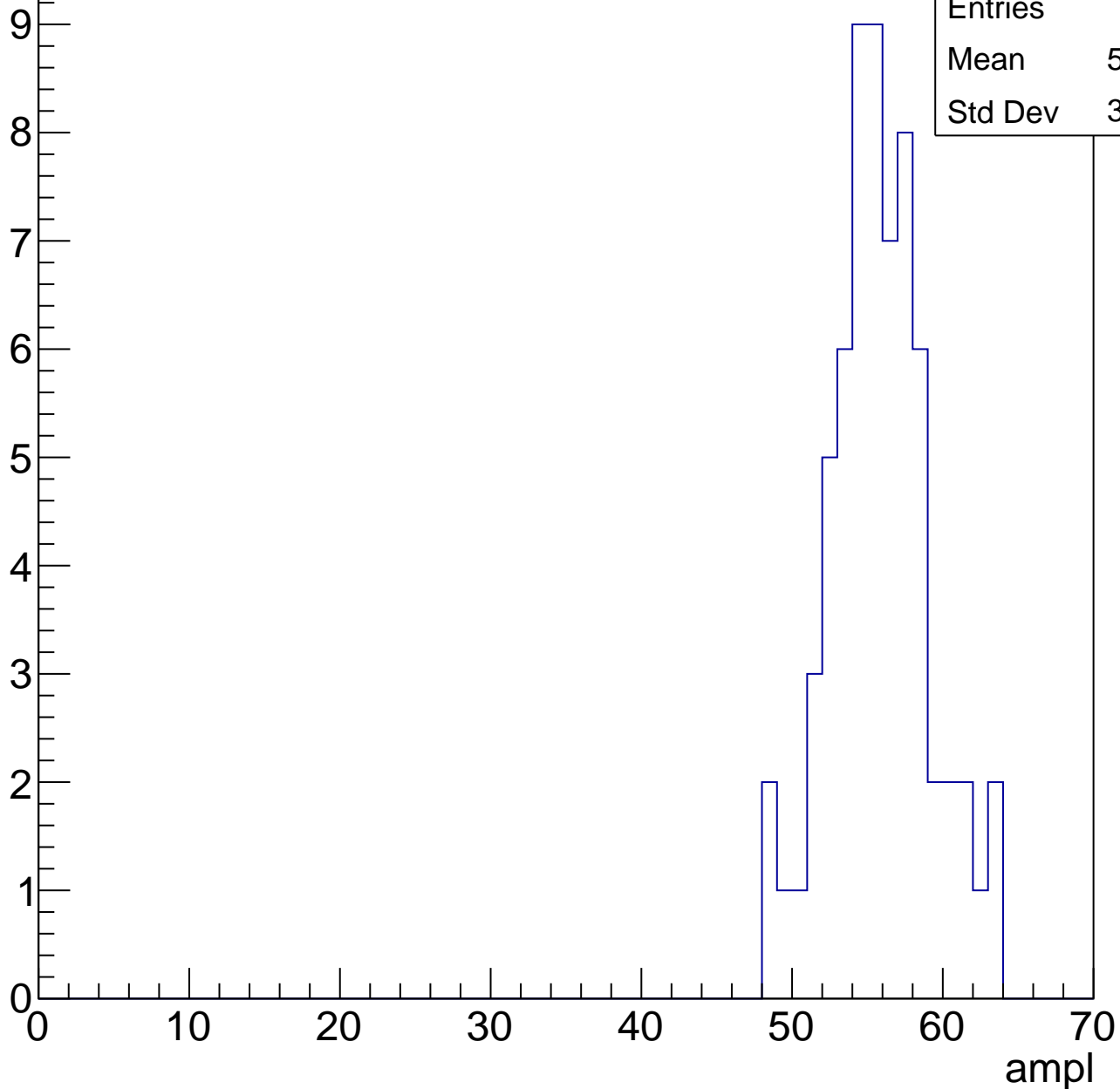
Entries	74
Mean	47.77
Std Dev	3.689



# B0L000S, U25-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch77, adc5

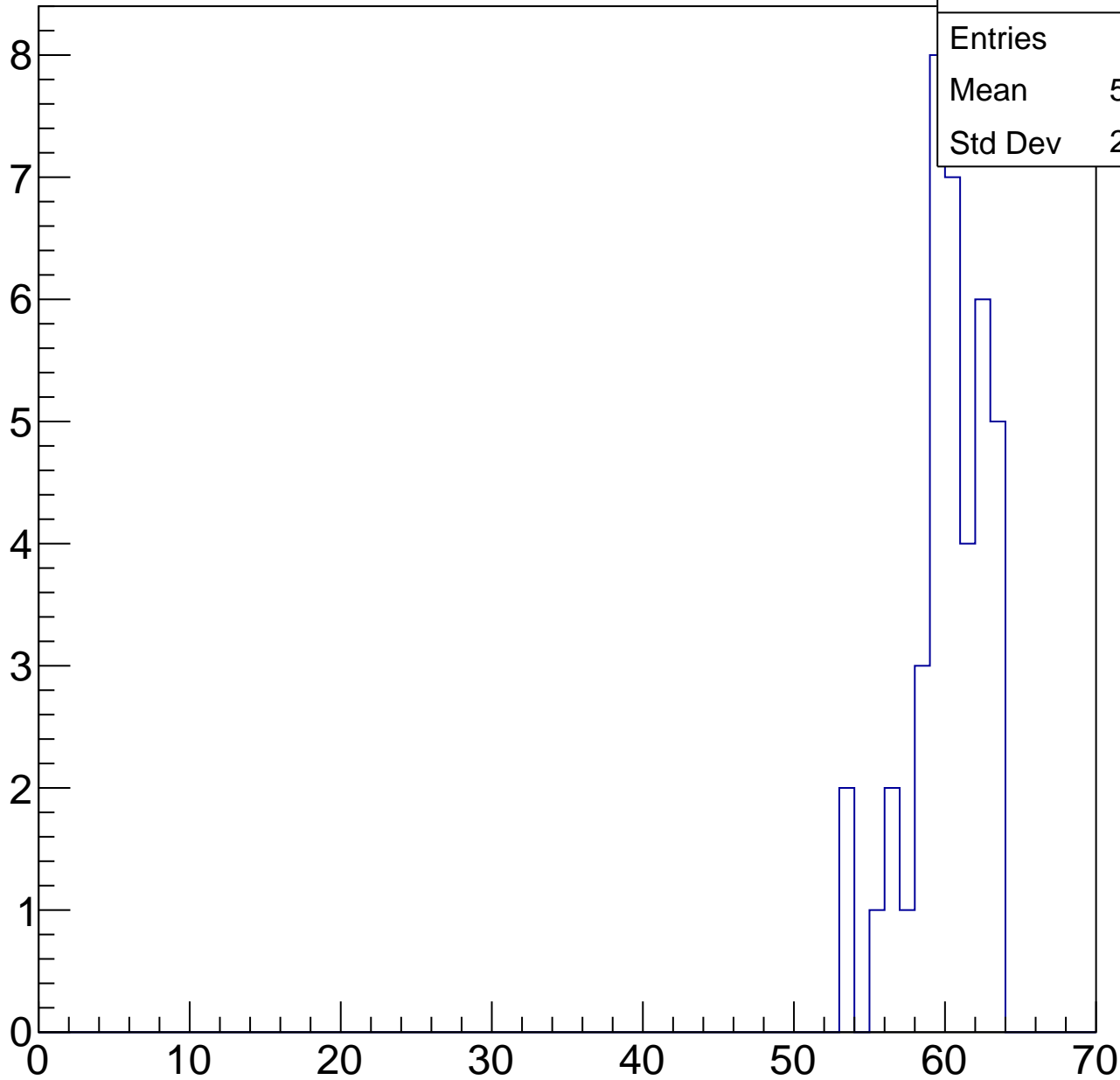
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	39
Mean	59.67
Std Dev	2.545

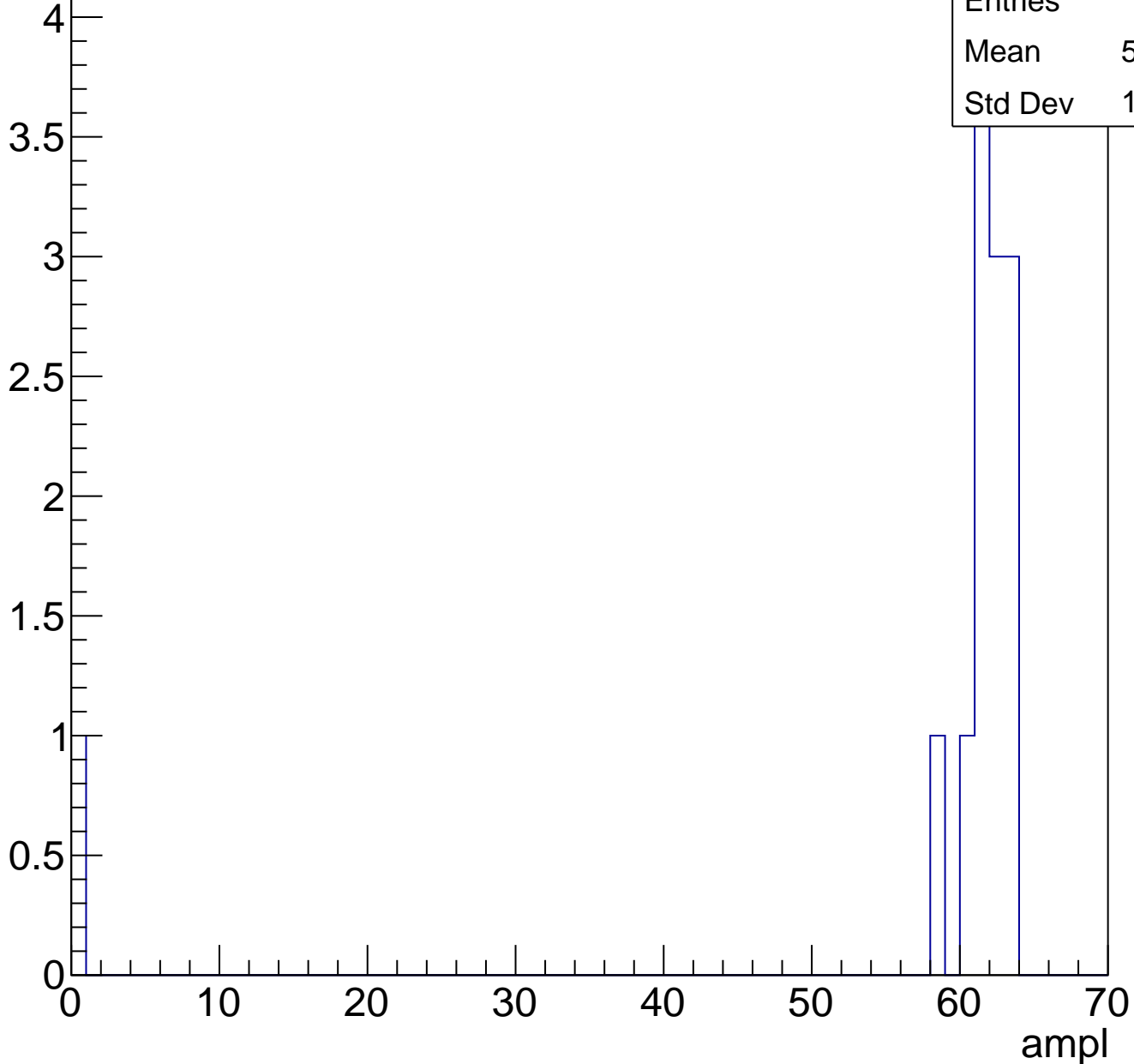
ampl



# B0L000S, U25-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch78, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	73
Mean	29.77
Std Dev	2.982

**Gaus mean : 30.0984**

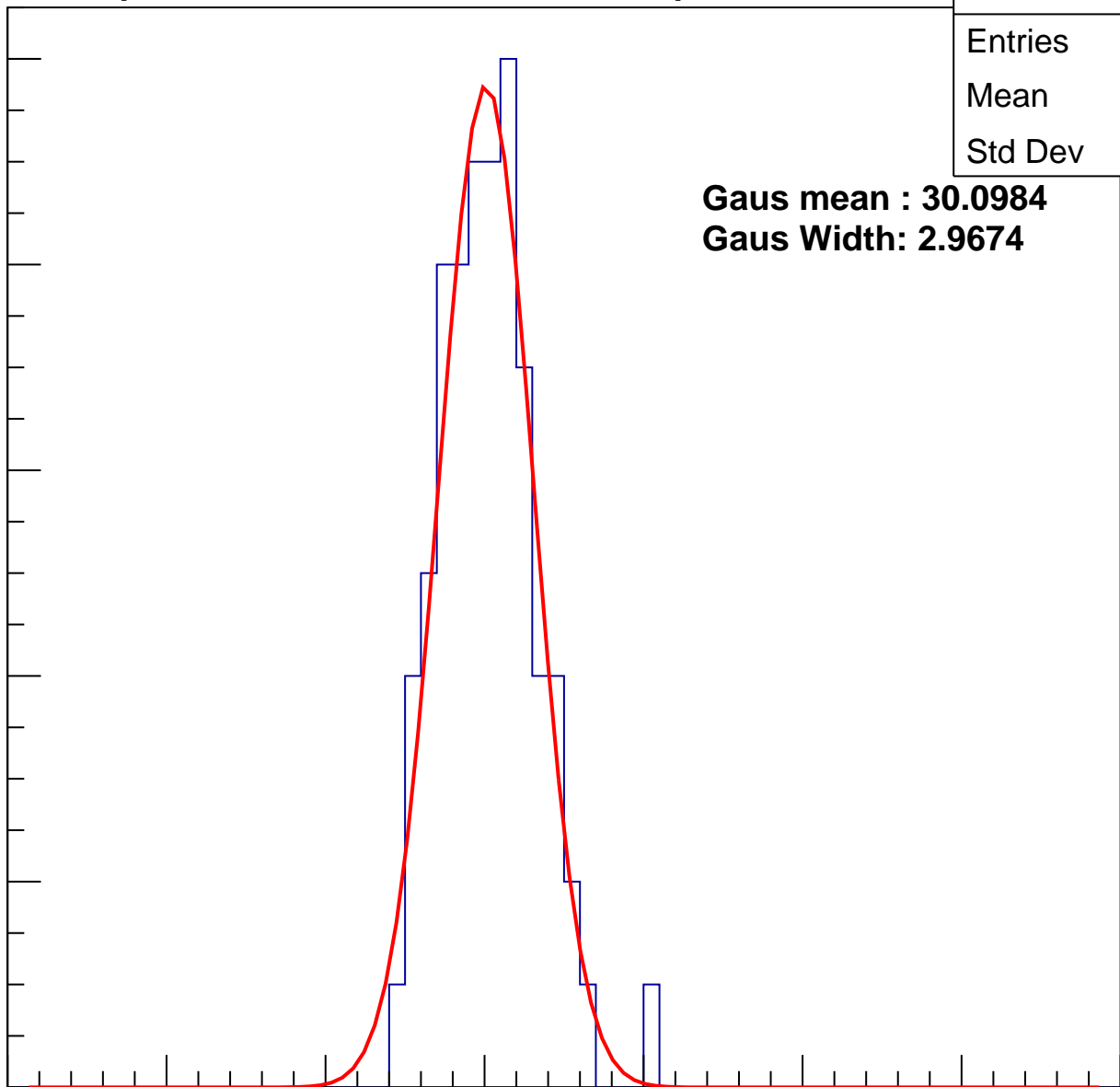
**Gaus Width: 2.9674**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch78, adc1

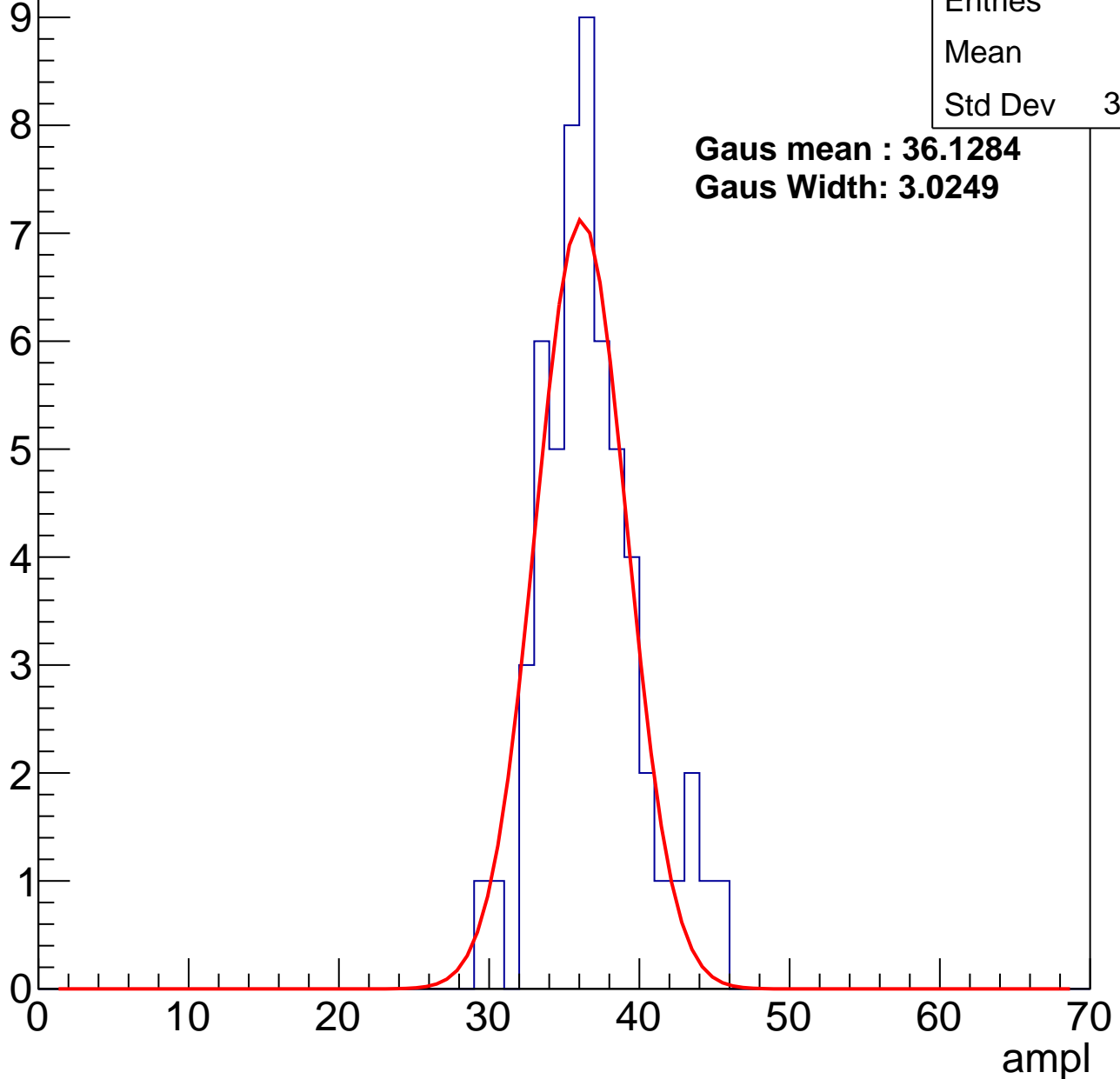
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.3
Std Dev	3.295

**Gaus mean : 36.1284**

**Gaus Width: 3.0249**



# B0L000S, U25-ch78, adc2

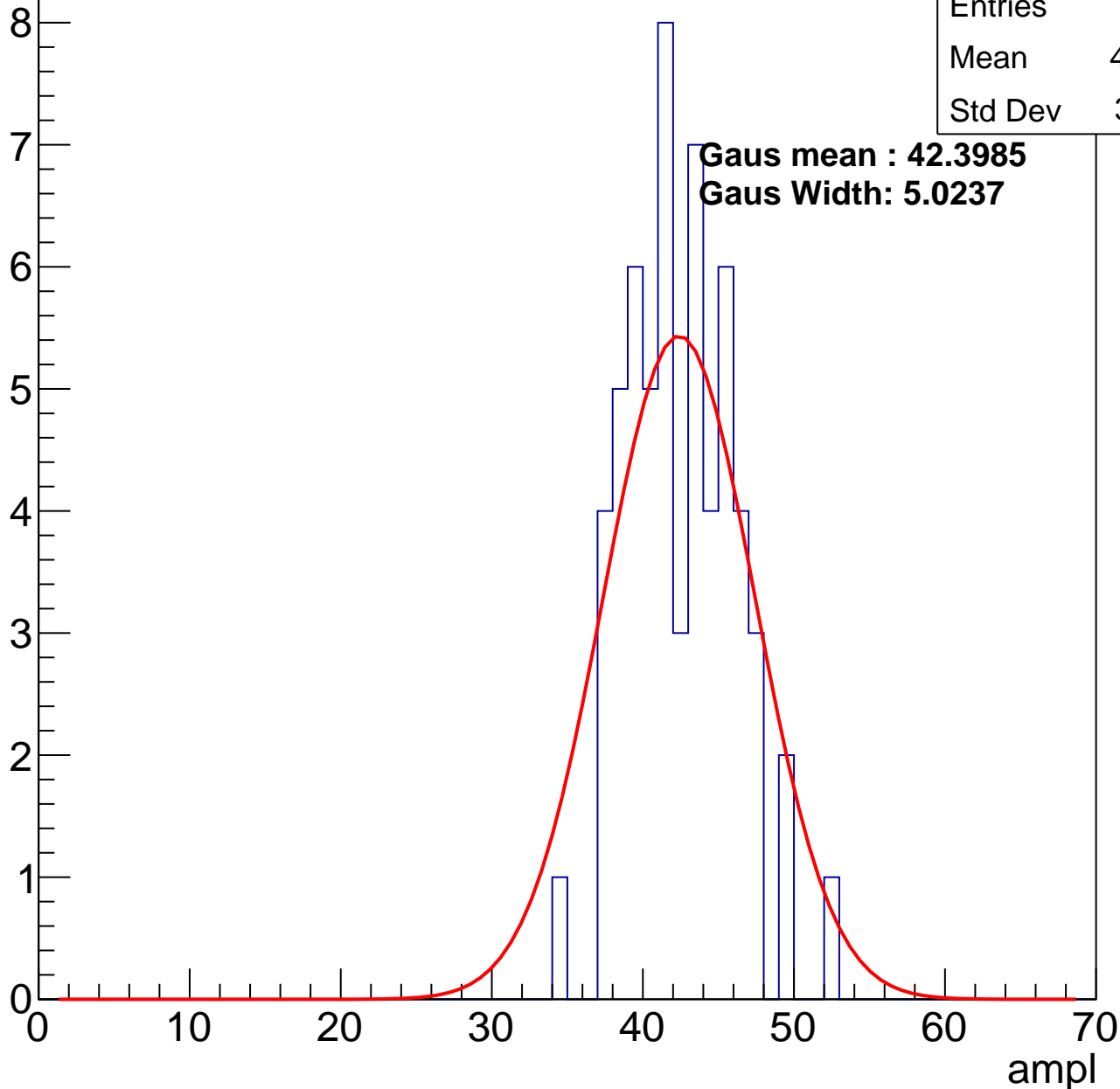
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.07
Std Dev	3.541

**Gaus mean : 42.3985**

**Gaus Width: 5.0237**

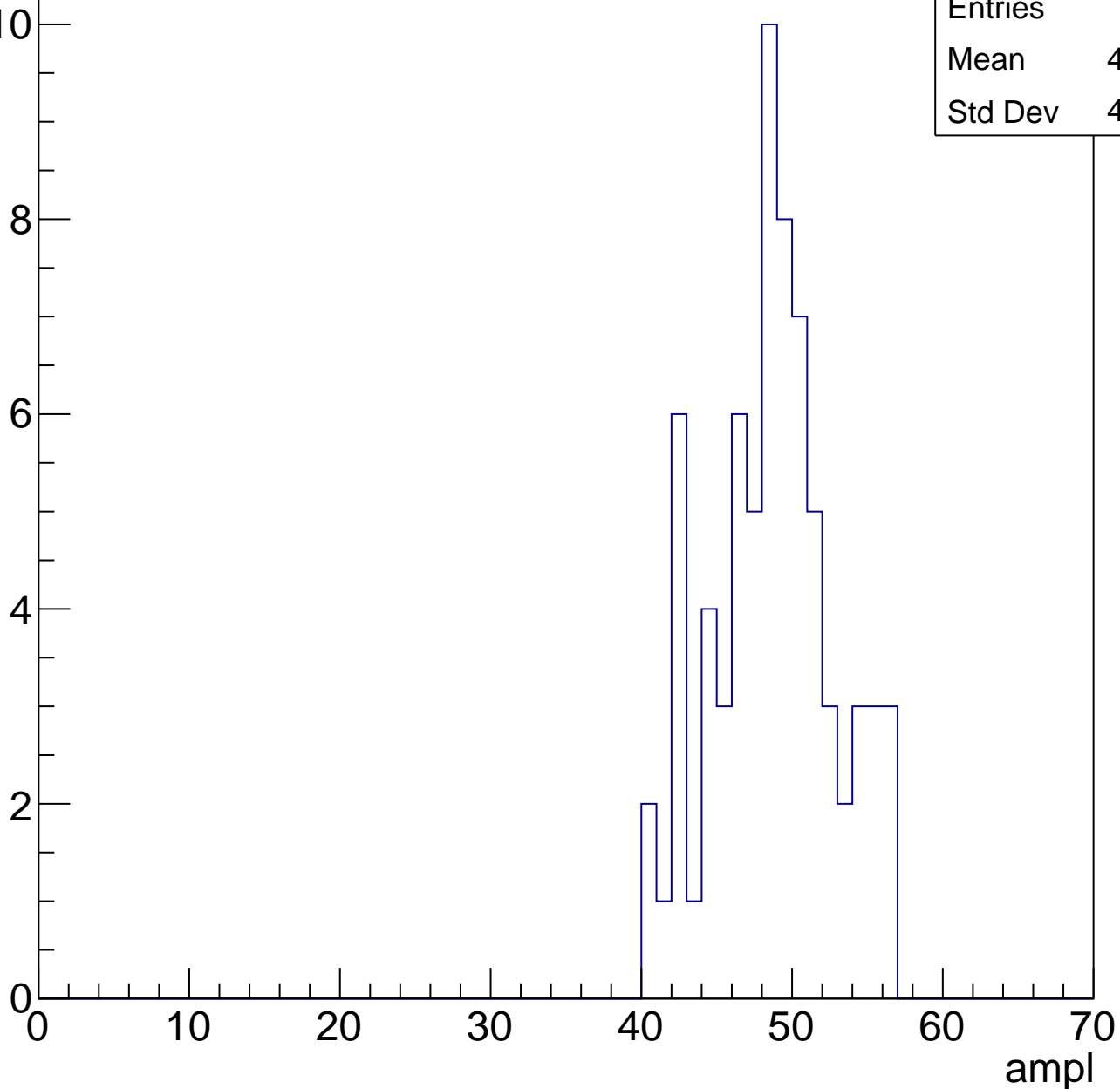


# B0L000S, U25-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

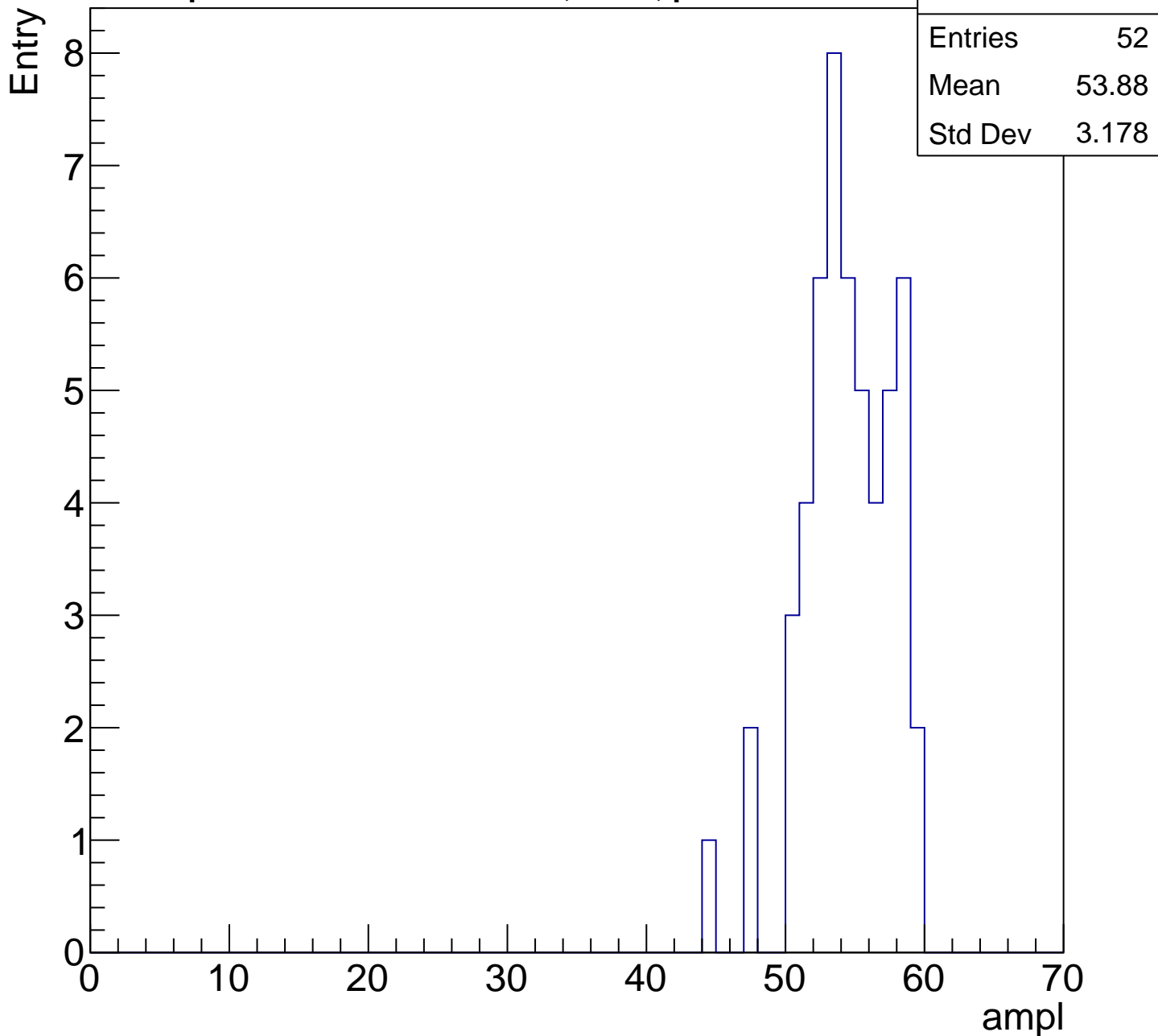
Entry

Entries	72
Mean	48.22
Std Dev	4.015



# B0L000S, U25-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

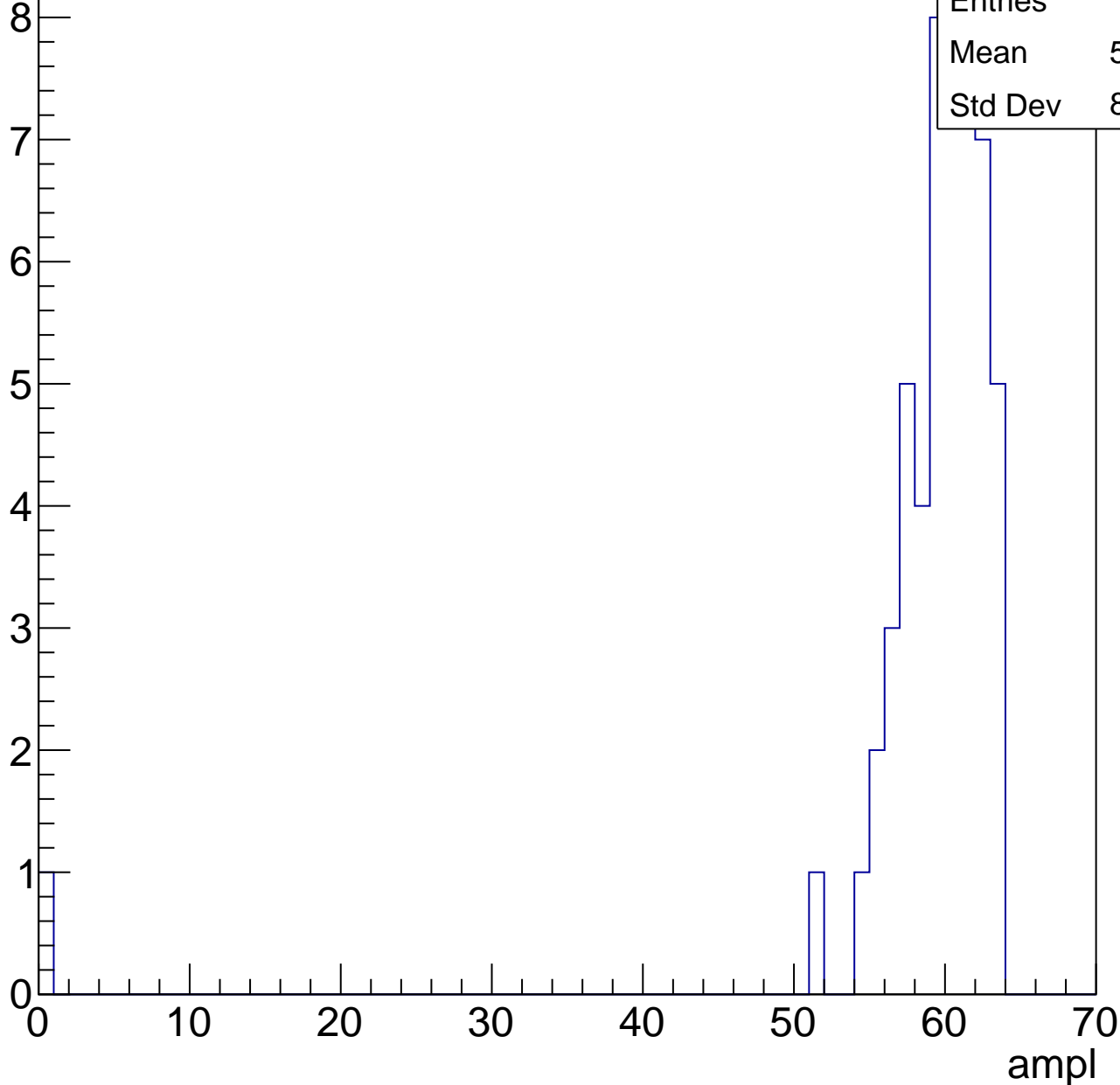


# B0L000S, U25-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.28
Std Dev	8.477

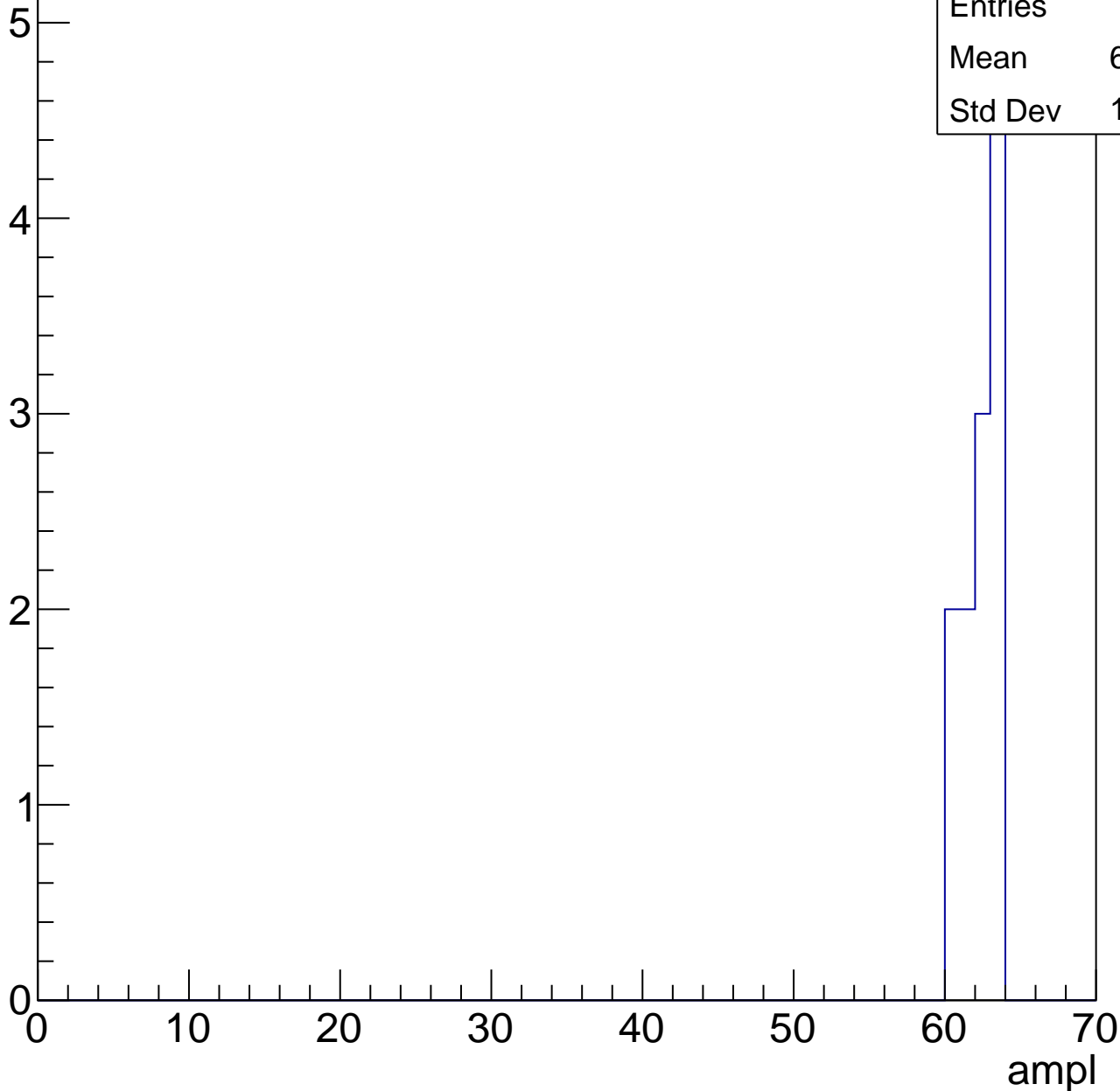


# B0L000S, U25-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	12
Mean	61.92
Std Dev	1.115





# B0L000S, U25-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch79, adc0

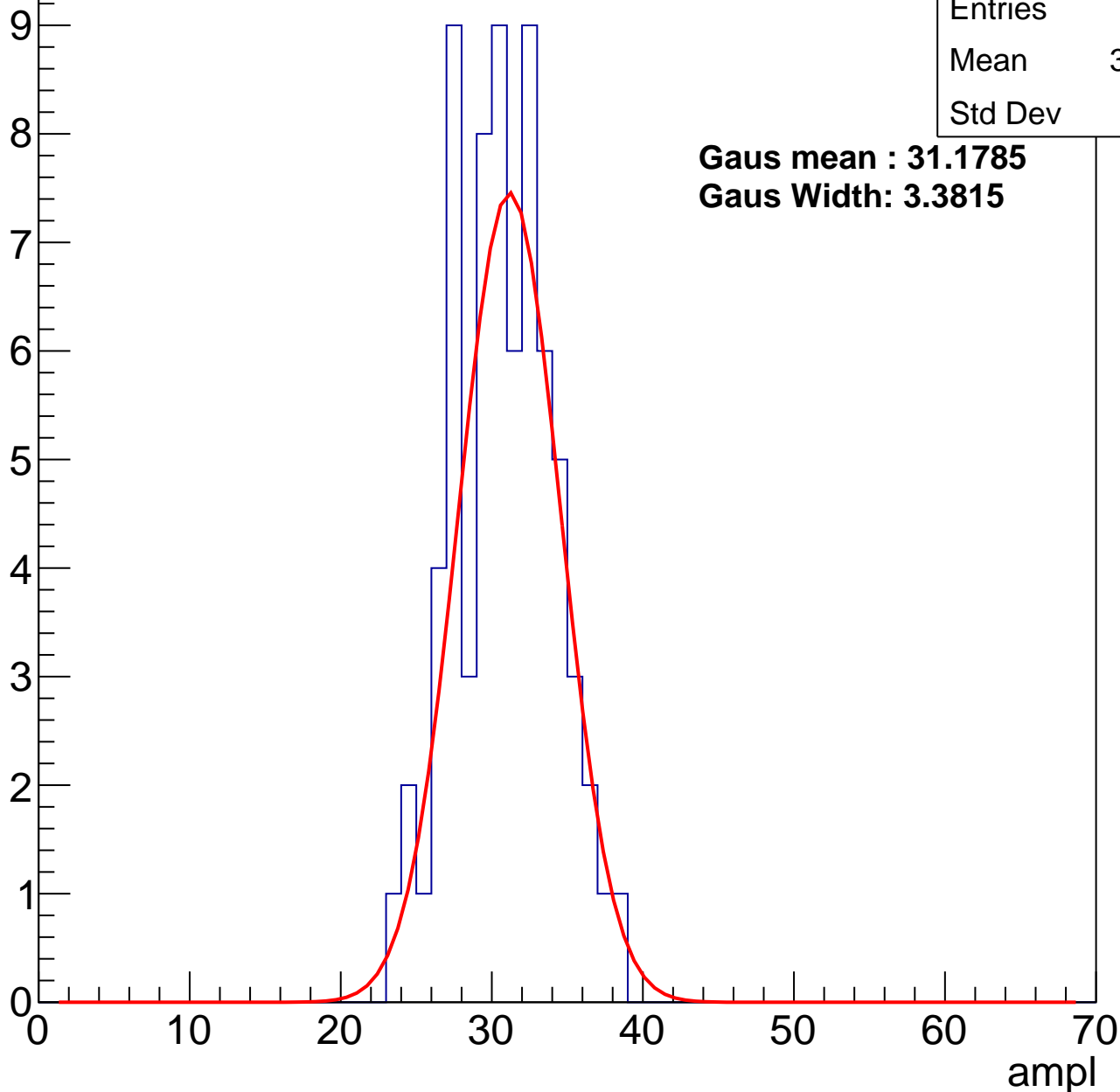
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	30.33
Std Dev	3.25

**Gaus mean : 31.1785**

**Gaus Width: 3.3815**



# B0L000S, U25-ch79, adc1

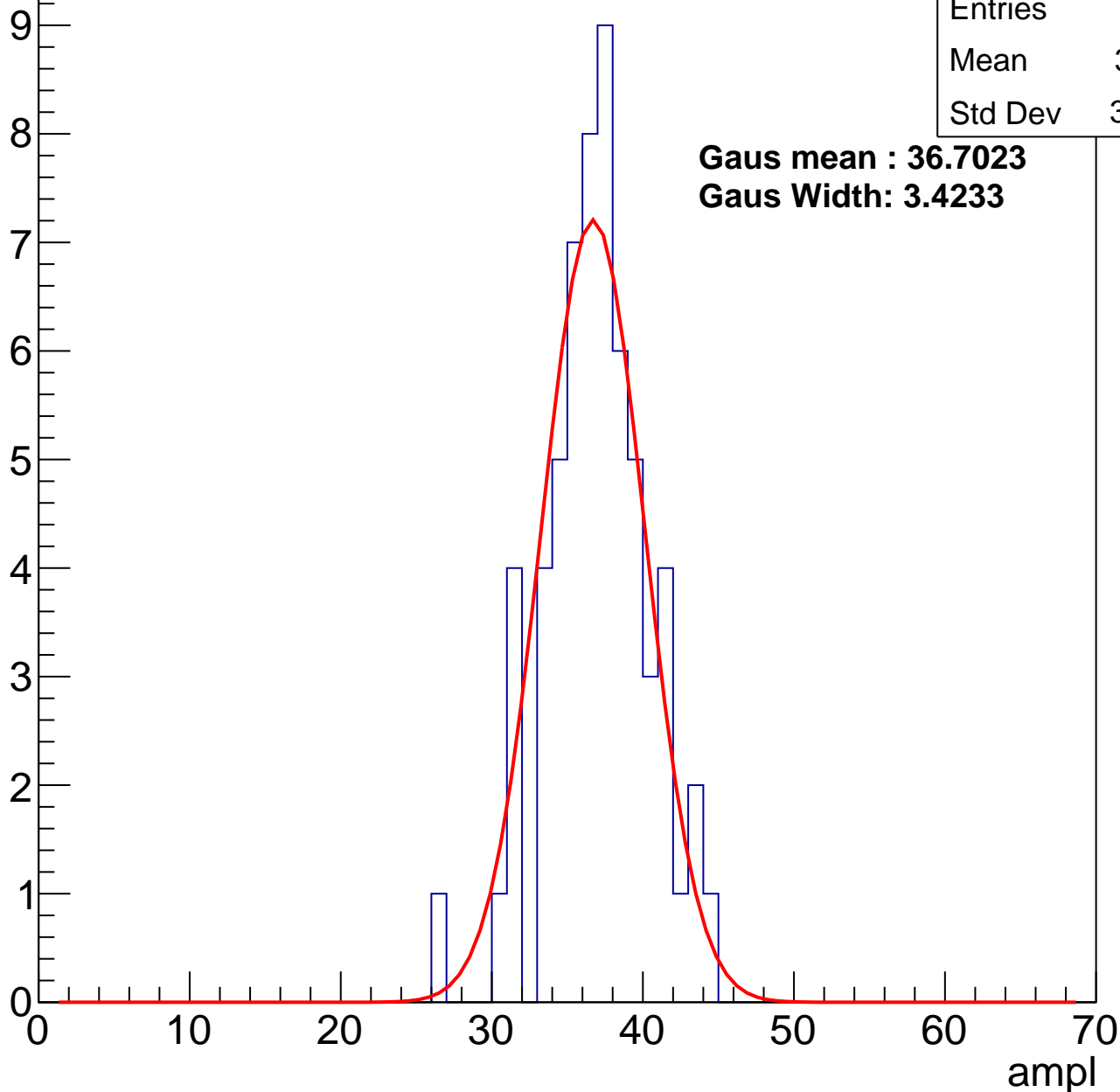
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	36.51
Std Dev	3.405

**Gaus mean : 36.7023**

**Gaus Width: 3.4233**



# B0L000S, U25-ch79, adc2

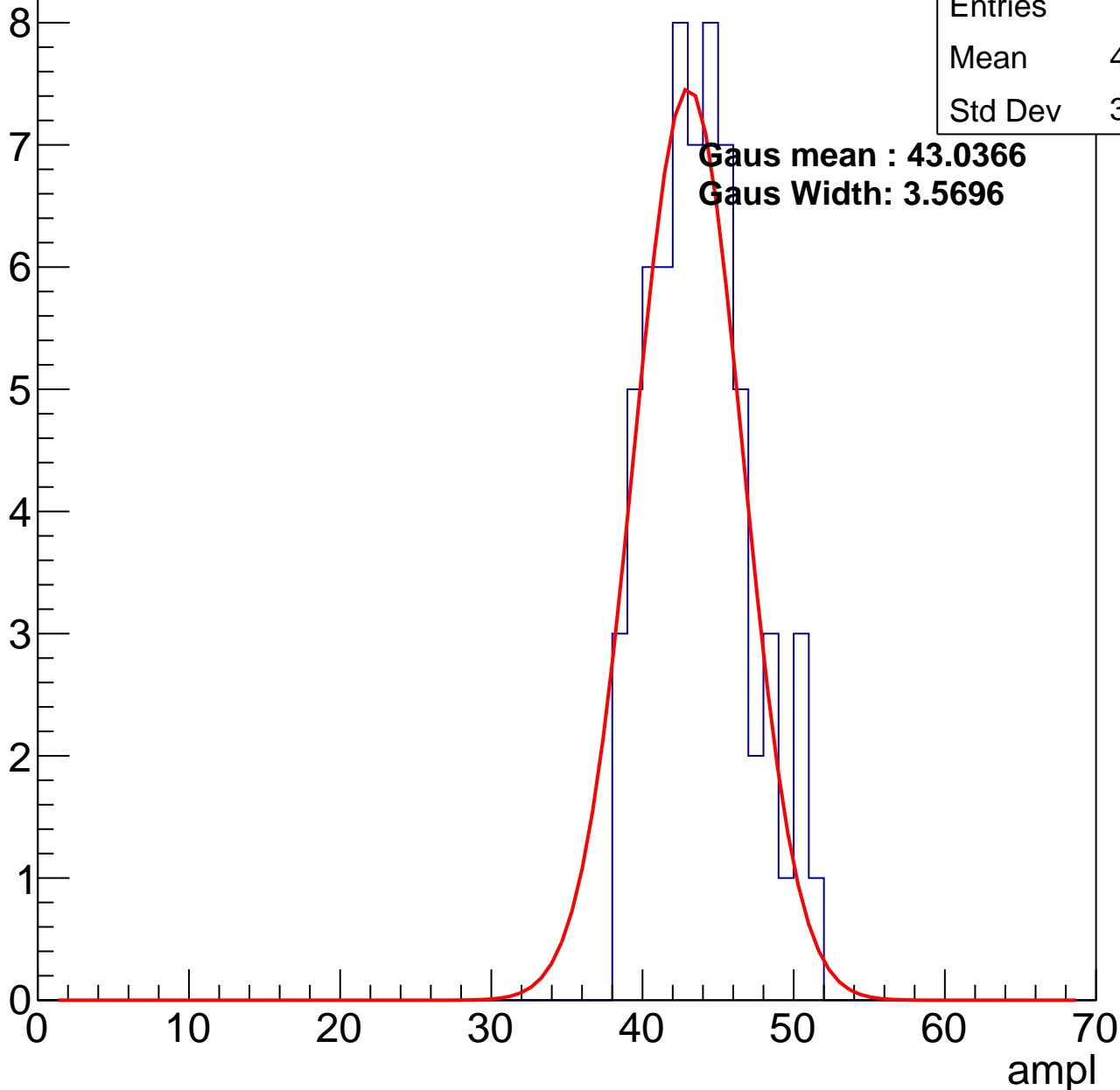
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.34
Std Dev	3.207

**Gaus mean : 43.0366**

**Gaus Width: 3.5696**

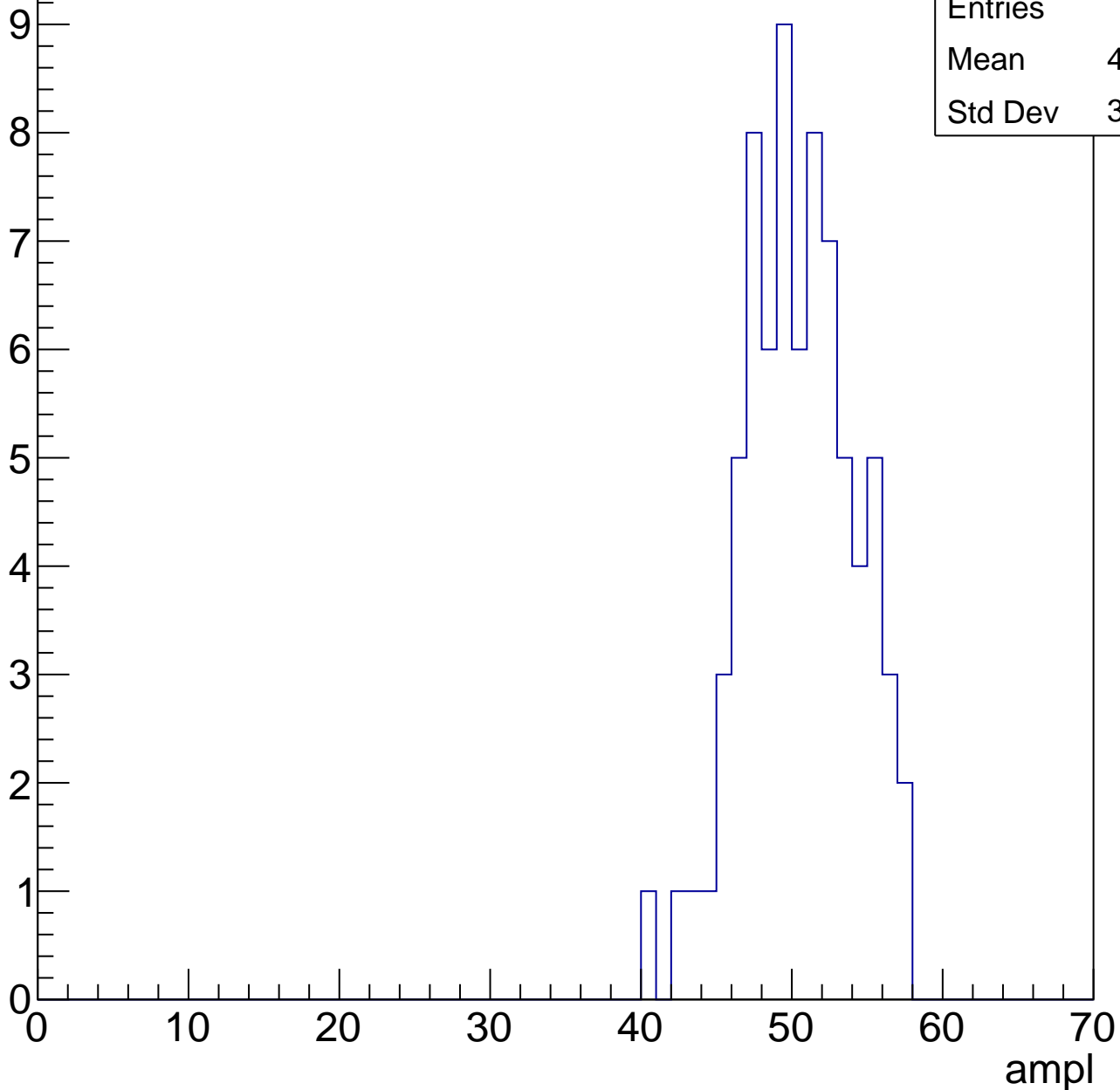


# B0L000S, U25-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	49.99
Std Dev	3.624

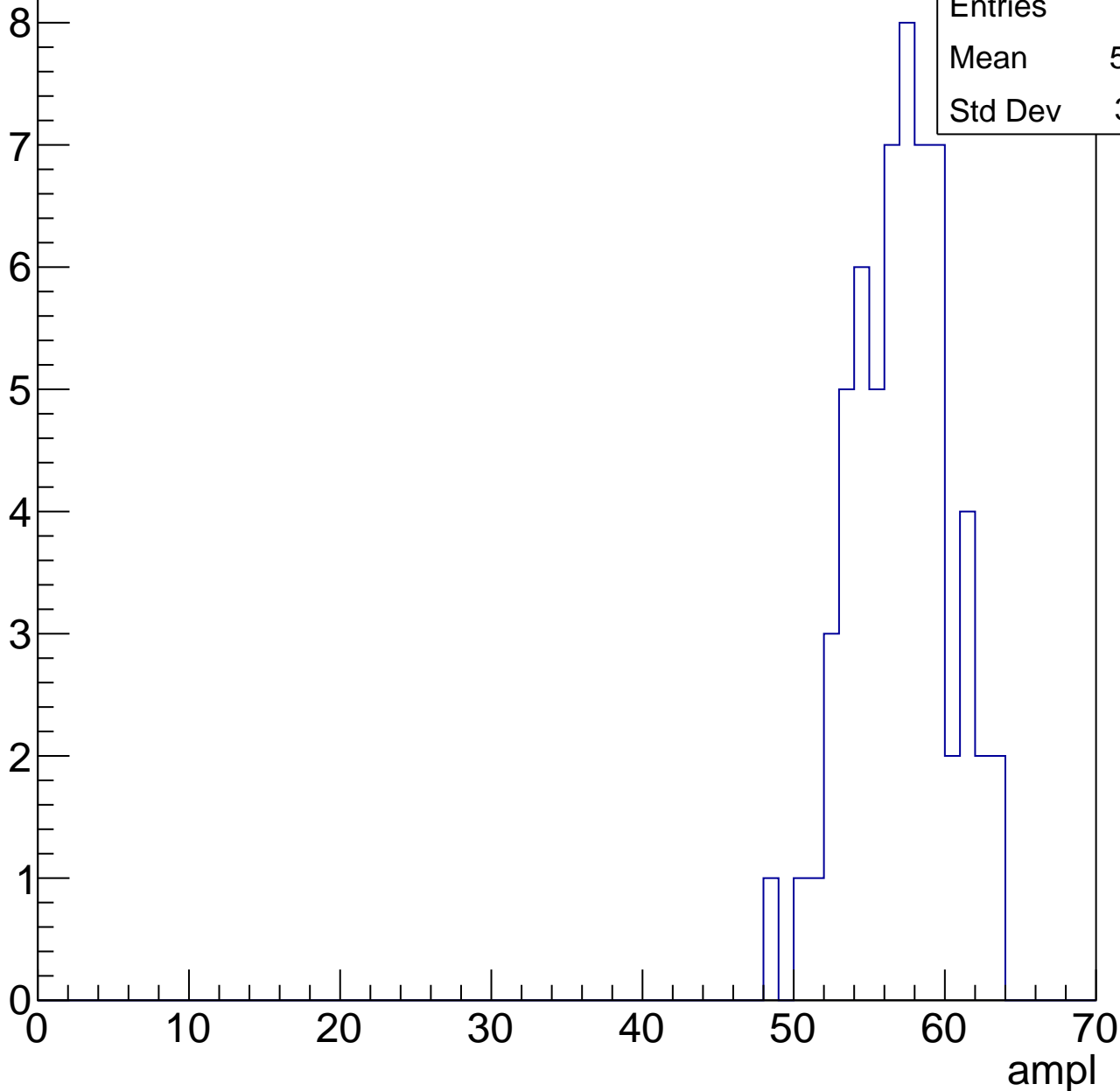


# B0L000S, U25-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

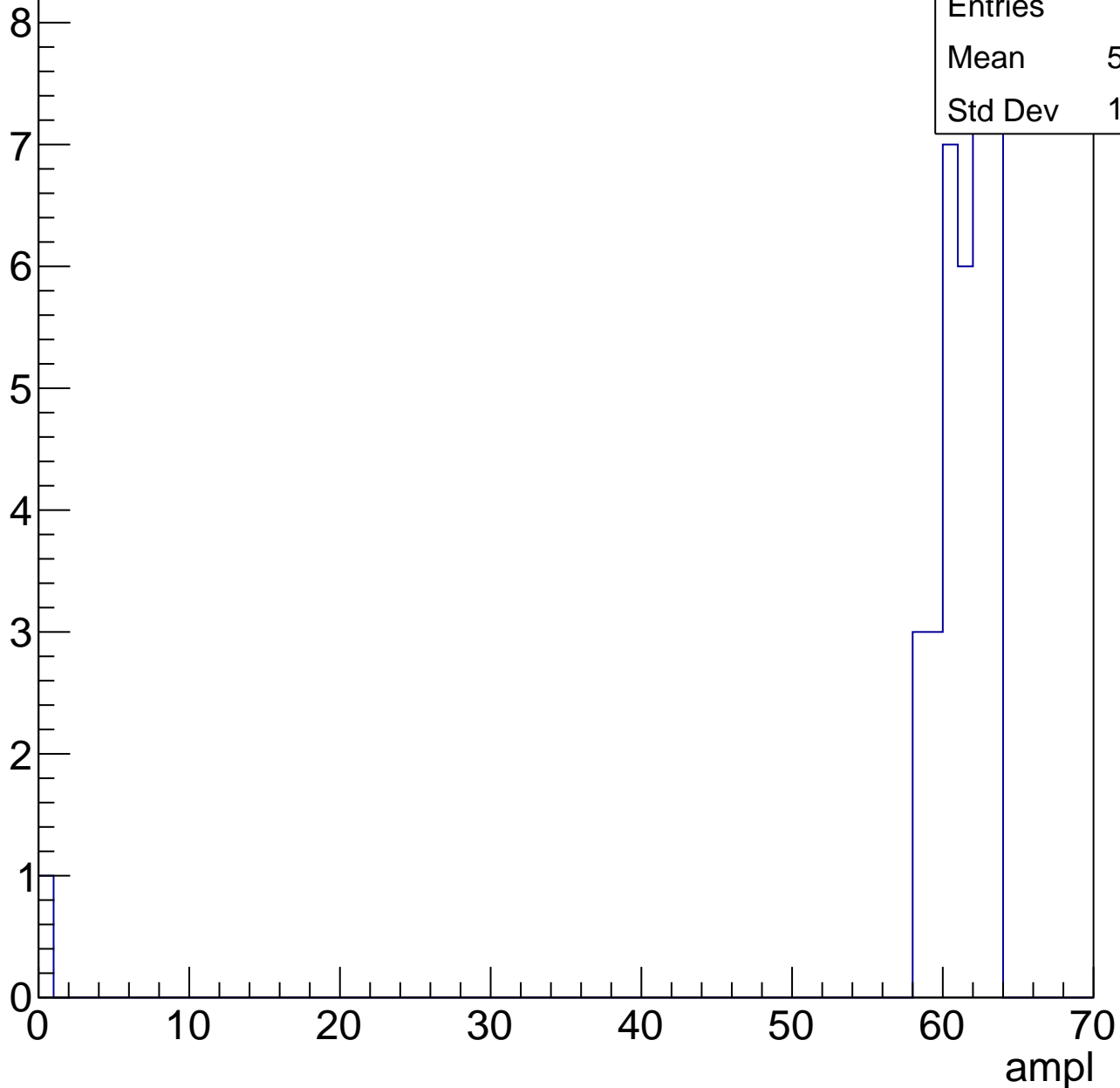
Entries	61
Mean	56.56
Std Dev	3.211



# B0L000S, U25-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

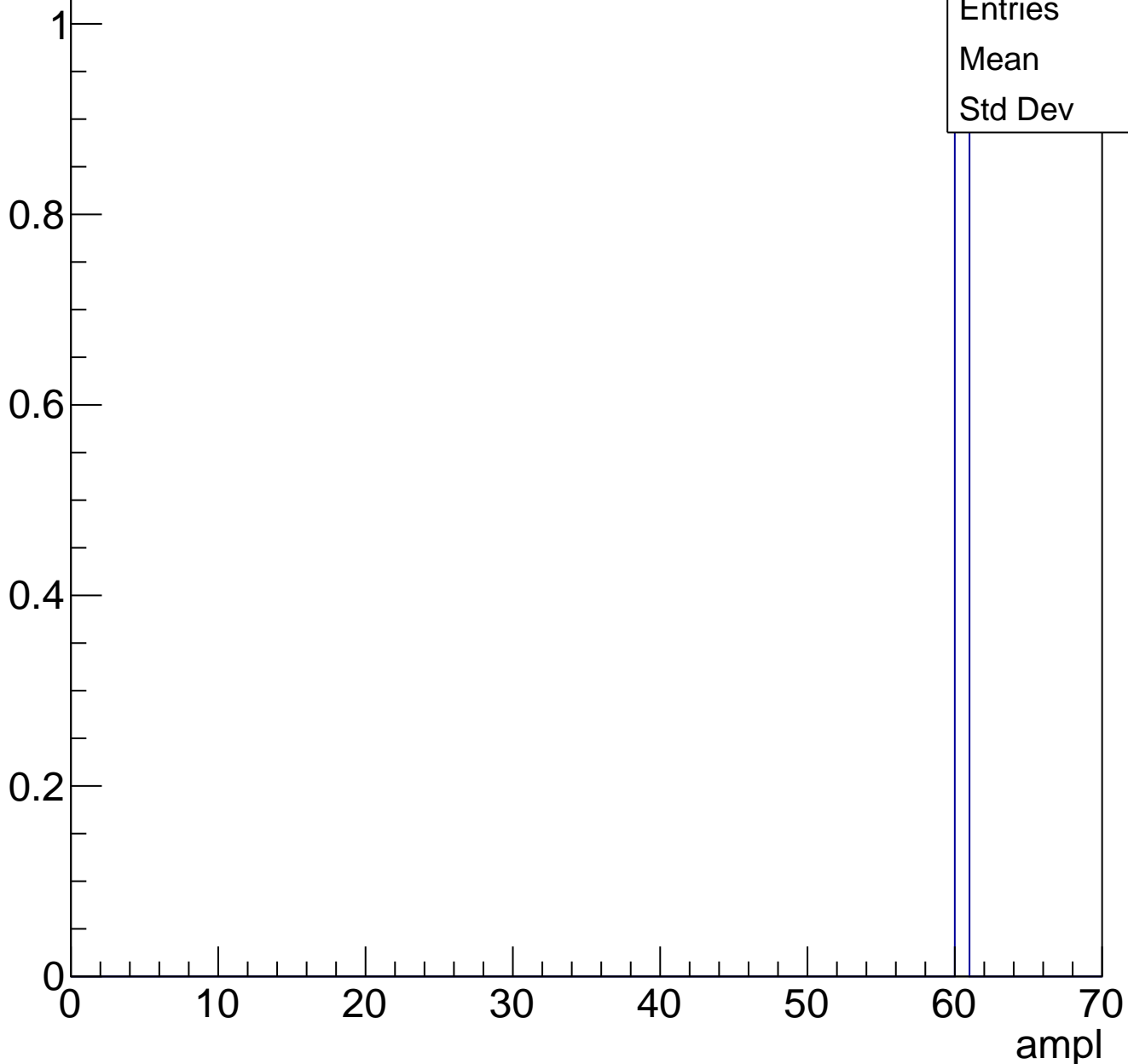
Entry



# B0L000S, U25-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch80, adc0

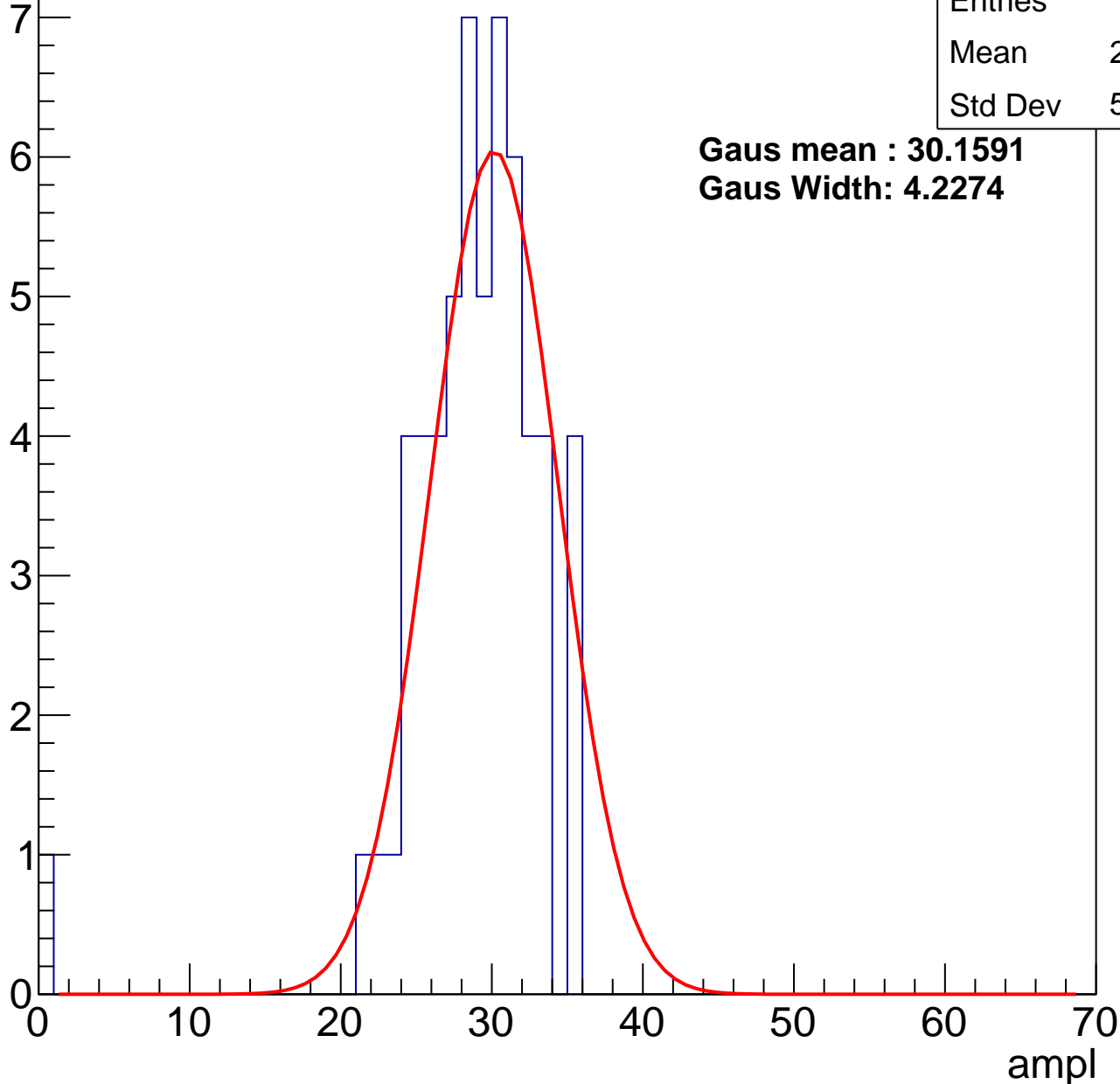
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	28.24
Std Dev	5.015

**Gaus mean : 30.1591**

**Gaus Width: 4.2274**



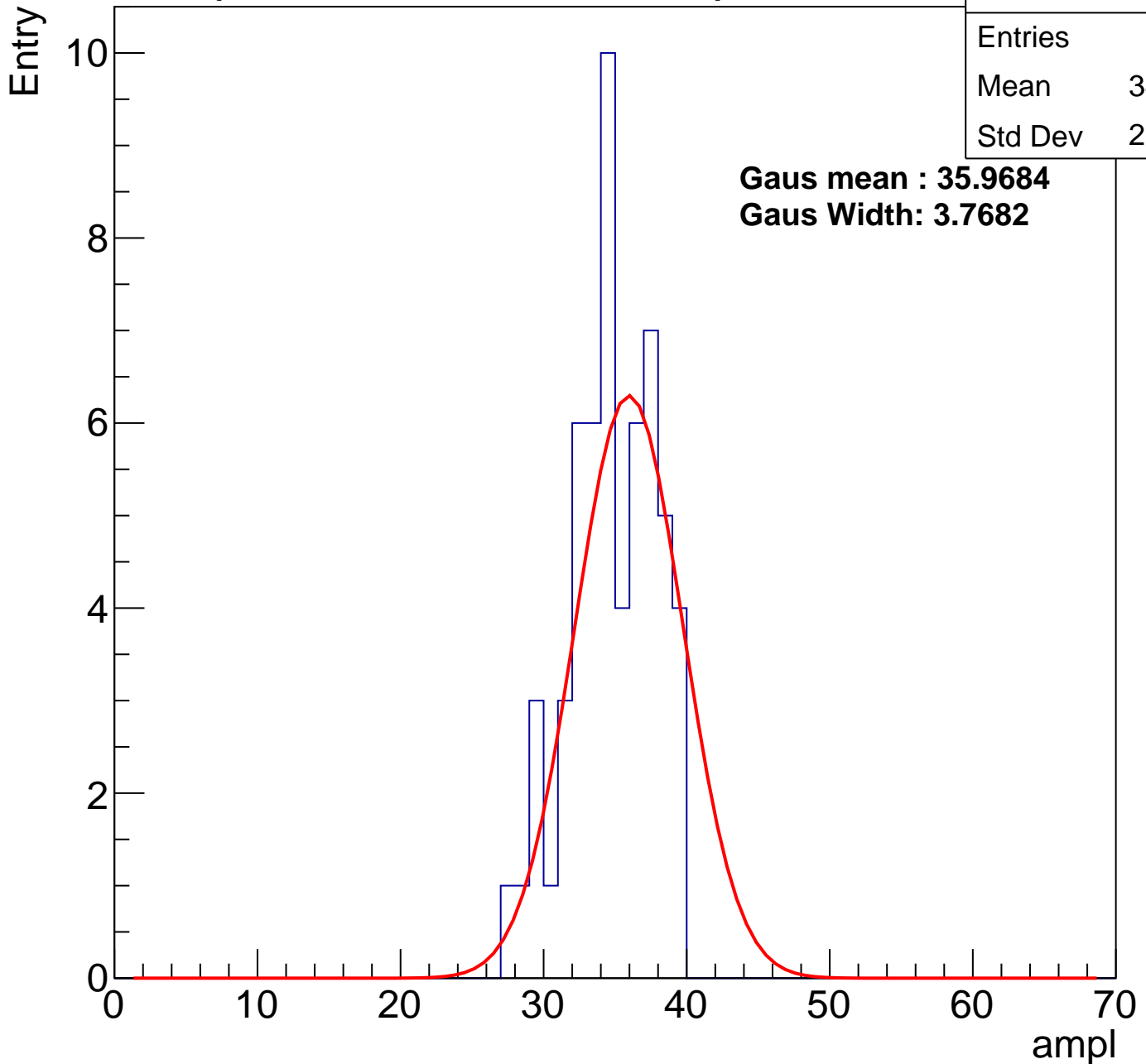
# B0L000S, U25-ch80, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	57
Mean	34.32
Std Dev	2.957

**Gaus mean : 35.9684**

**Gaus Width: 3.7682**



# B0L000S, U25-ch80, adc2

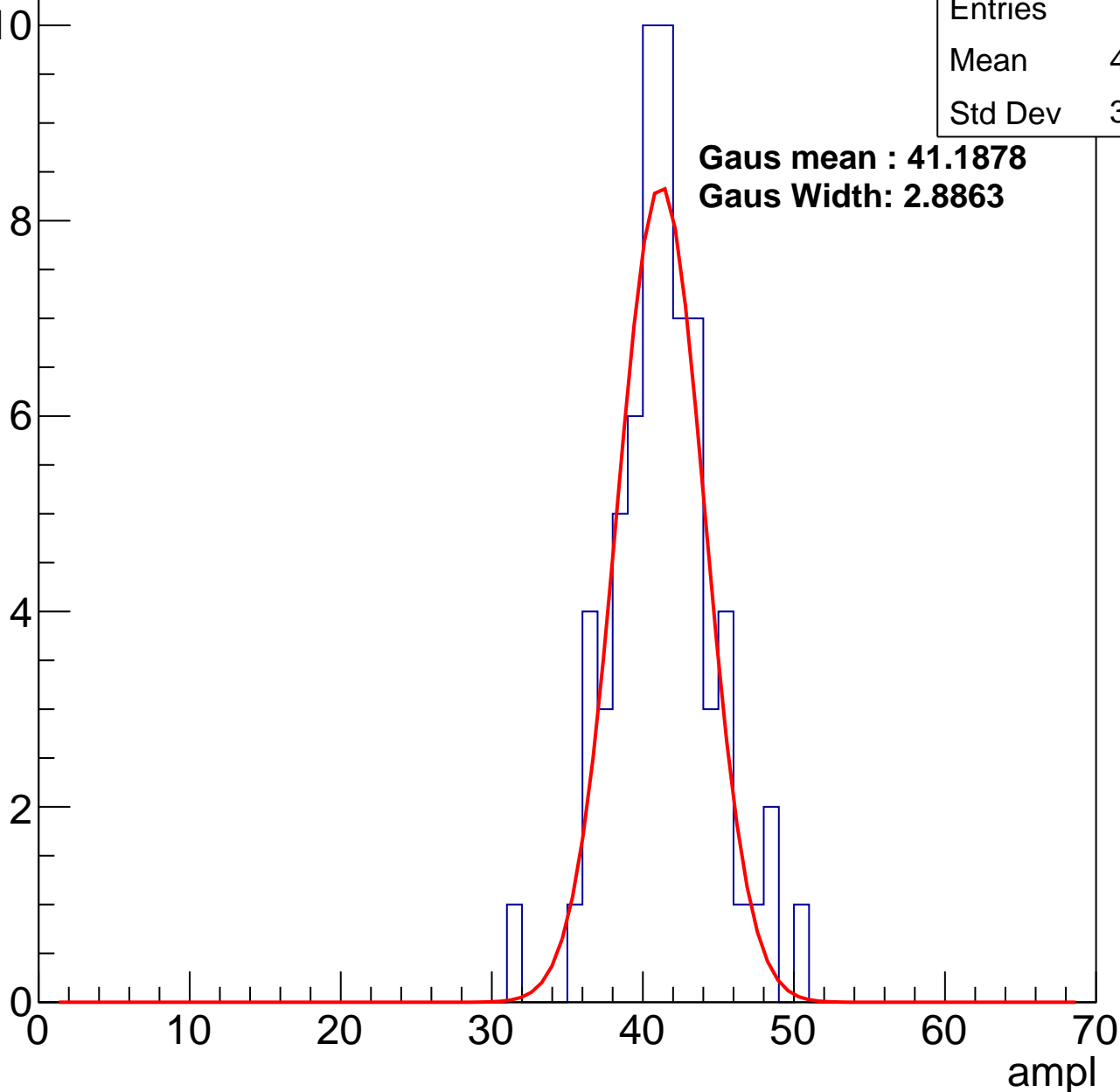
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	40.92
Std Dev	3.323

**Gaus mean : 41.1878**

**Gaus Width: 2.8863**

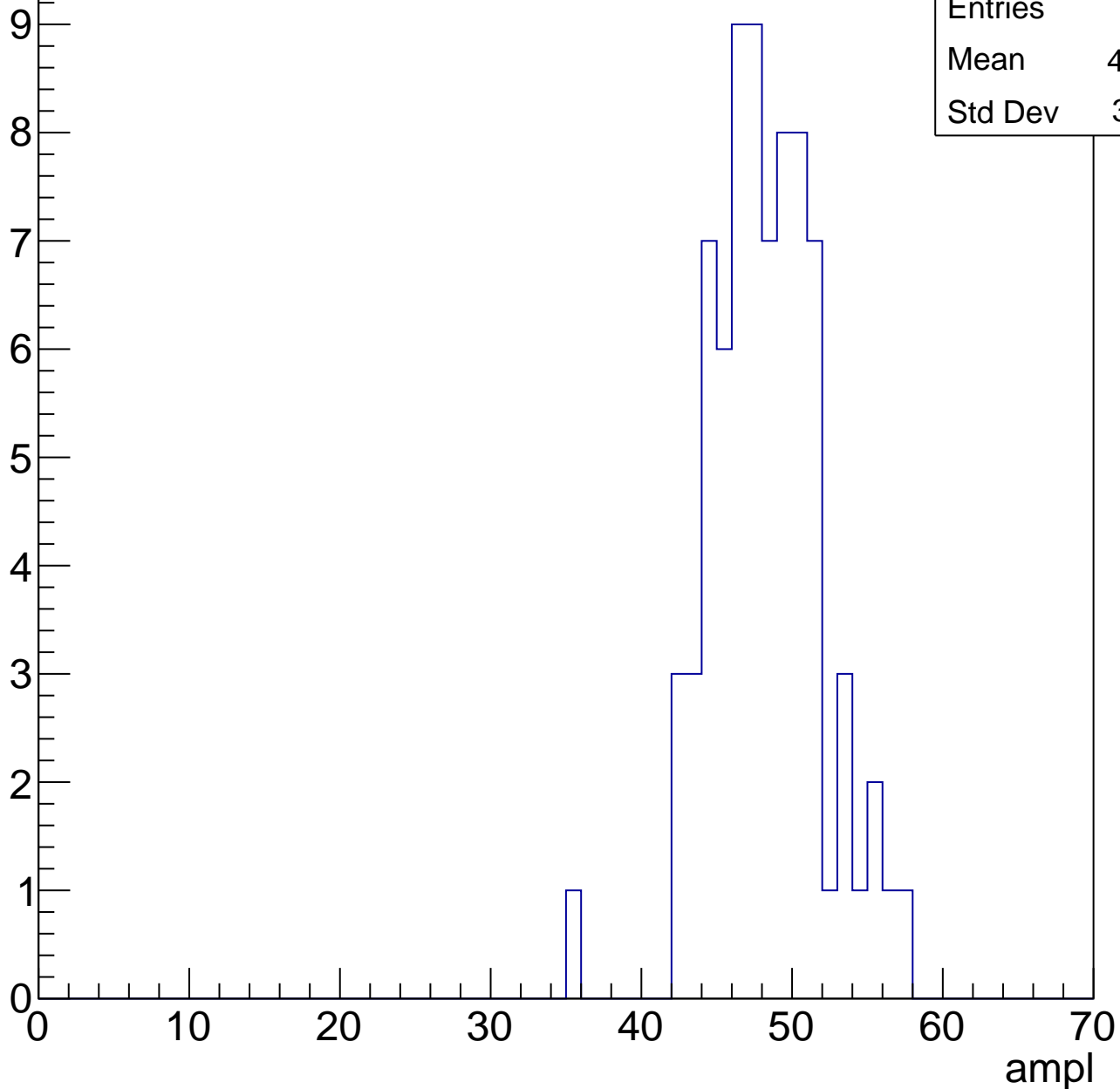


# B0L000S, U25-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	47.77
Std Dev	3.661

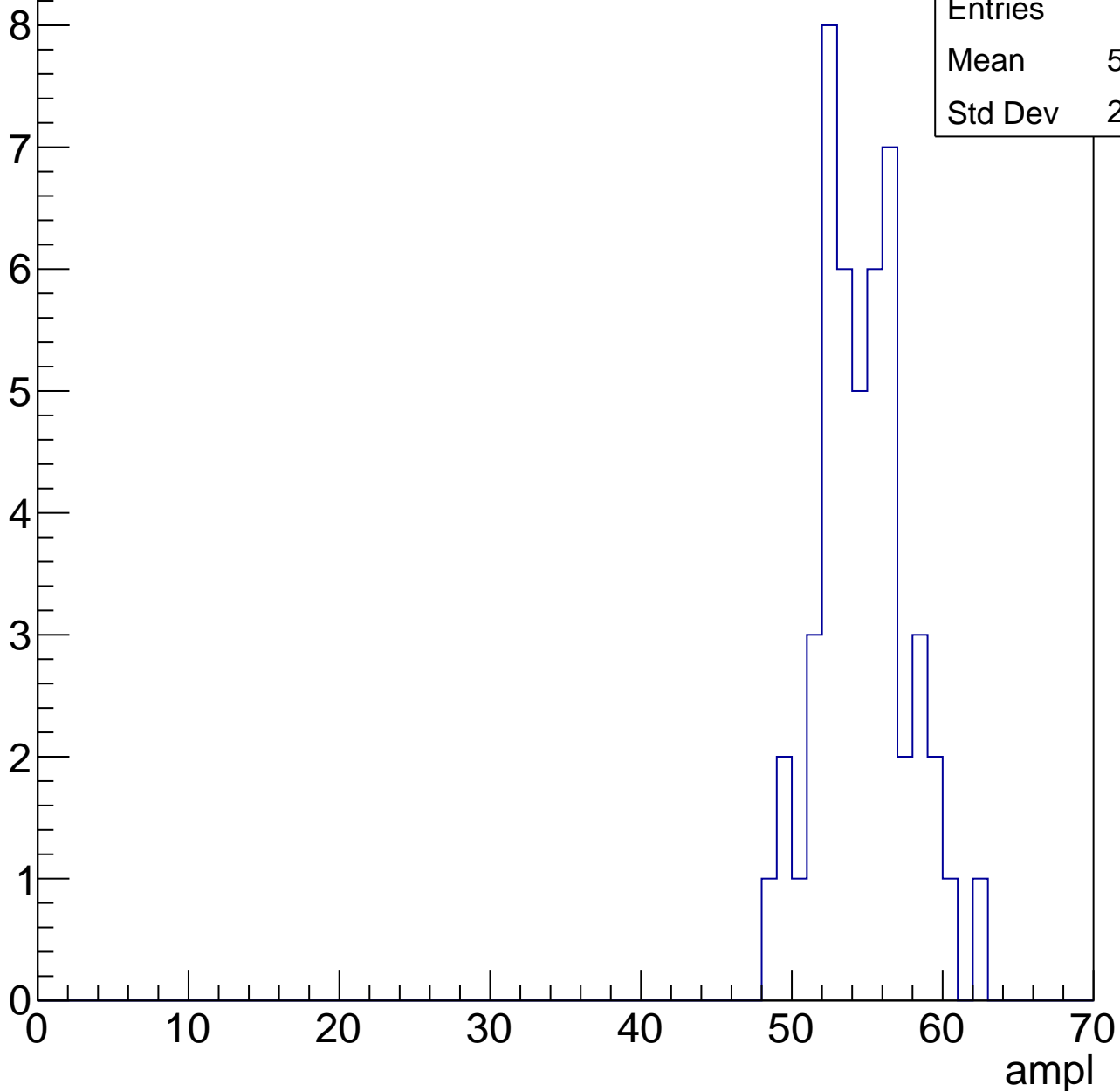


# B0L000S, U25-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	54.23
Std Dev	2.939

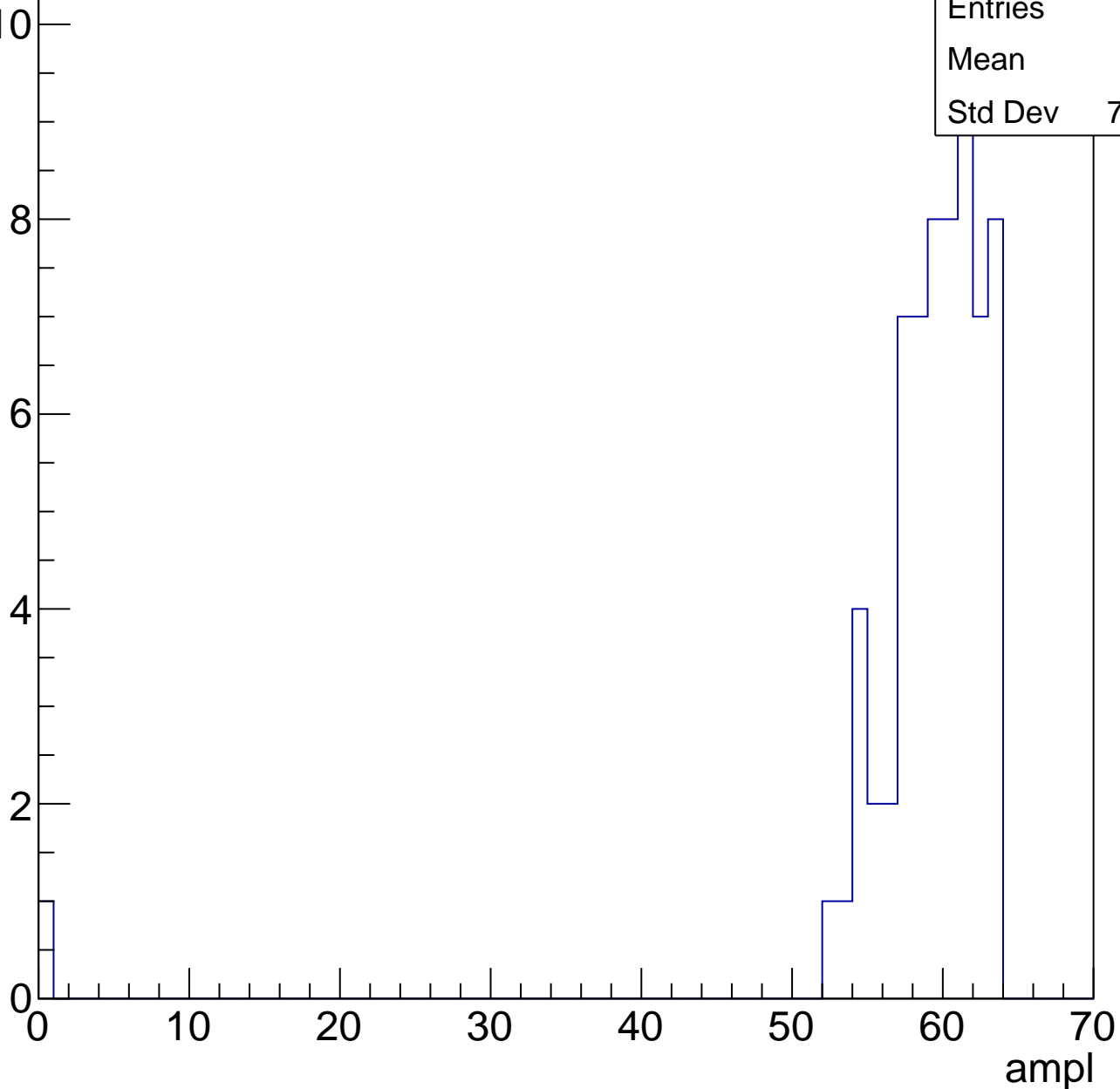


# B0L000S, U25-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	58.3
Std Dev	7.744



# B0L000S, U25-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch81, adc0

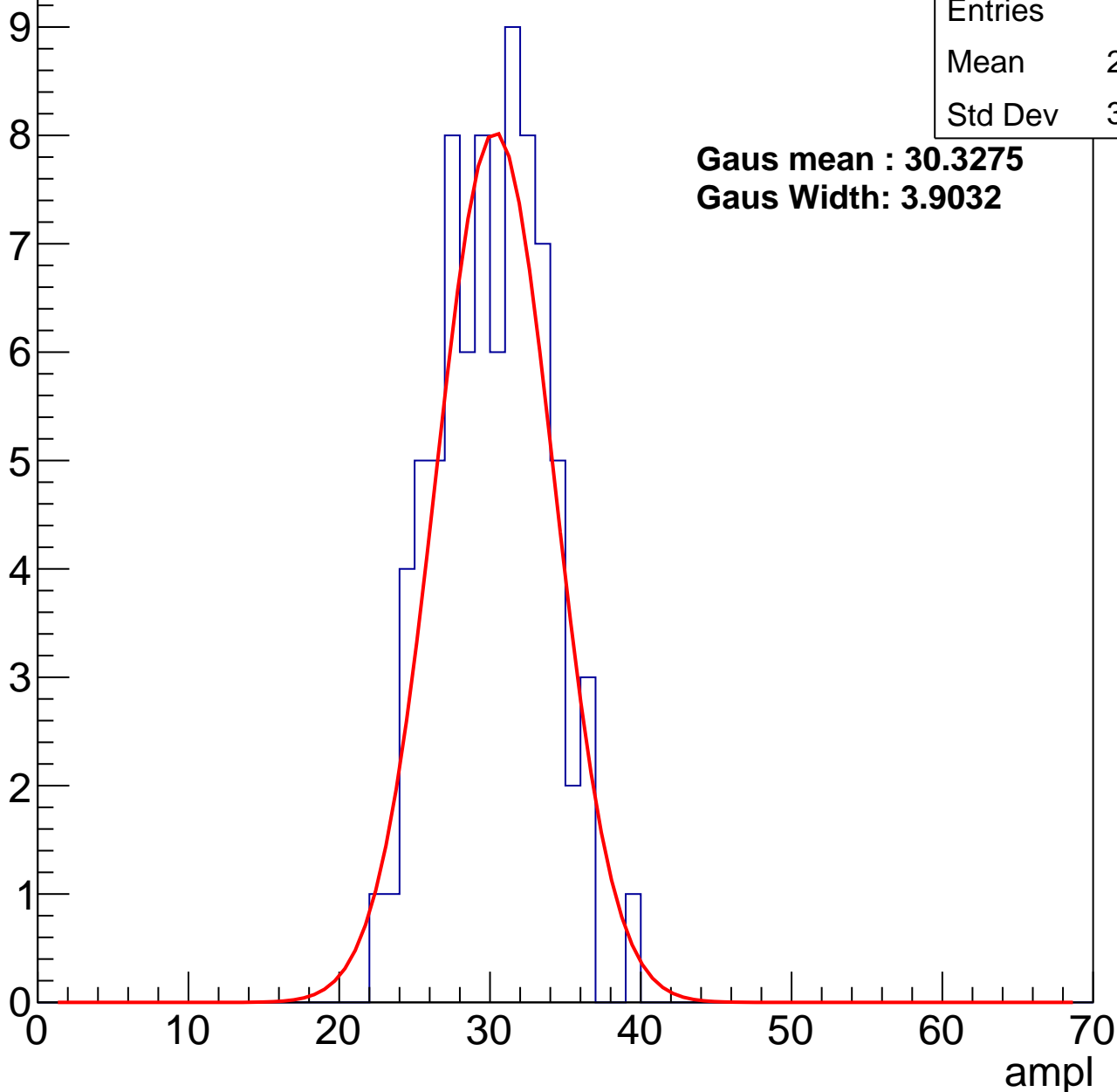
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	29.68
Std Dev	3.524

**Gaus mean : 30.3275**

**Gaus Width: 3.9032**



# B0L000S, U25-ch81, adc1

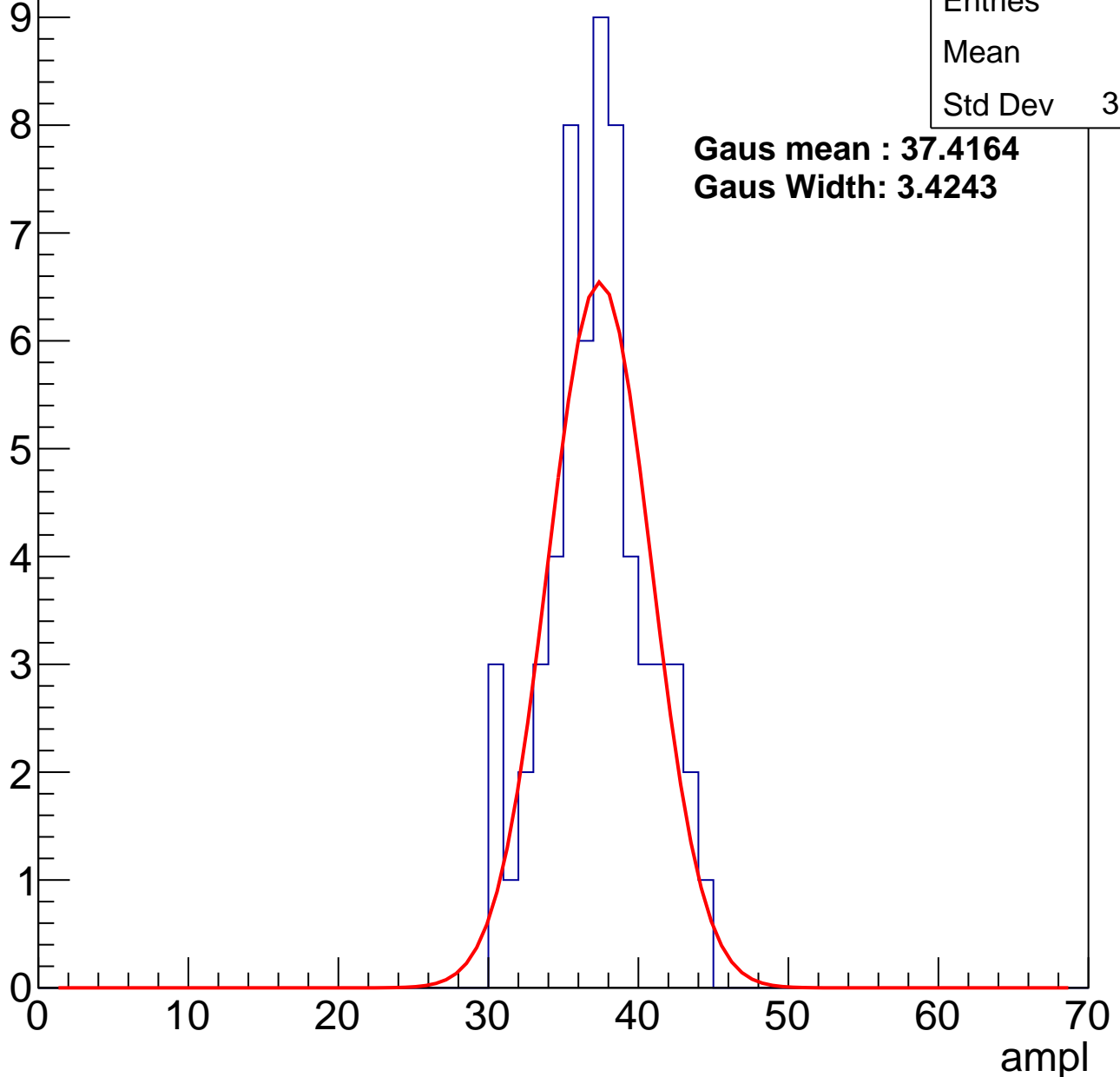
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	36.8
Std Dev	3.285

**Gaus mean : 37.4164**

**Gaus Width: 3.4243**



# B0L000S, U25-ch81, adc2

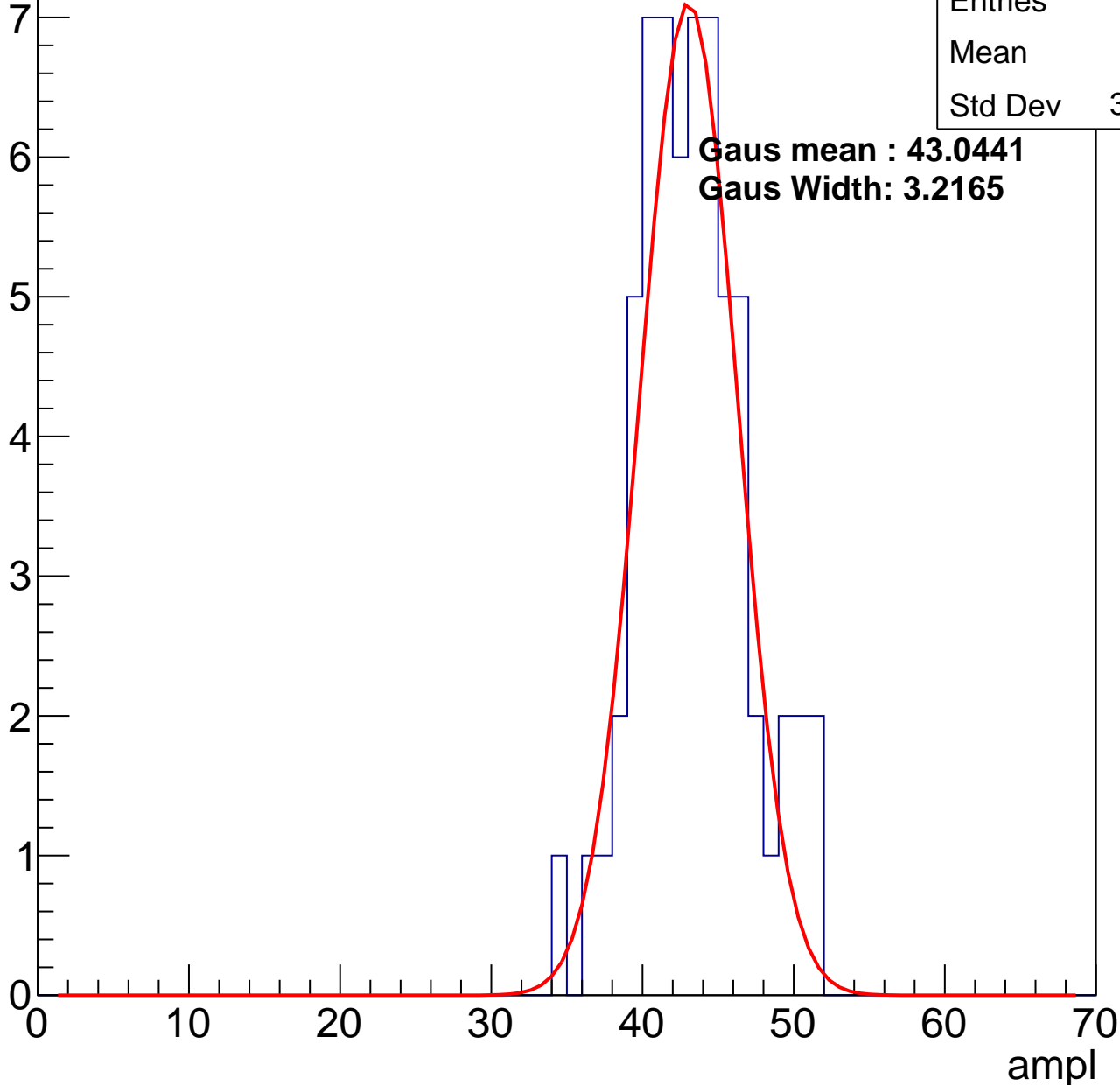
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	42.9
Std Dev	3.606

**Gaus mean : 43.0441**

**Gaus Width: 3.2165**

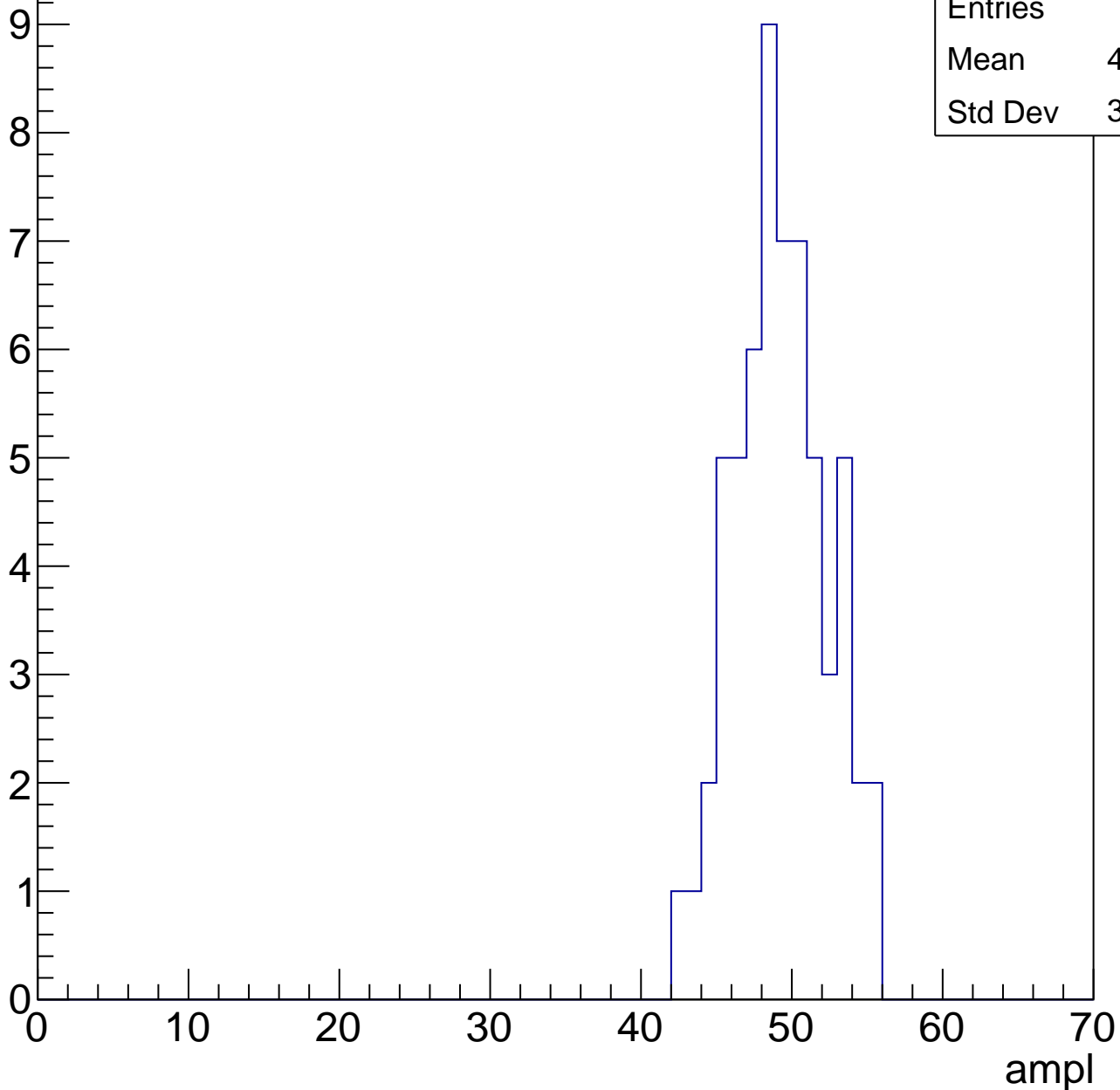


# B0L000S, U25-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	48.82
Std Dev	3.019

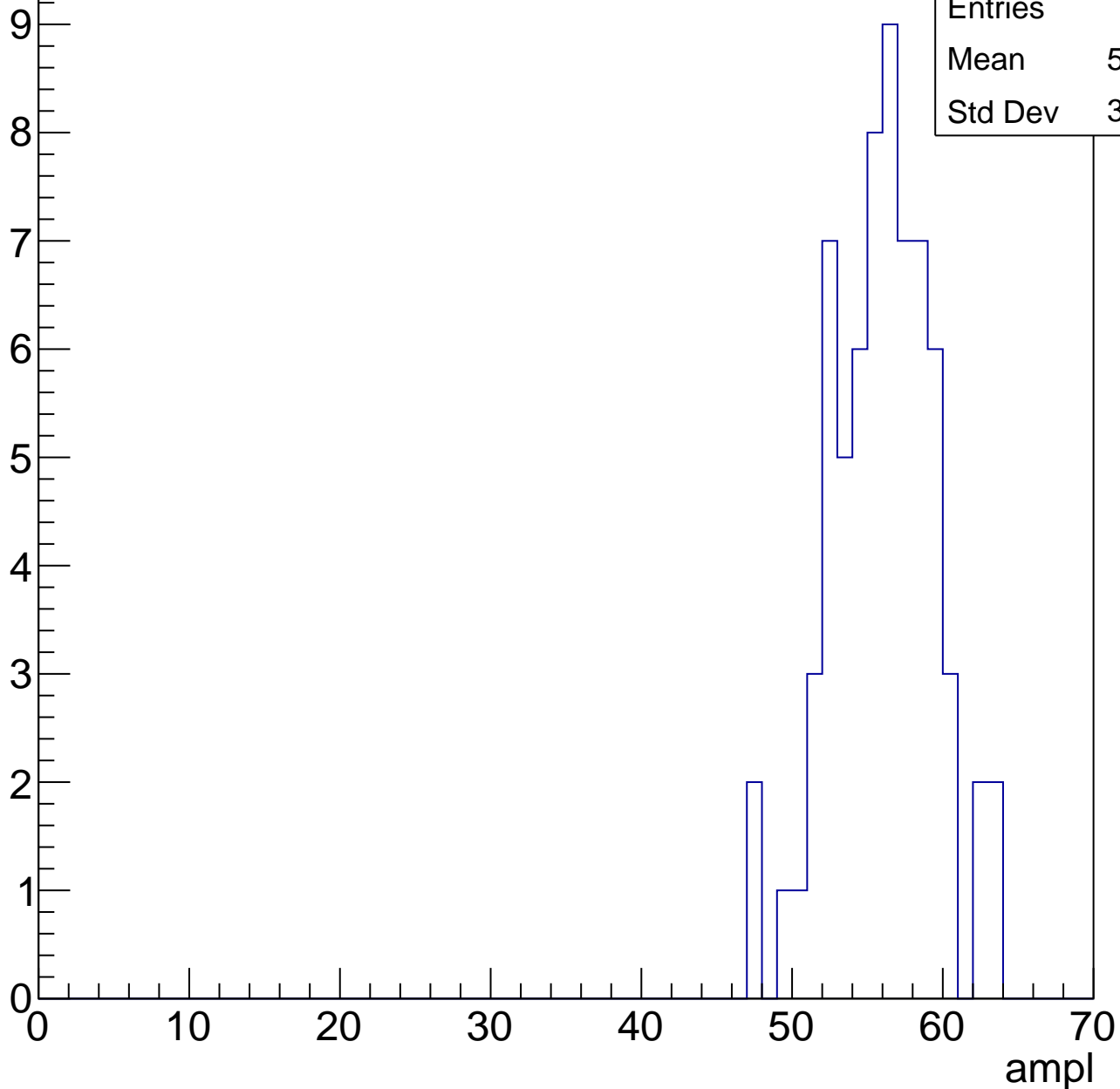


# B0L000S, U25-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	55.54
Std Dev	3.412

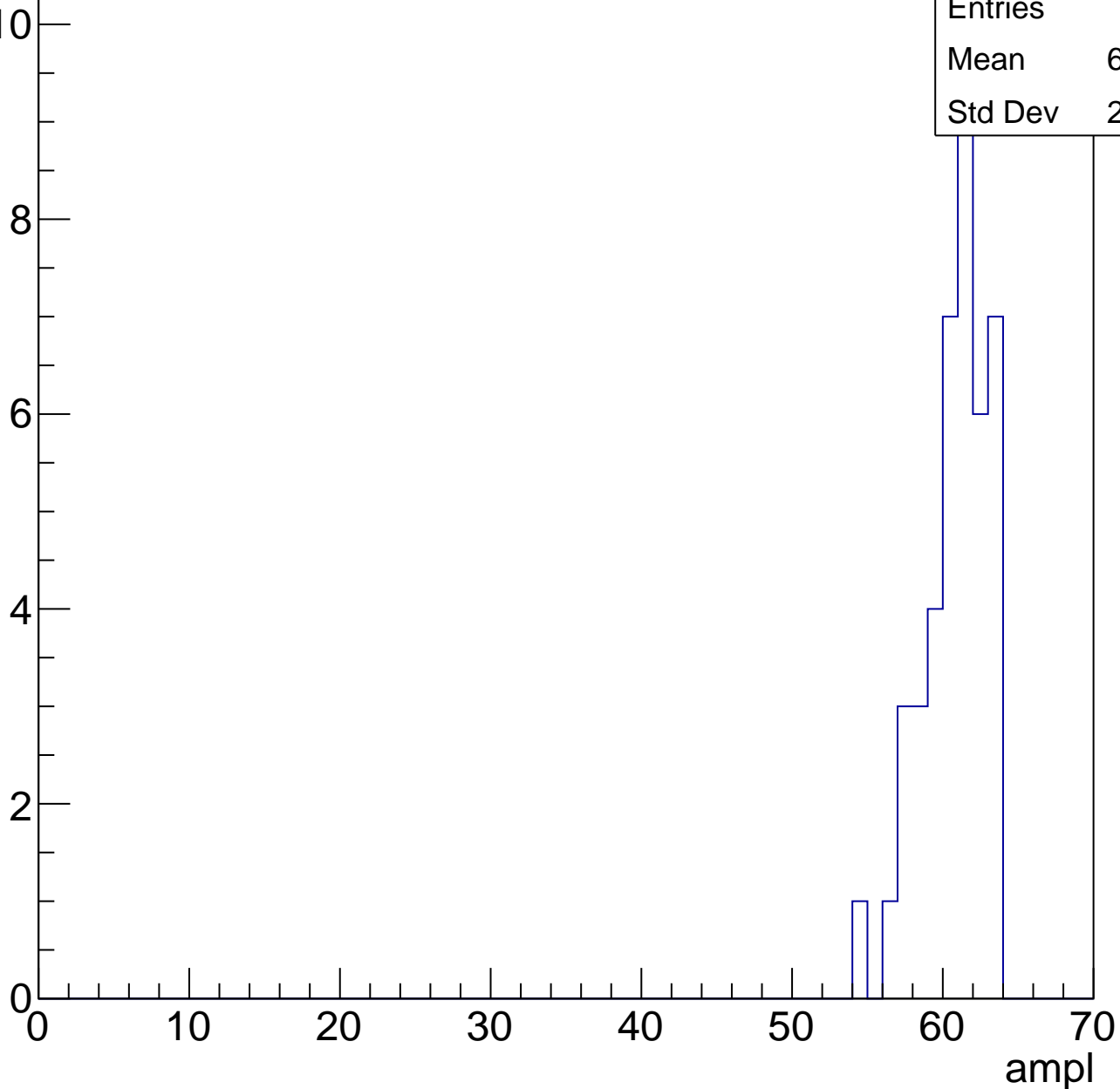


# B0L000S, U25-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	60.33
Std Dev	2.112



# B0L000S, U25-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

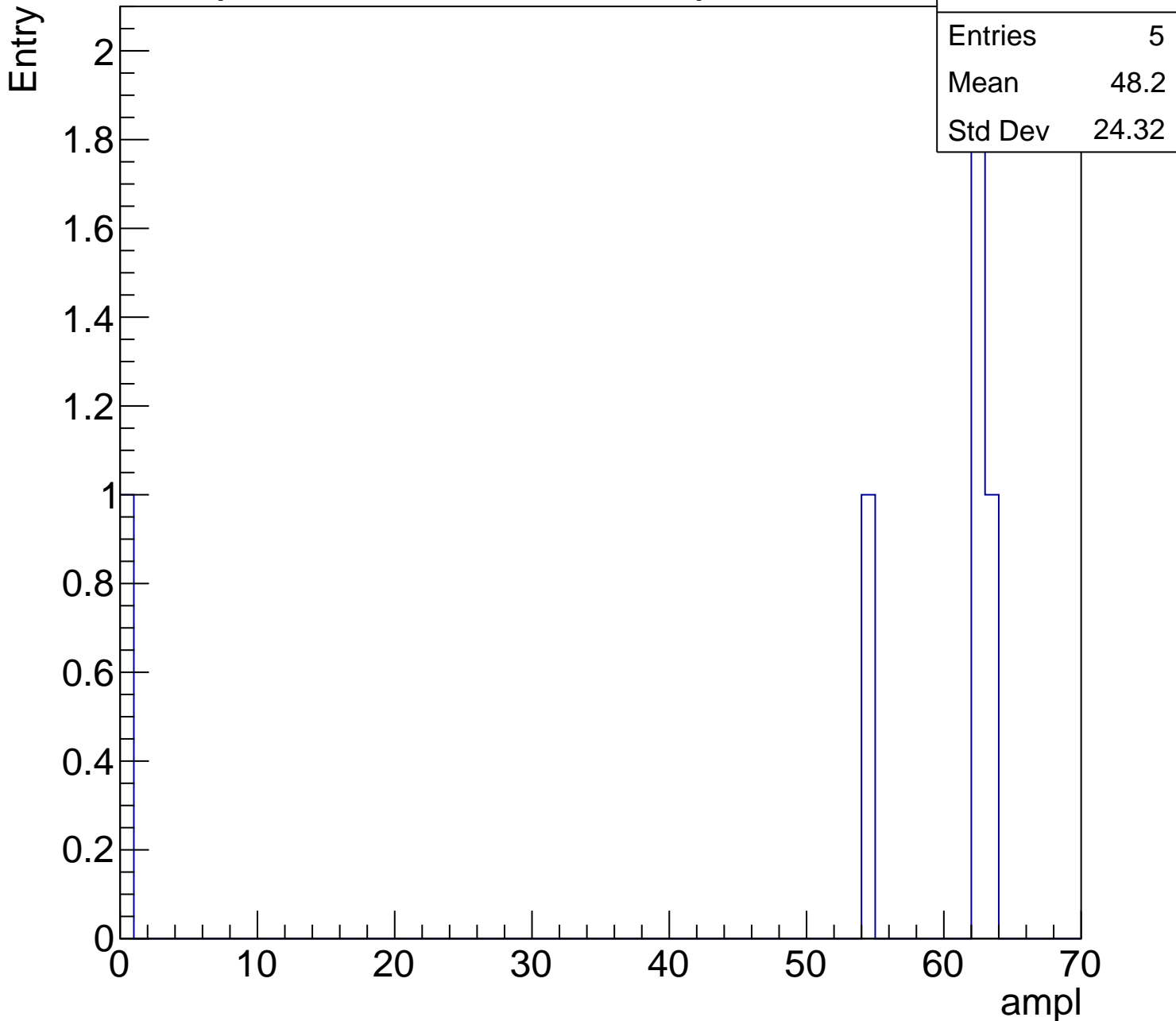
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	48.2
Std Dev	24.32

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch82, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	28.11
Std Dev	4.792

**Gaus mean : 28.6849**

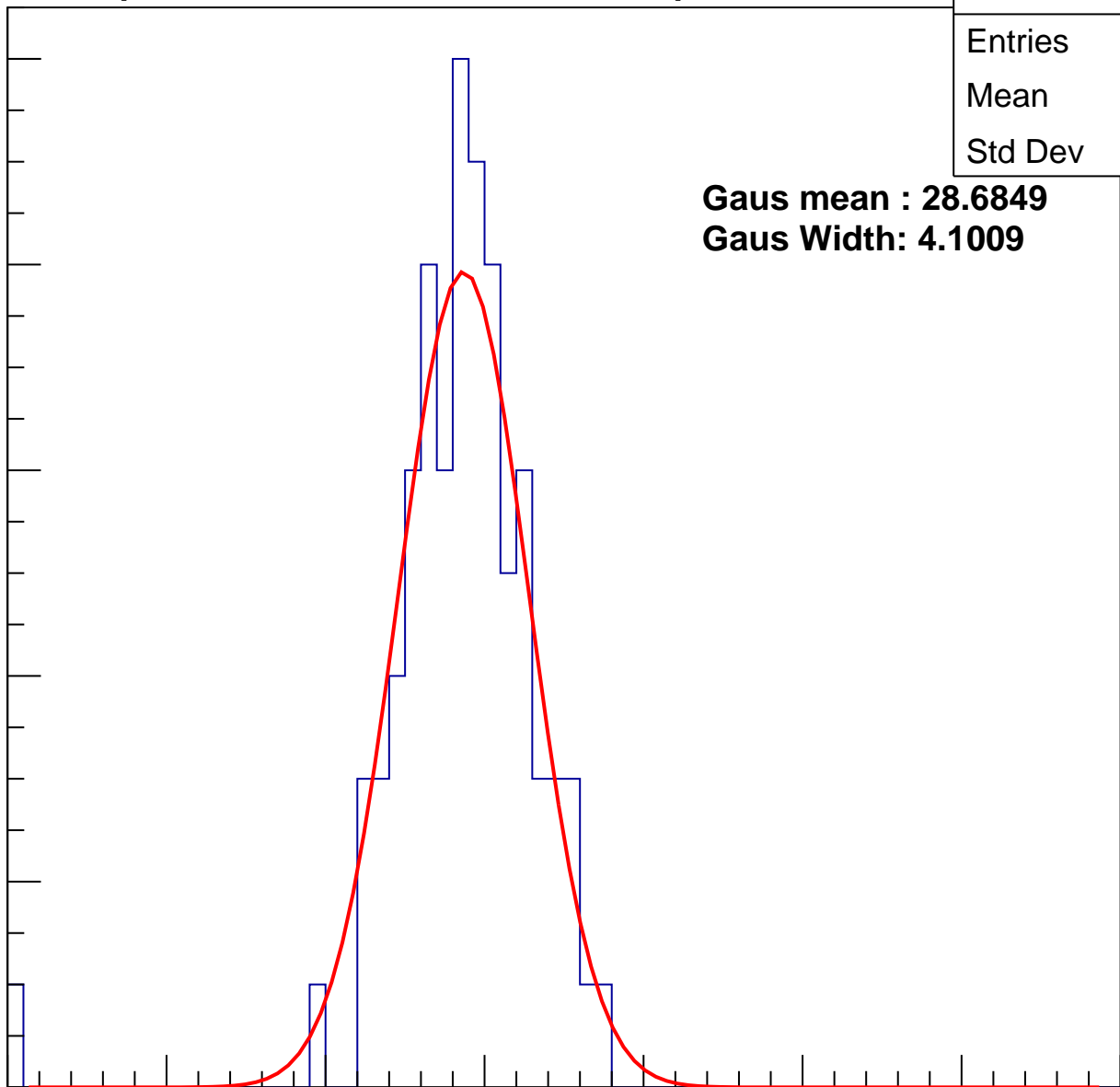
**Gaus Width: 4.1009**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch82, adc1

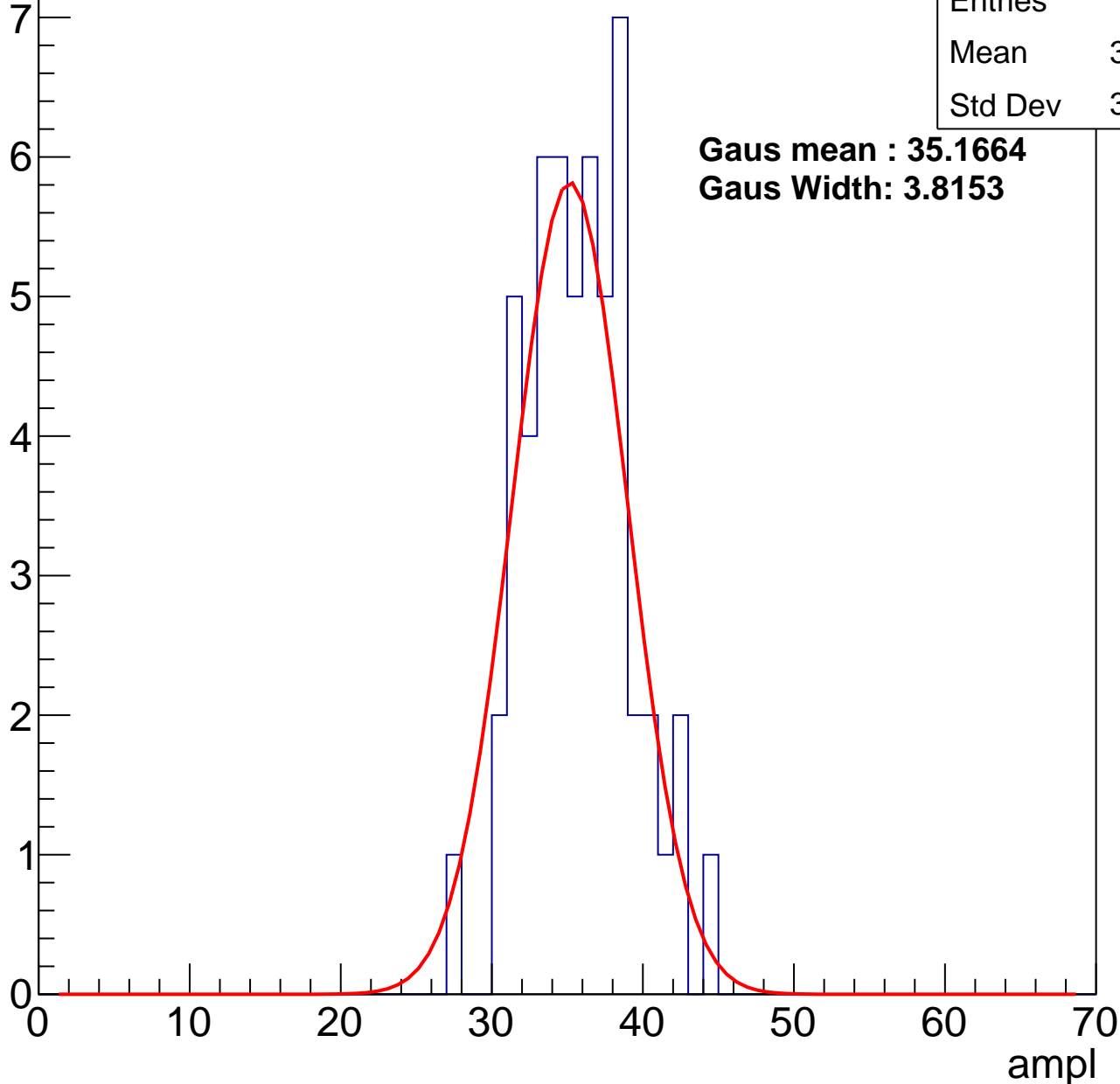
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	35.29
Std Dev	3.415

**Gaus mean : 35.1664**

**Gaus Width: 3.8153**



# B0L000S, U25-ch82, adc2

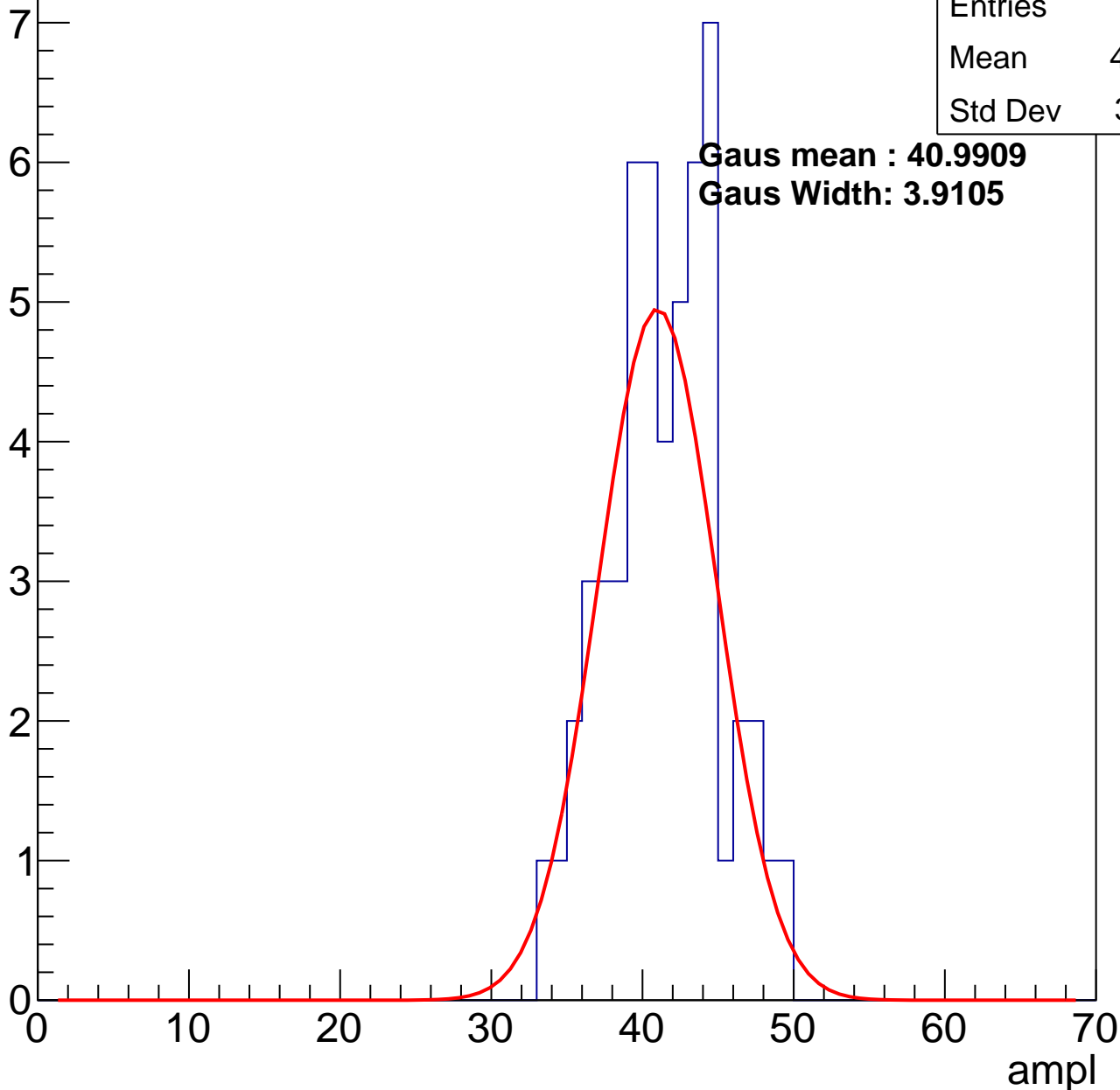
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	40.96
Std Dev	3.621

**Gaus mean : 40.9909**

**Gaus Width: 3.9105**

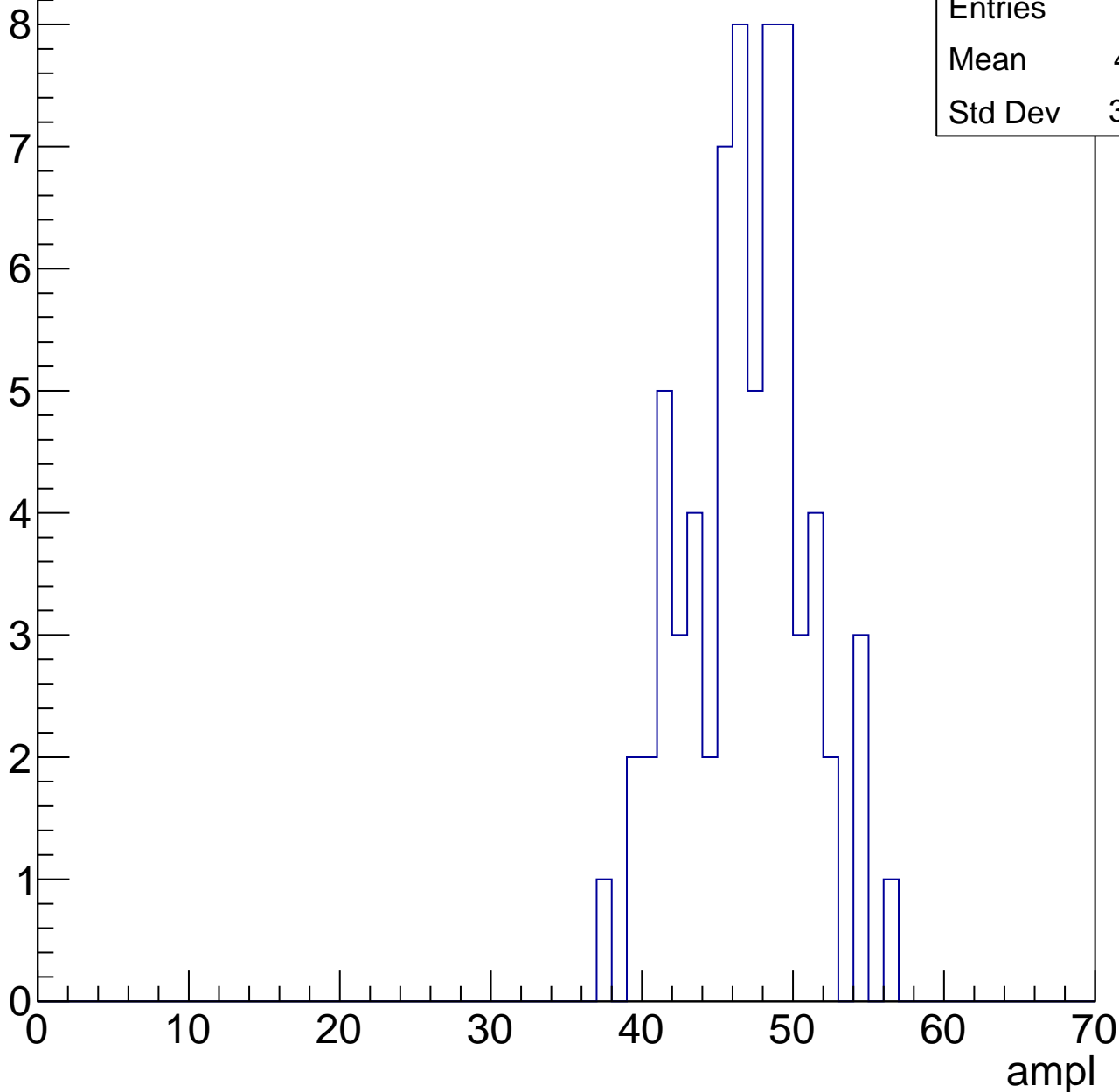


# B0L000S, U25-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	46.41
Std Dev	3.994



# B0L000S, U25-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	81
Mean	53.88
Std Dev	3.55

Entry

10

8

6

4

2

0

0

10

20

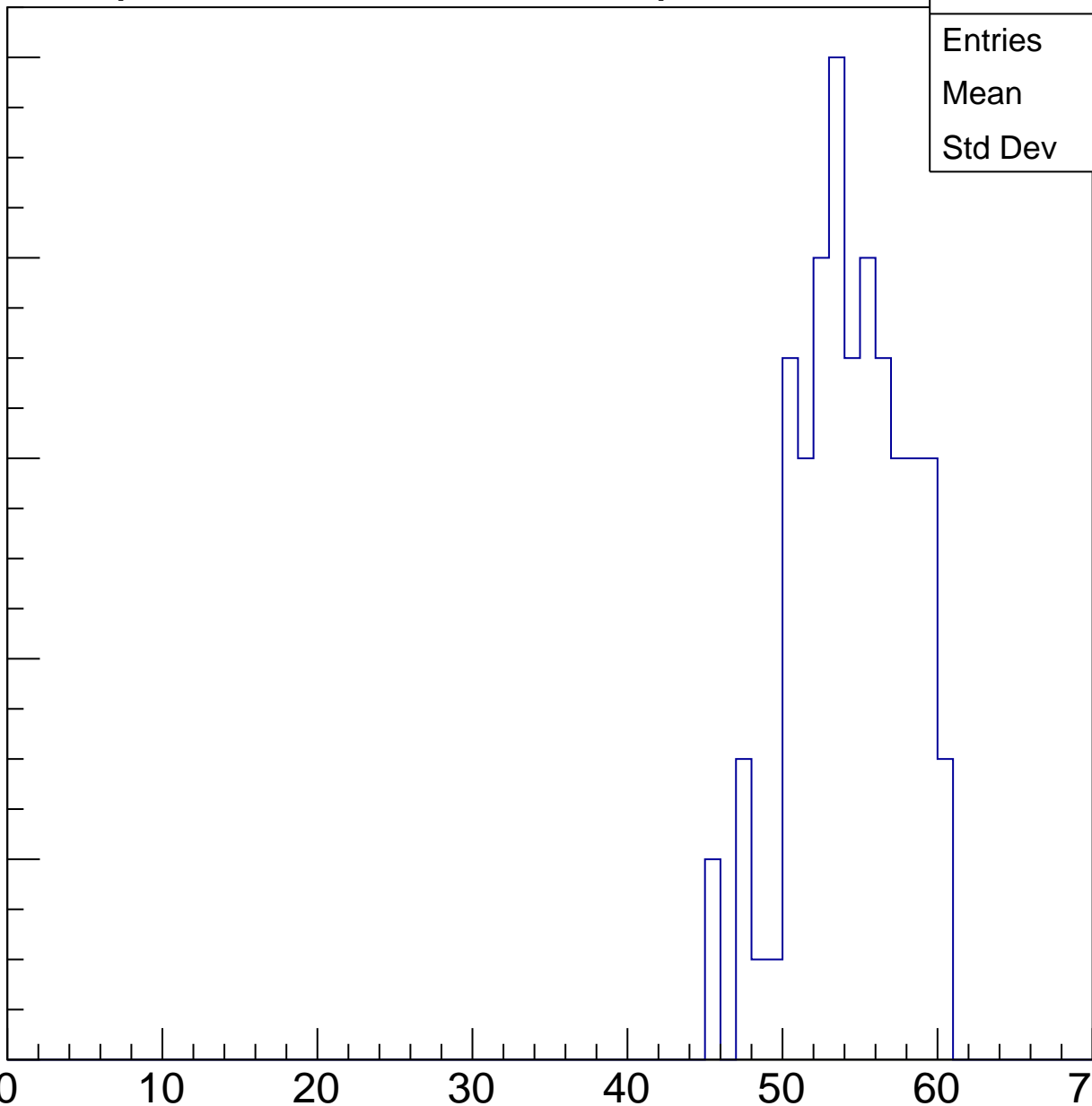
30

40

50

60

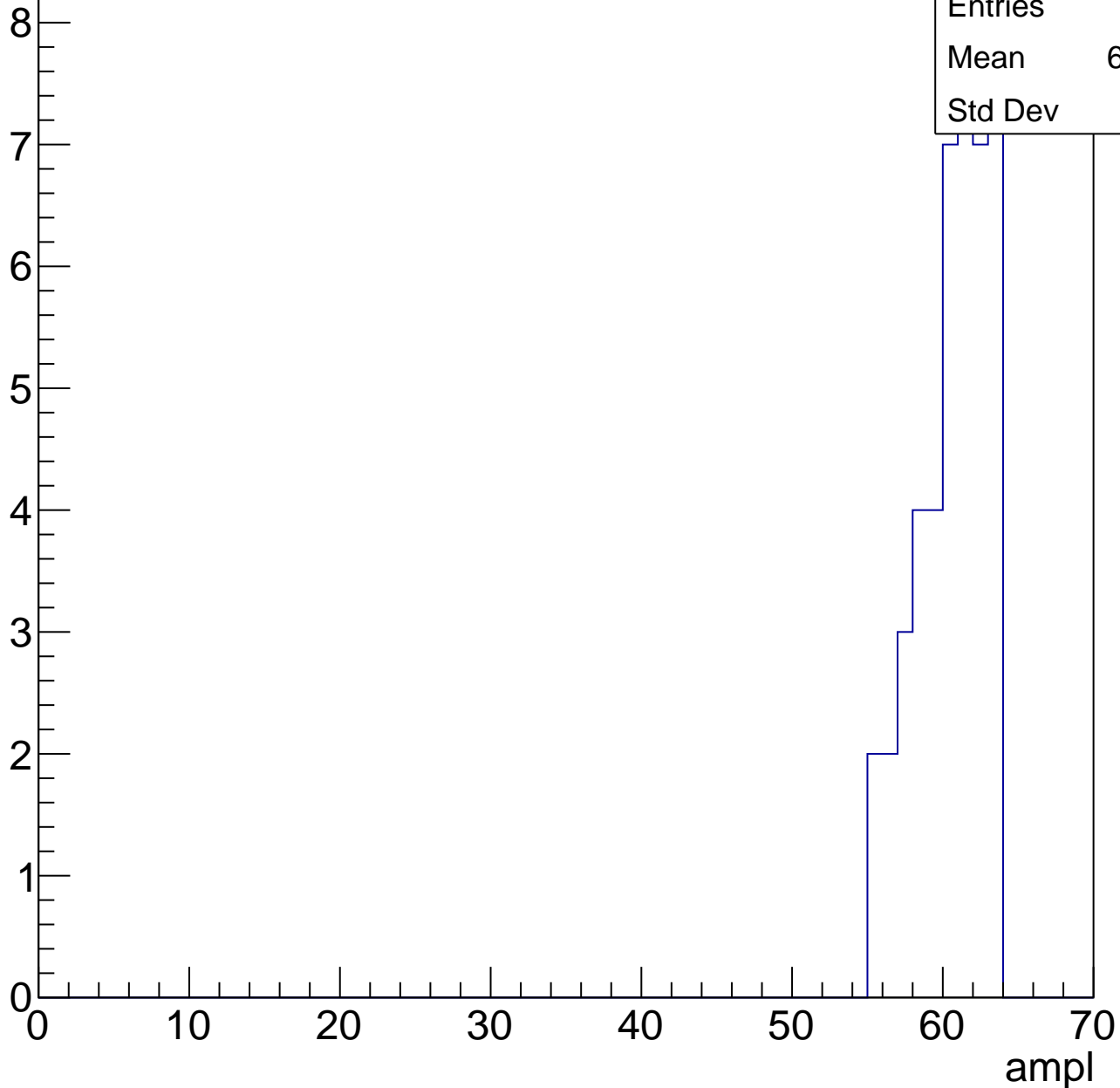
ampl



# B0L000S, U25-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

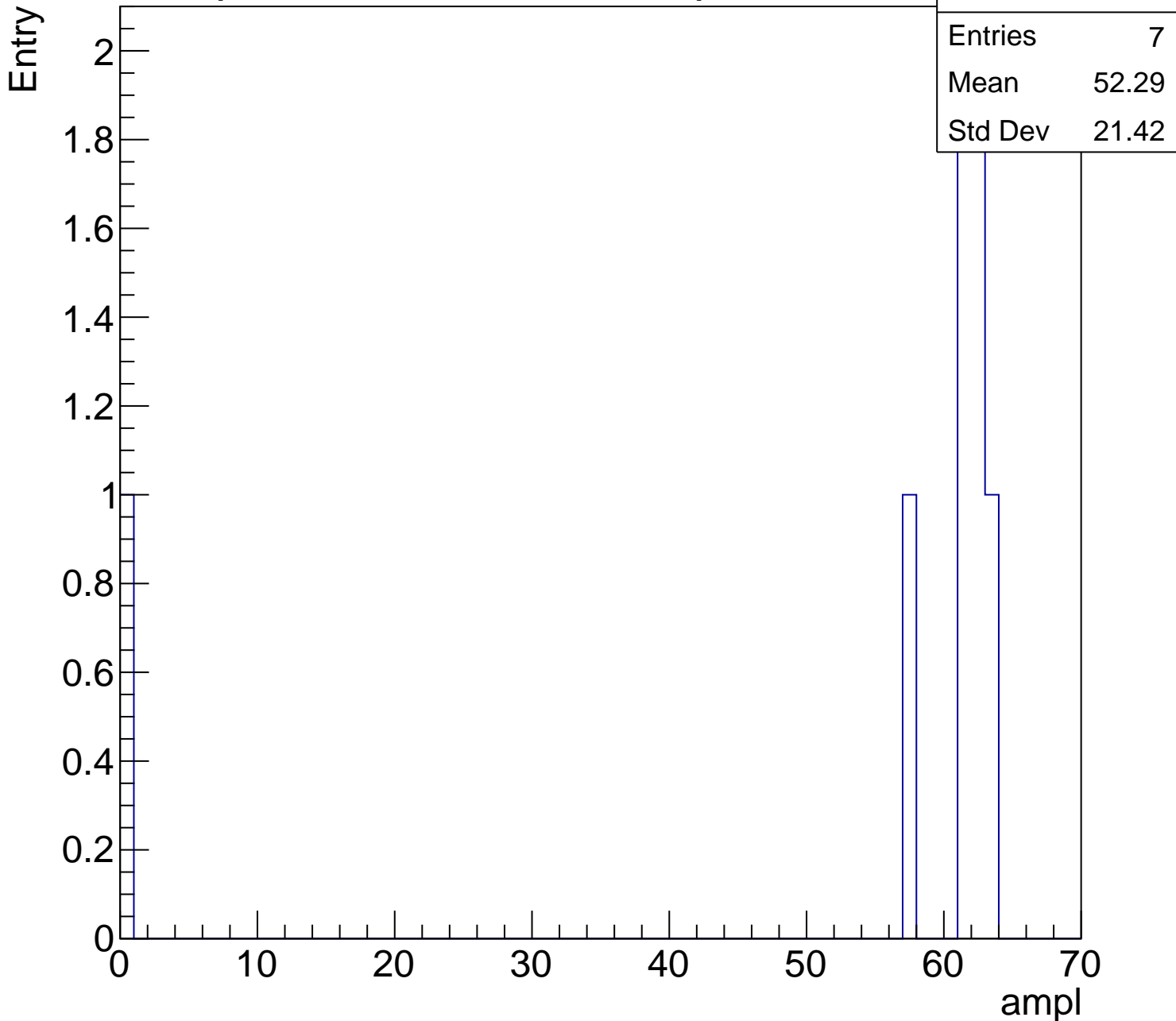
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	52.29
Std Dev	21.42

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch83, adc0

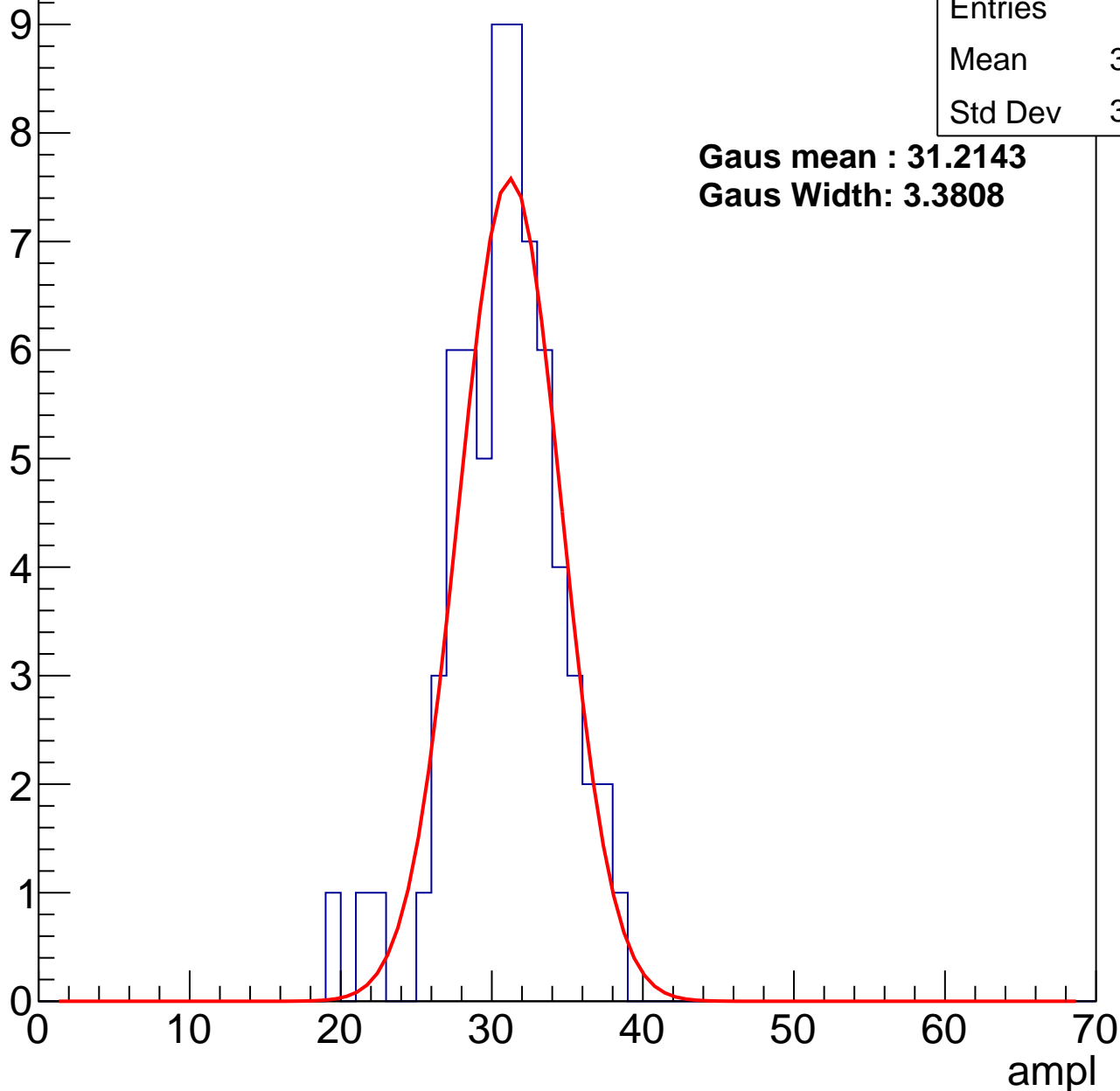
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	30.39
Std Dev	3.616

**Gaus mean : 31.2143**

**Gaus Width: 3.3808**



# B0L000S, U25-ch83, adc1

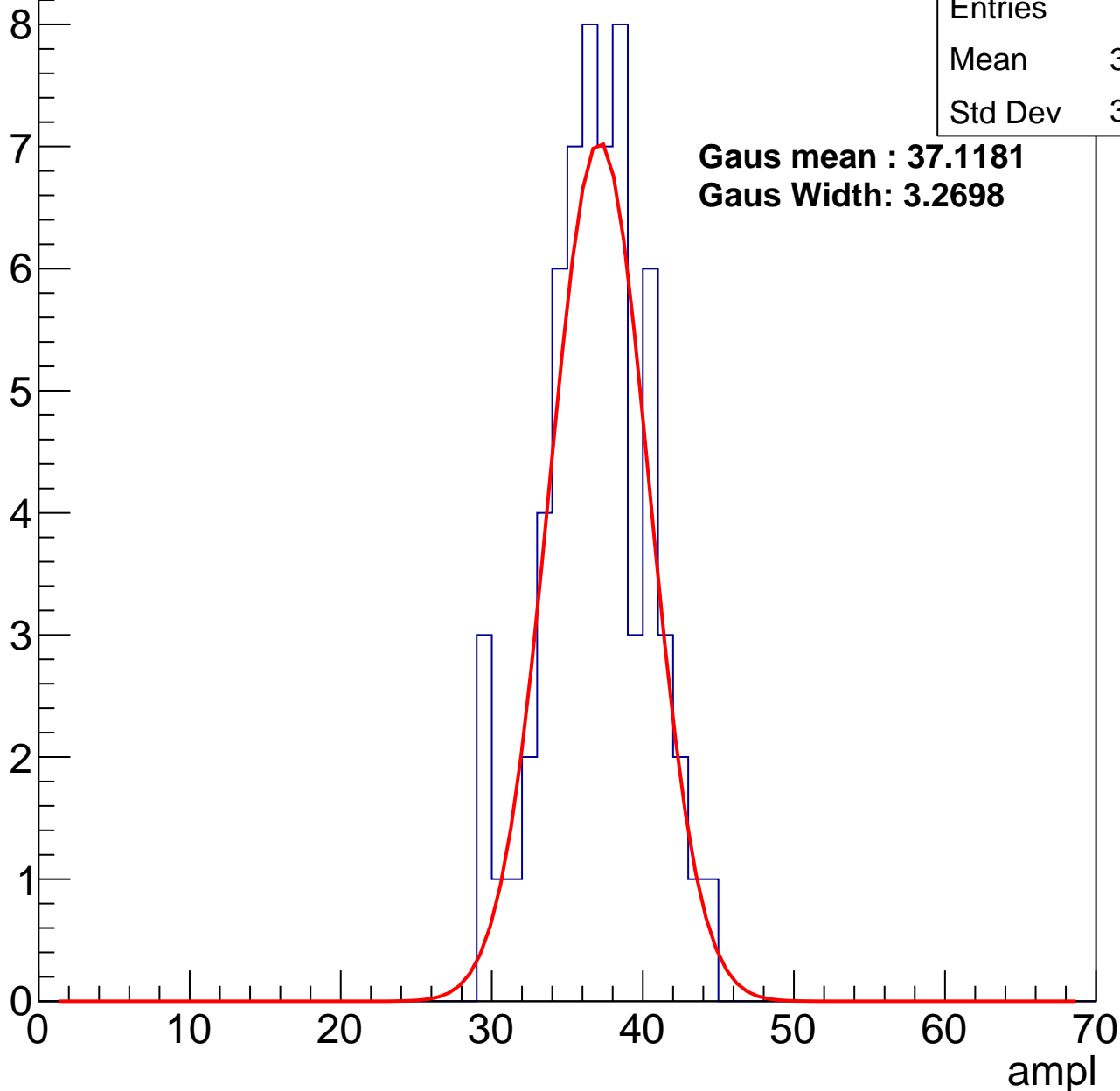
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.43
Std Dev	3.375

**Gaus mean : 37.1181**

**Gaus Width: 3.2698**



# B0L000S, U25-ch83, adc2

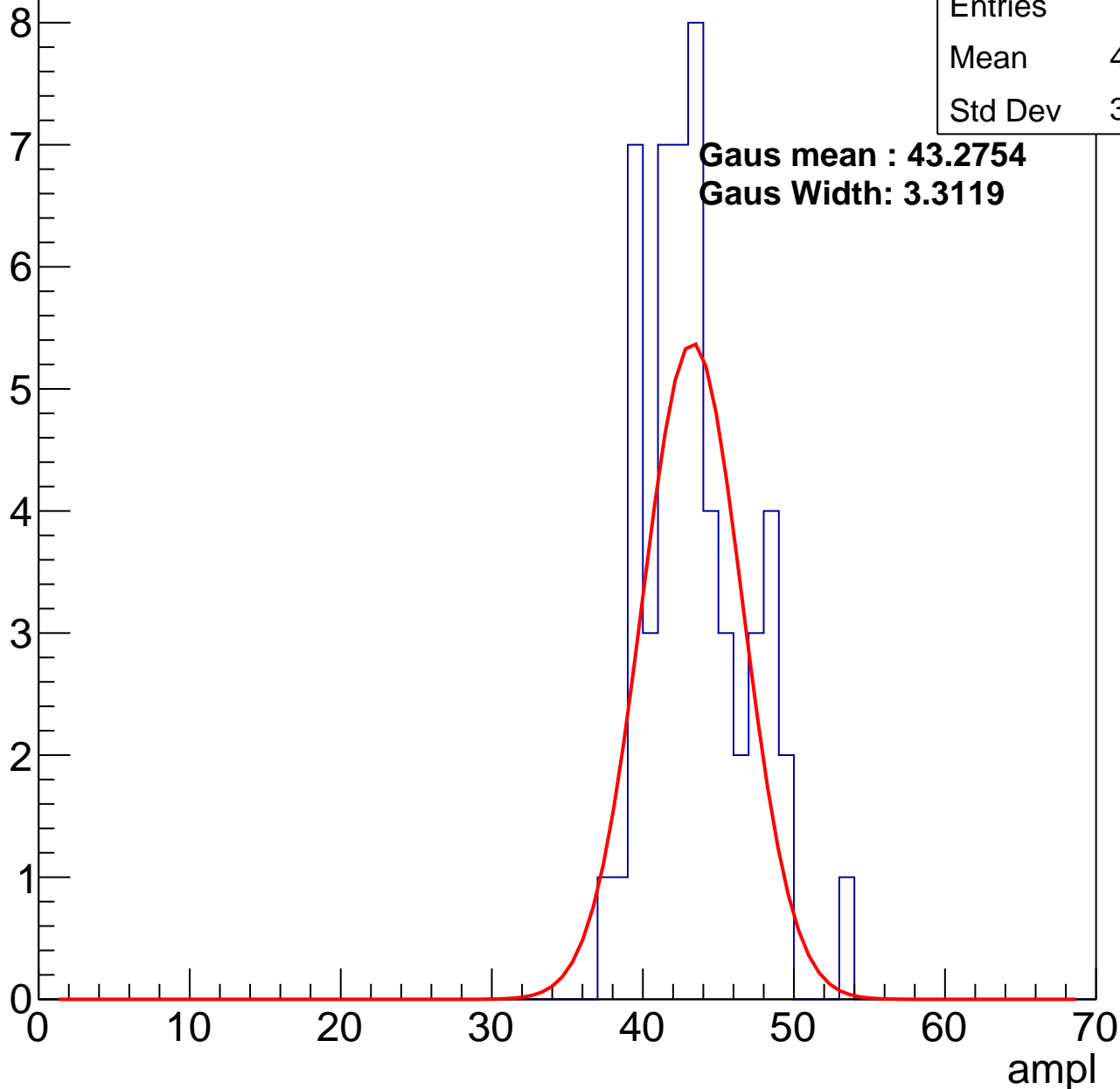
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	43.02
Std Dev	3.334

**Gaus mean : 43.2754**

**Gaus Width: 3.3119**

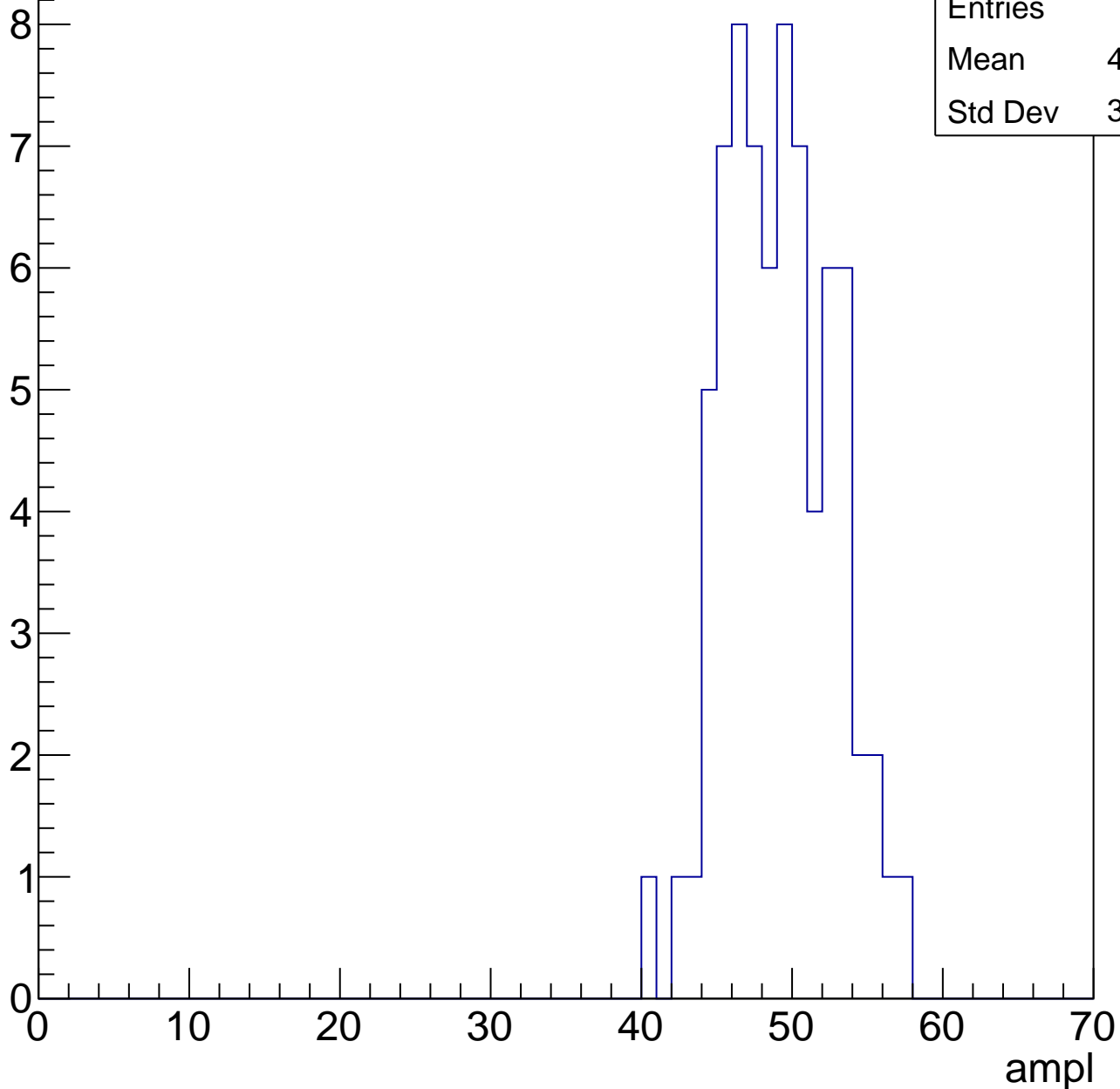


# B0L000S, U25-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	48.66
Std Dev	3.528

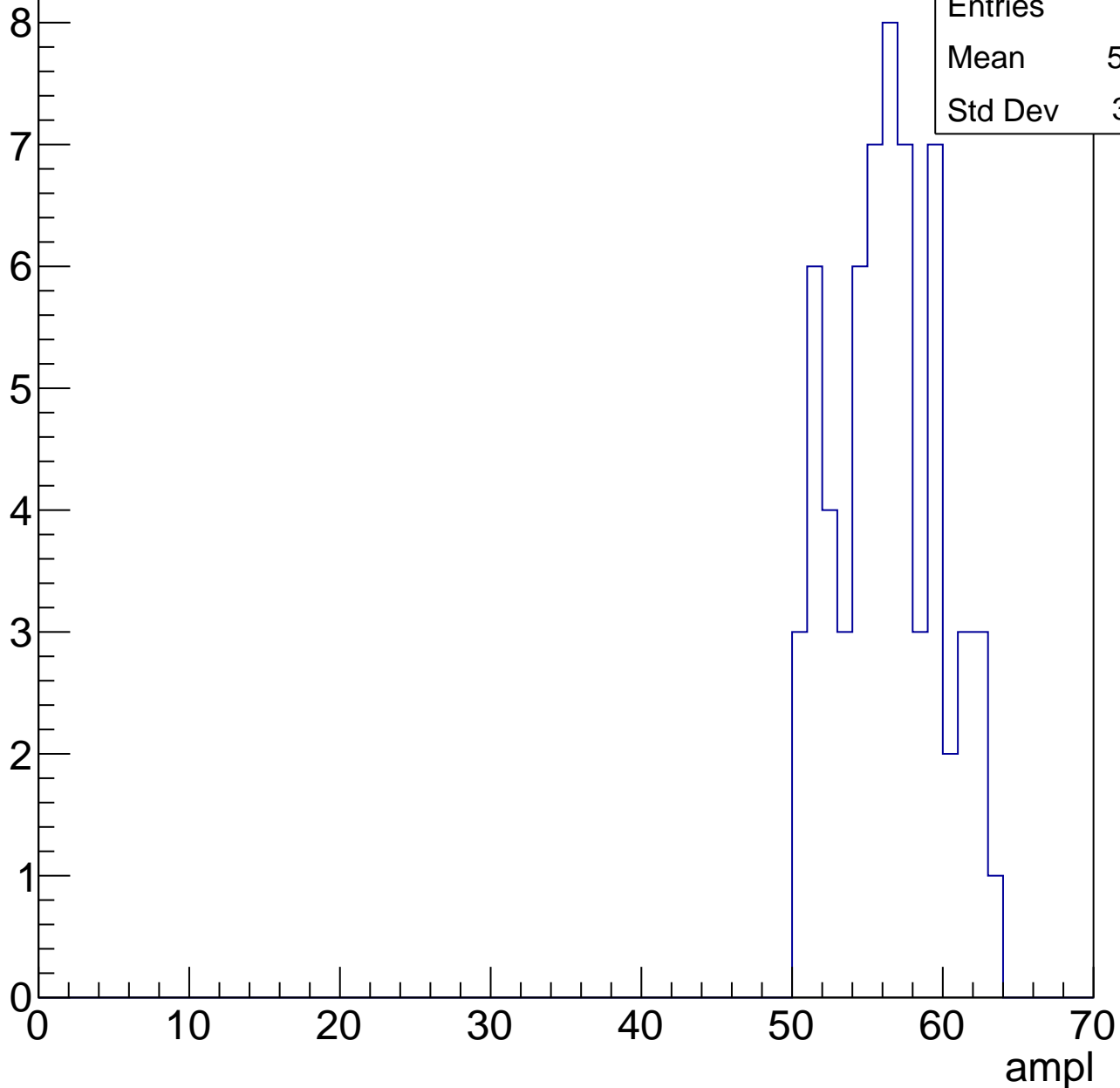


# B0L000S, U25-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	55.84
Std Dev	3.391



# B0L000S, U25-ch83, adc5

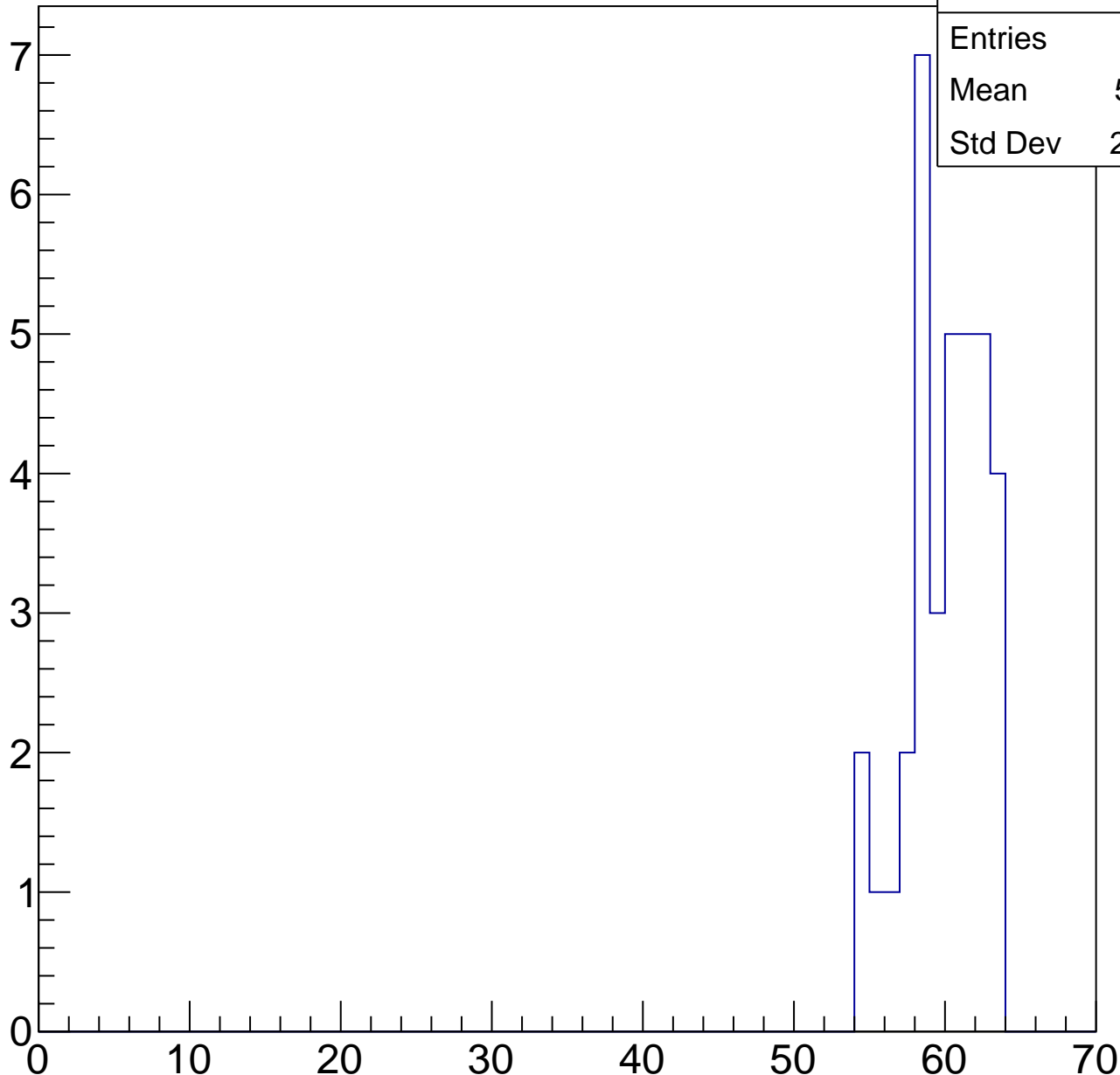
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	35
Mean	59.51
Std Dev	2.477

ampl

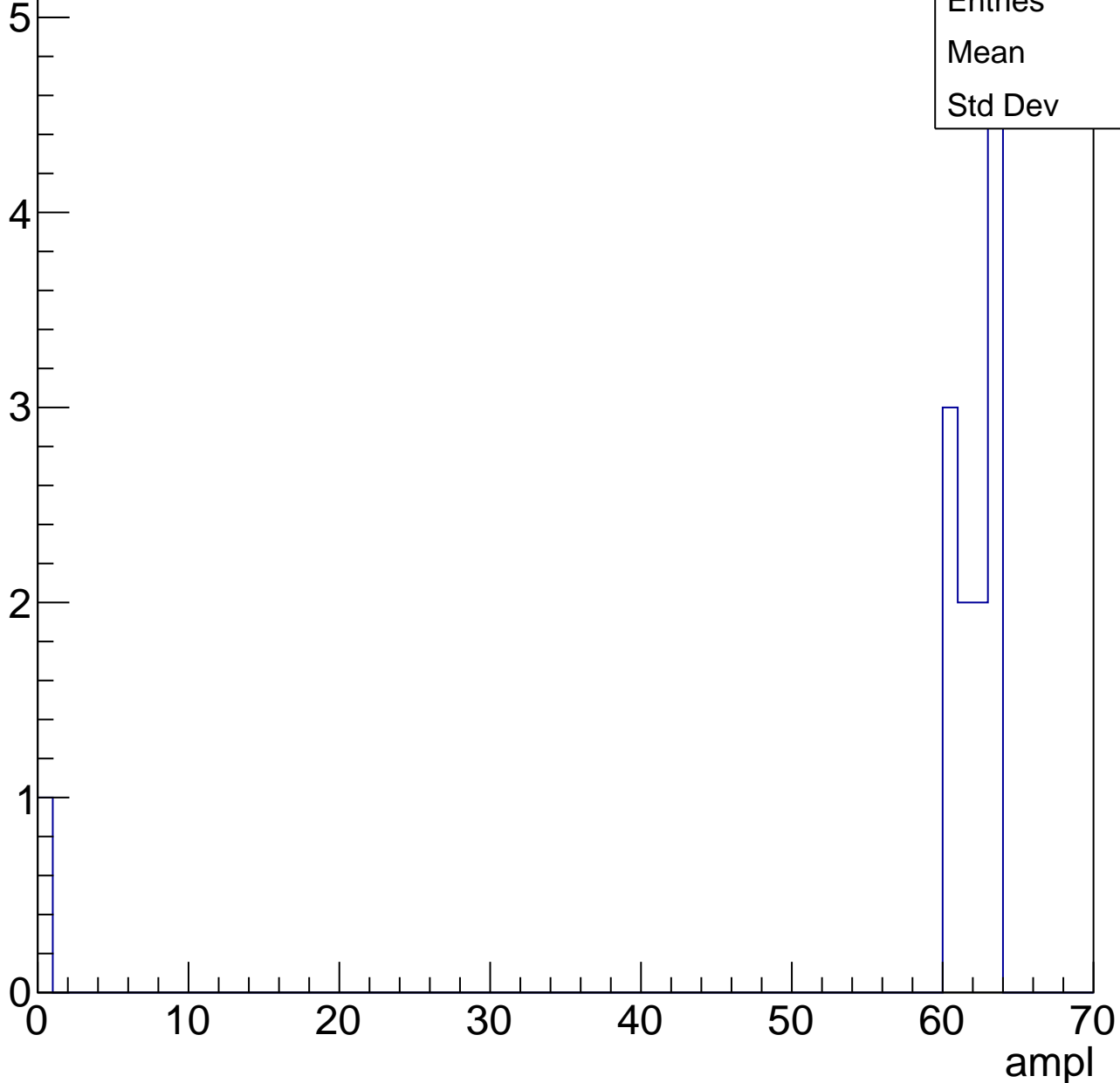


# B0L000S, U25-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	13
Mean	57
Std Dev	16.5





# B0L000S, U25-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch84, adc0

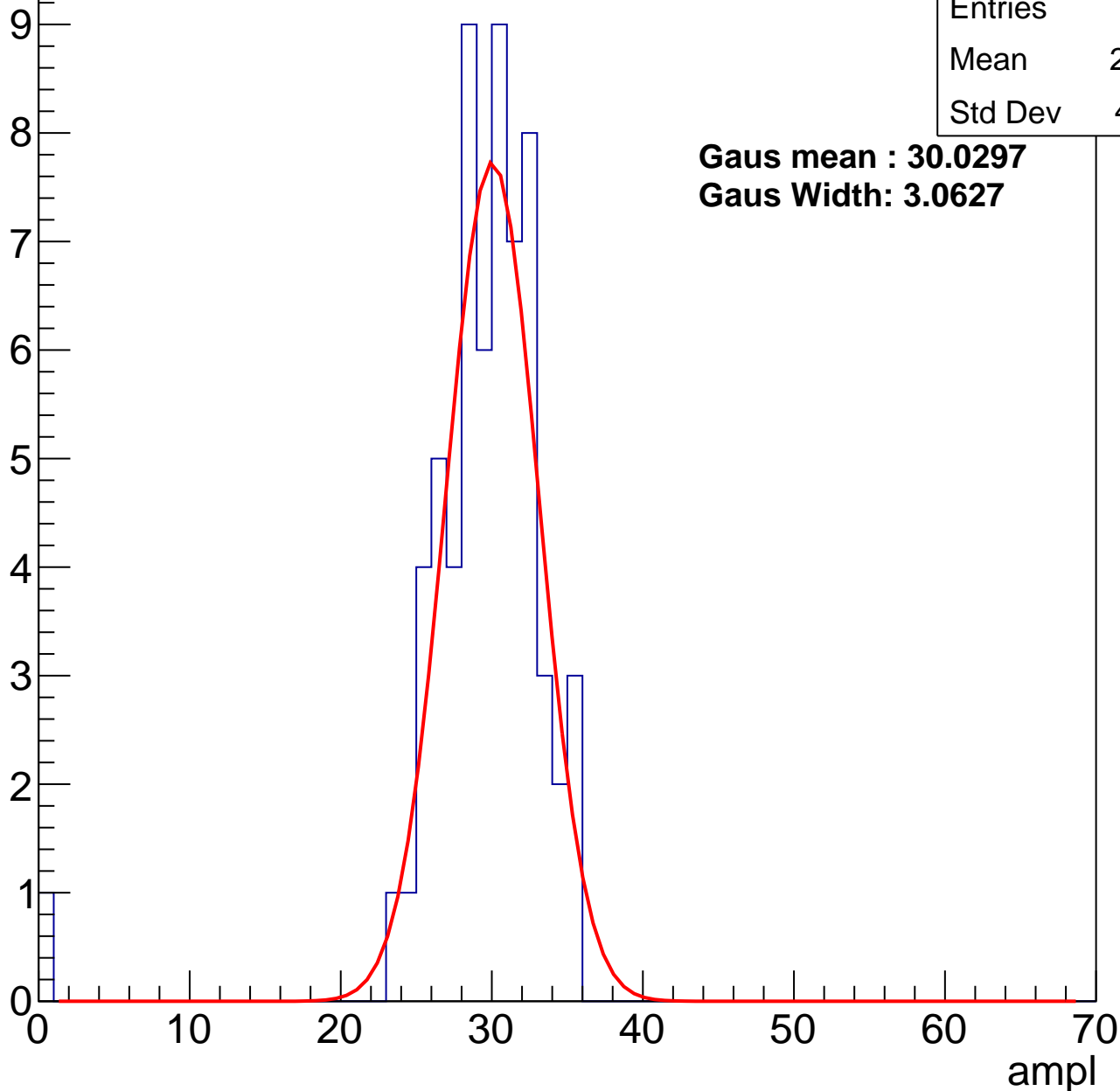
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	28.98
Std Dev	4.631

**Gaus mean : 30.0297**

**Gaus Width: 3.0627**



# B0L000S, U25-ch84, adc1

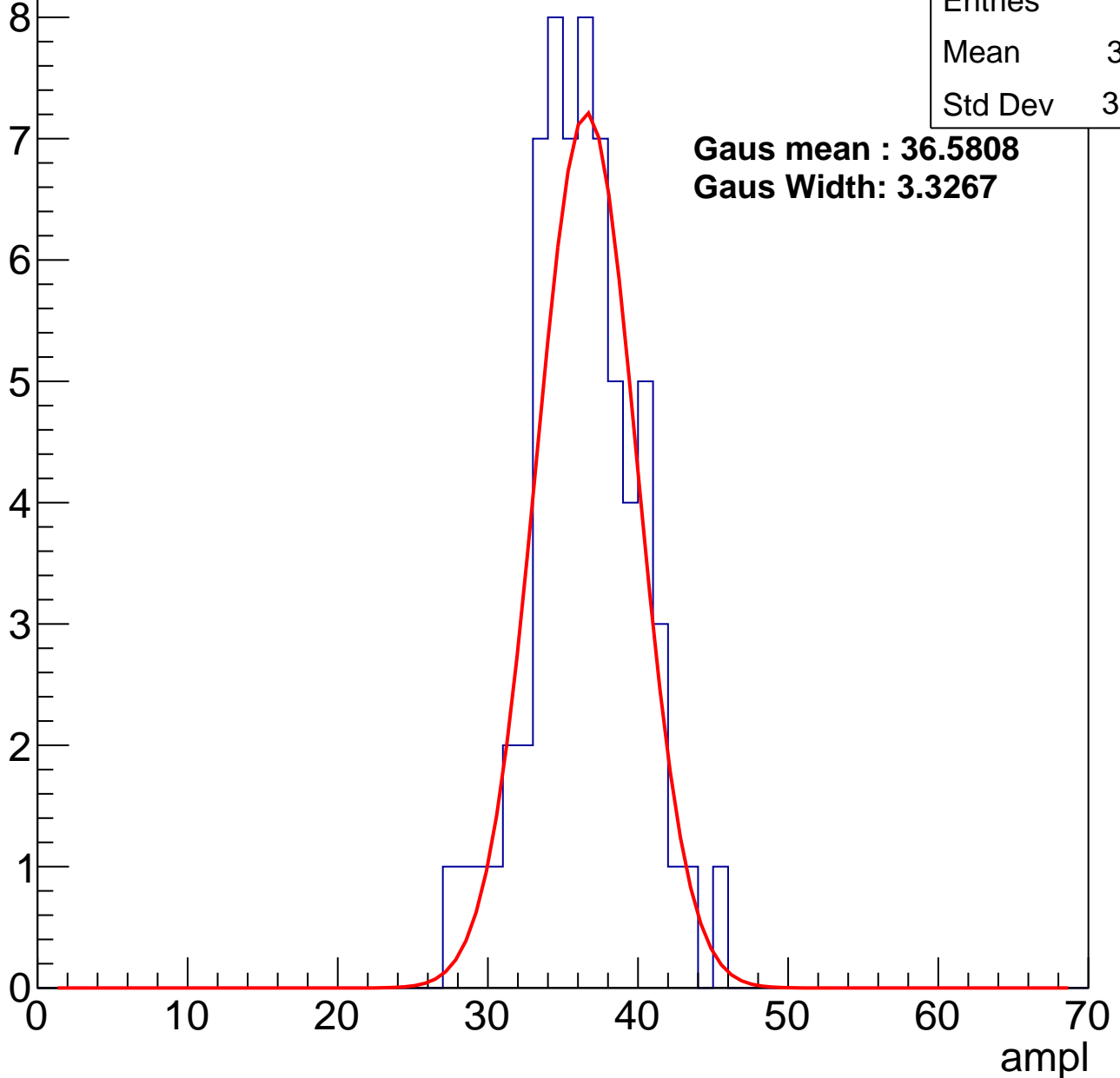
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	35.91
Std Dev	3.503

**Gaus mean : 36.5808**

**Gaus Width: 3.3267**



# B0L000S, U25-ch84, adc2

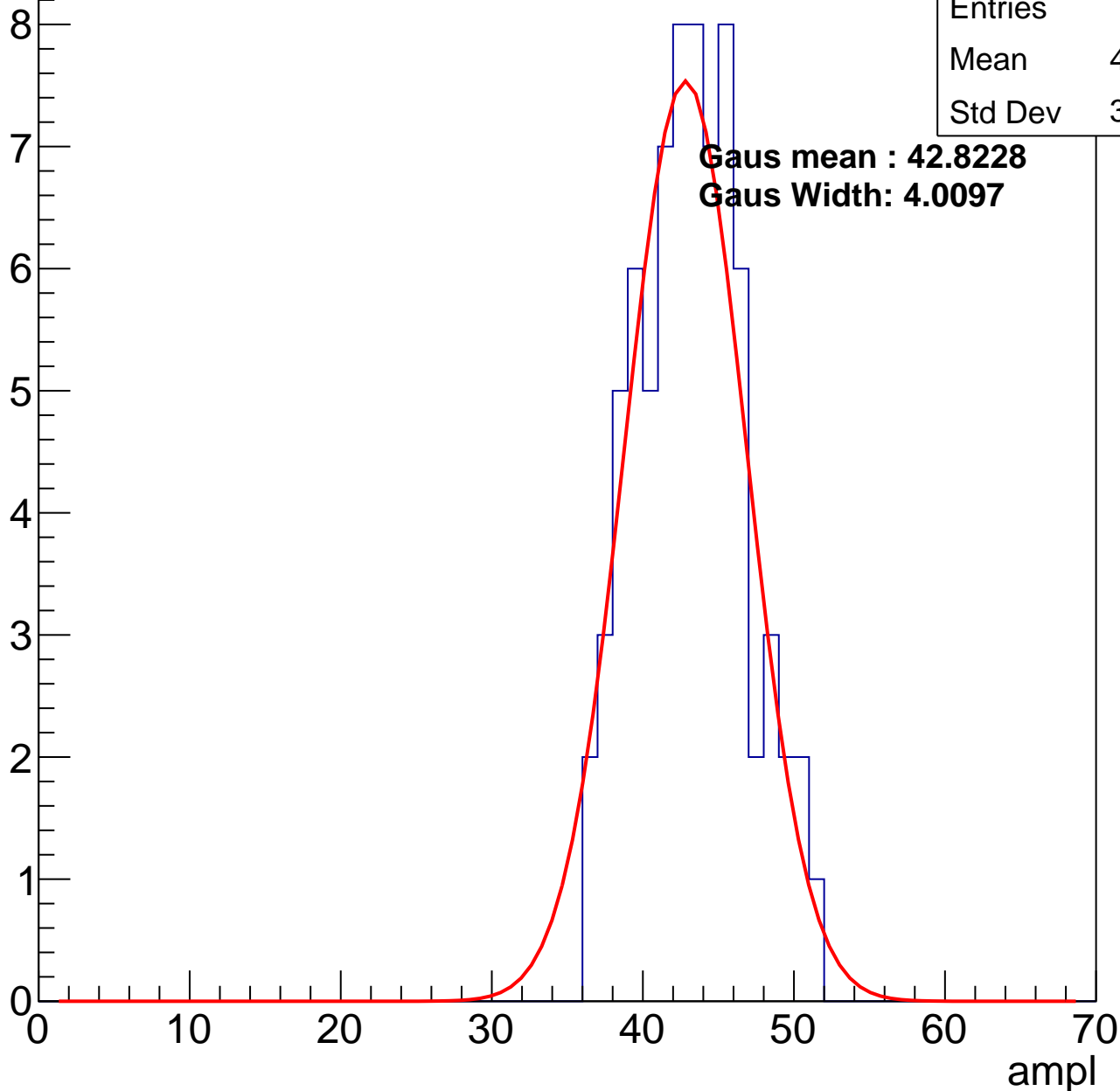
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	42.73
Std Dev	3.534

**Gaus mean : 42.8228**

**Gaus Width: 4.0097**

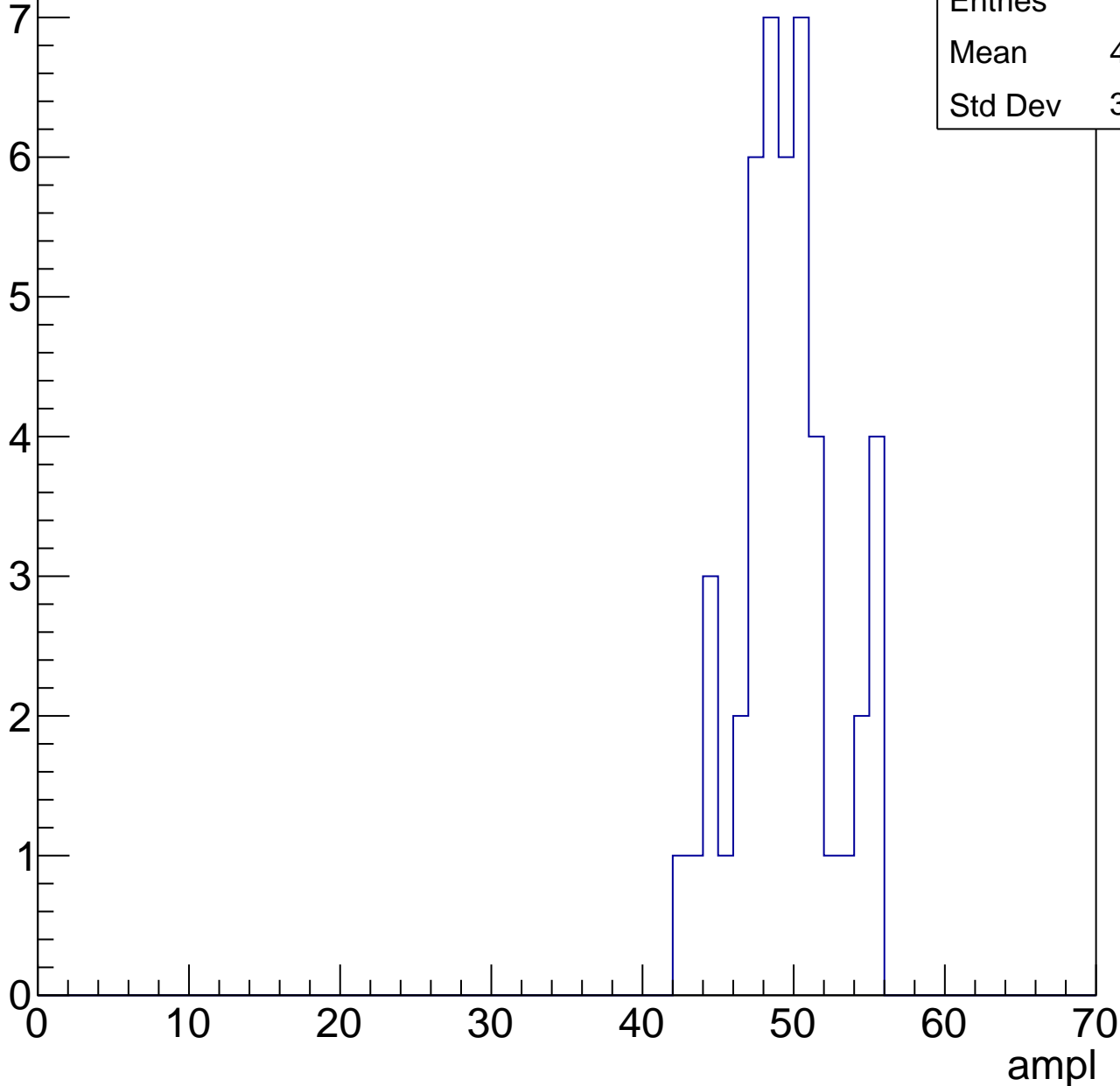


# B0L000S, U25-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

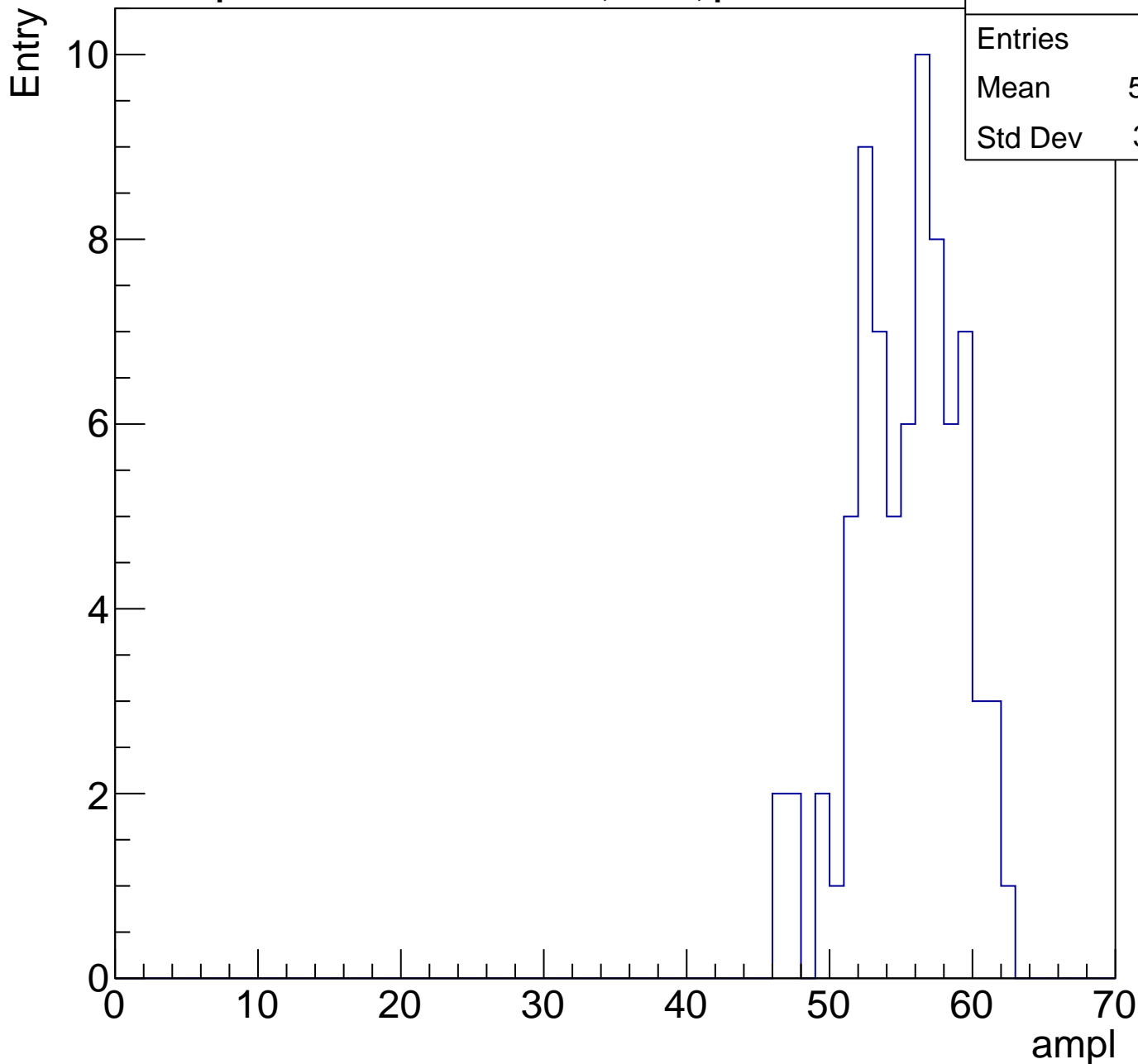
Entries	46
Mean	48.98
Std Dev	3.186



# B0L000S, U25-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	77
Mean	54.94
Std Dev	3.641

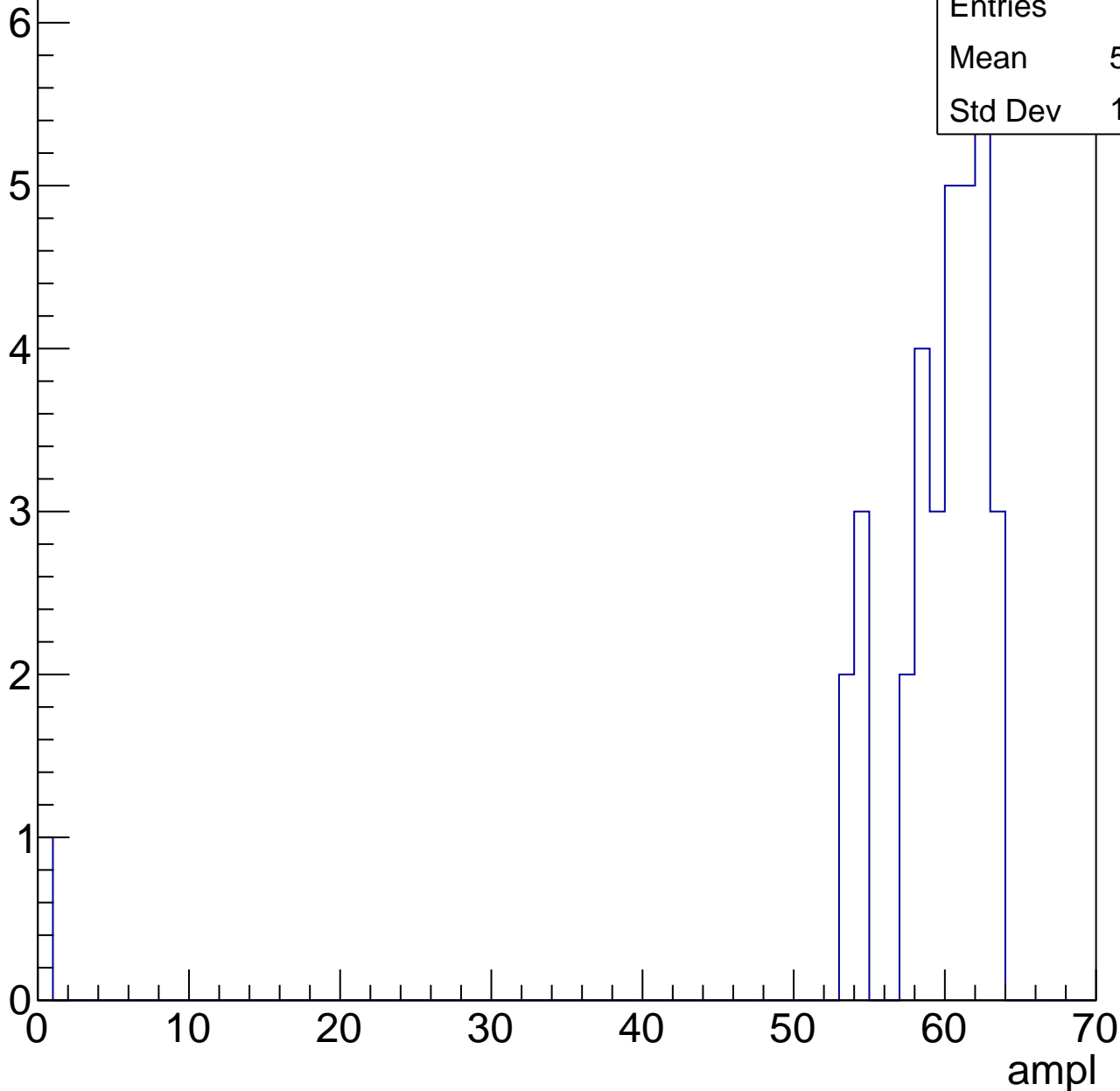


# B0L000S, U25-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	57.56
Std Dev	10.43

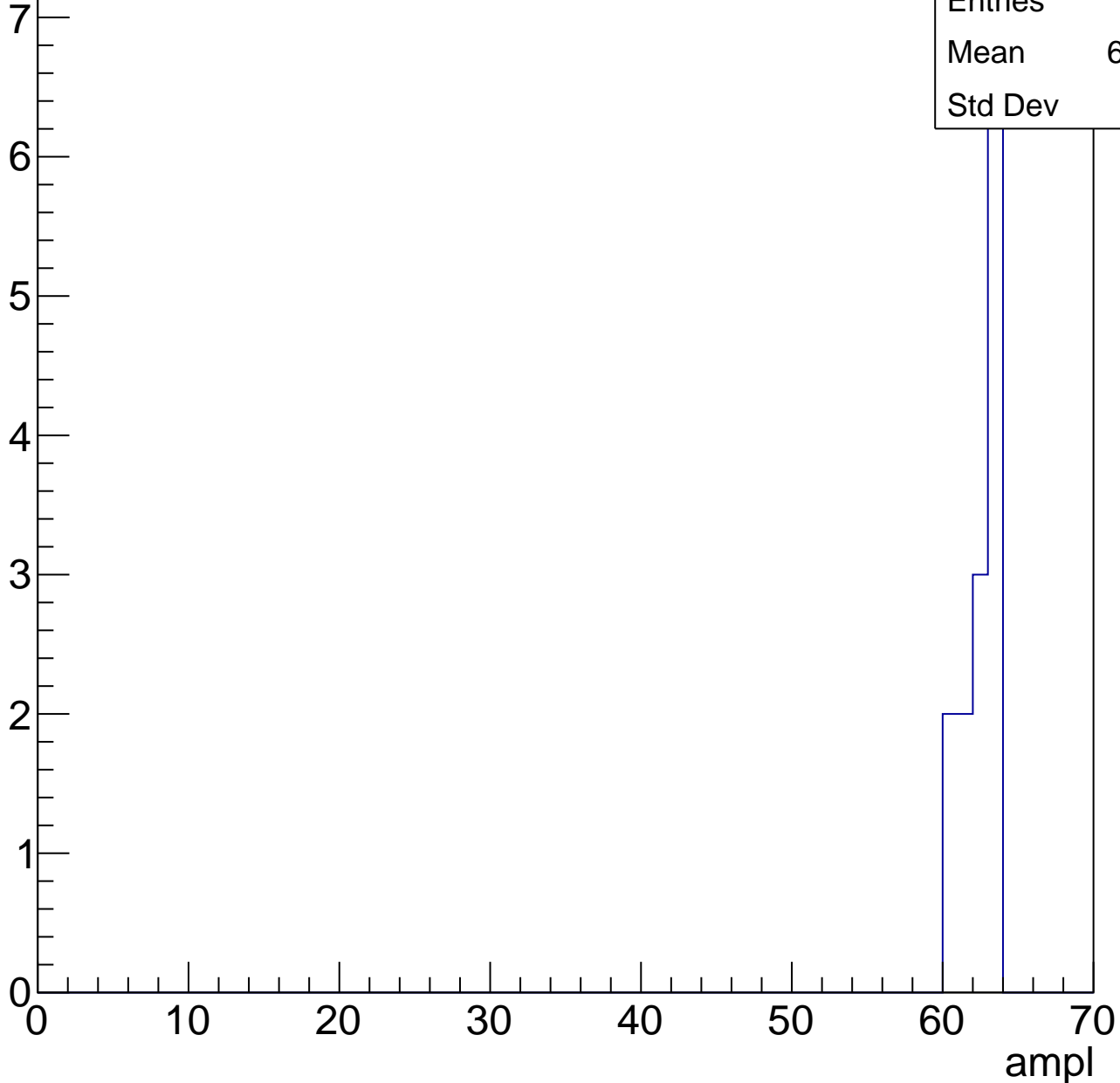


# B0L000S, U25-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	62.07
Std Dev	1.1





# B0L000S, U25-ch84, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch85, adc0

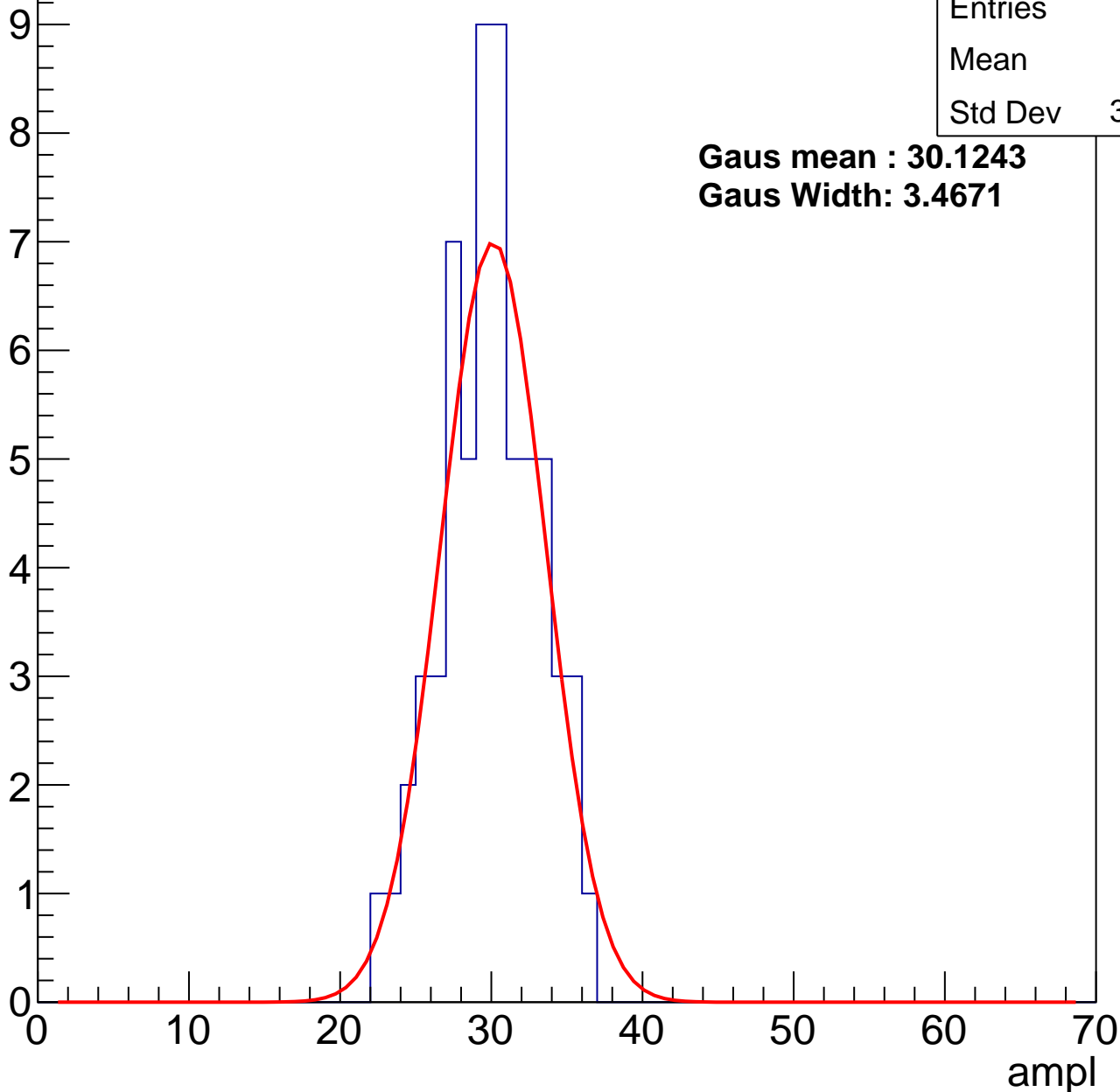
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	29.5
Std Dev	3.146

**Gaus mean : 30.1243**

**Gaus Width: 3.4671**



# B0L000S, U25-ch85, adc1

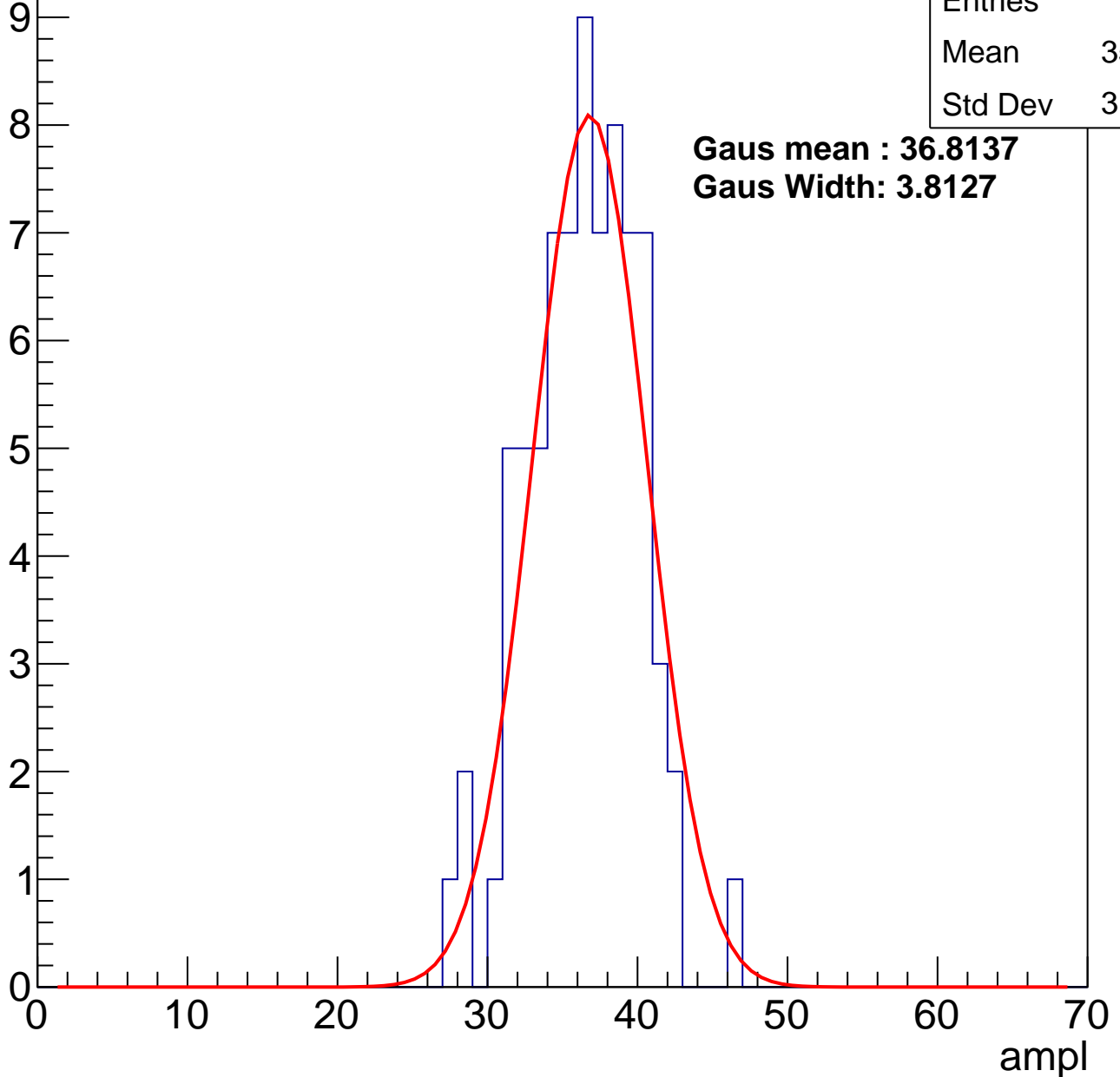
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	35.96
Std Dev	3.587

**Gaus mean : 36.8137**

**Gaus Width: 3.8127**



# B0L000S, U25-ch85, adc2

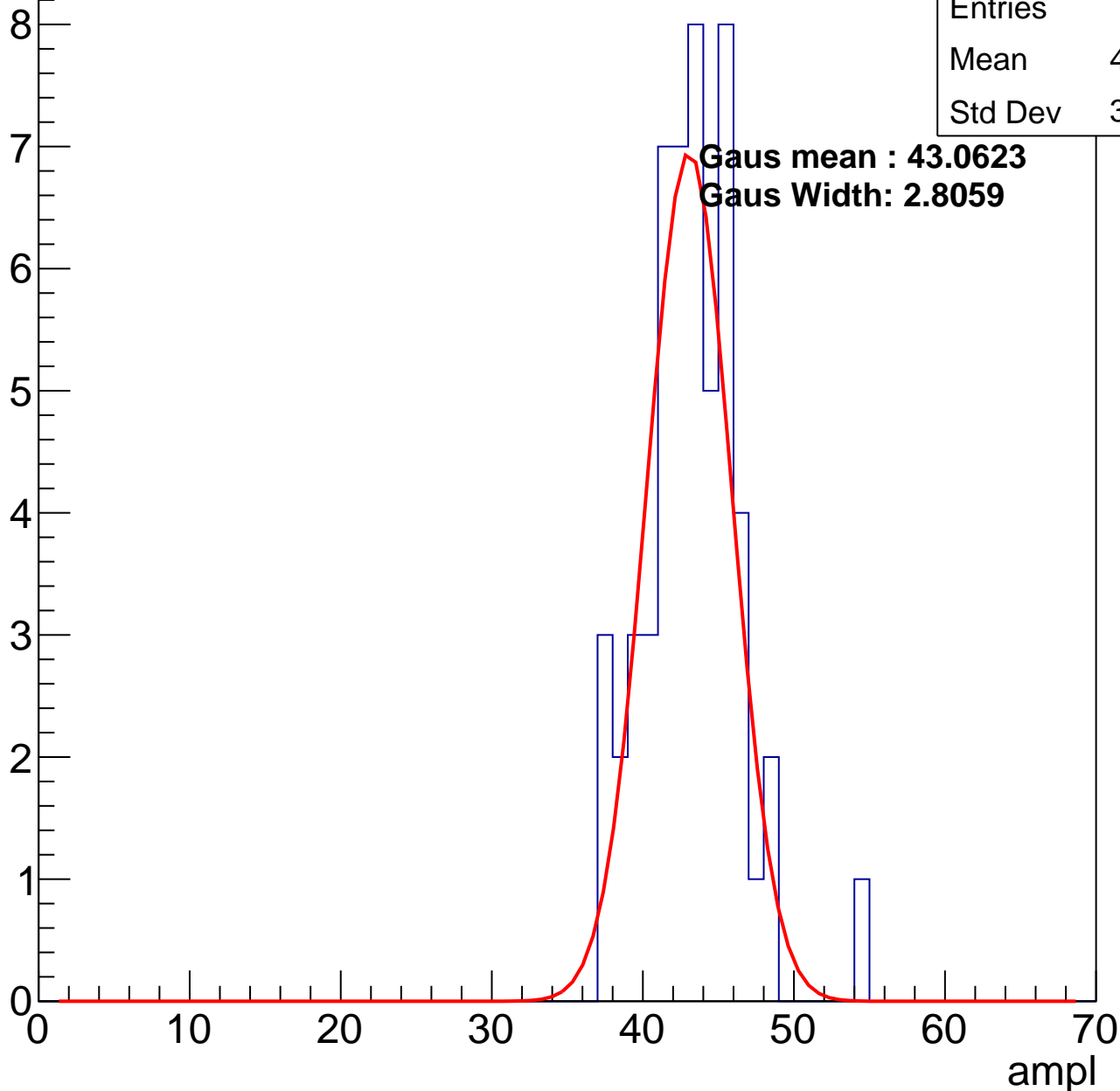
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	42.78
Std Dev	3.119

**Gaus mean : 43.0623**

**Gaus Width: 2.8059**

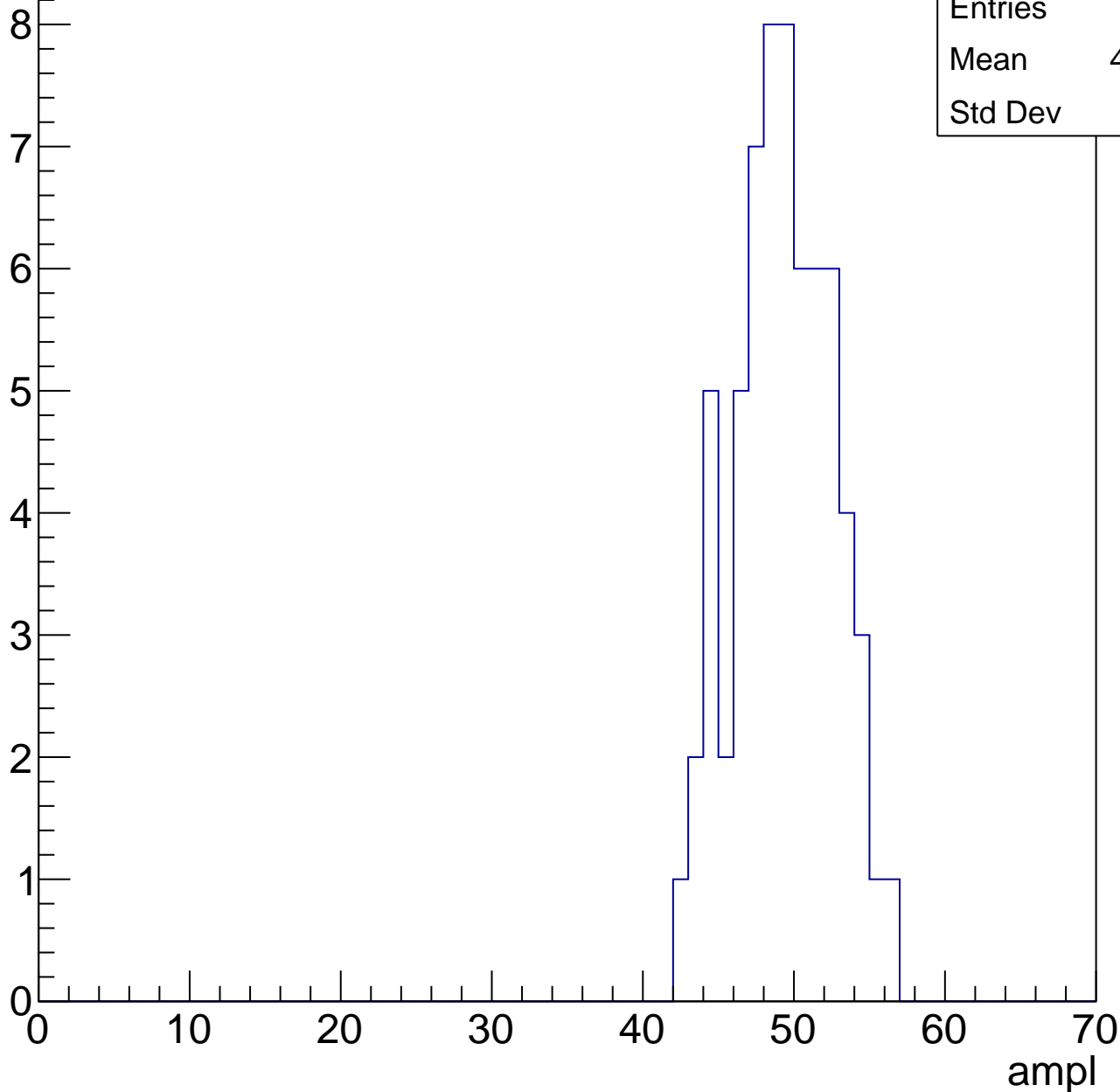


# B0L000S, U25-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

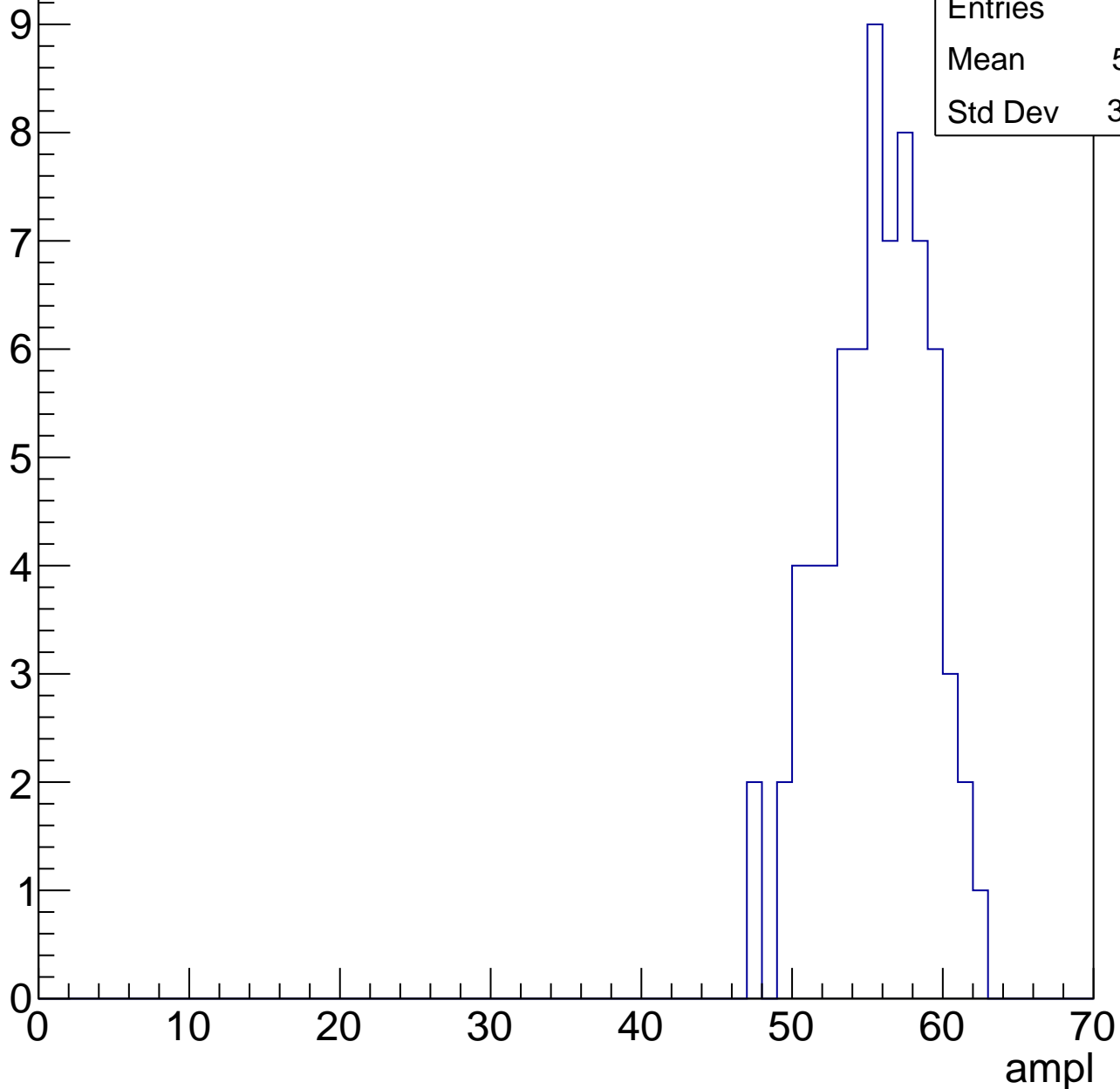
Entries	65
Mean	48.86
Std Dev	3.2



# B0L000S, U25-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

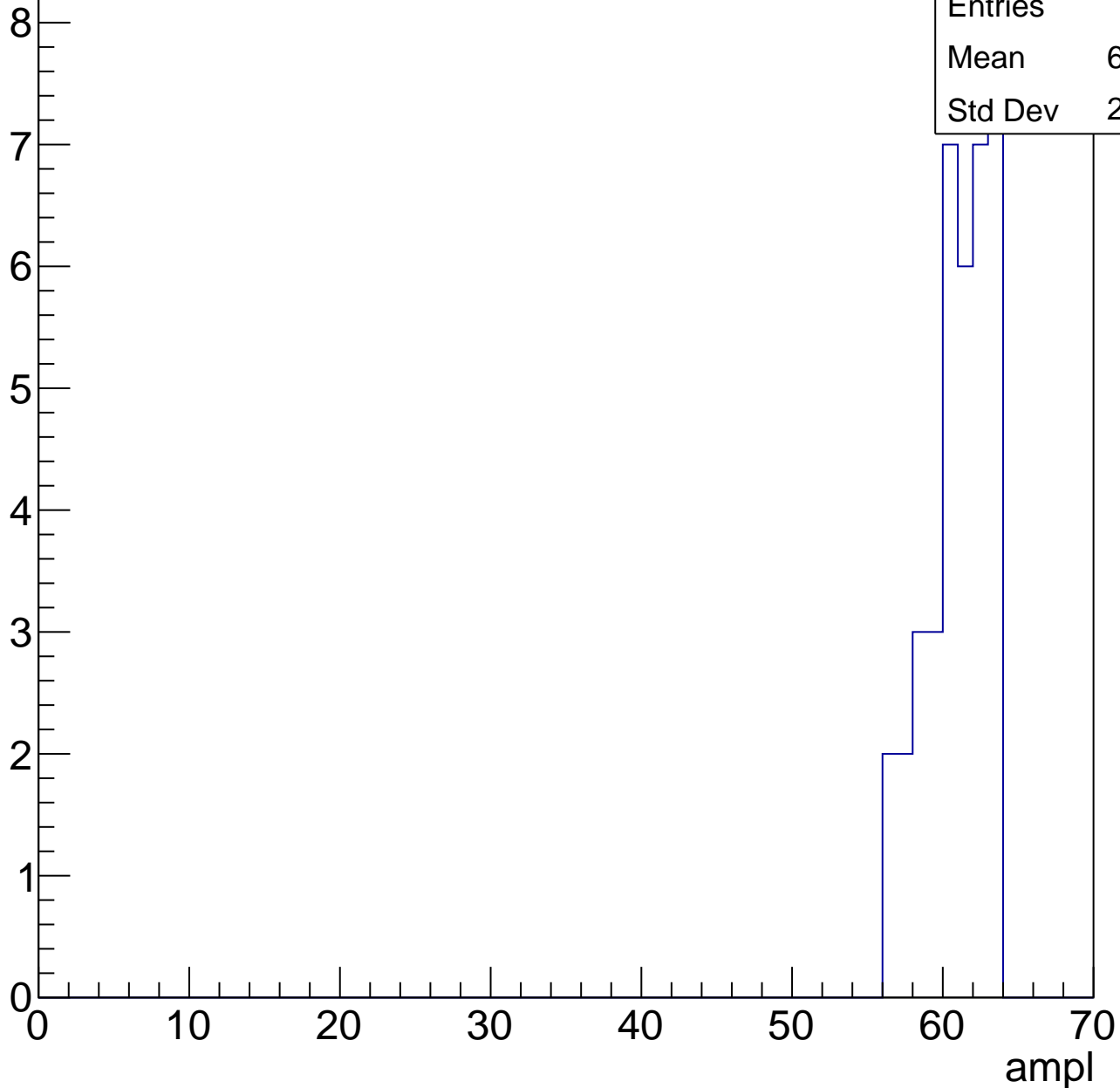


Entries	71
Mean	55.11
Std Dev	3.405

# B0L000S, U25-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	38
Mean	60.55
Std Dev	2.048

# B0L000S, U25-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

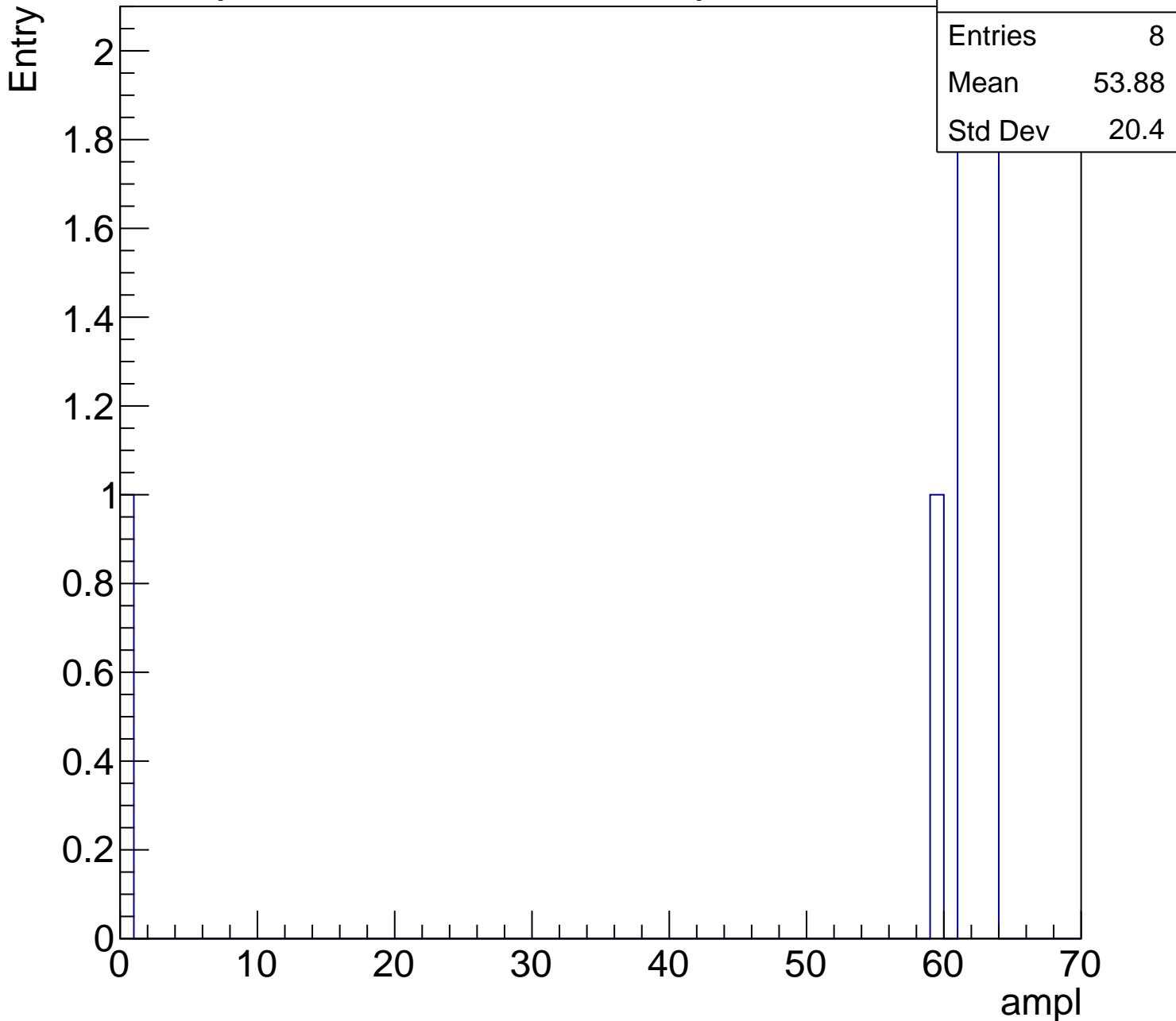
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.88
Std Dev	20.4

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch86, adc0

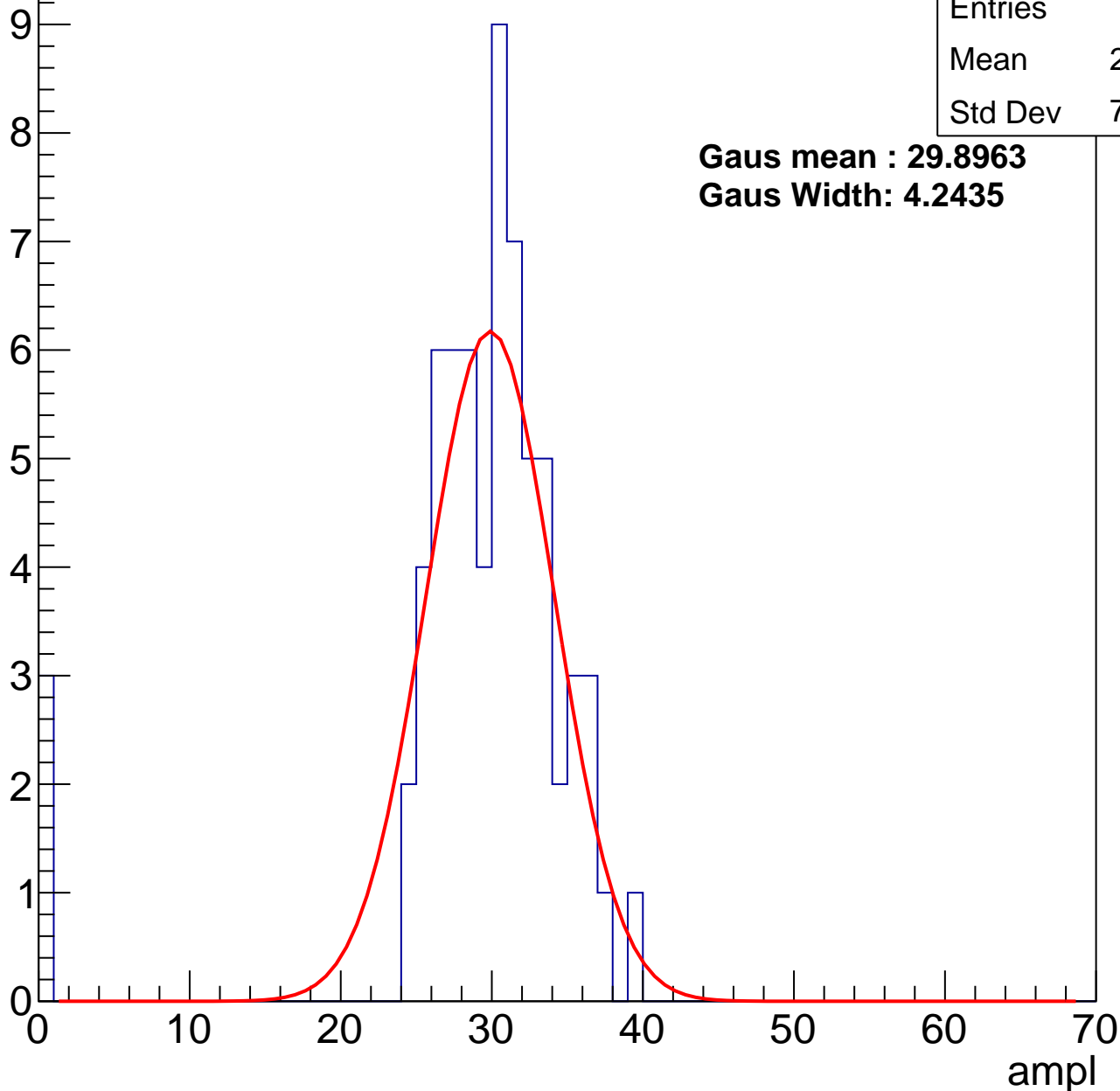
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	28.64
Std Dev	7.064

**Gaus mean : 29.8963**

**Gaus Width: 4.2435**



# B0L000S, U25-ch86, adc1

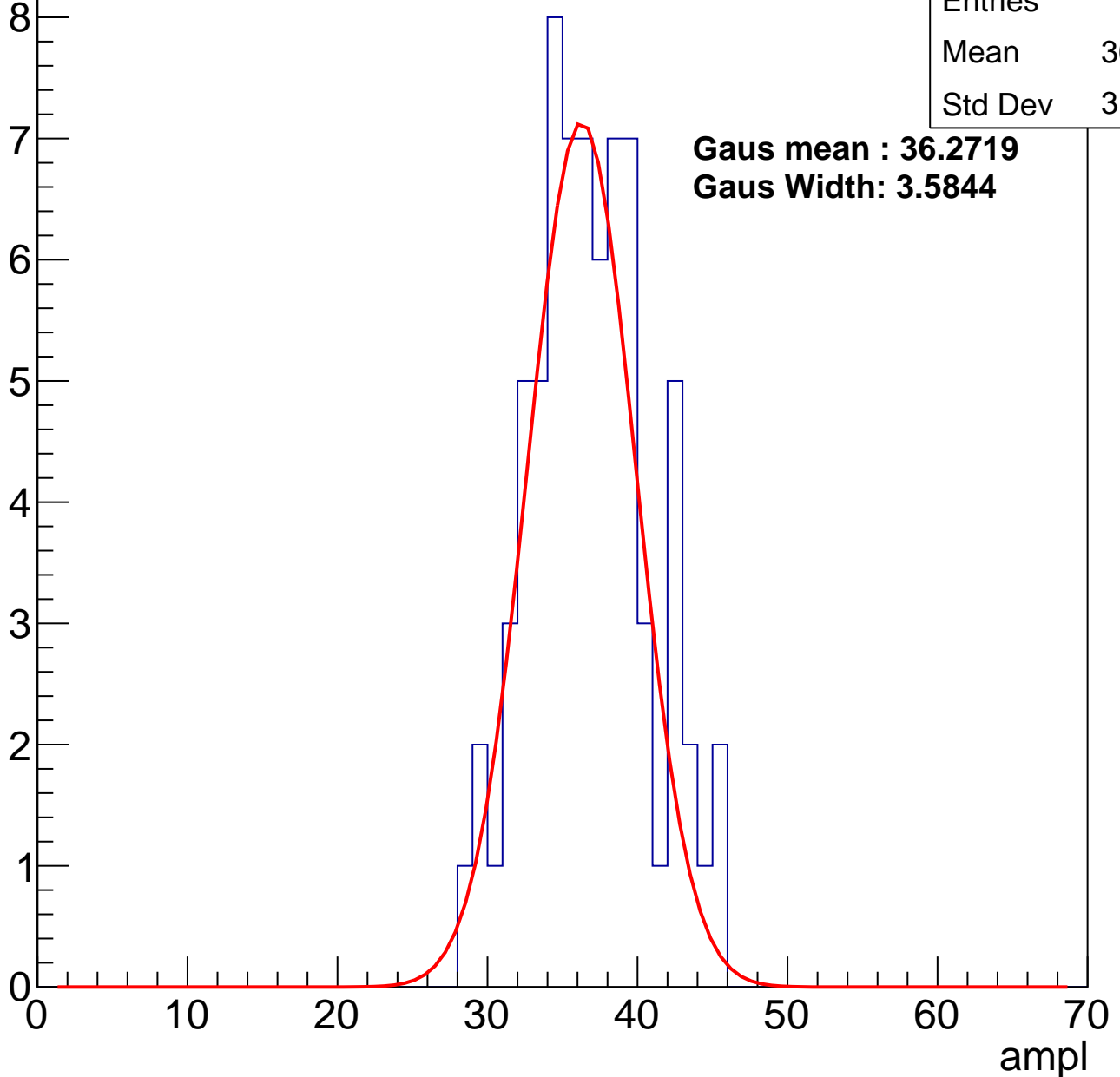
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	36.37
Std Dev	3.884

**Gaus mean : 36.2719**

**Gaus Width: 3.5844**



# B0L000S, U25-ch86, adc2

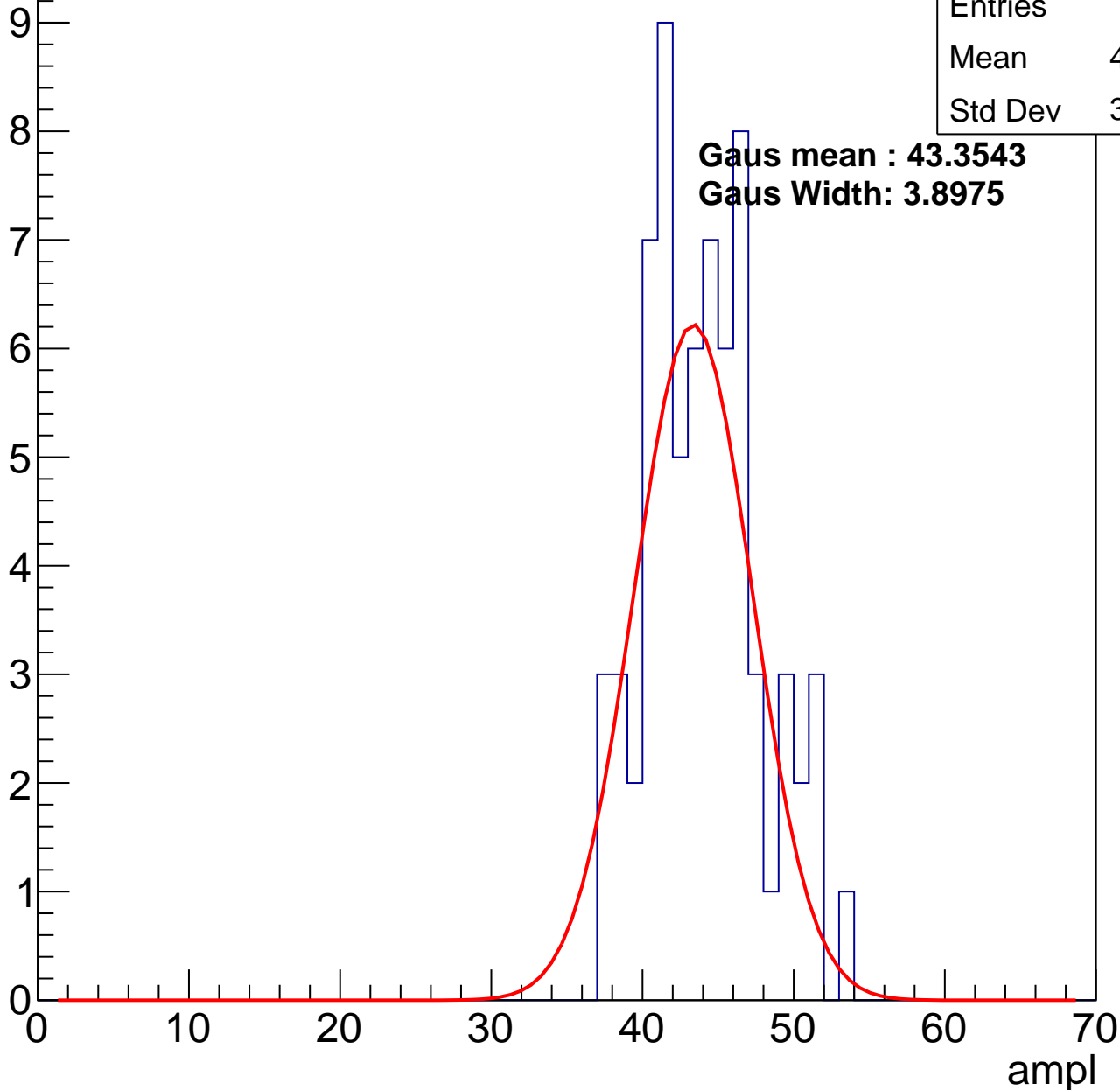
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.59
Std Dev	3.747

**Gaus mean : 43.3543**

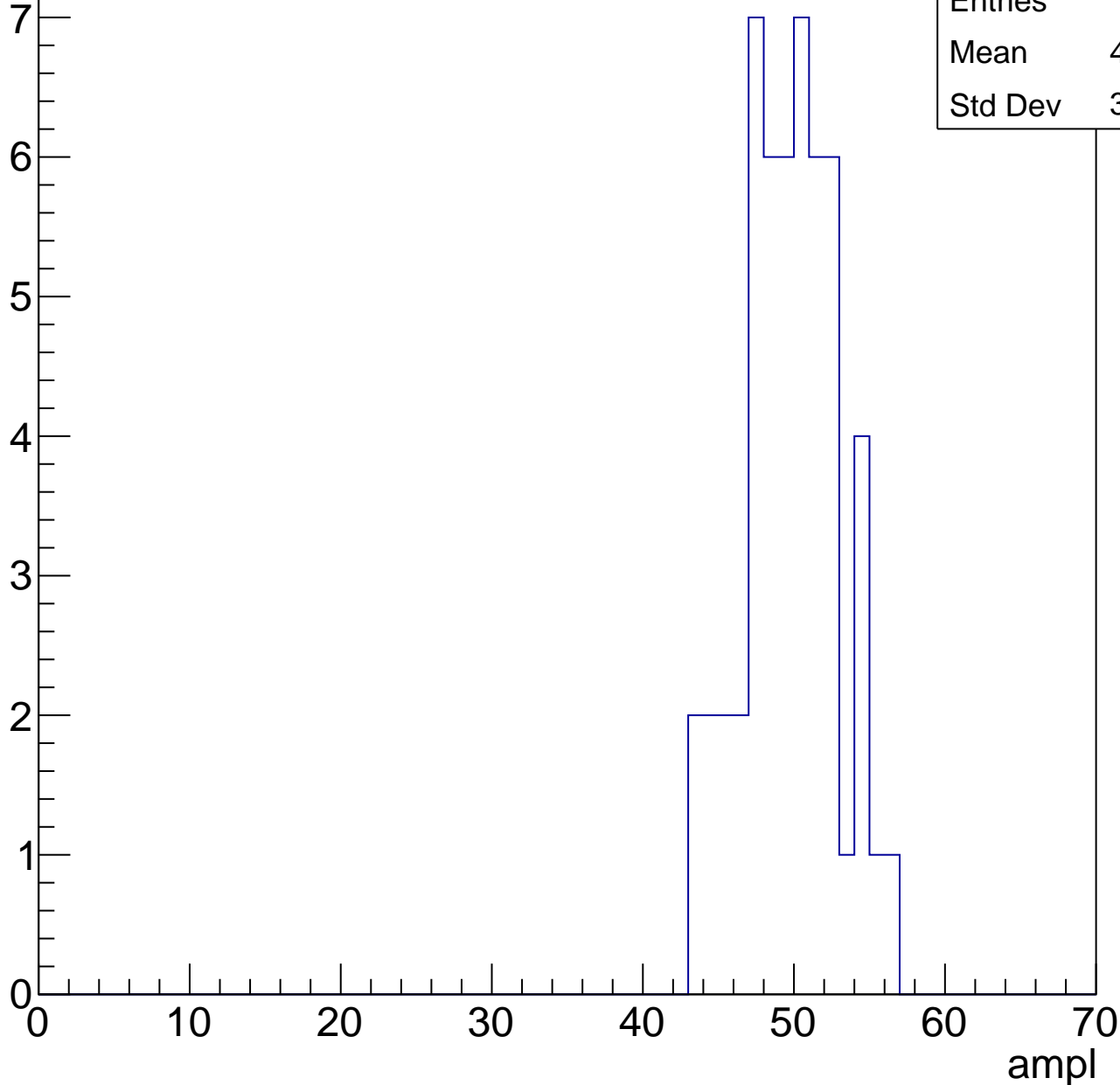
**Gaus Width: 3.8975**



# B0L000S, U25-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



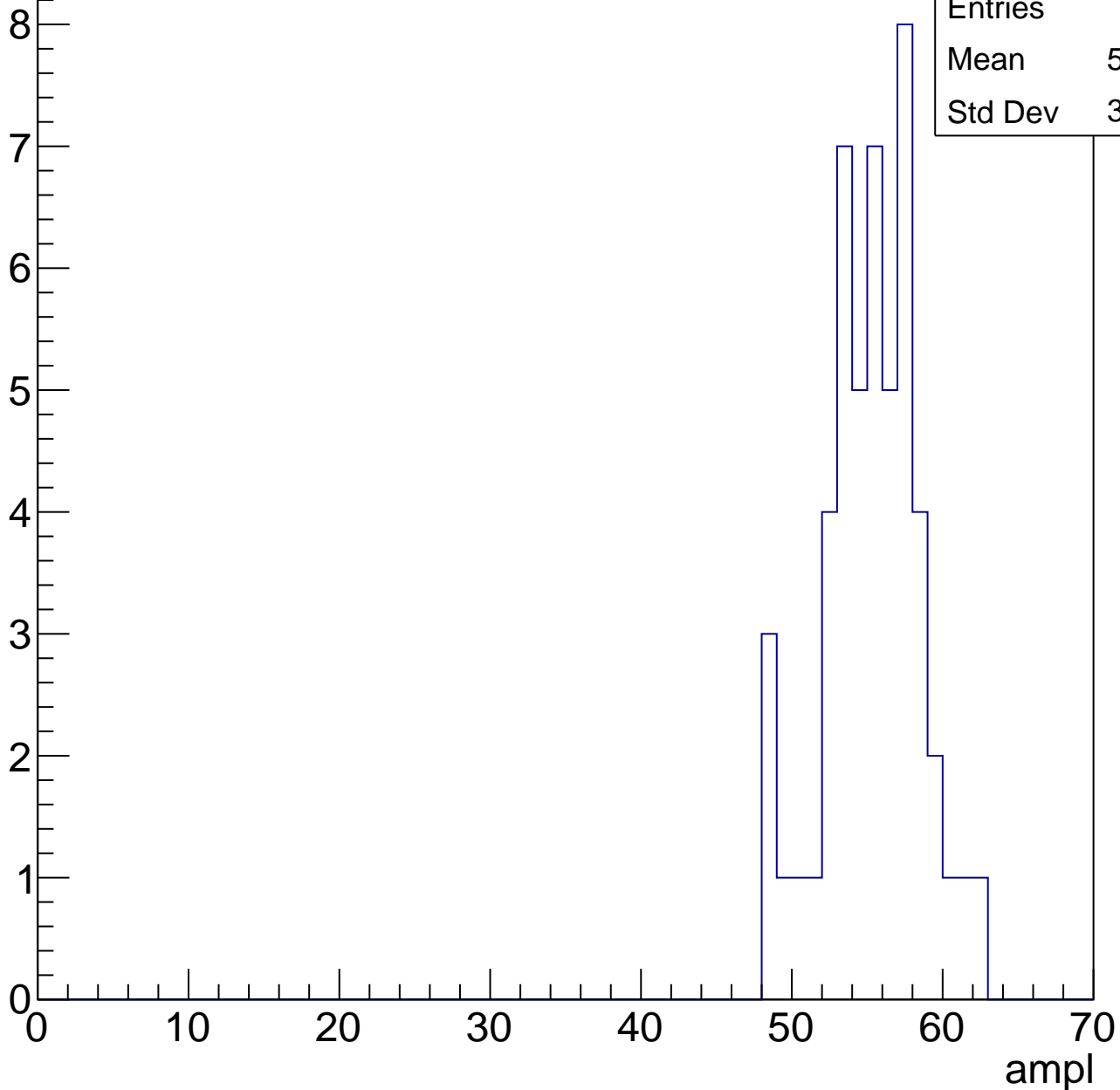
Entries	53
Mean	49.34
Std Dev	3.028

# B0L000S, U25-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	54.84
Std Dev	3.152

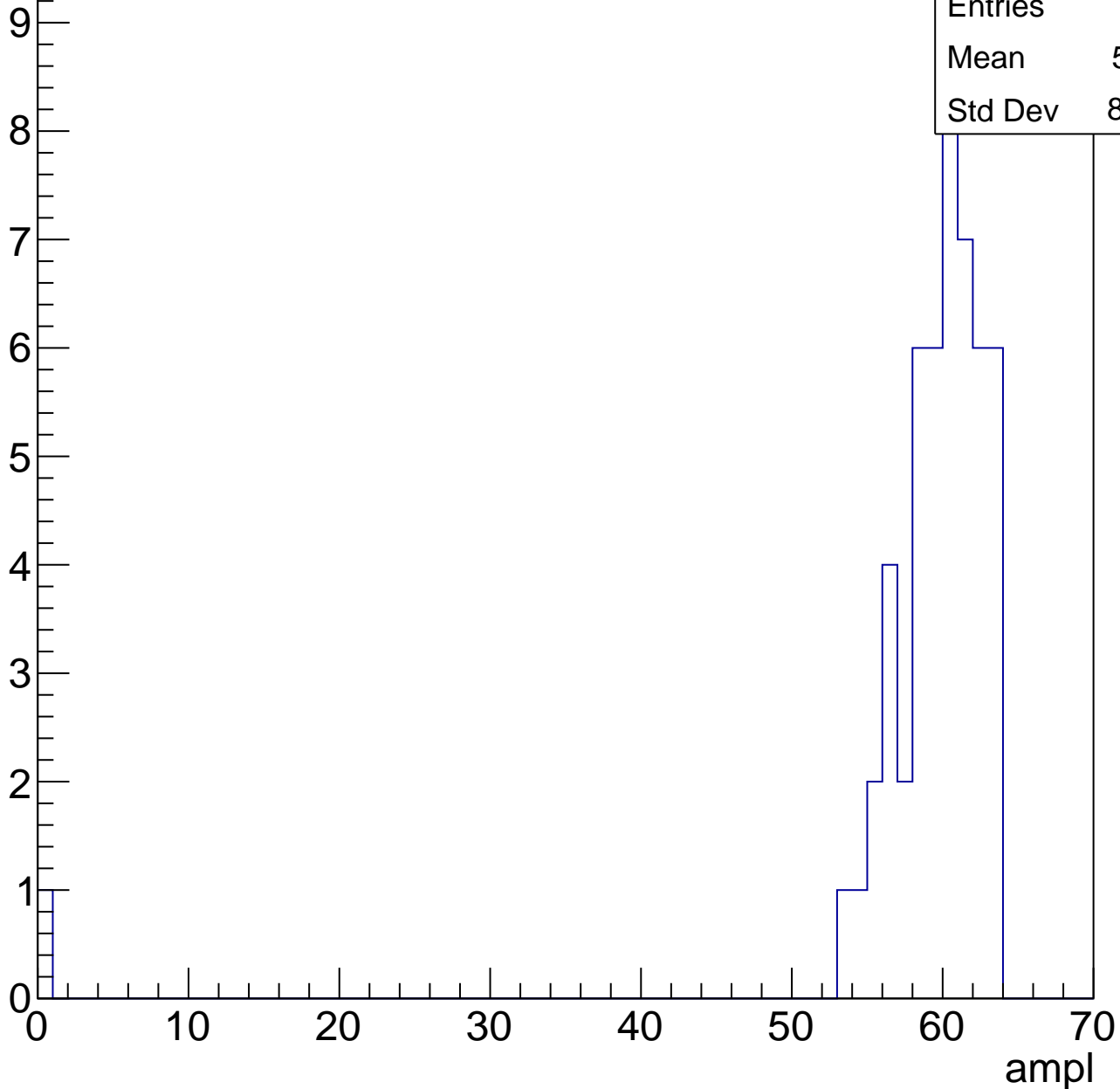


# B0L000S, U25-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

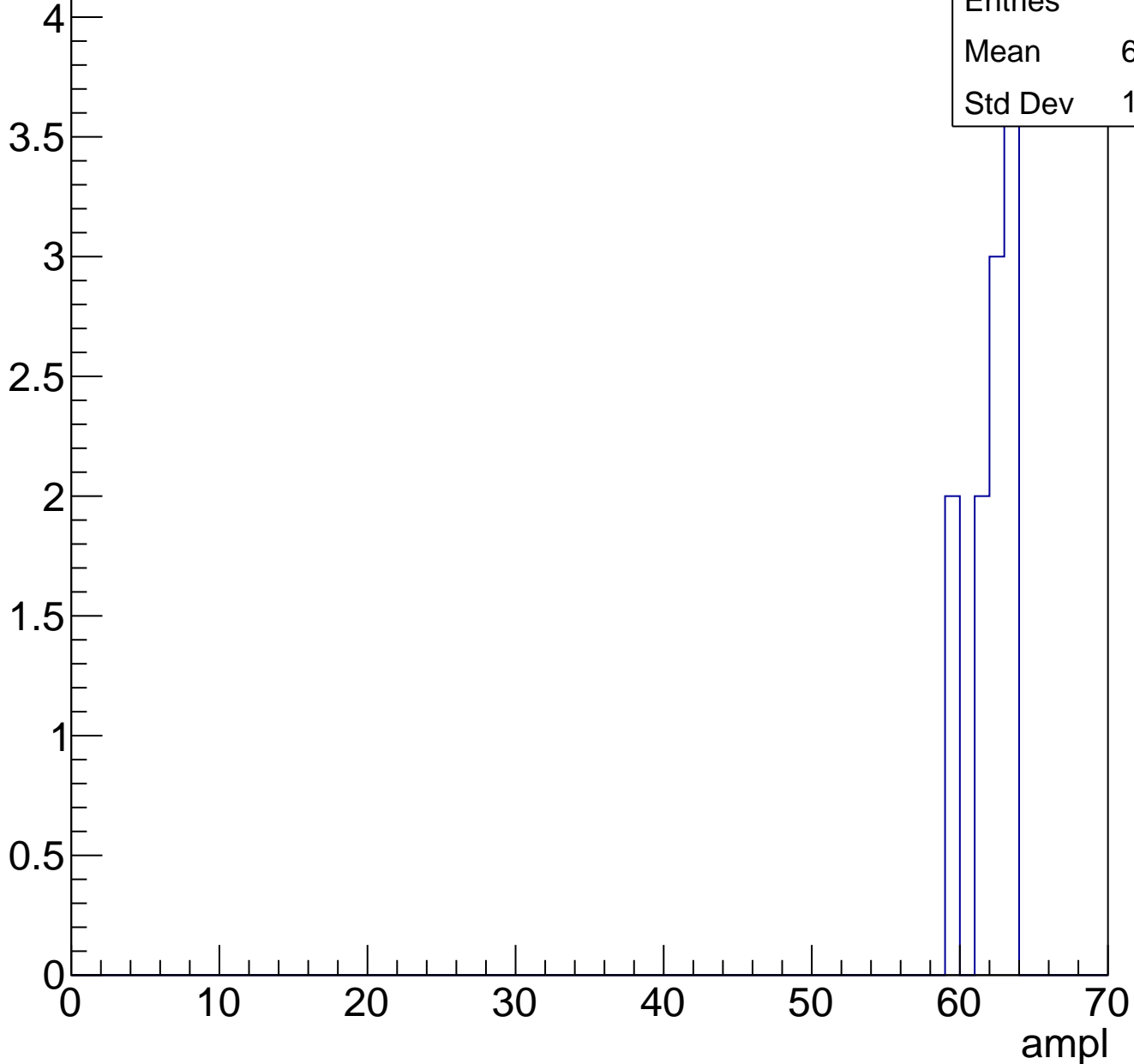
Entries	51
Mean	58.31
Std Dev	8.617



# B0L000S, U25-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch87, adc0

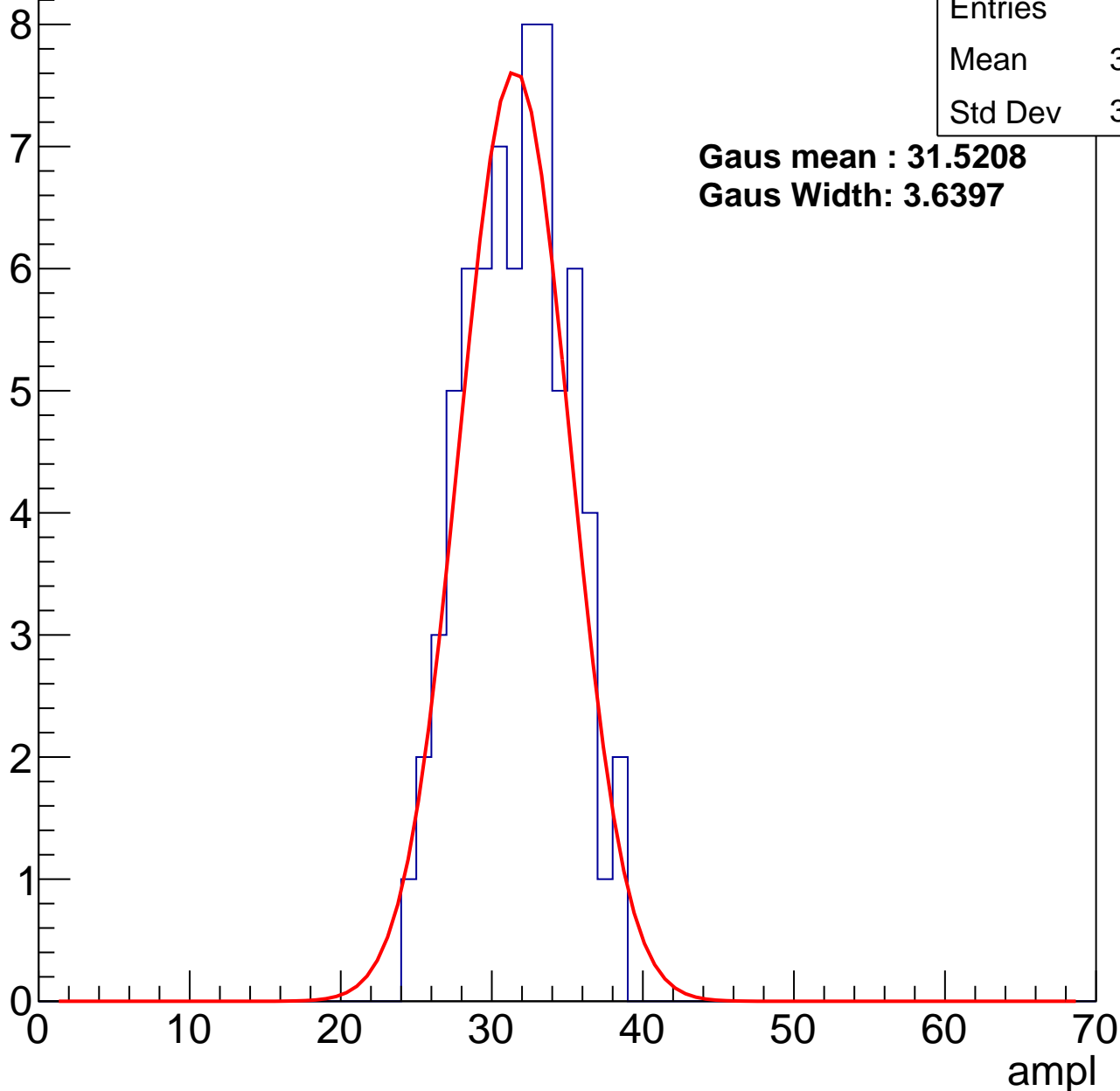
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	31.17
Std Dev	3.325

**Gaus mean : 31.5208**

**Gaus Width: 3.6397**



# B0L000S, U25-ch87, adc1

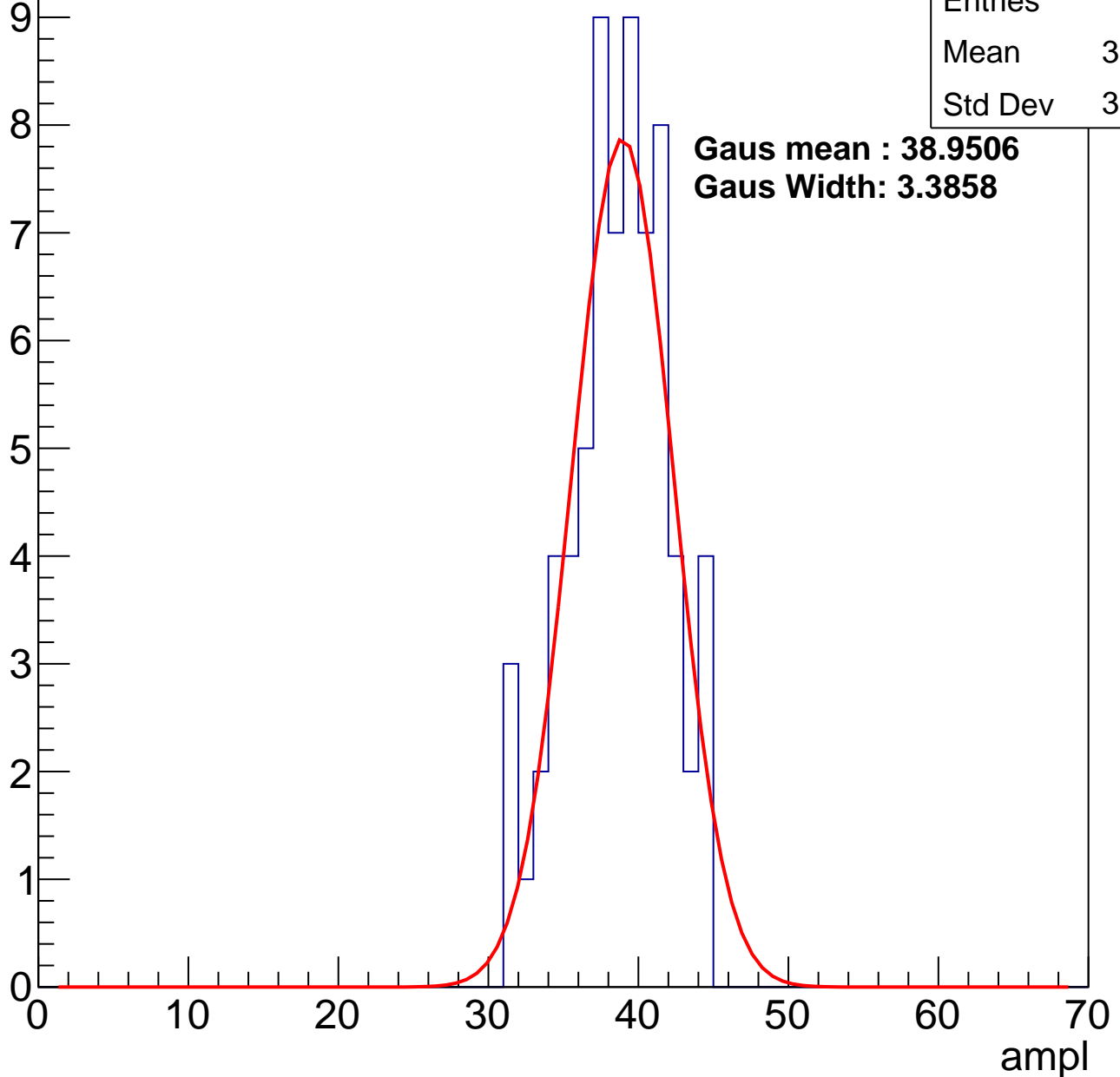
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	38.19
Std Dev	3.245

**Gaus mean : 38.9506**

**Gaus Width: 3.3858**



# B0L000S, U25-ch87, adc2

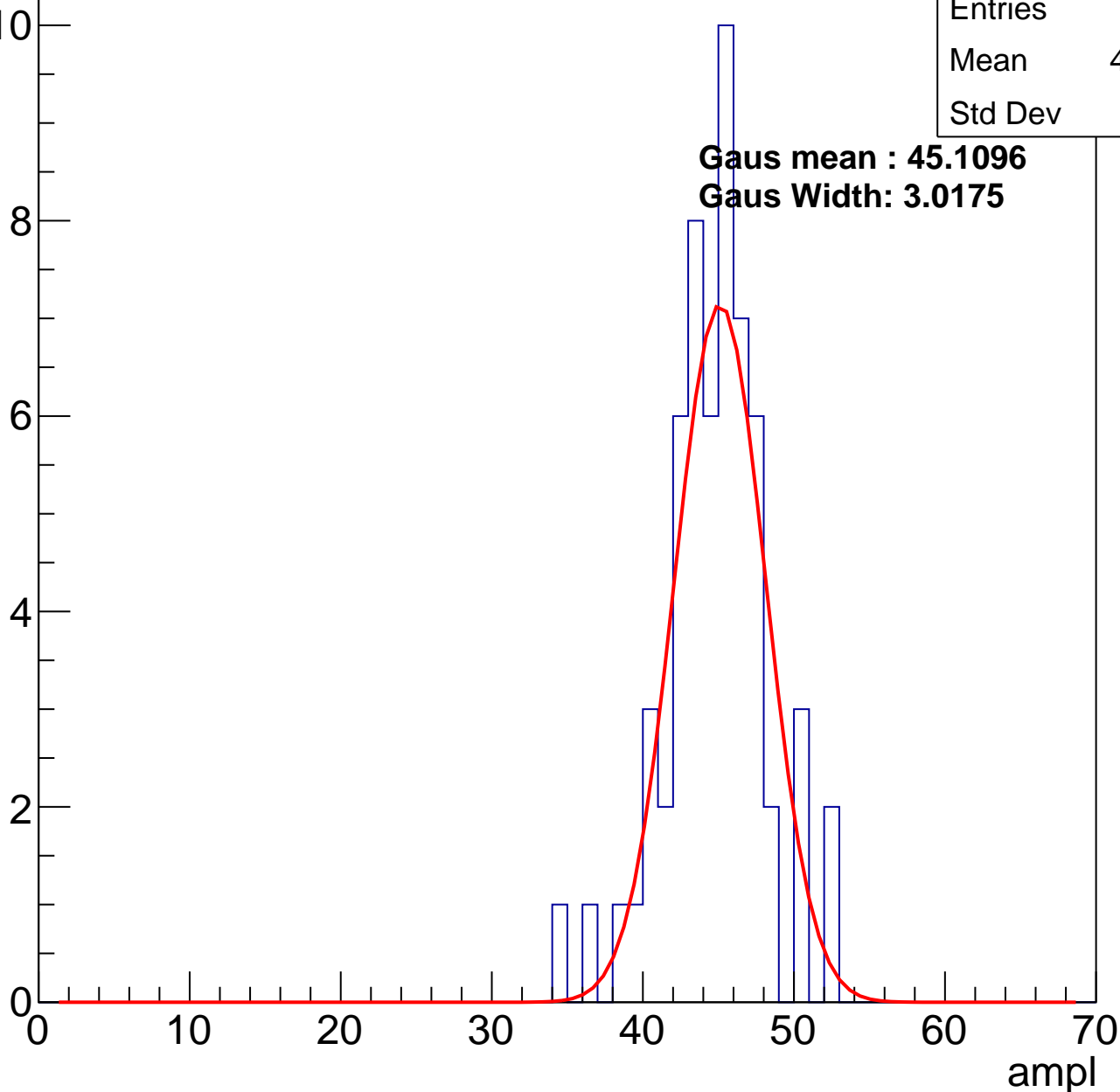
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	44.29
Std Dev	3.39

**Gaus mean : 45.1096**

**Gaus Width: 3.0175**



# B0L000S, U25-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	72
Mean	51.38
Std Dev	3.242

Entry

10

8

6

4

2

0

0

10

20

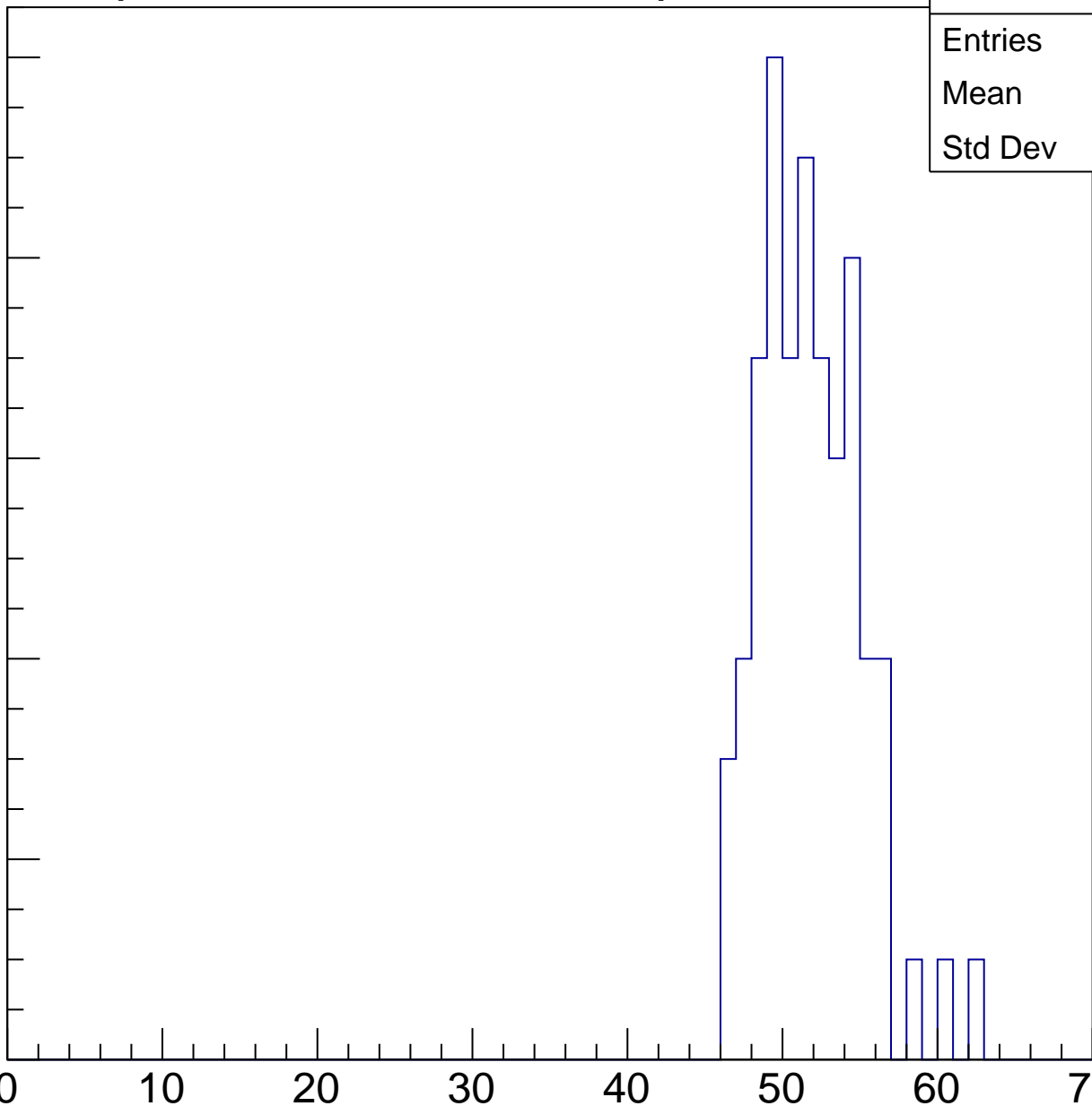
30

40

50

60

ampl

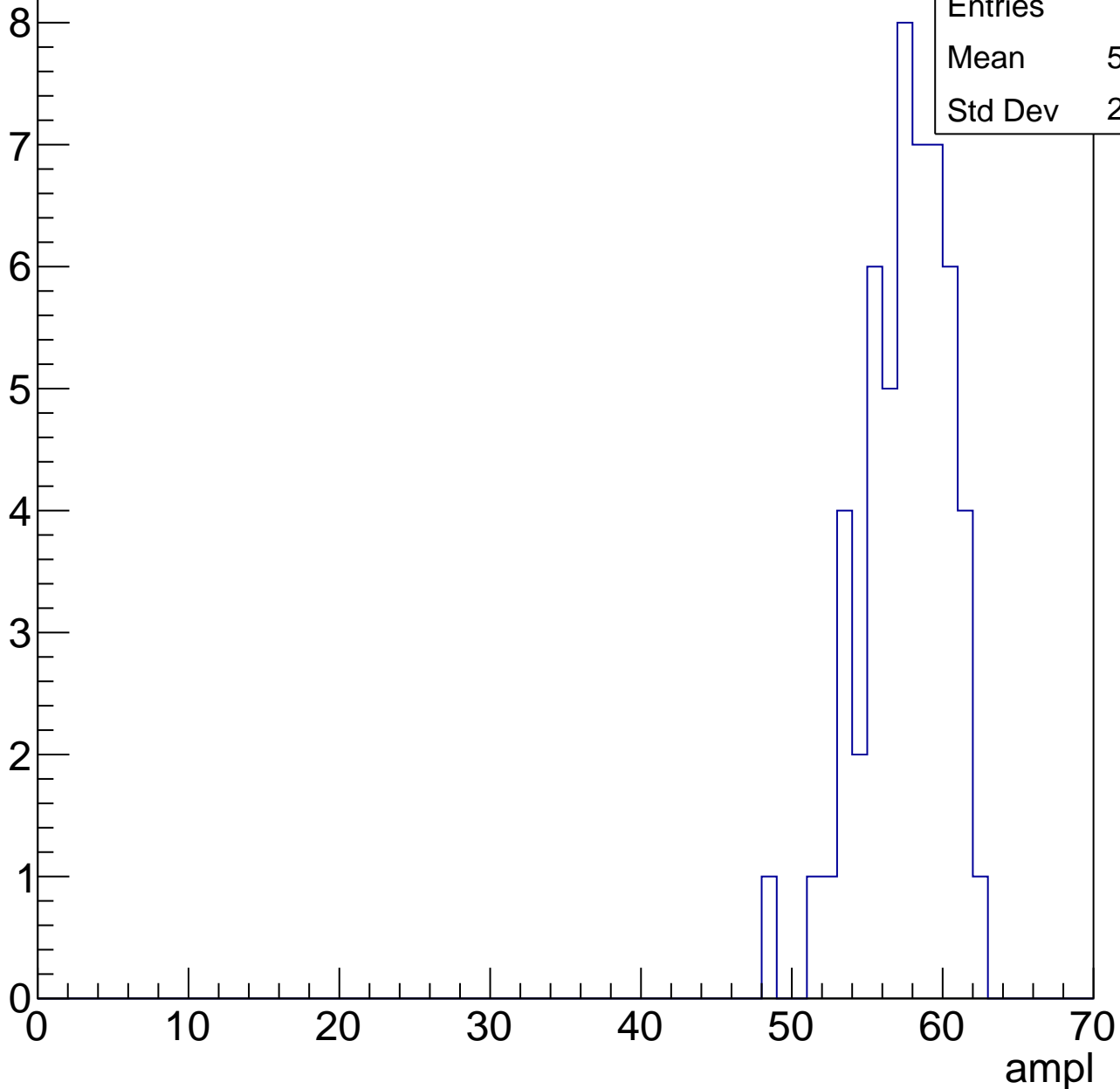


# B0L000S, U25-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	57.02
Std Dev	2.852

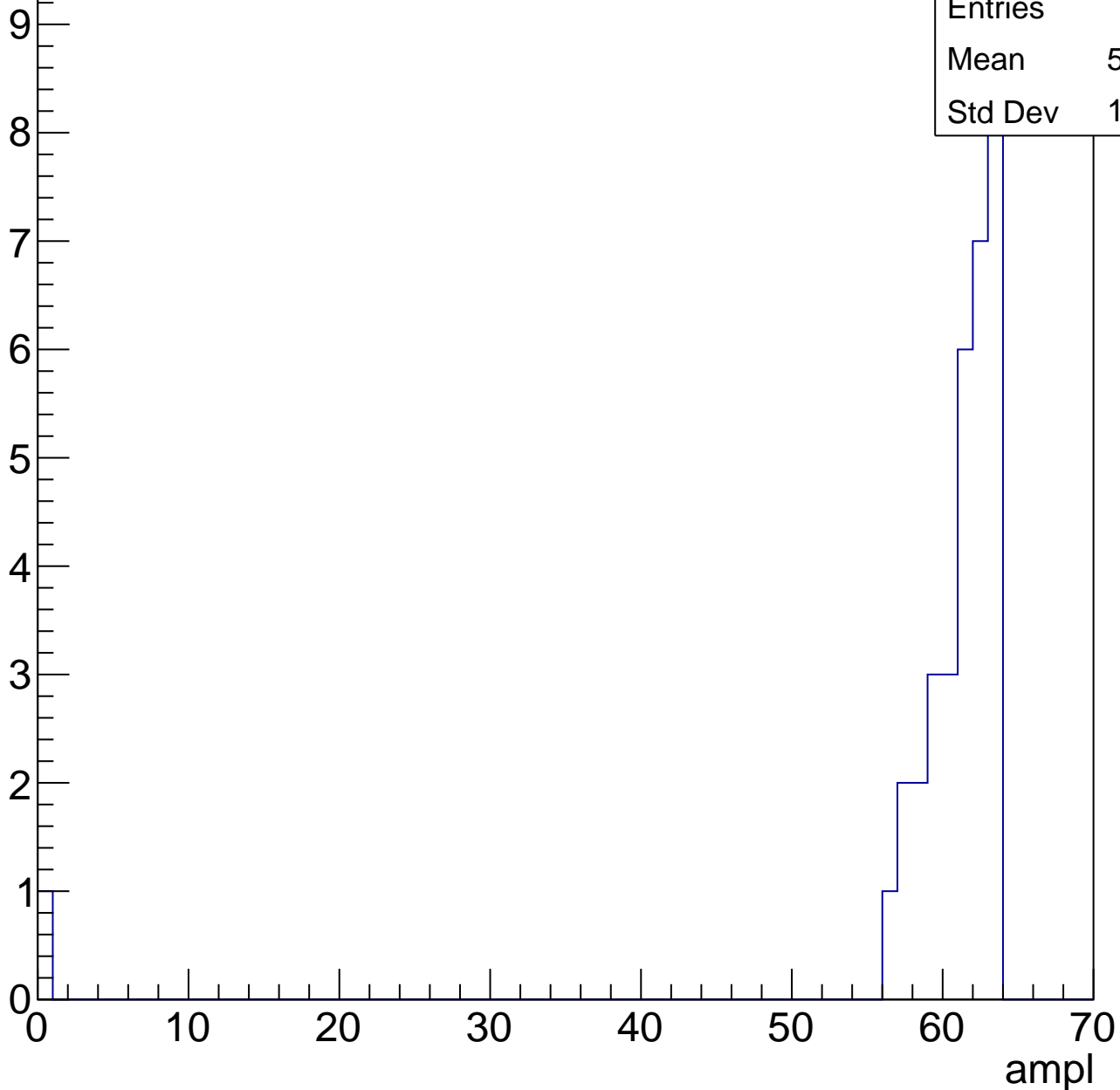


# B0L000S, U25-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	59.12
Std Dev	10.48



# B0L000S, U25-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch88, adc0

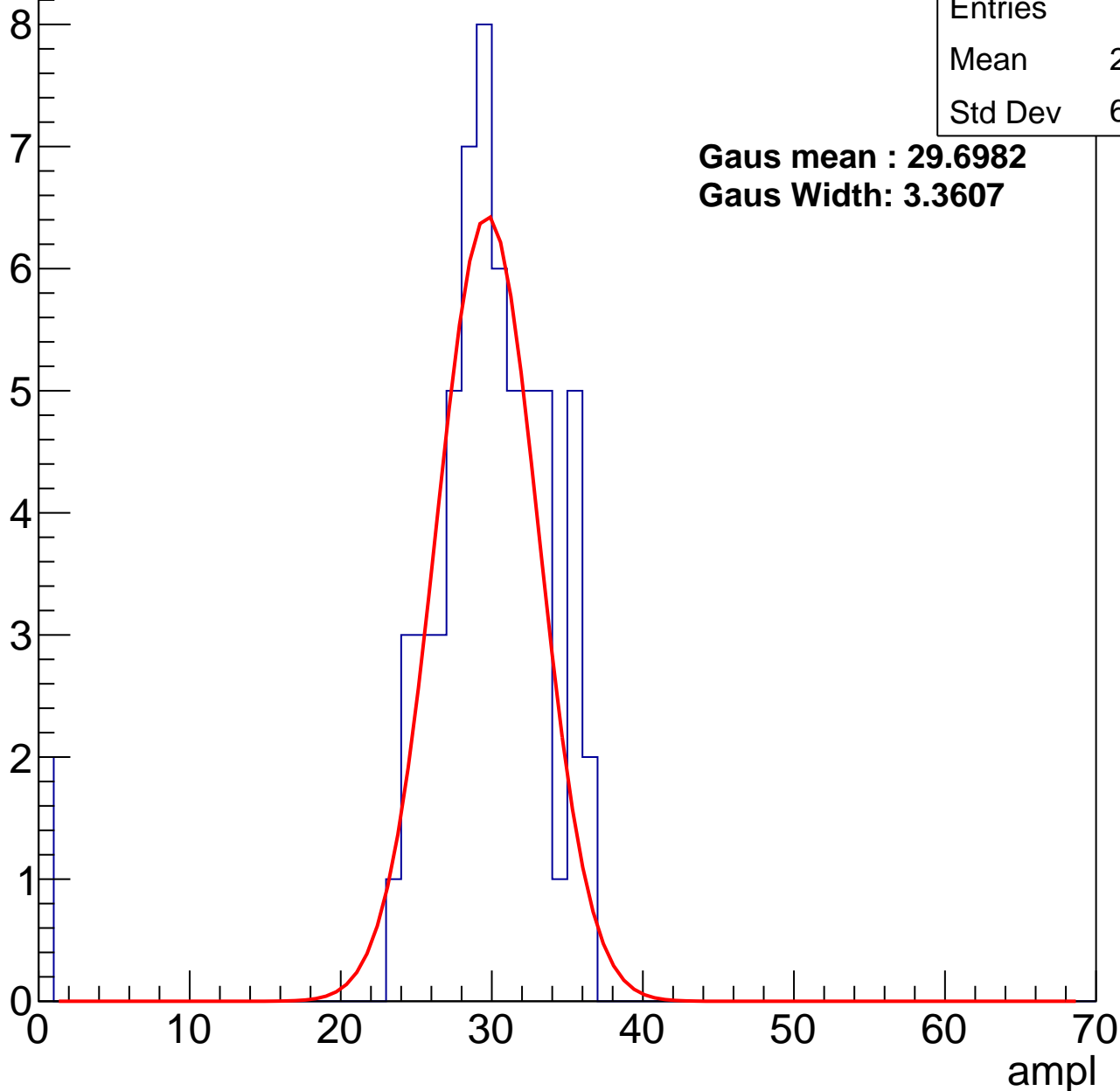
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	28.72
Std Dev	6.199

**Gaus mean : 29.6982**

**Gaus Width: 3.3607**



# B0L000S, U25-ch88, adc1

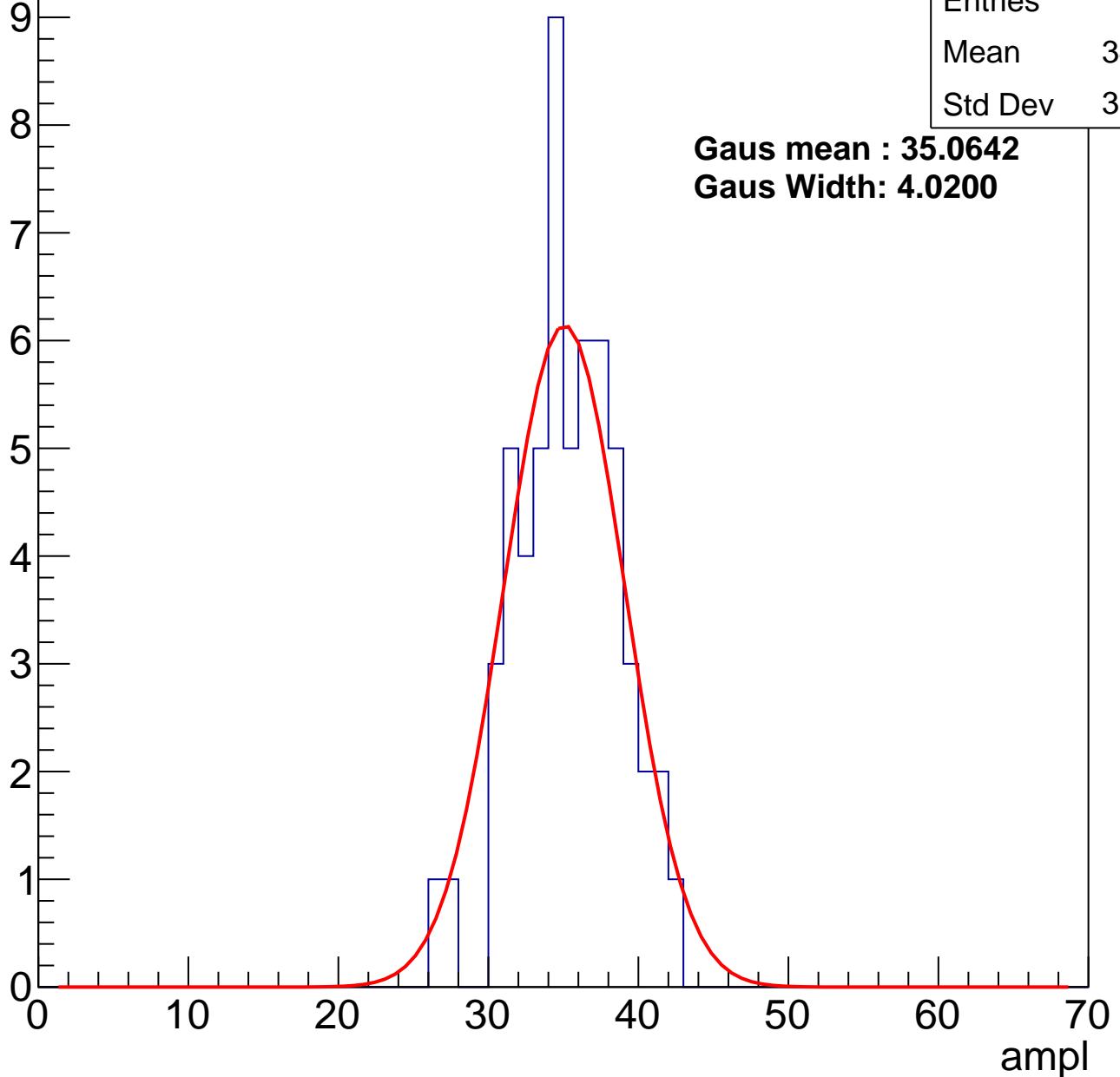
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	34.84
Std Dev	3.367

**Gaus mean : 35.0642**

**Gaus Width: 4.0200**



# B0L000S, U25-ch88, adc2

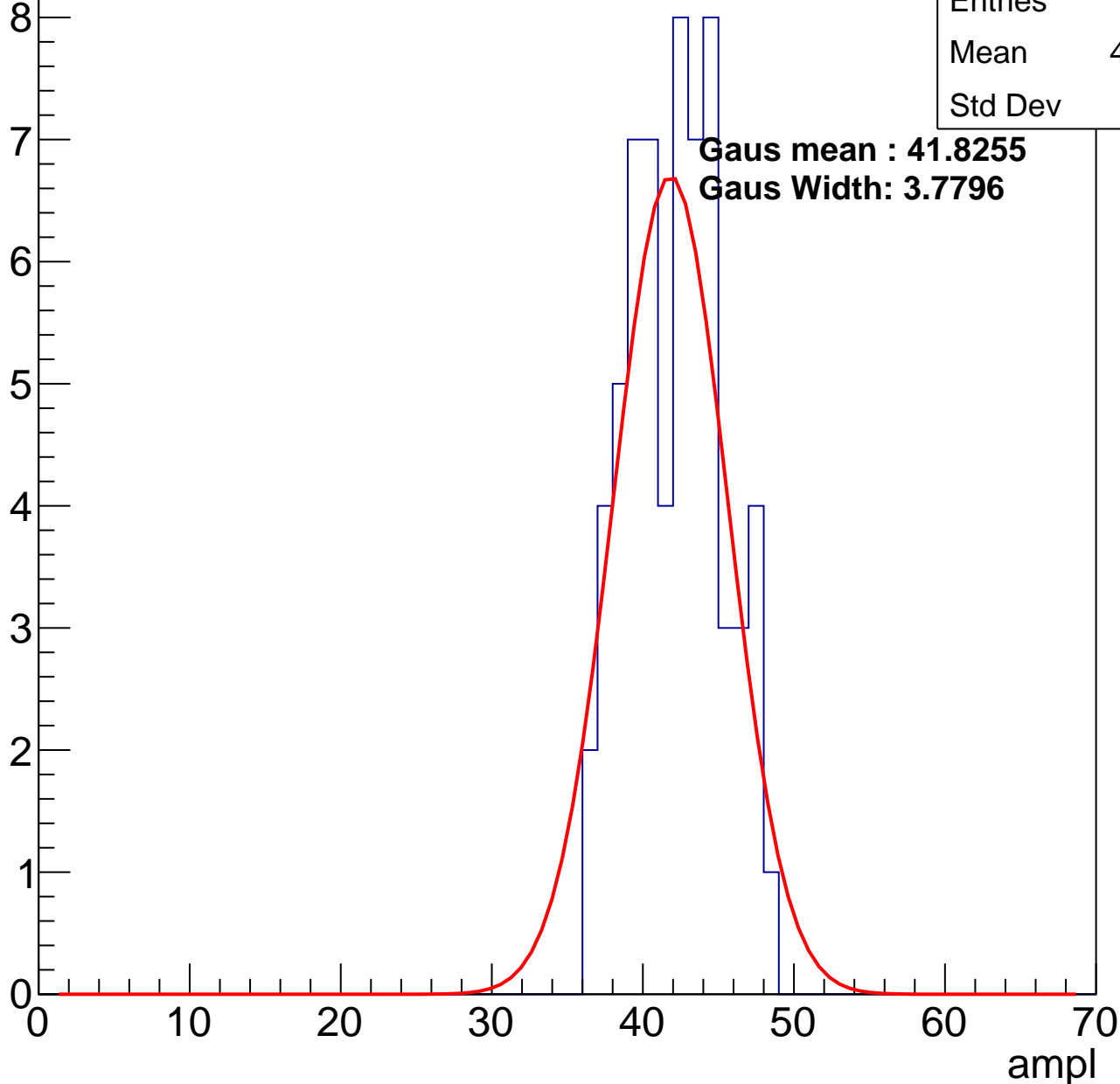
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	41.67
Std Dev	3.06

**Gaus mean : 41.8255**

**Gaus Width: 3.7796**

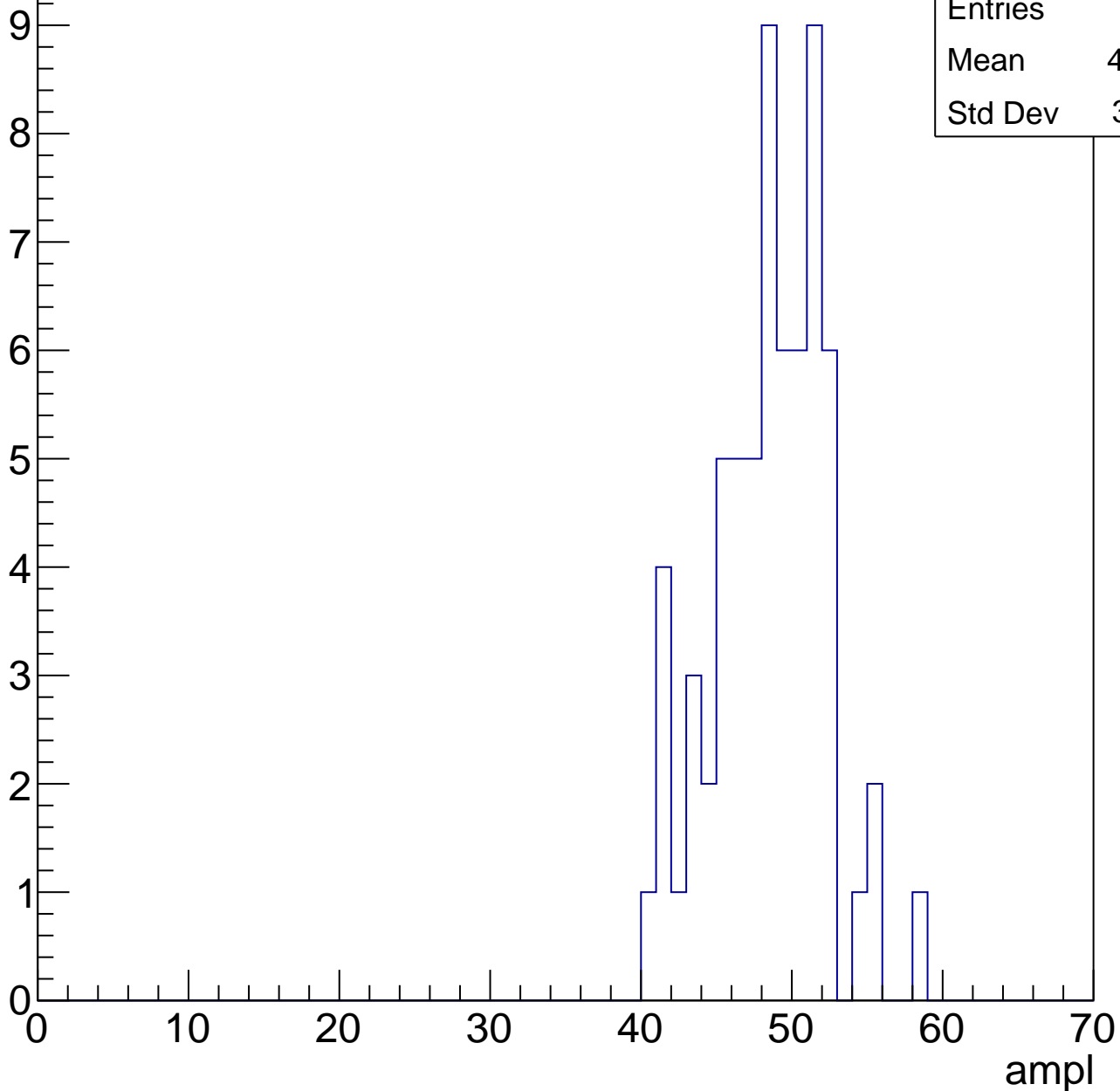


# B0L000S, U25-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	48.06
Std Dev	3.721

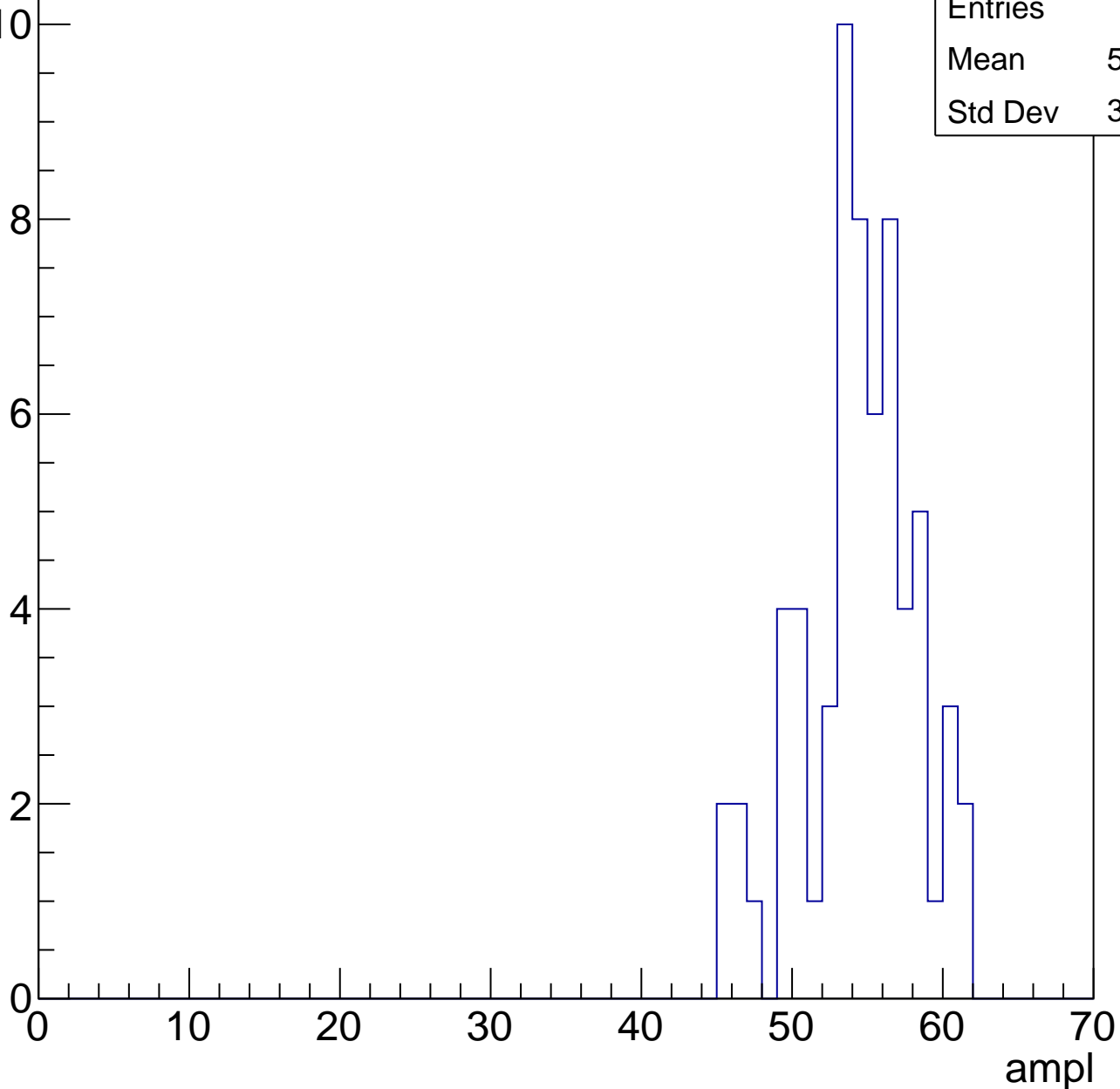


# B0L000S, U25-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

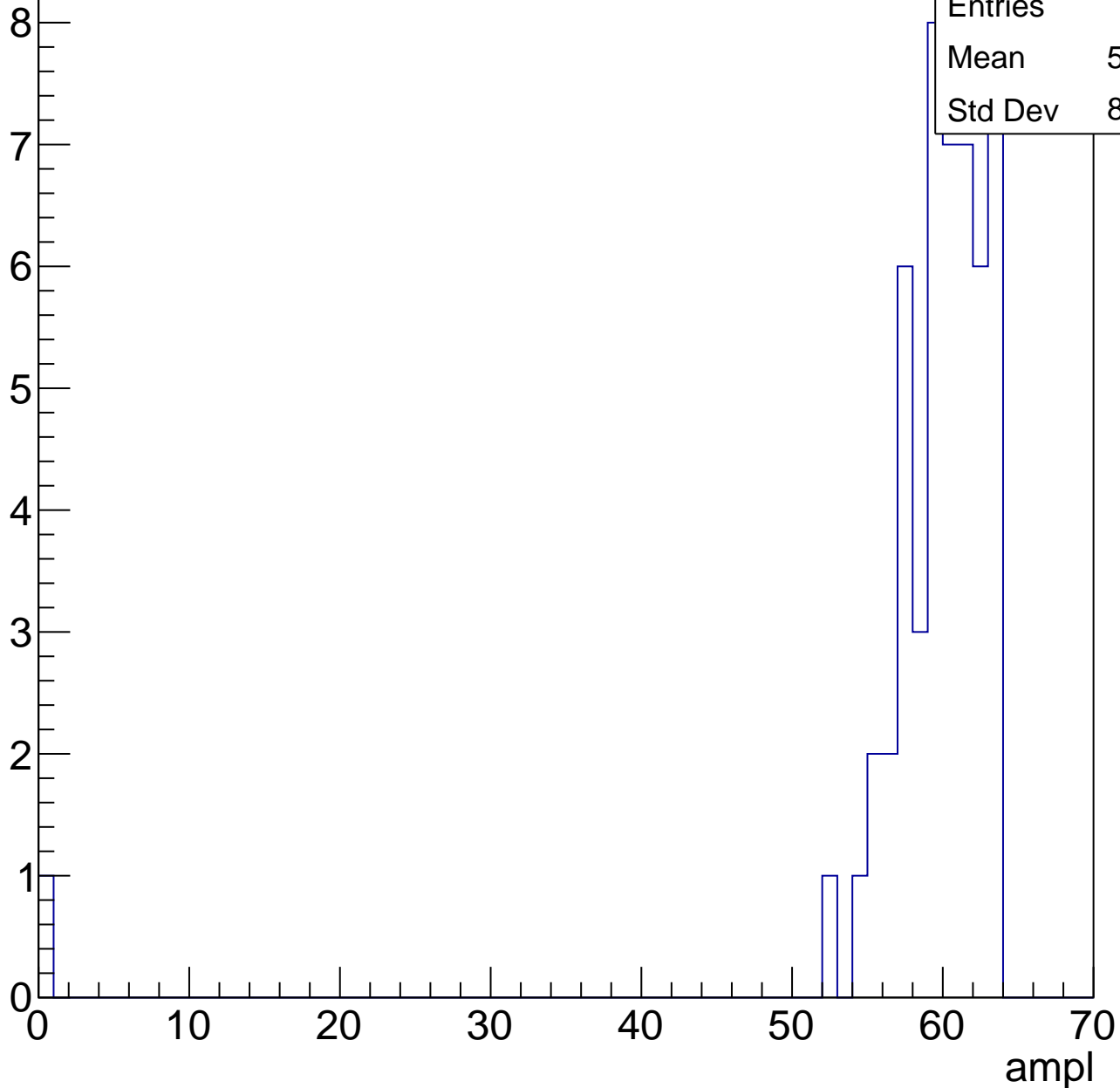
Entries	64
Mean	53.92
Std Dev	3.789



# B0L000S, U25-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

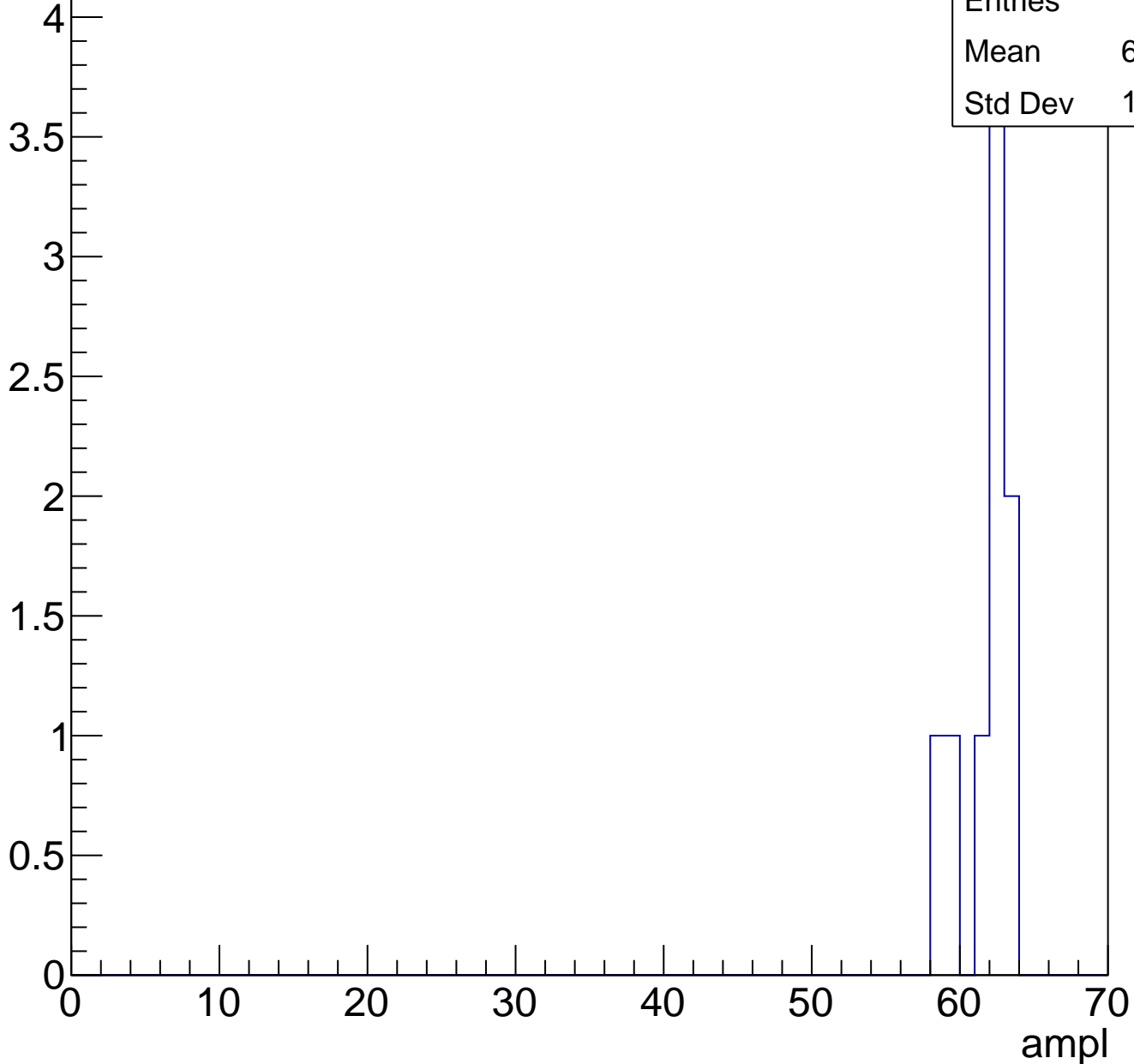
Entry



# B0L000S, U25-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch89, adc0

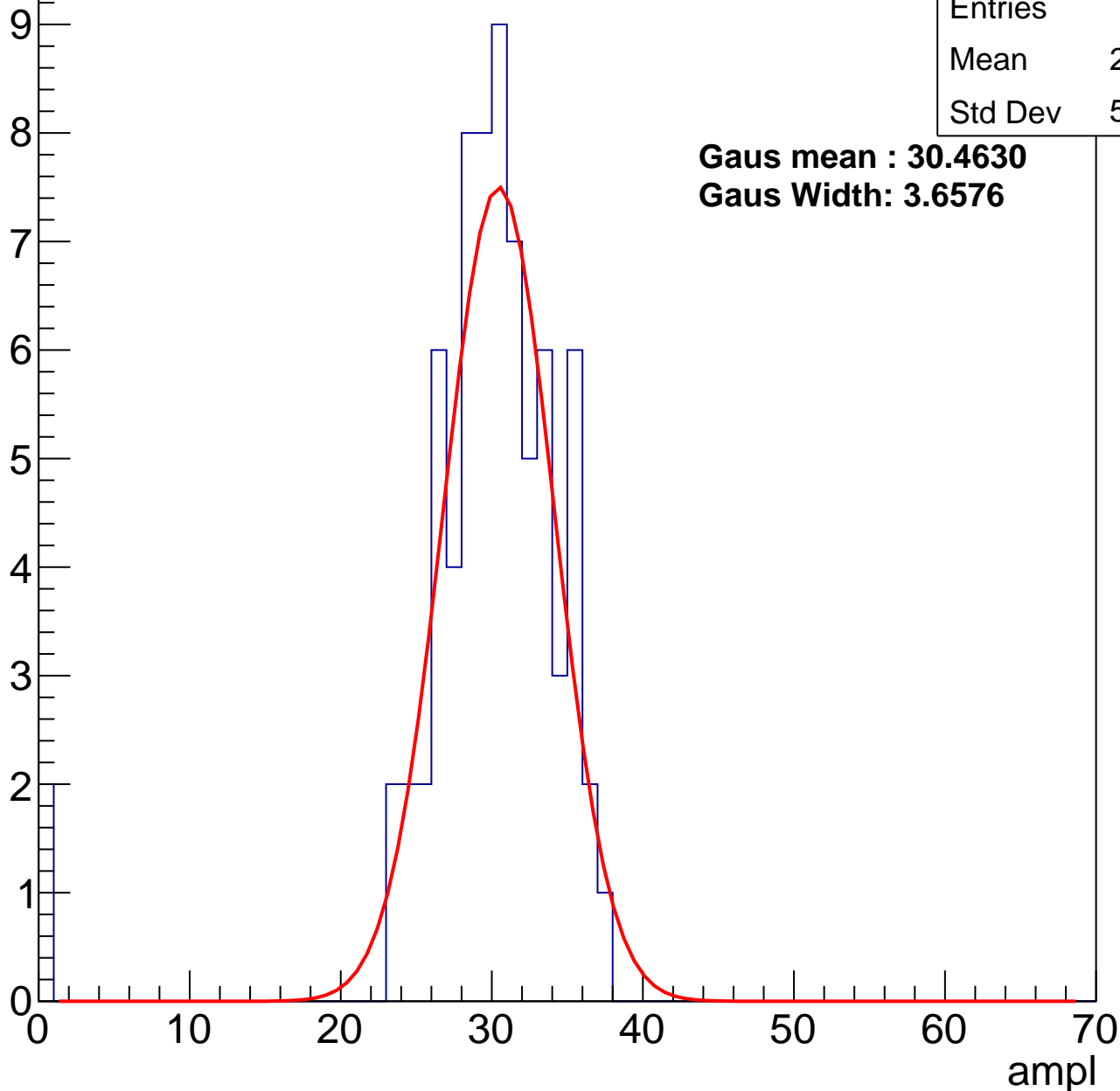
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	29.18
Std Dev	5.902

**Gaus mean : 30.4630**

**Gaus Width: 3.6576**



# B0L000S, U25-ch89, adc1

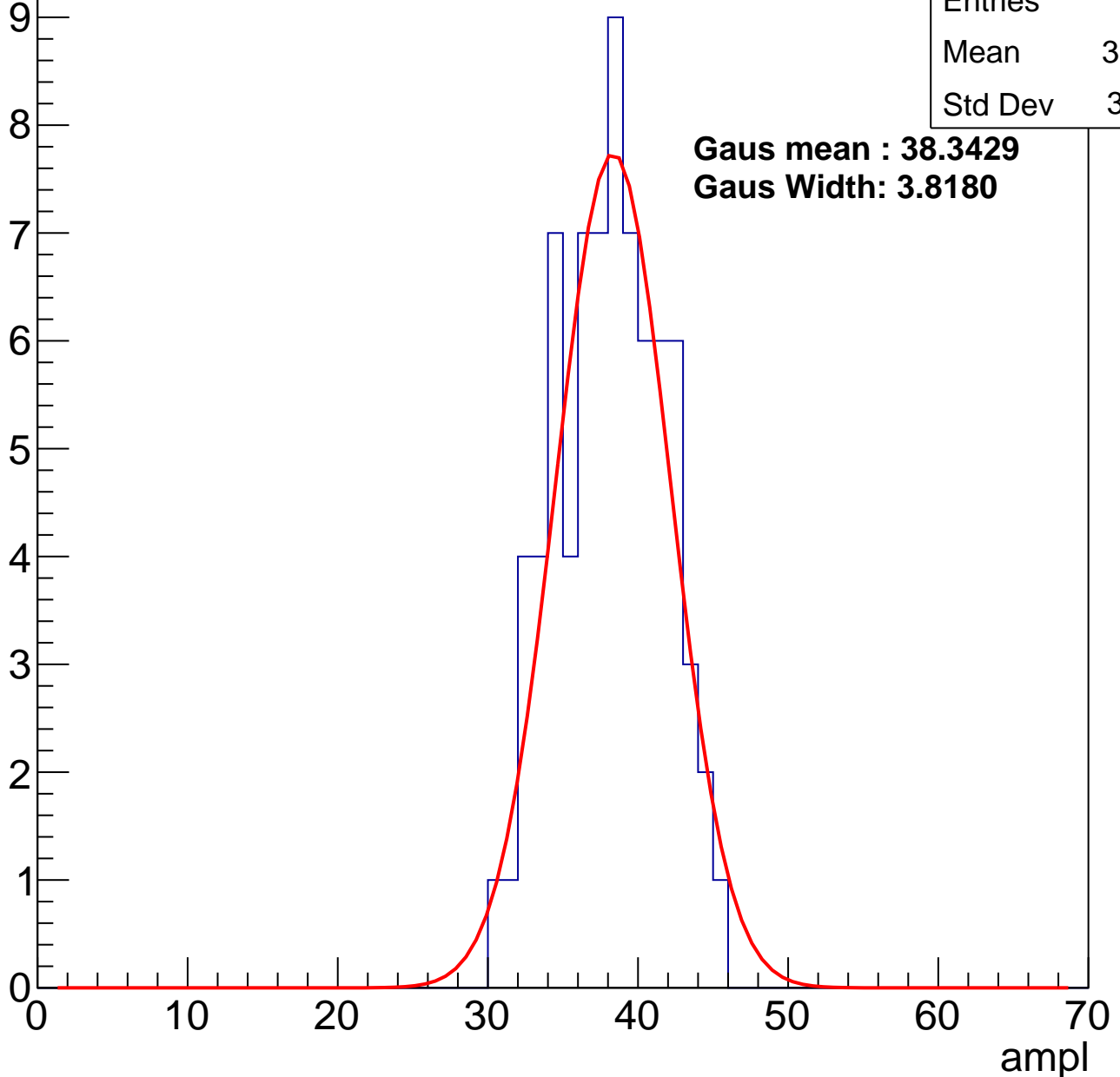
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	37.67
Std Dev	3.481

**Gaus mean : 38.3429**

**Gaus Width: 3.8180**



# B0L000S, U25-ch89, adc2

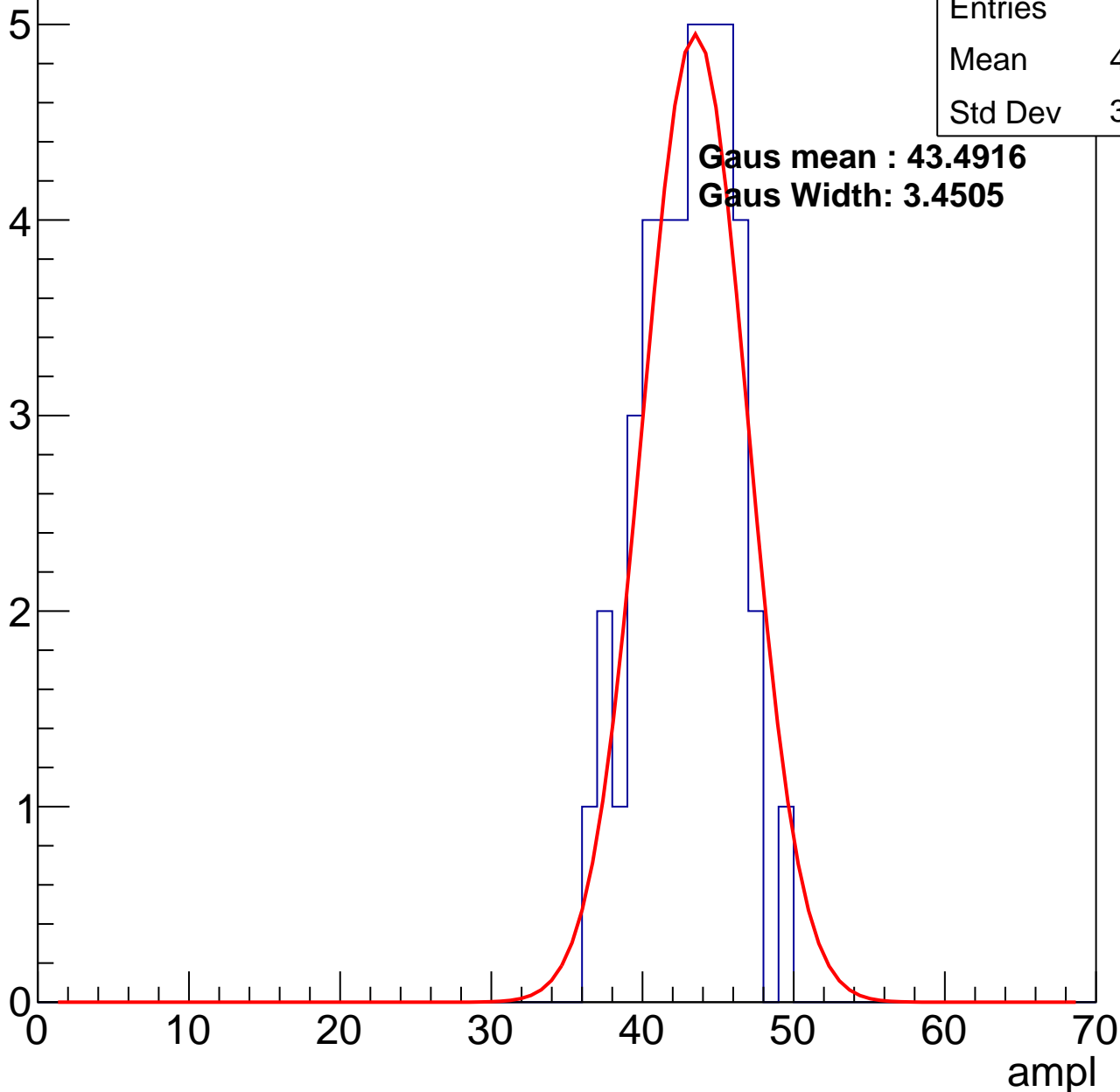
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	42.54
Std Dev	3.005

**Gaus mean : 43.4916**

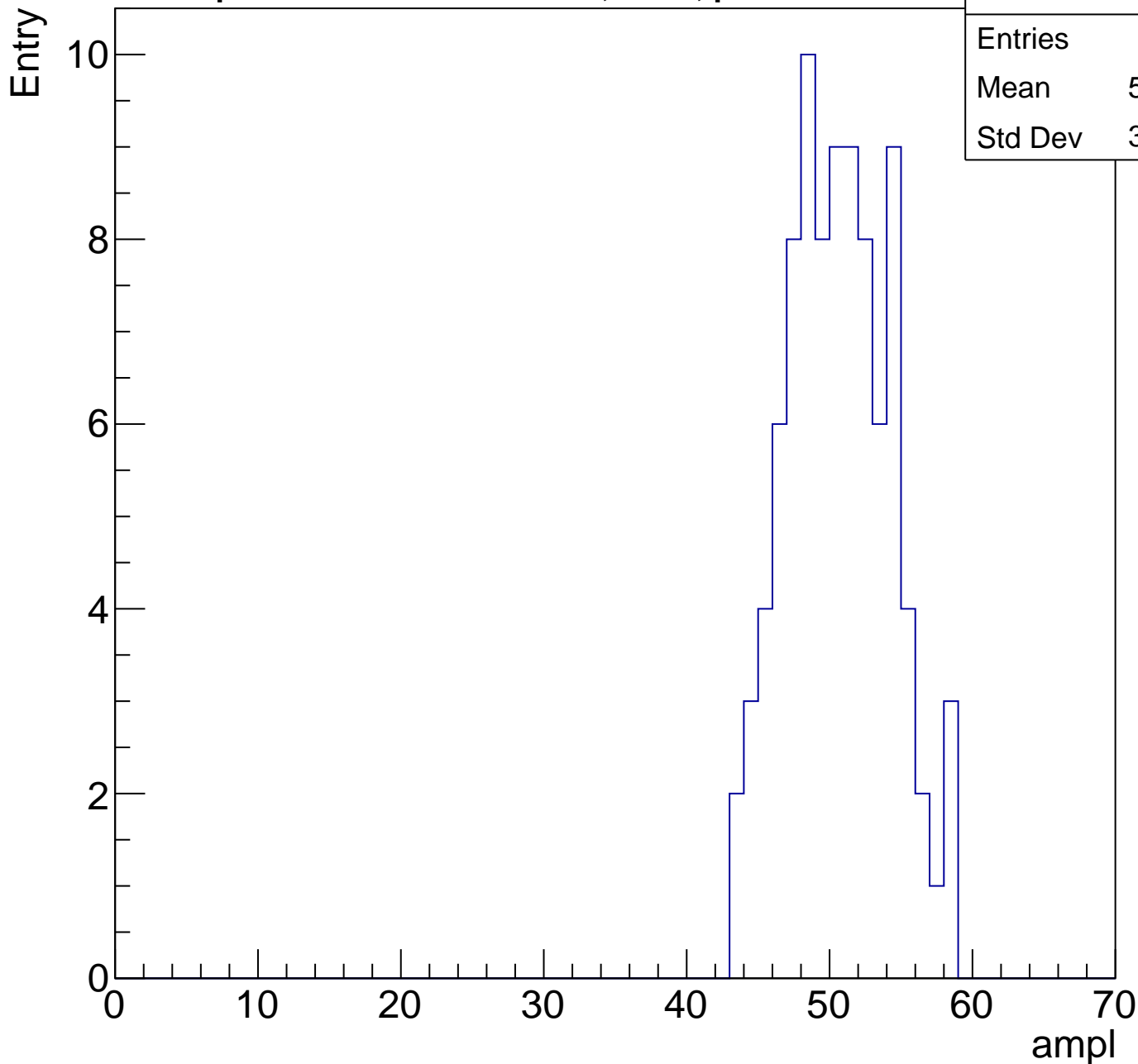
**Gaus Width: 3.4505**



# B0L000S, U25-ch89, adc3

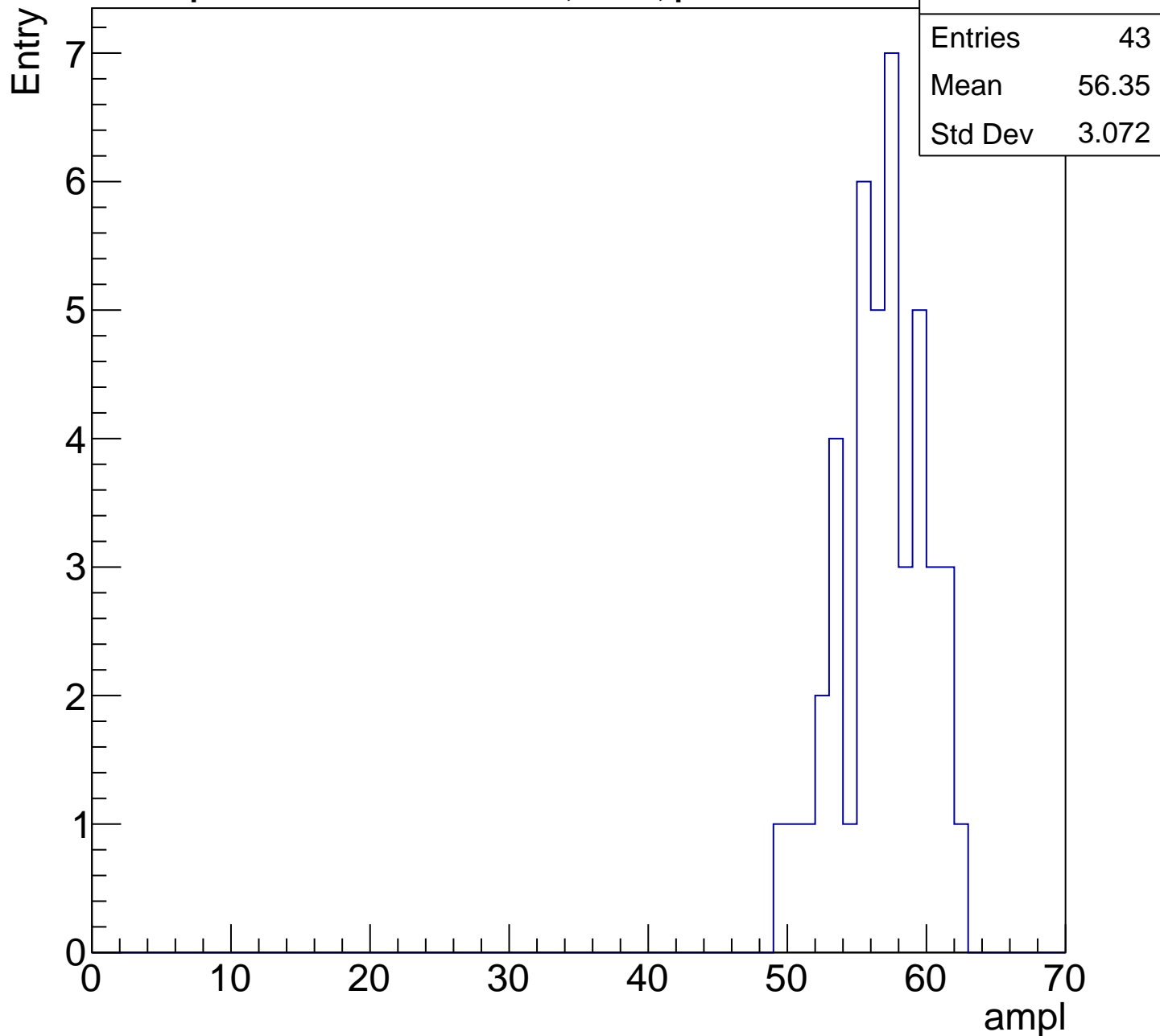
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	92
Mean	50.15
Std Dev	3.569



# B0L000S, U25-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

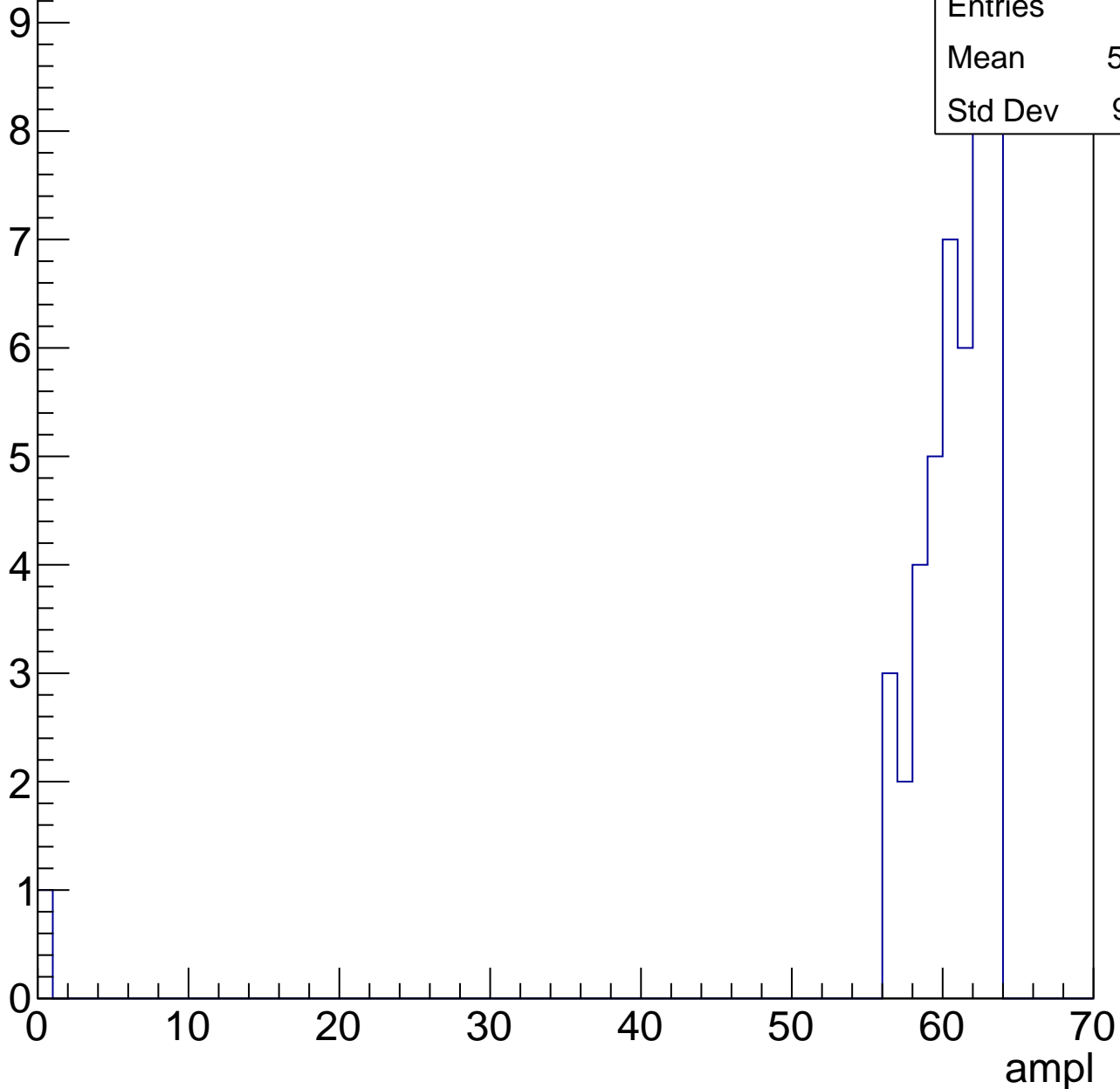


# B0L000S, U25-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	59.04
Std Dev	9.141



# B0L000S, U25-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch90, adc0

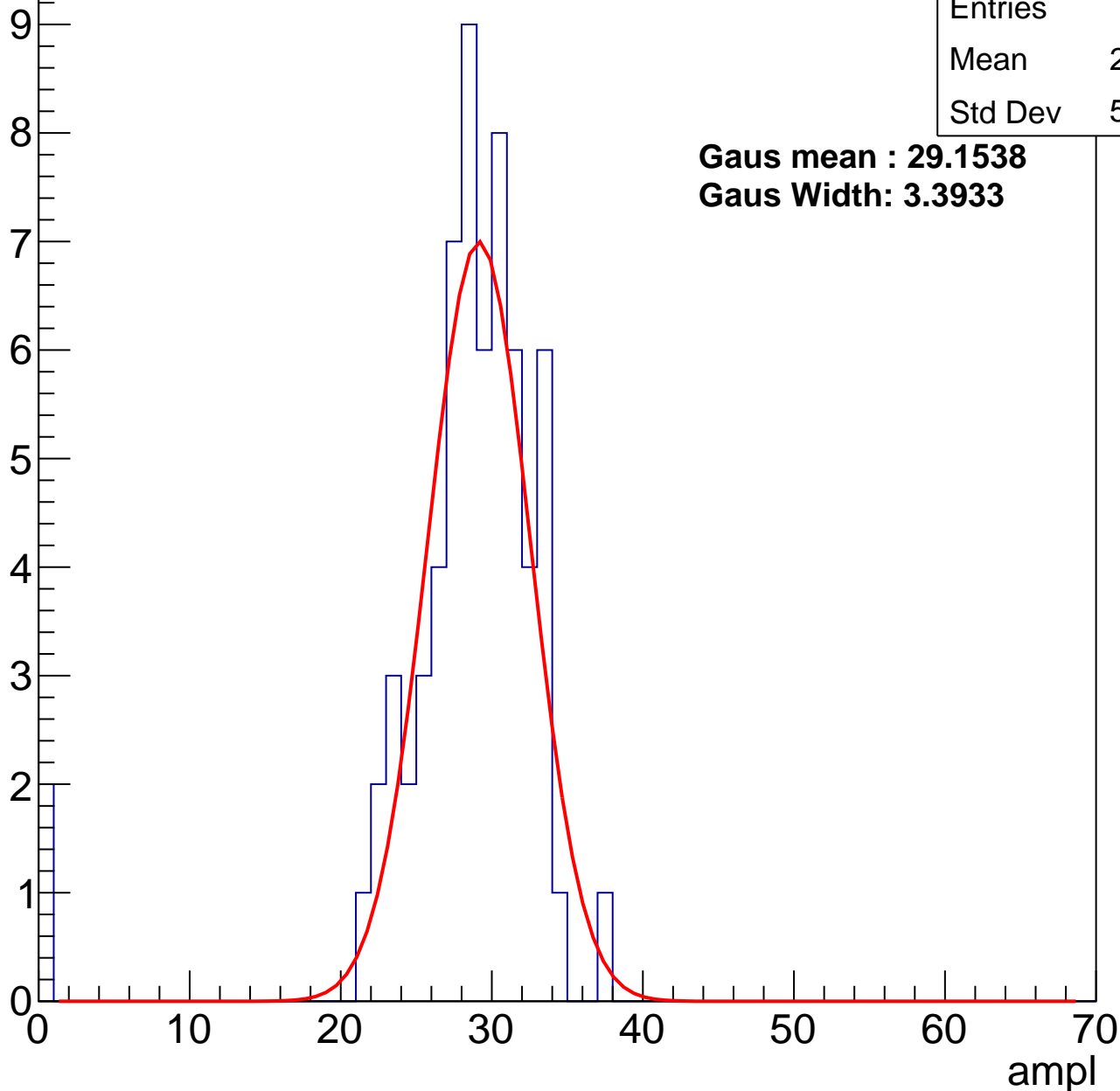
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	27.68
Std Dev	5.899

**Gaus mean : 29.1538**

**Gaus Width: 3.3933**



# B0L000S, U25-ch90, adc1

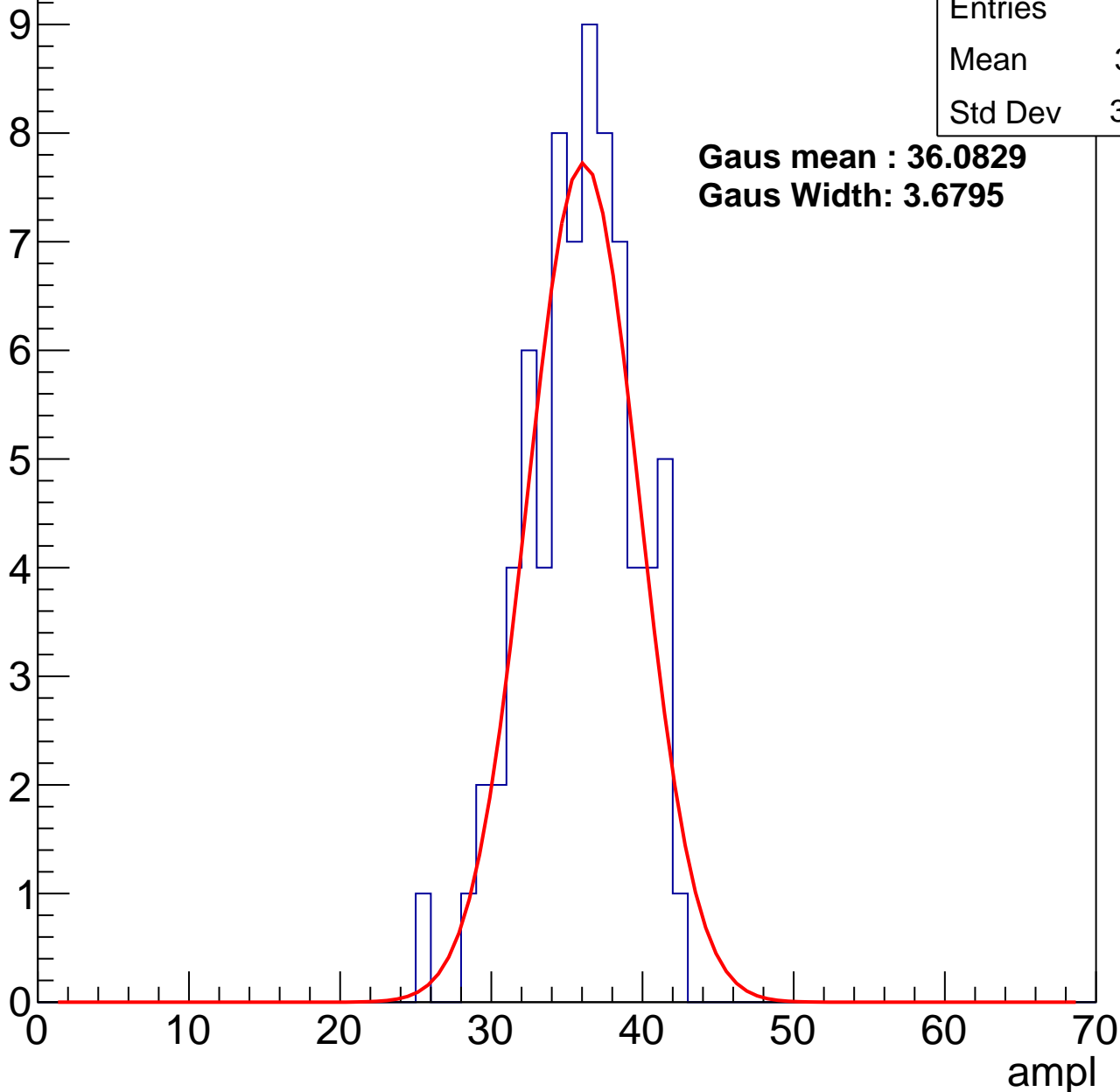
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	35.41
Std Dev	3.507

**Gaus mean : 36.0829**

**Gaus Width: 3.6795**



# B0L000S, U25-ch90, adc2

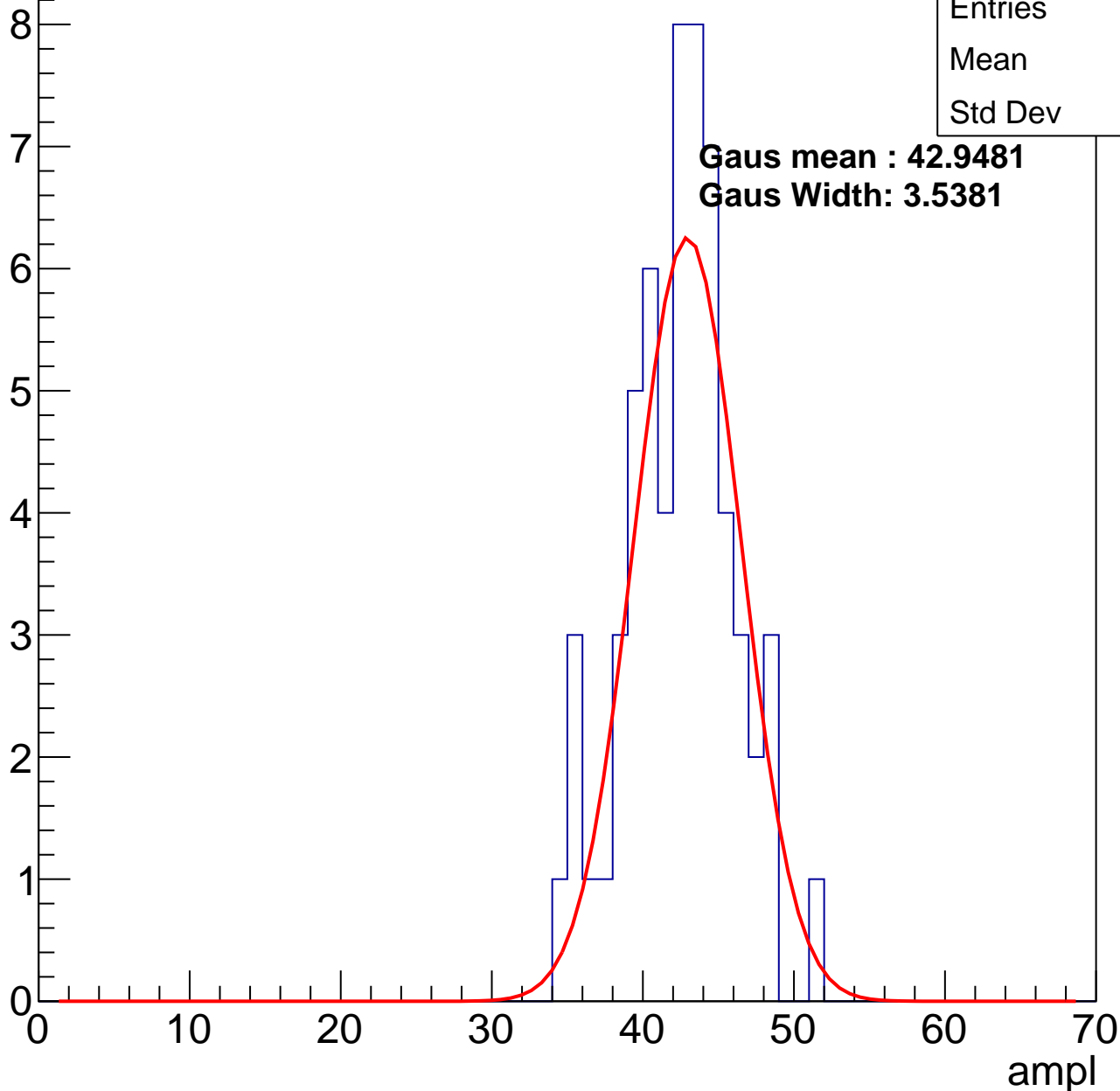
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42
Std Dev	3.54

**Gaus mean : 42.9481**

**Gaus Width: 3.5381**

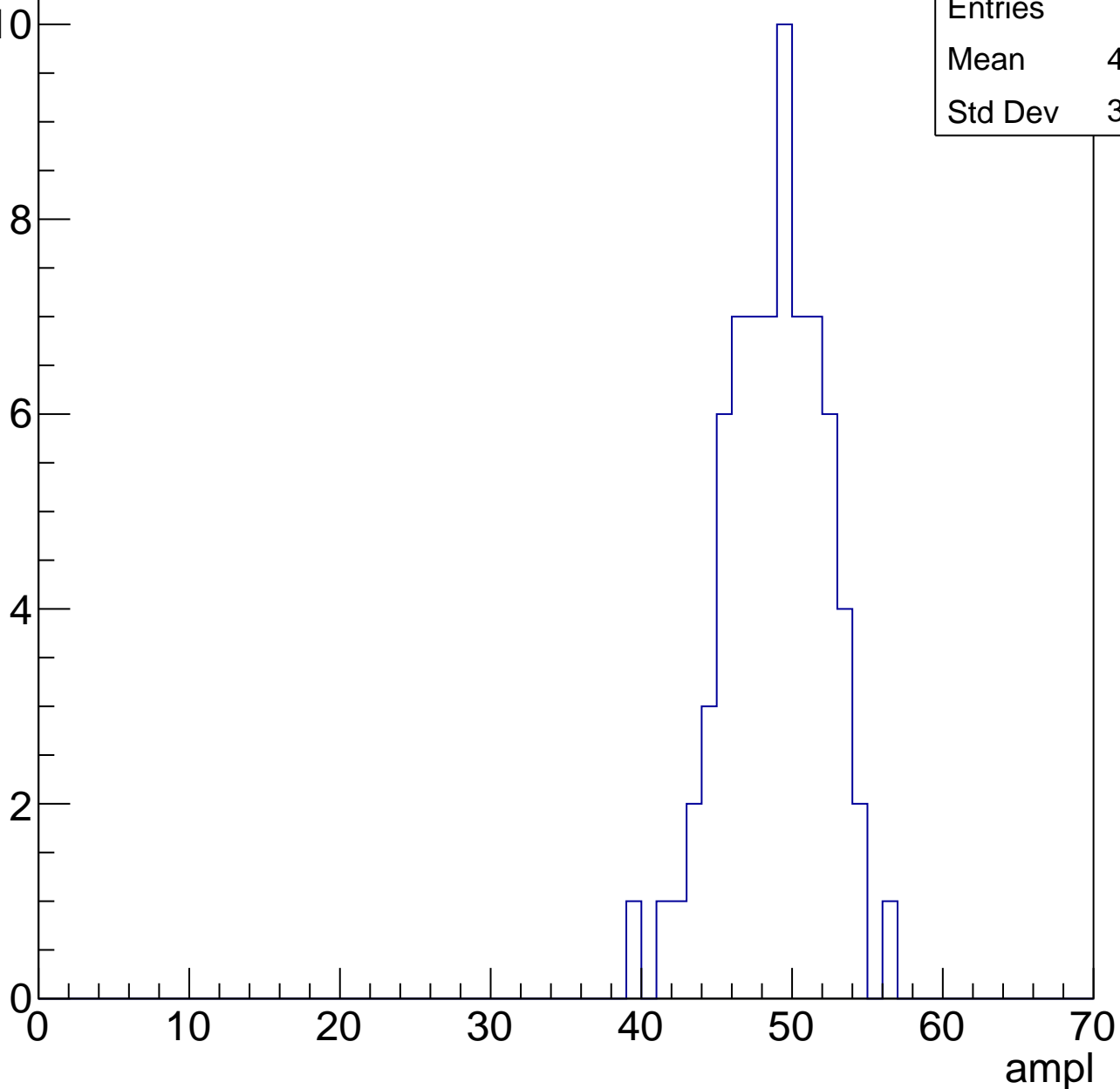


# B0L000S, U25-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.36
Std Dev	3.284

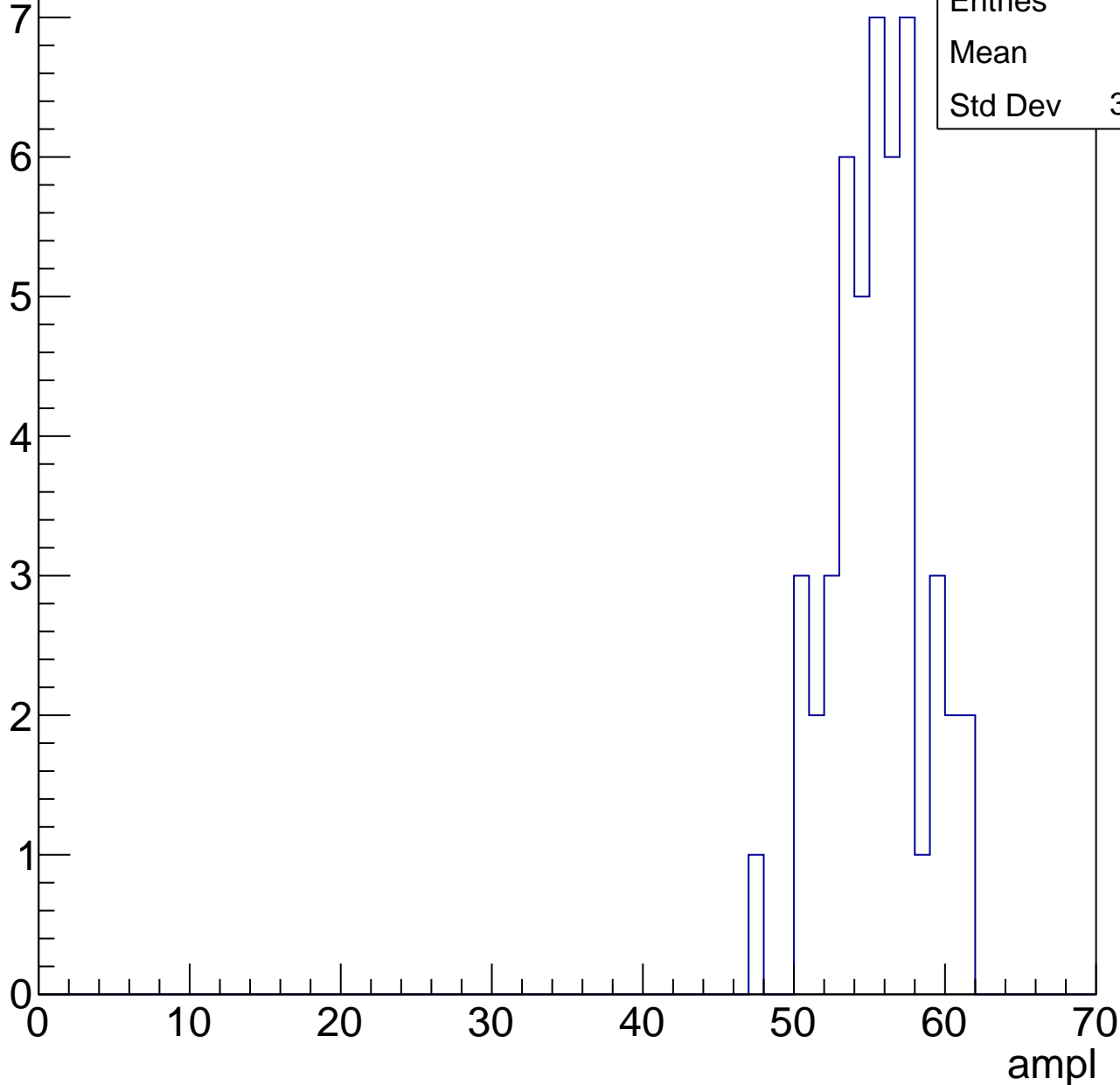


# B0L000S, U25-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	55
Std Dev	3.028

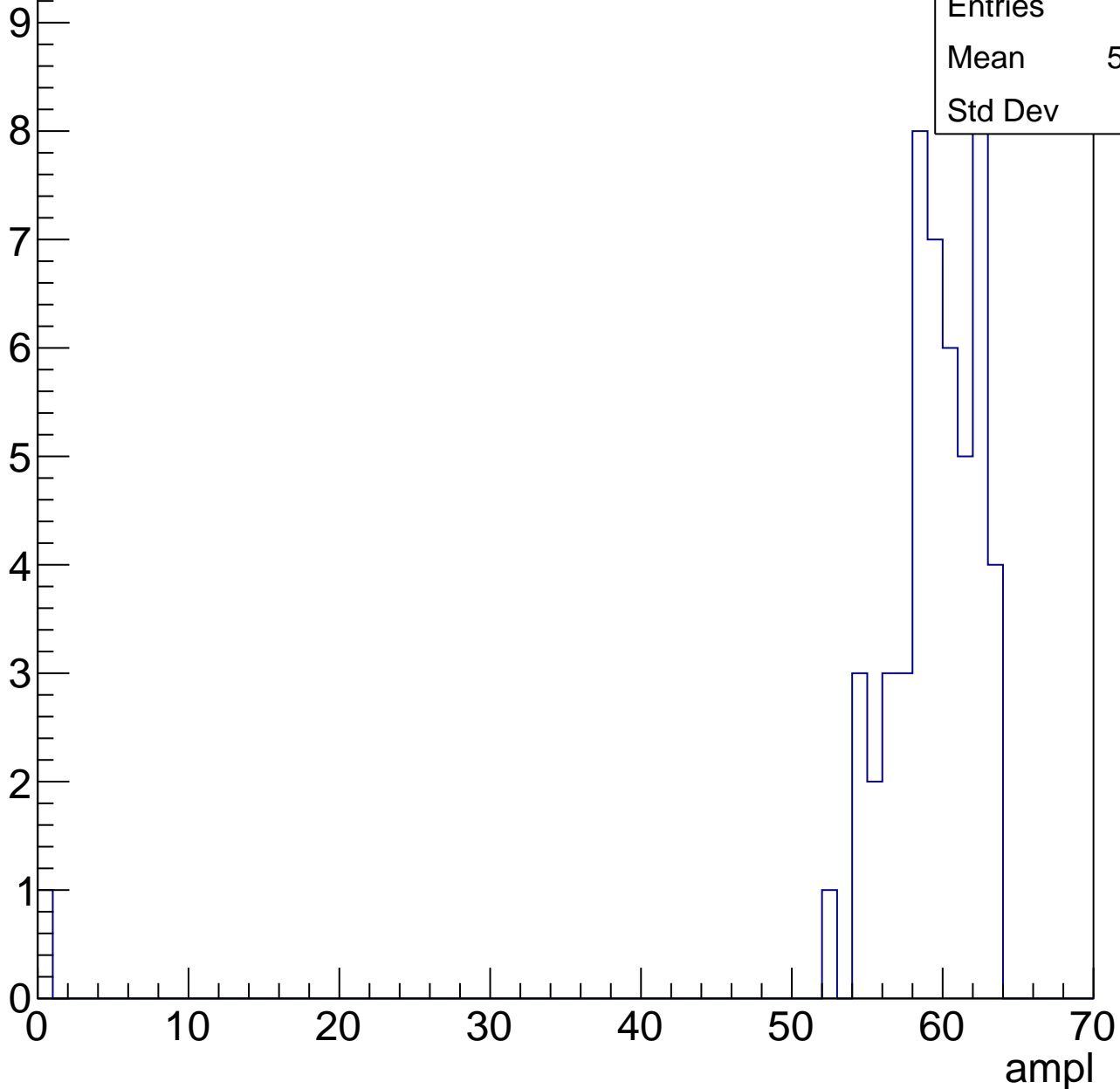


# B0L000S, U25-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	57.98
Std Dev	8.55

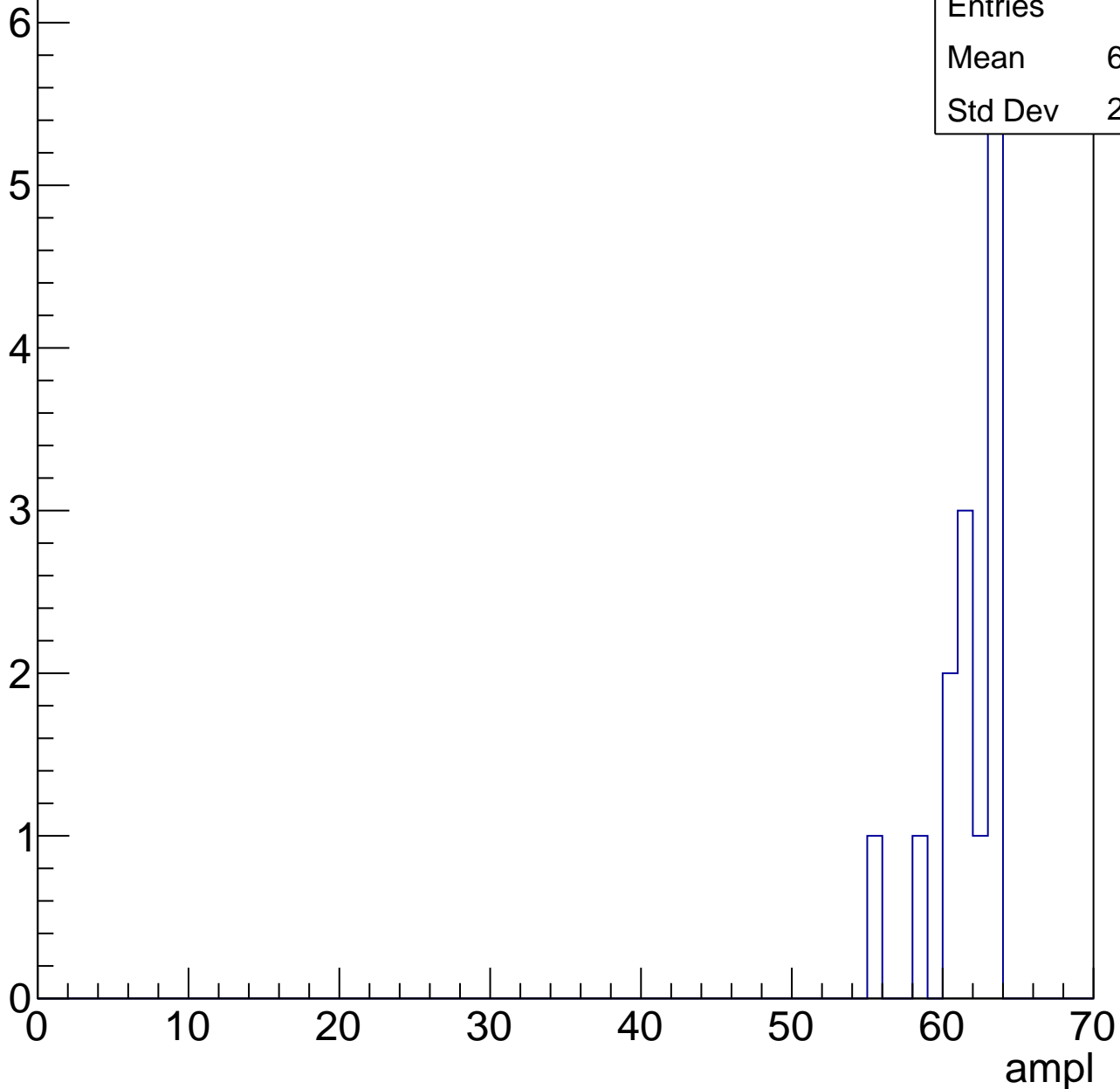


# B0L000S, U25-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	14
Mean	61.14
Std Dev	2.263





# B0L000S, U25-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch91, adc0

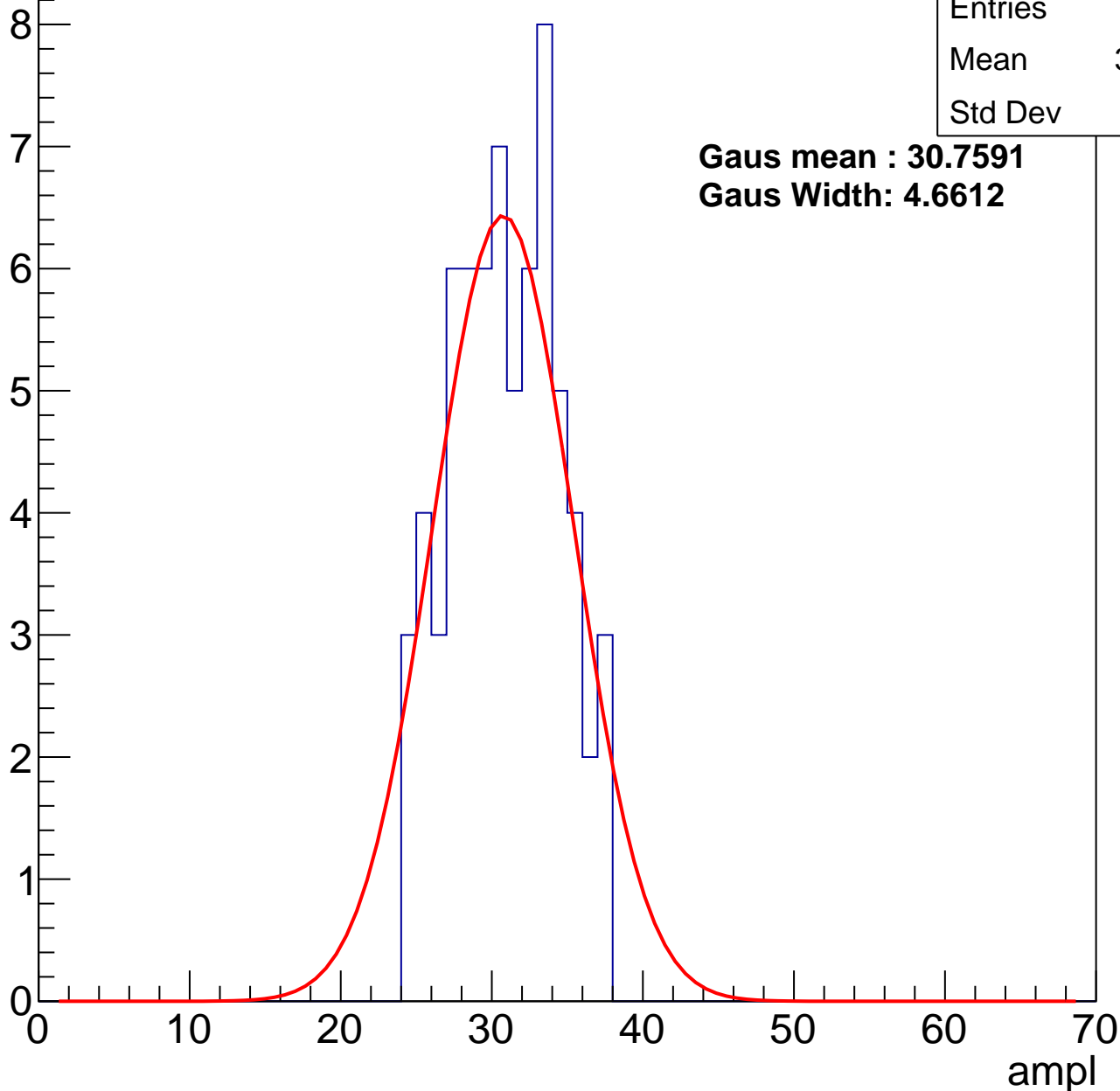
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	30.41
Std Dev	3.49

**Gaus mean : 30.7591**

**Gaus Width: 4.6612**



# B0L000S, U25-ch91, adc1

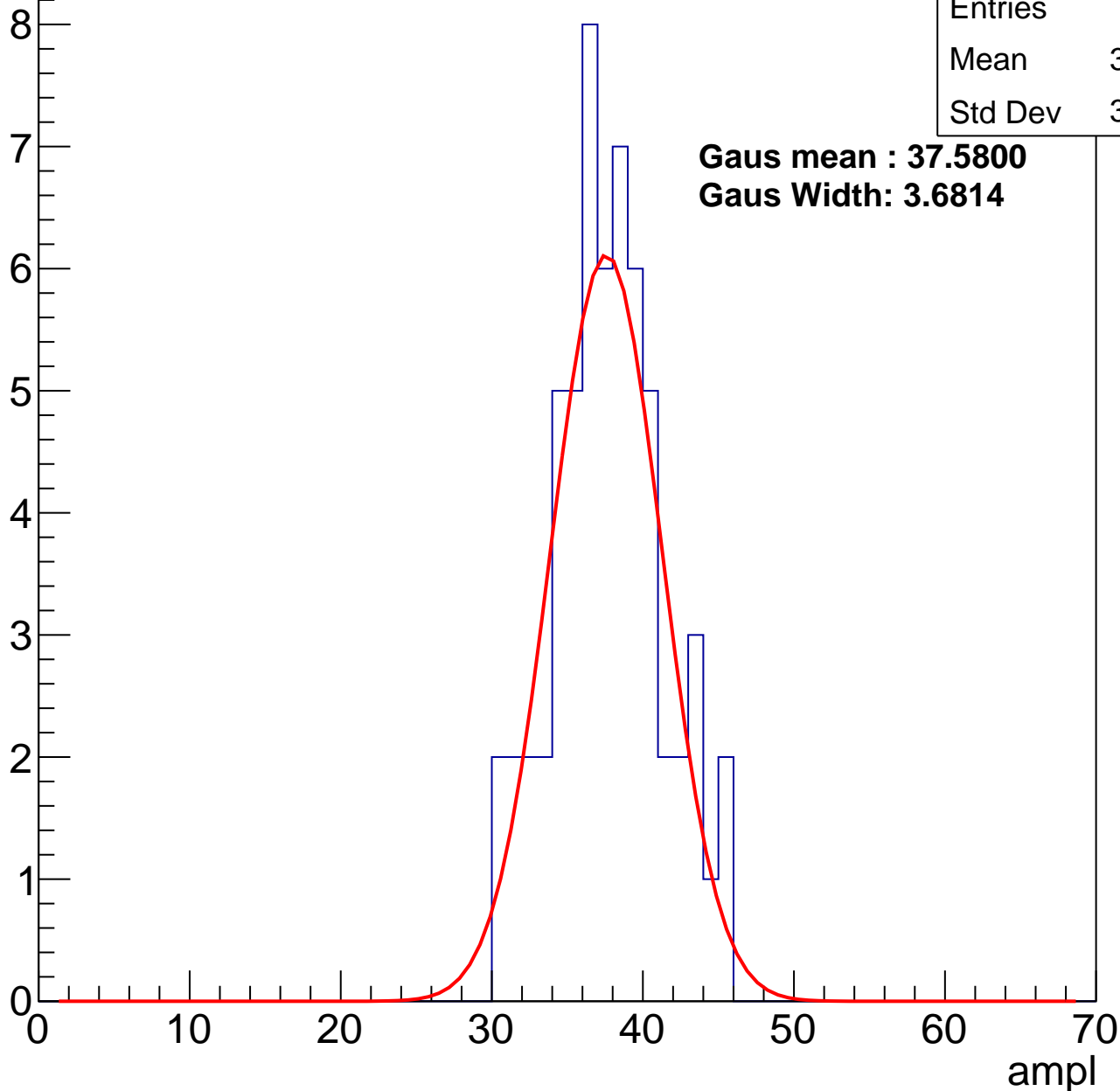
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	37.27
Std Dev	3.568

**Gaus mean : 37.5800**

**Gaus Width: 3.6814**



# B0L000S, U25-ch91, adc2

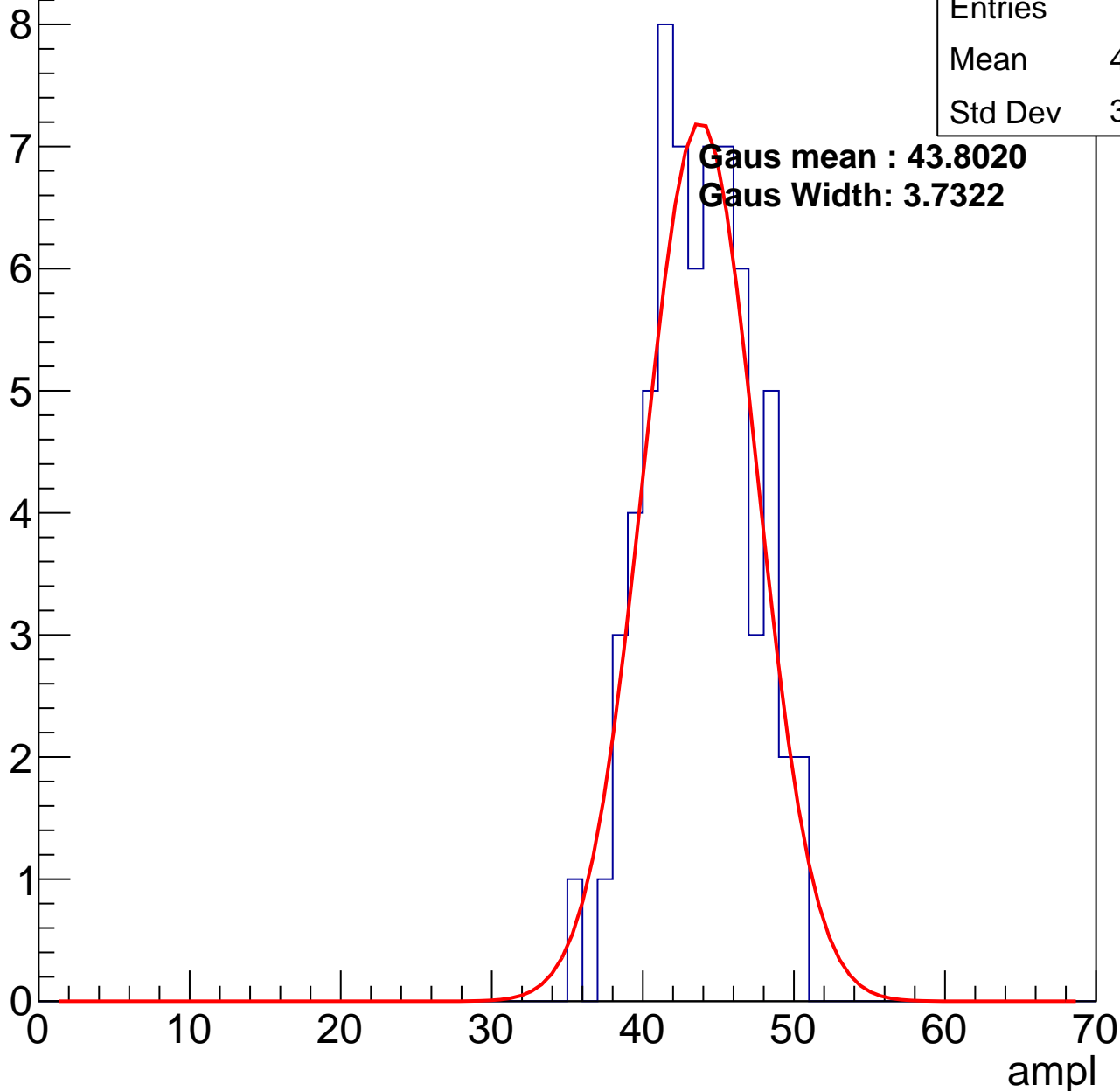
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	43.28
Std Dev	3.345

**Gaus mean : 43.8020**

**Gaus Width: 3.7322**

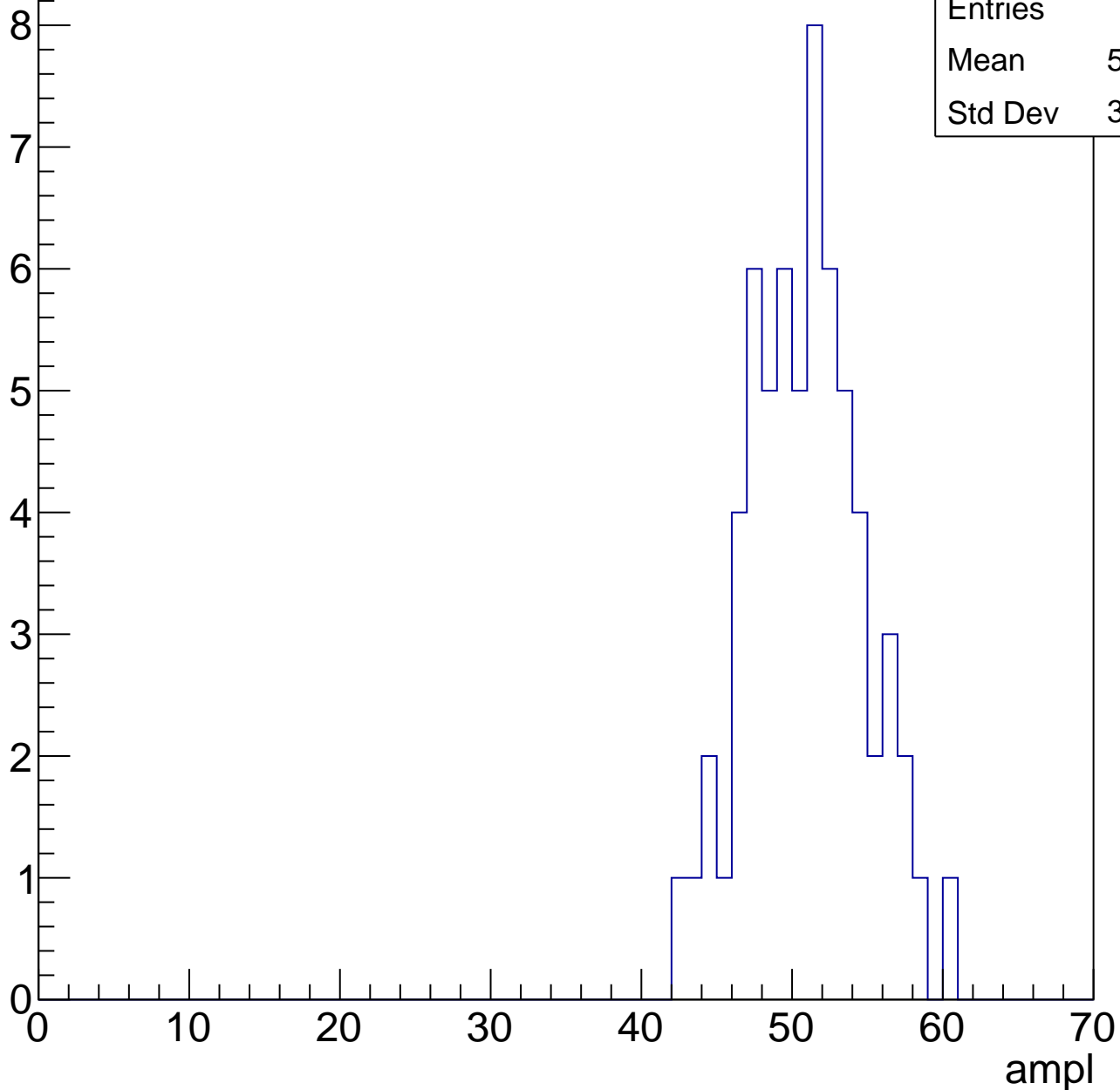


# B0L000S, U25-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	50.46
Std Dev	3.783

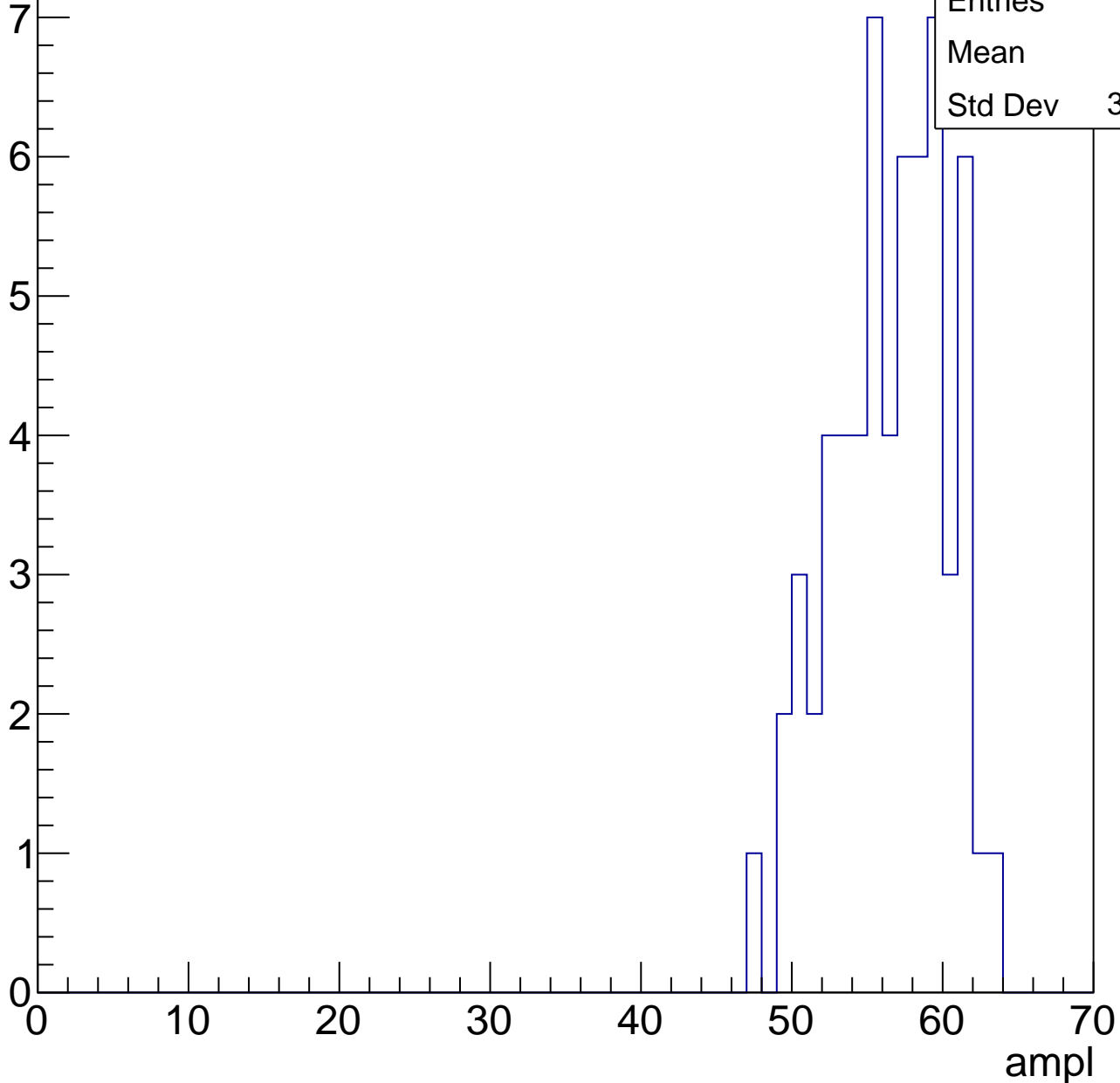


# B0L000S, U25-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	56
Std Dev	3.702

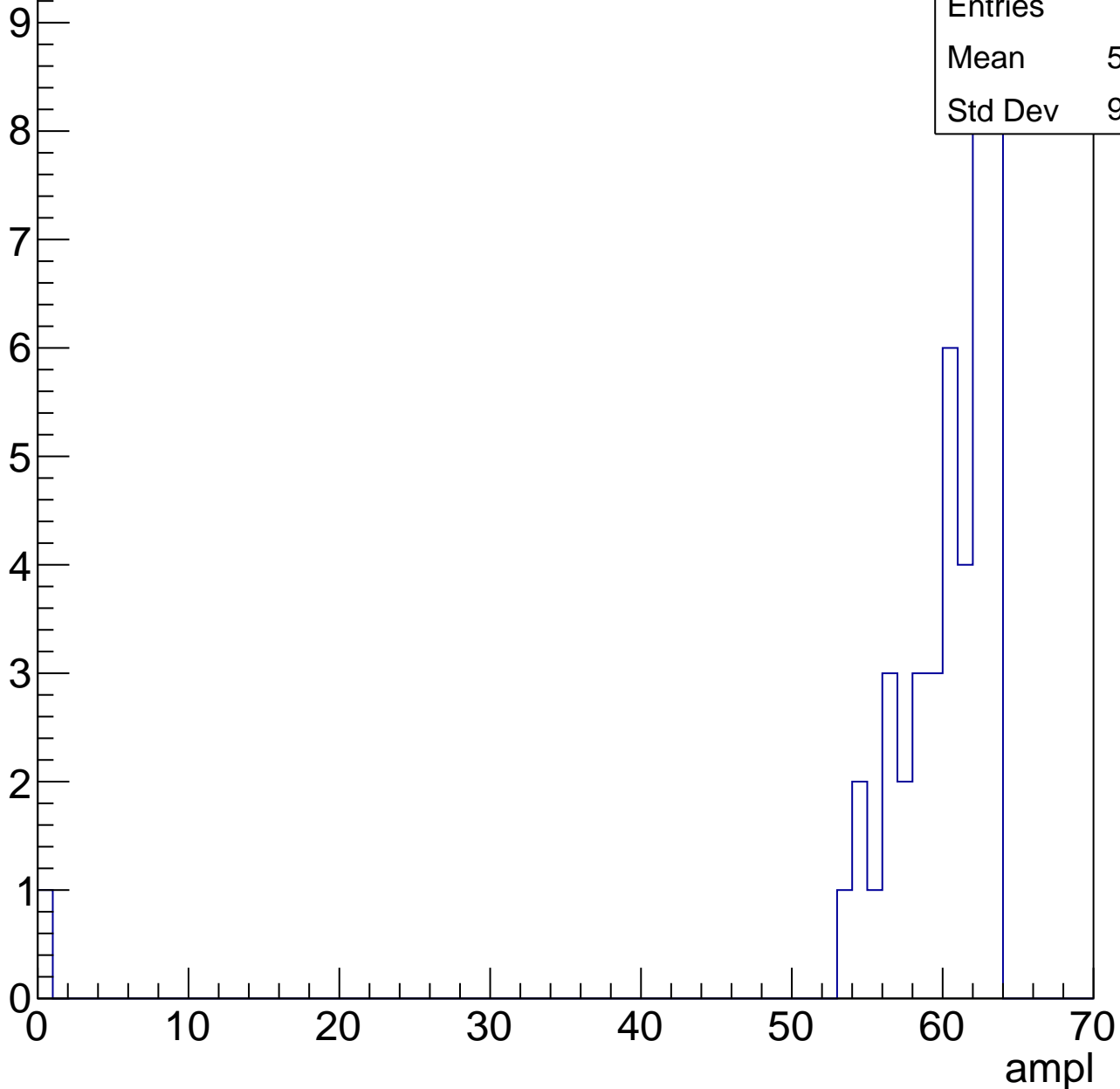


# B0L000S, U25-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.59
Std Dev	9.367



# B0L000S, U25-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U25-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch92, adc0

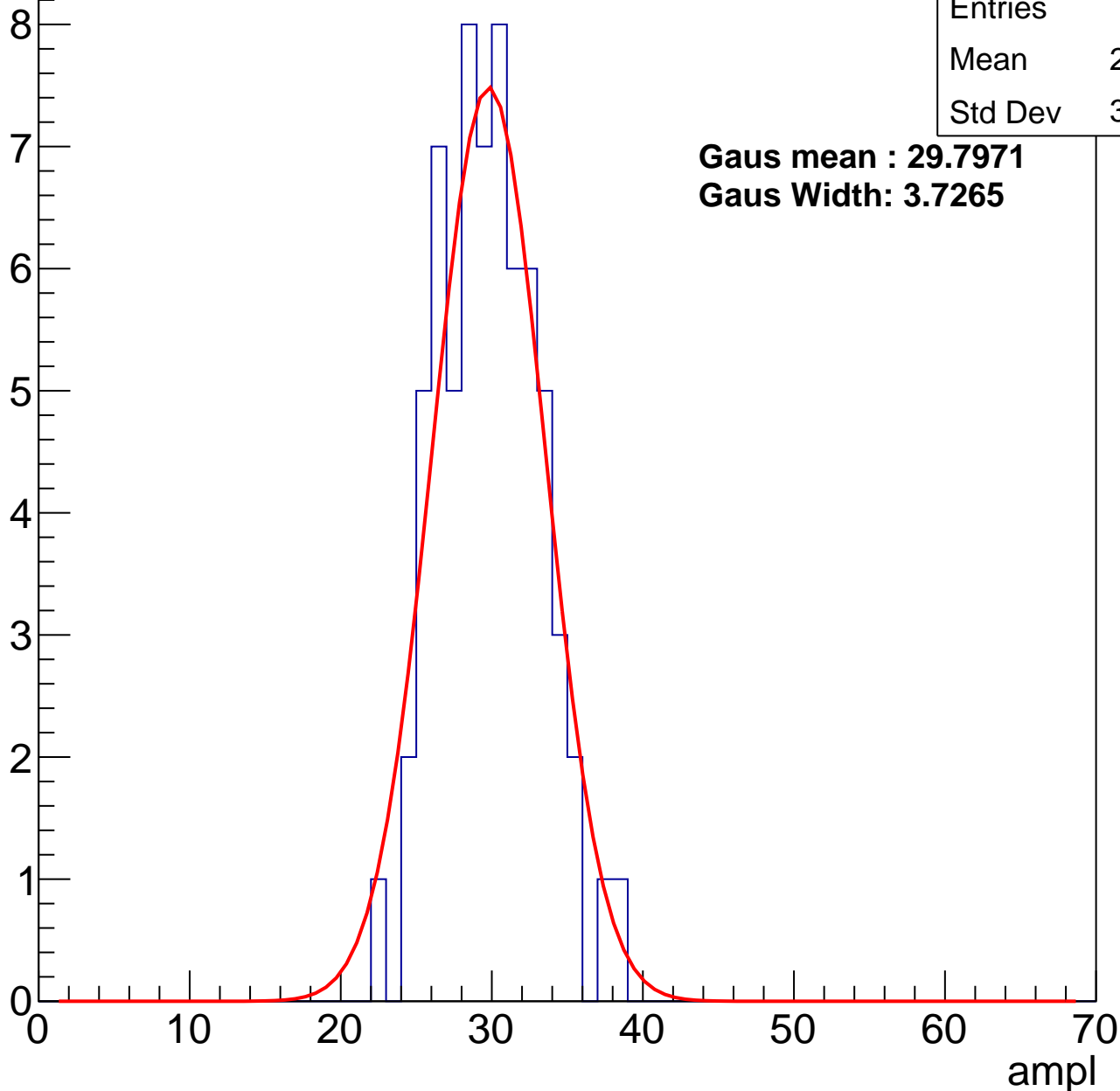
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	29.39
Std Dev	3.273

**Gaus mean : 29.7971**

**Gaus Width: 3.7265**



# B0L000S, U25-ch92, adc1

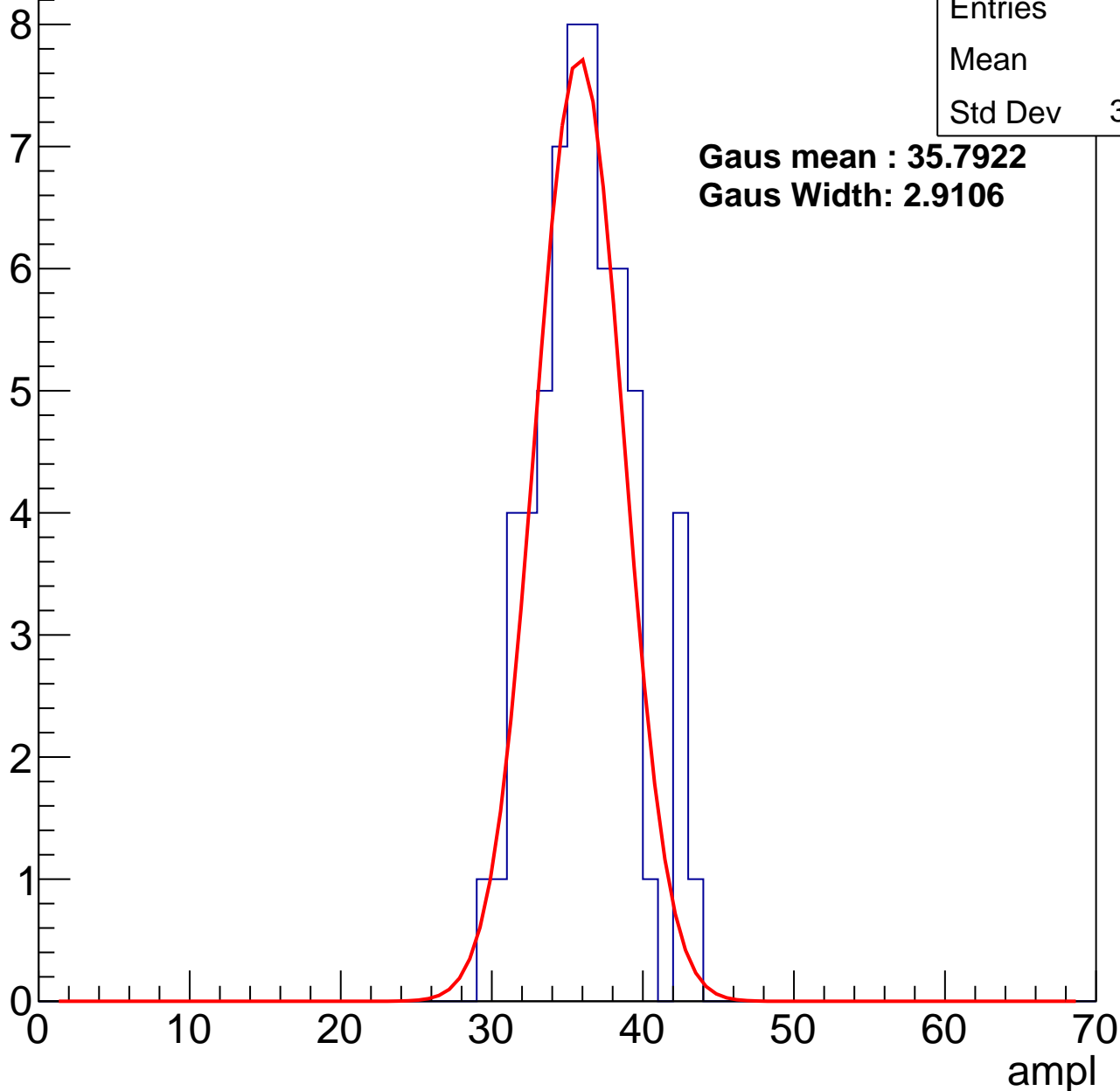
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	35.7
Std Dev	3.159

**Gaus mean : 35.7922**

**Gaus Width: 2.9106**



# B0L000S, U25-ch92, adc2

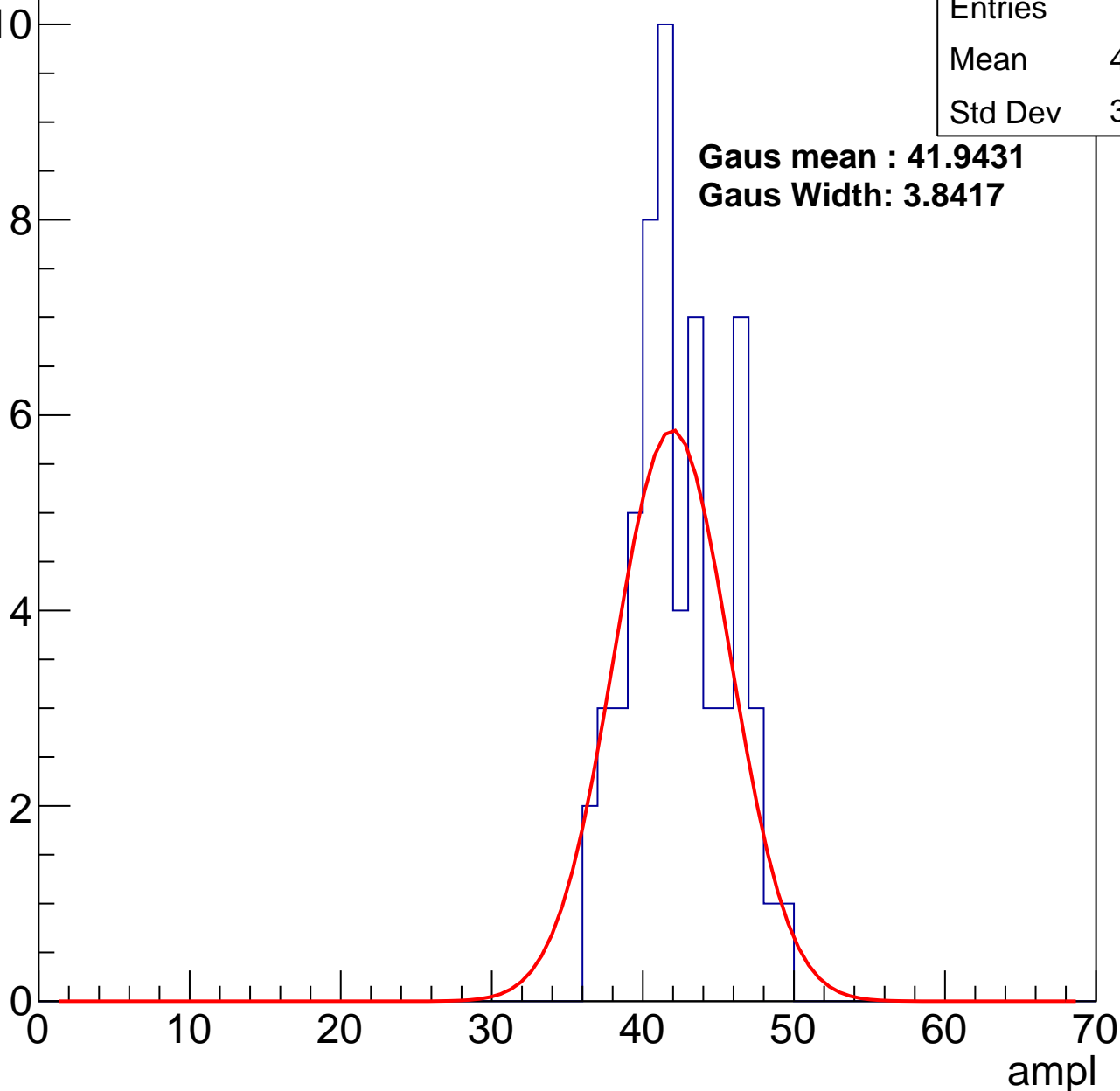
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	41.97
Std Dev	3.162

**Gaus mean : 41.9431**

**Gaus Width: 3.8417**

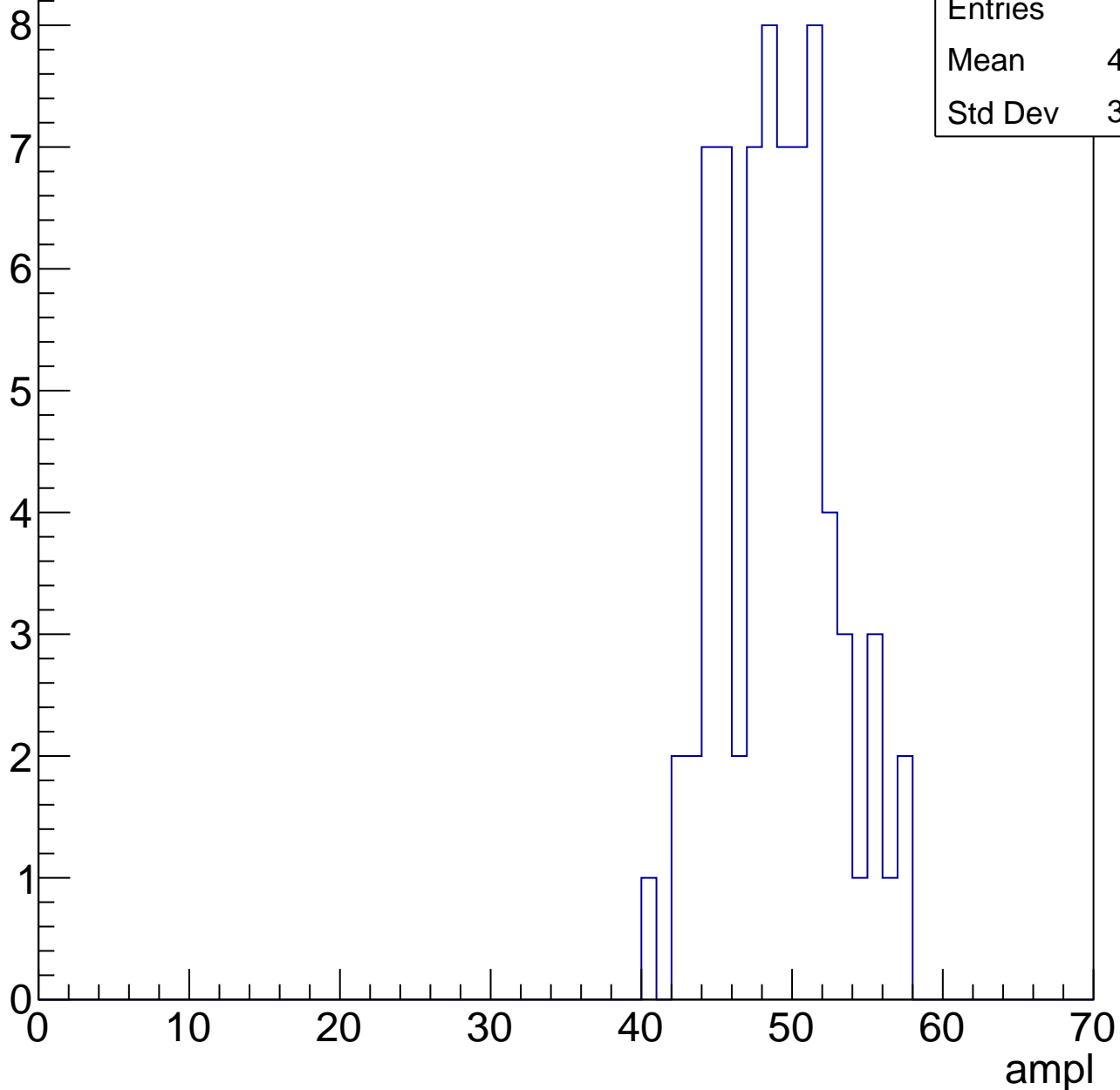


# B0L000S, U25-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	48.54
Std Dev	3.745

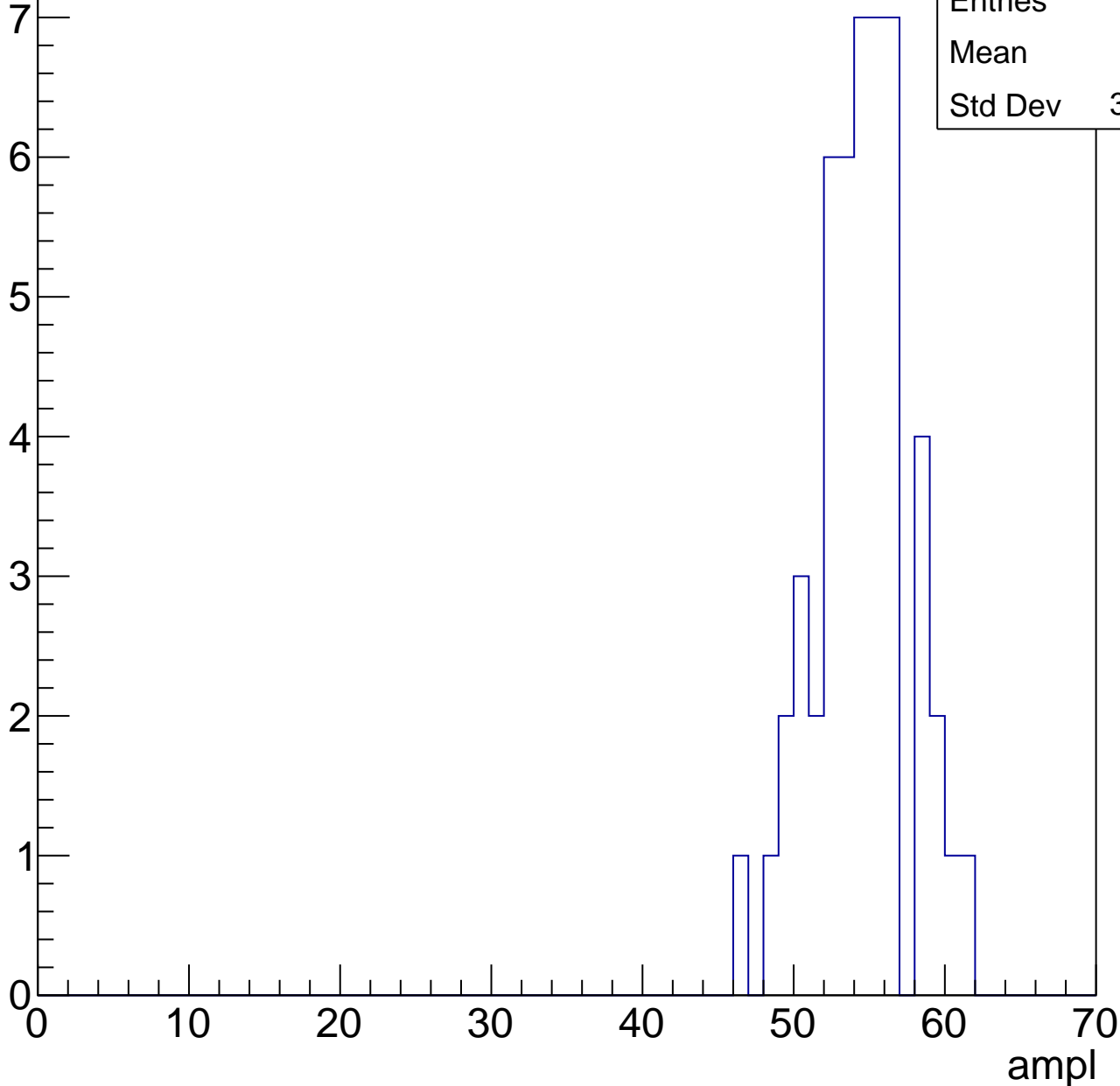


# B0L000S, U25-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	54
Std Dev	3.098

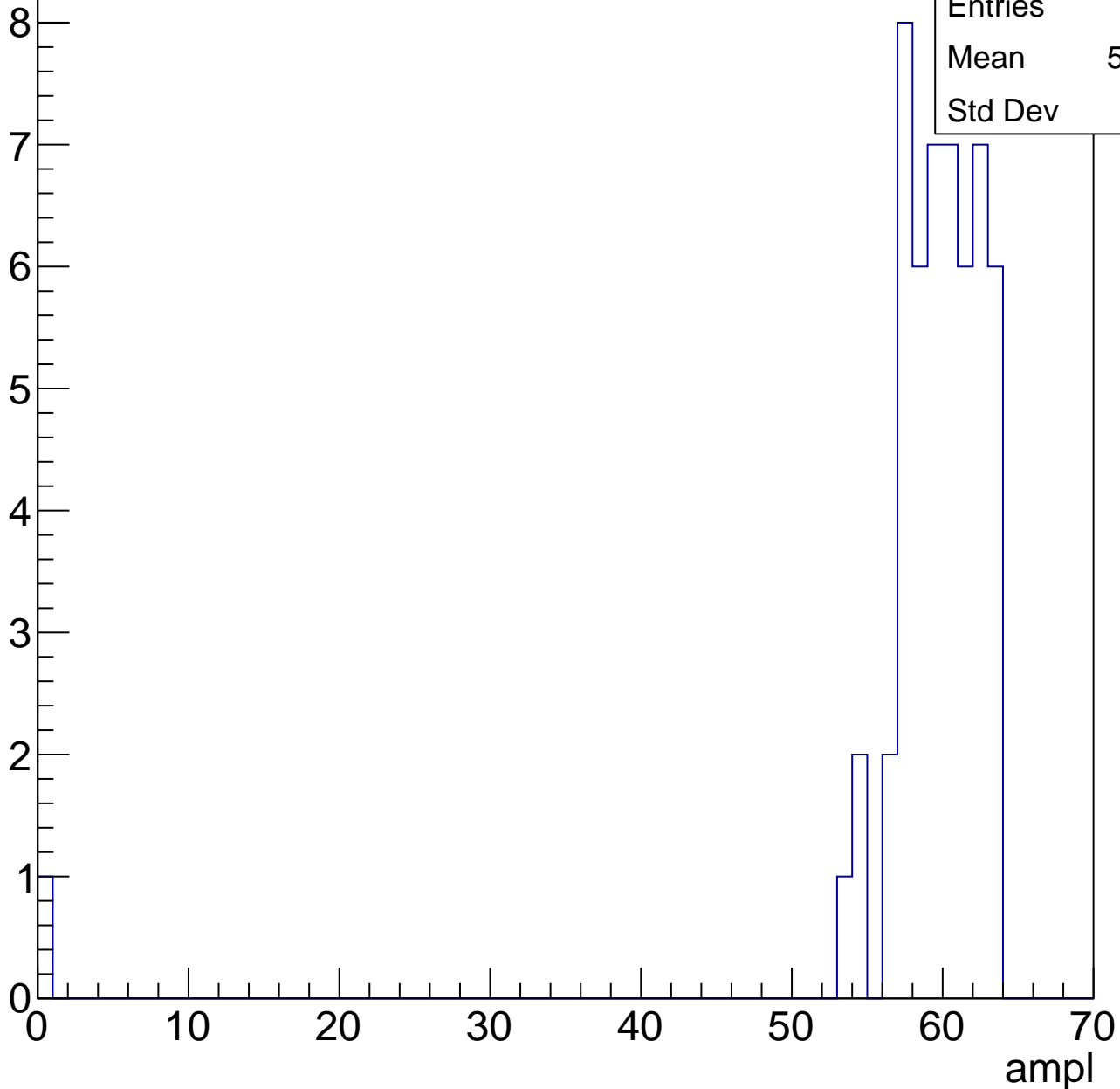


# B0L000S, U25-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	58.26
Std Dev	8.45



# B0L000S, U25-ch92, adc6

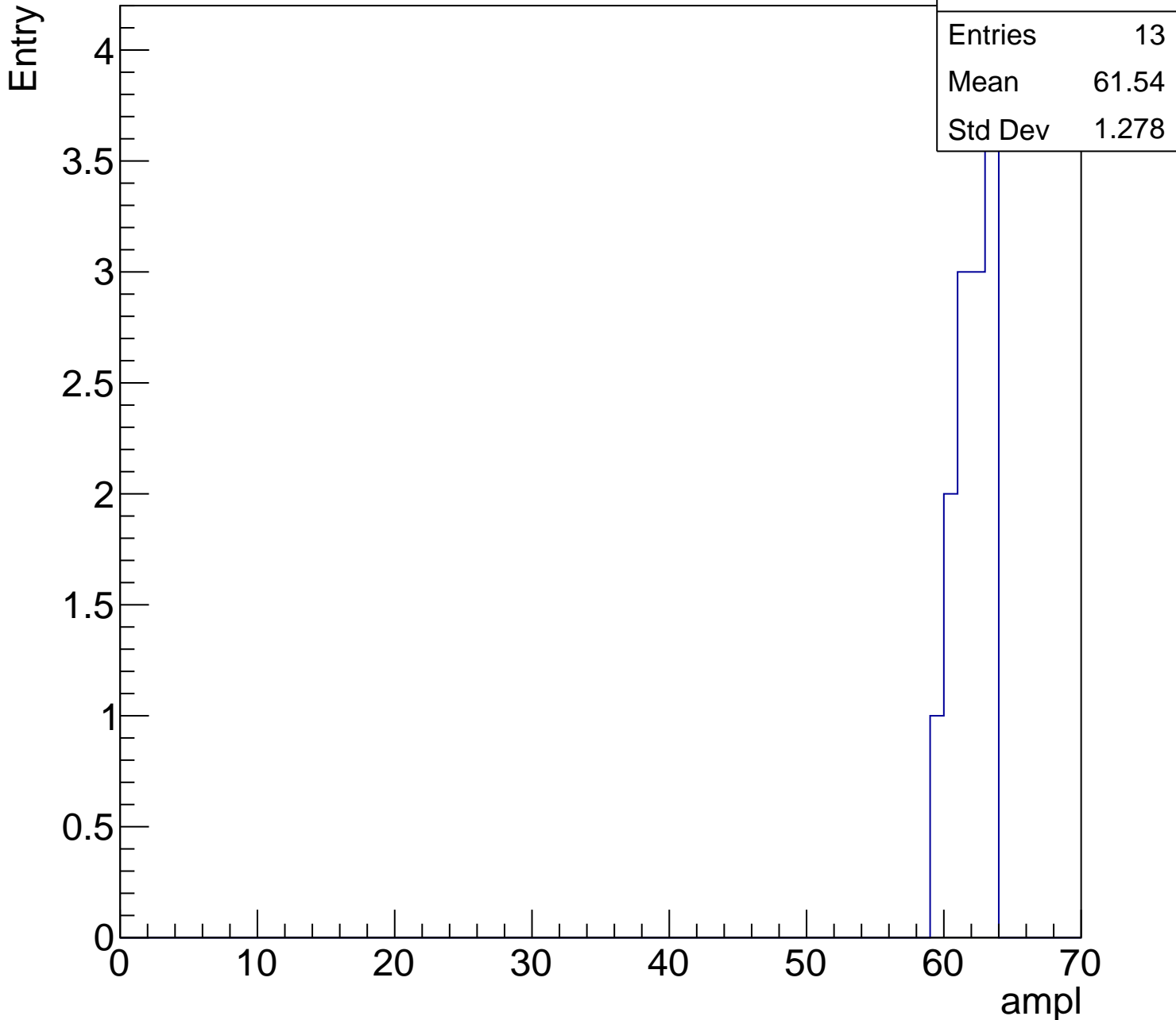
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

4  
3.5  
3  
2.5  
2  
1.5  
1  
0.5  
0

Entries	13
Mean	61.54
Std Dev	1.278

ampl





# B0L000S, U25-ch92, adc7

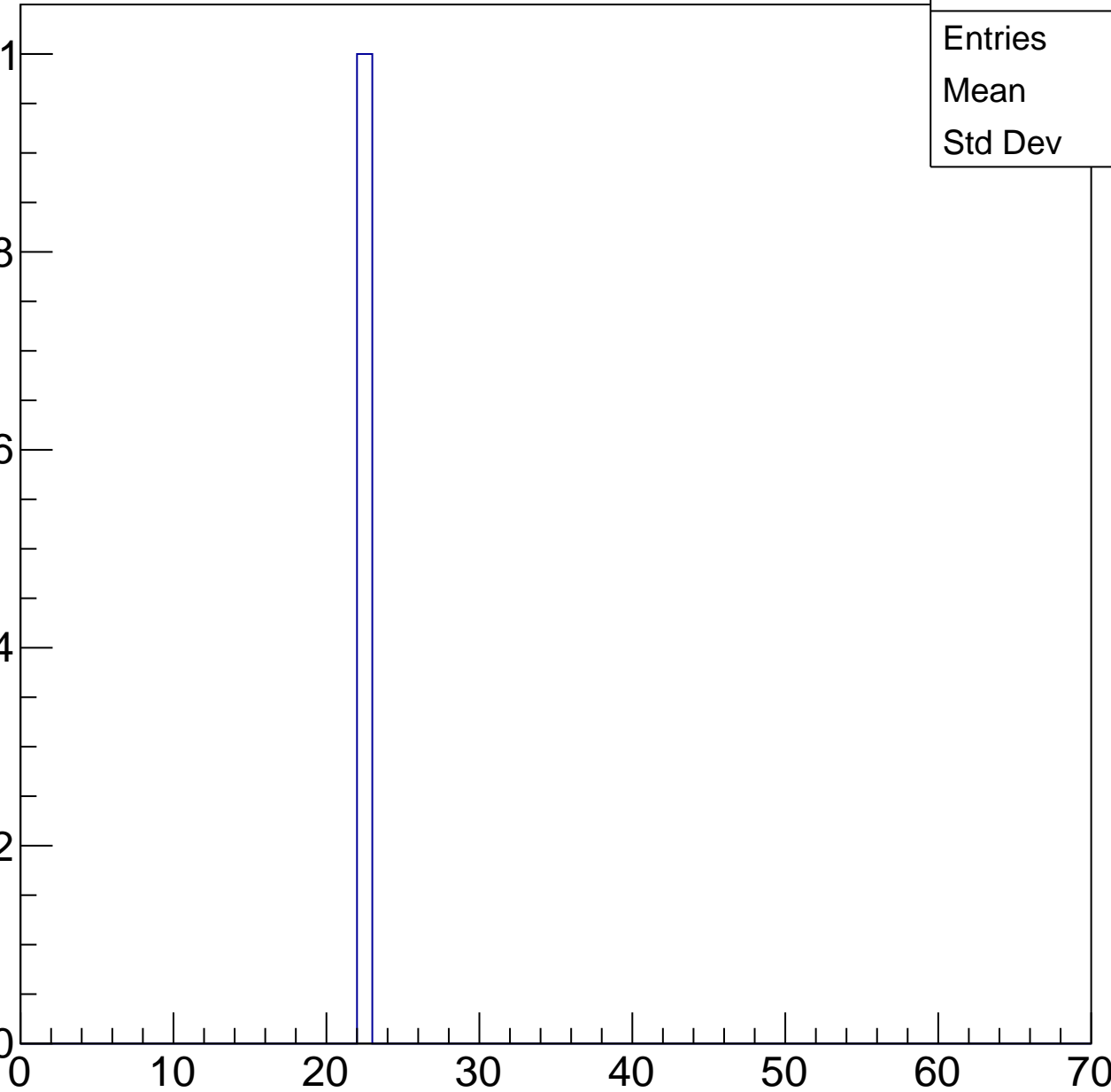
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	22
Std Dev	0

ampl



# B0L000S, U25-ch93, adc0

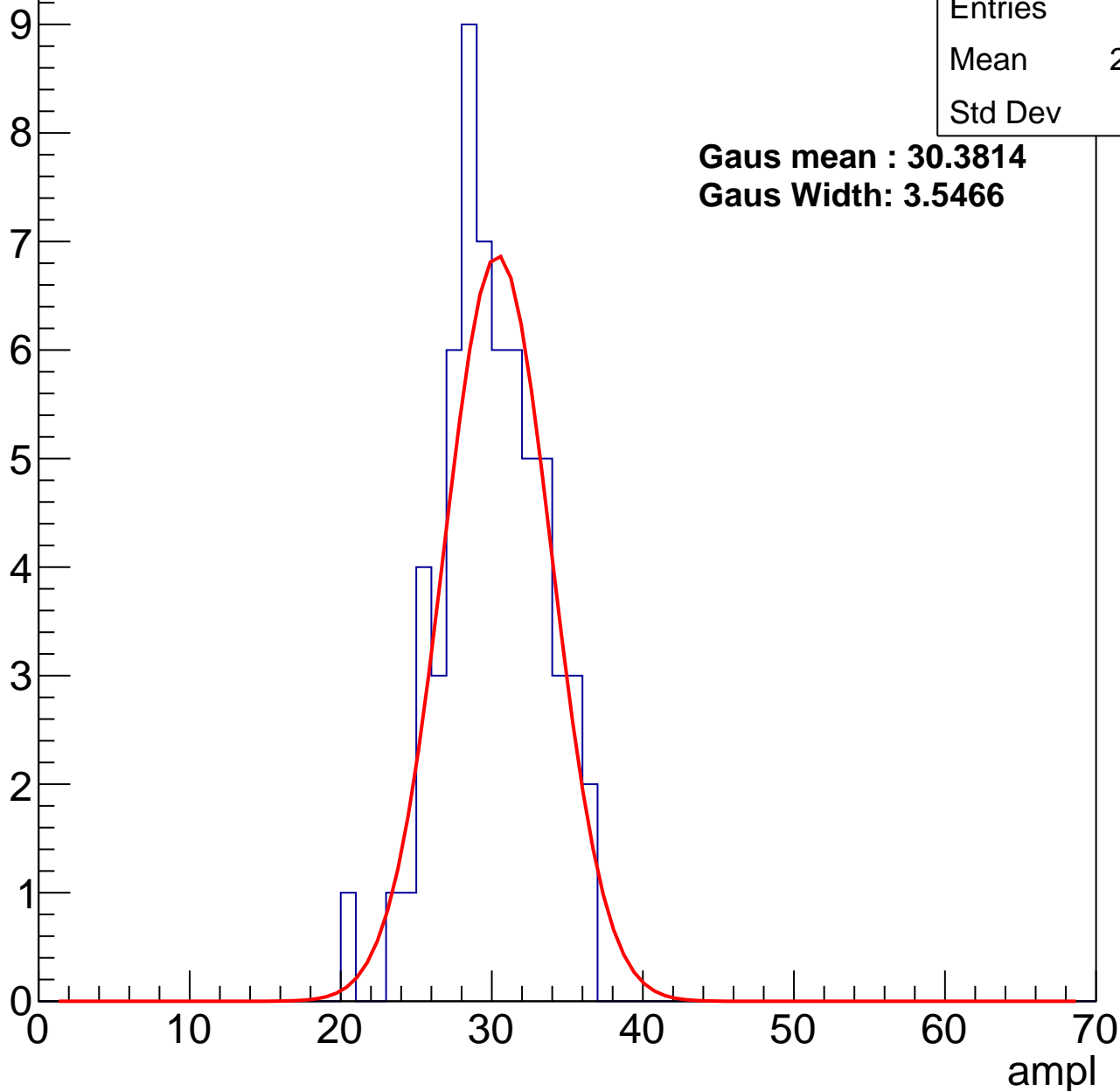
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	29.55
Std Dev	3.32

**Gaus mean : 30.3814**

**Gaus Width: 3.5466**



# B0L000S, U25-ch93, adc1

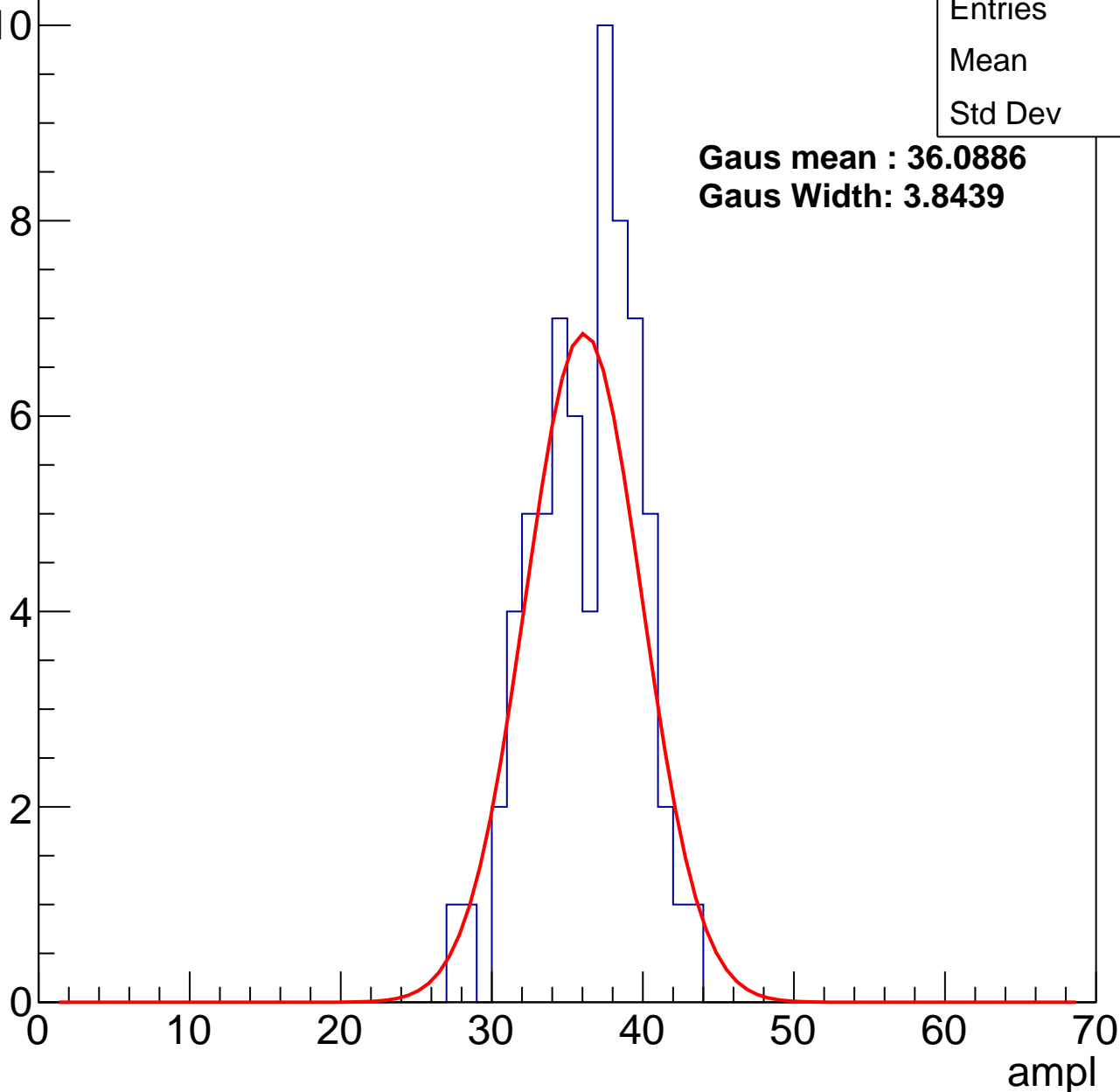
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	35.8
Std Dev	3.39

**Gaus mean : 36.0886**

**Gaus Width: 3.8439**



# B0L000S, U25-ch93, adc2

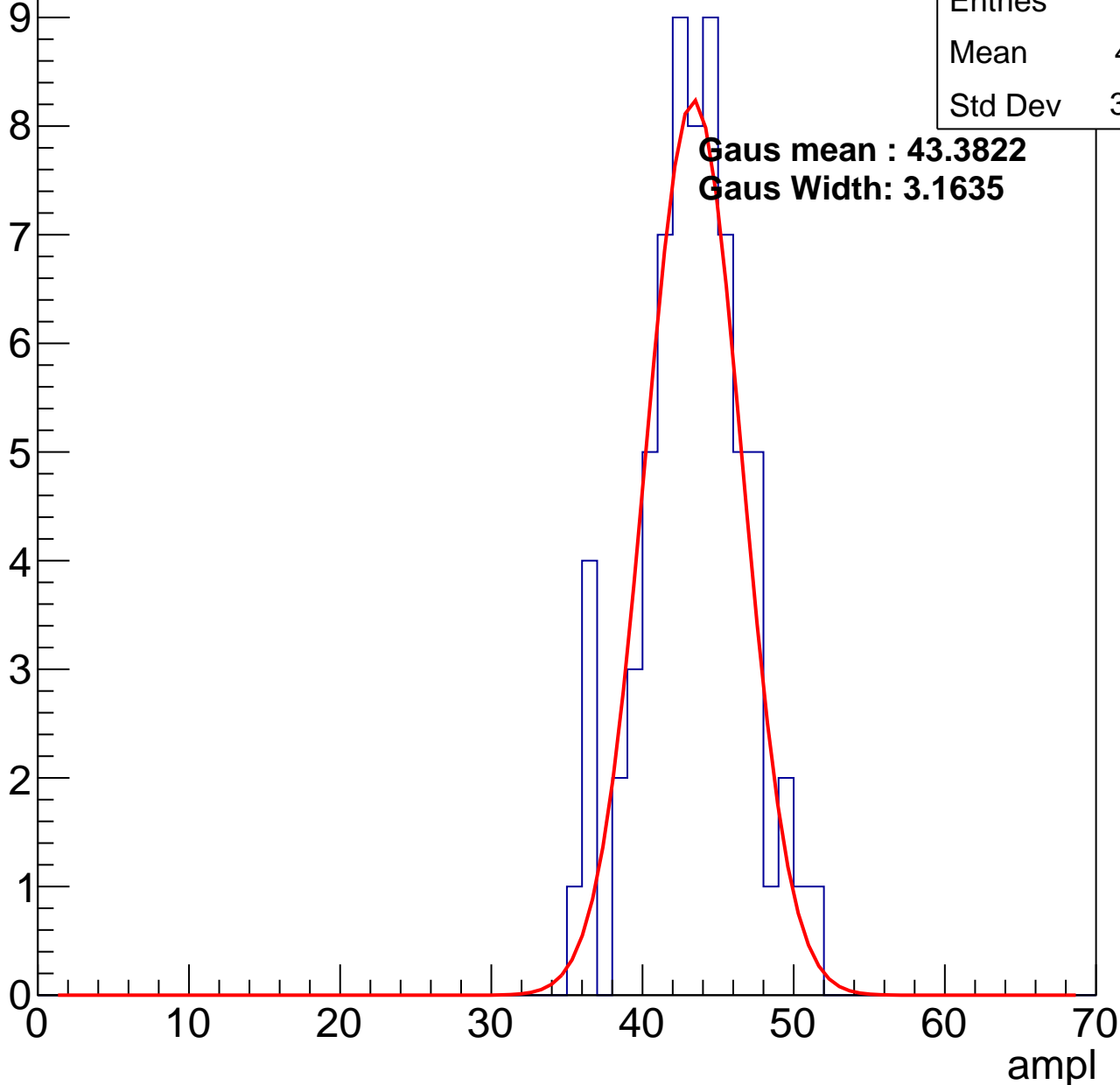
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.91
Std Dev	3.405

**Gaus mean : 43.3822**

**Gaus Width: 3.1635**

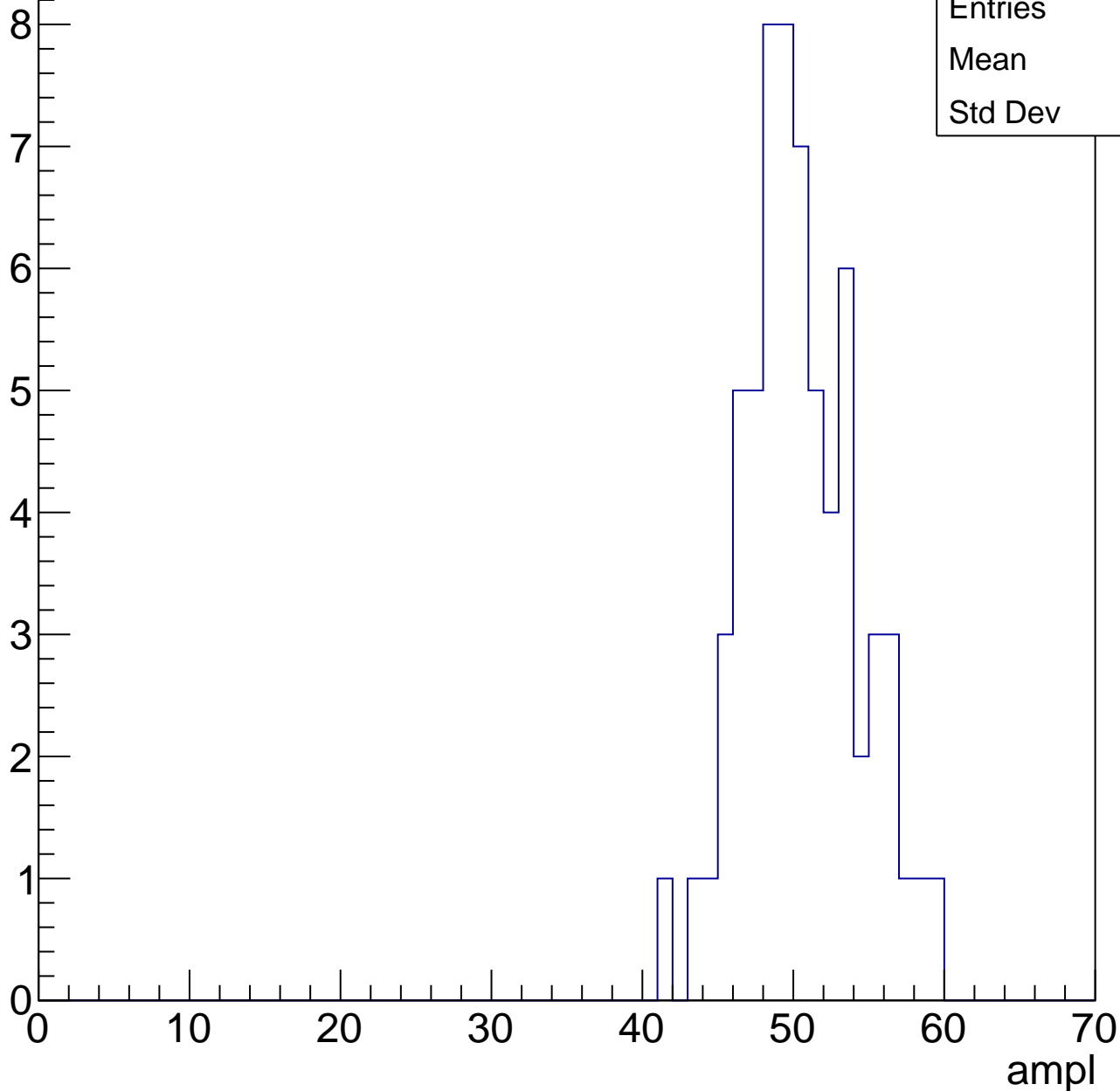


# B0L000S, U25-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

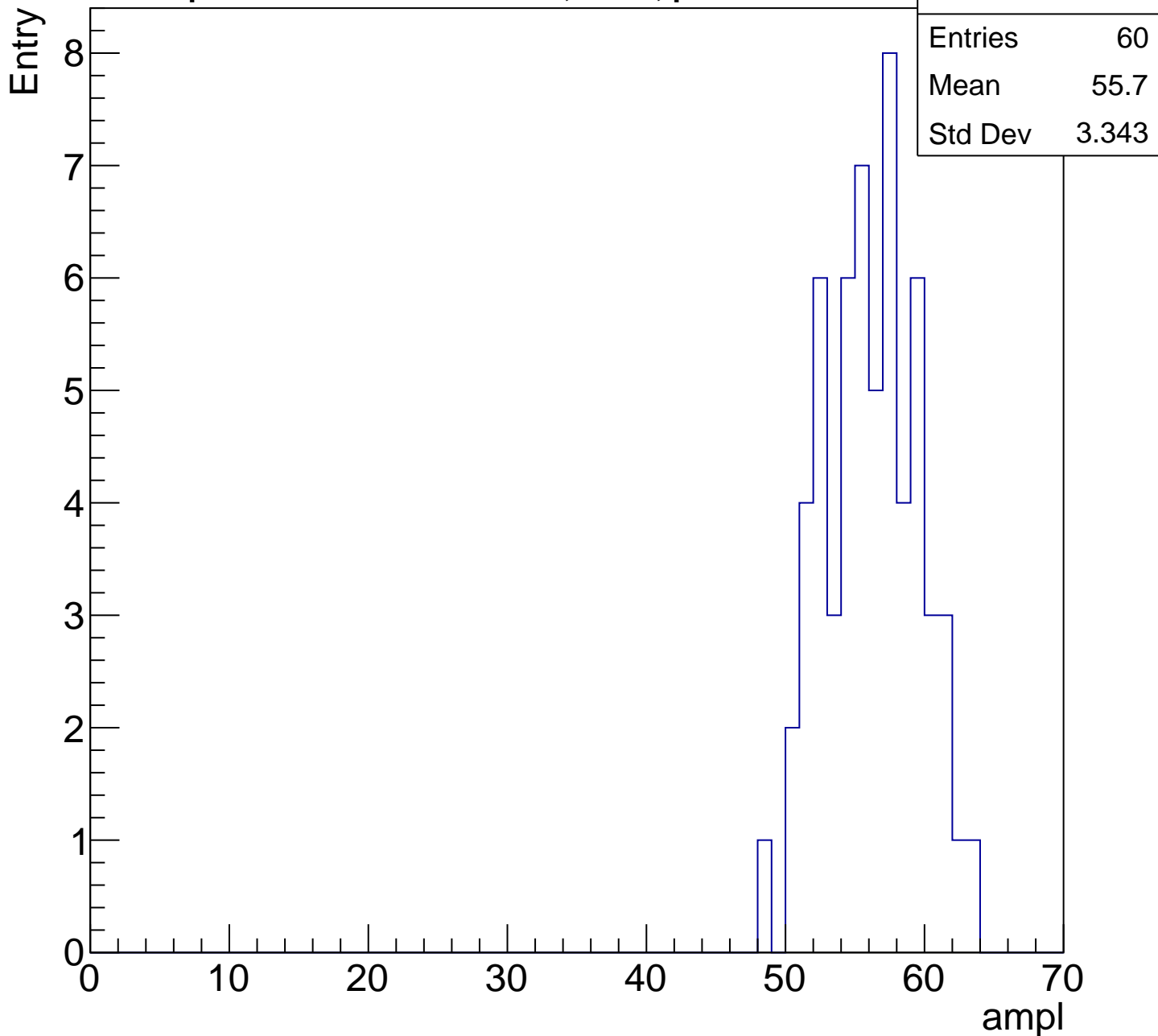
Entry

Entries	65
Mean	50
Std Dev	3.7



# B0L000S, U25-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

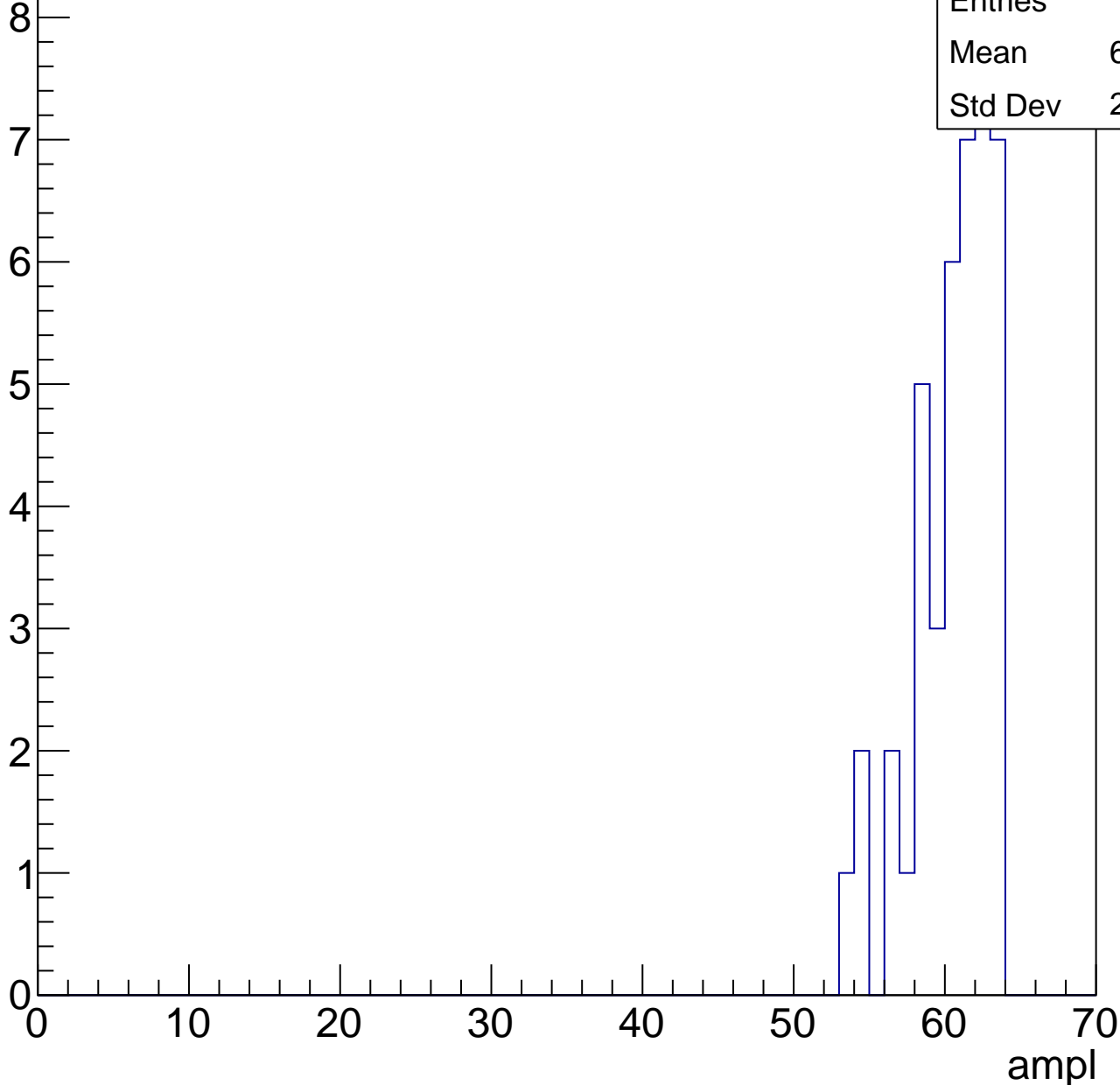


# B0L000S, U25-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

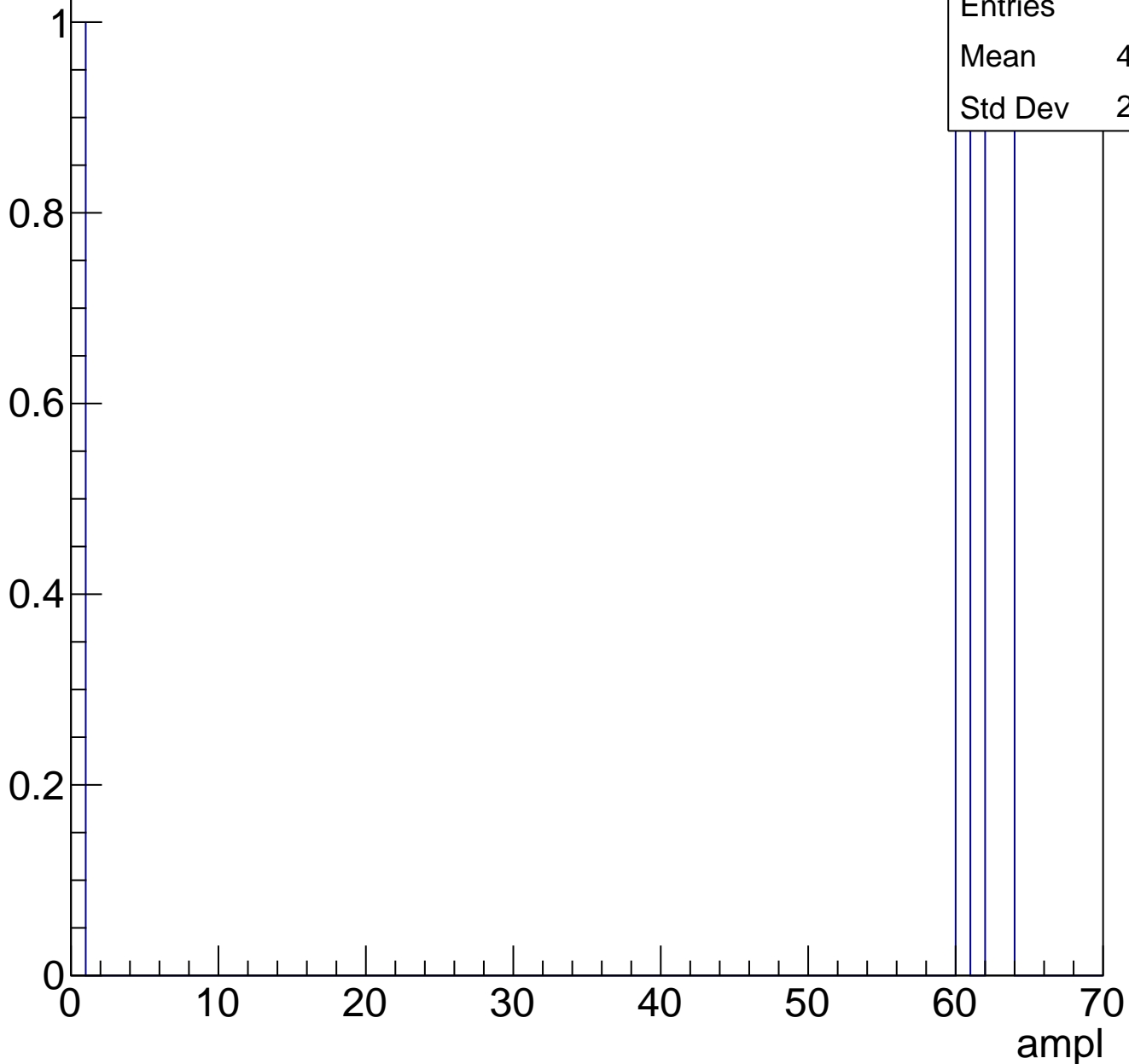
Entries	42
Mean	60.02
Std Dev	2.614



# B0L000S, U25-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch94, adc0

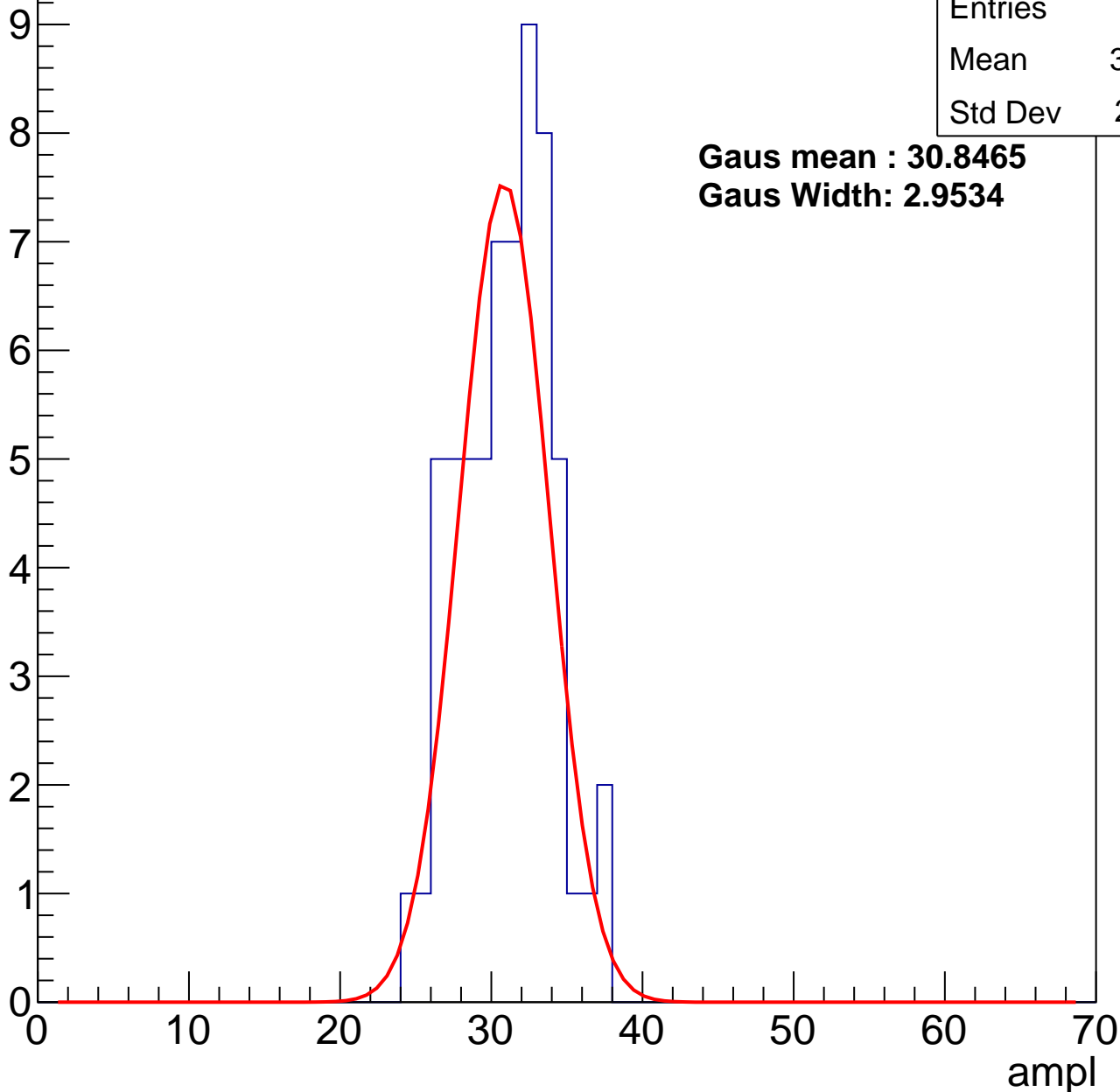
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	30.53
Std Dev	2.971

**Gaus mean : 30.8465**

**Gaus Width: 2.9534**



# B0L000S, U25-ch94, adc1

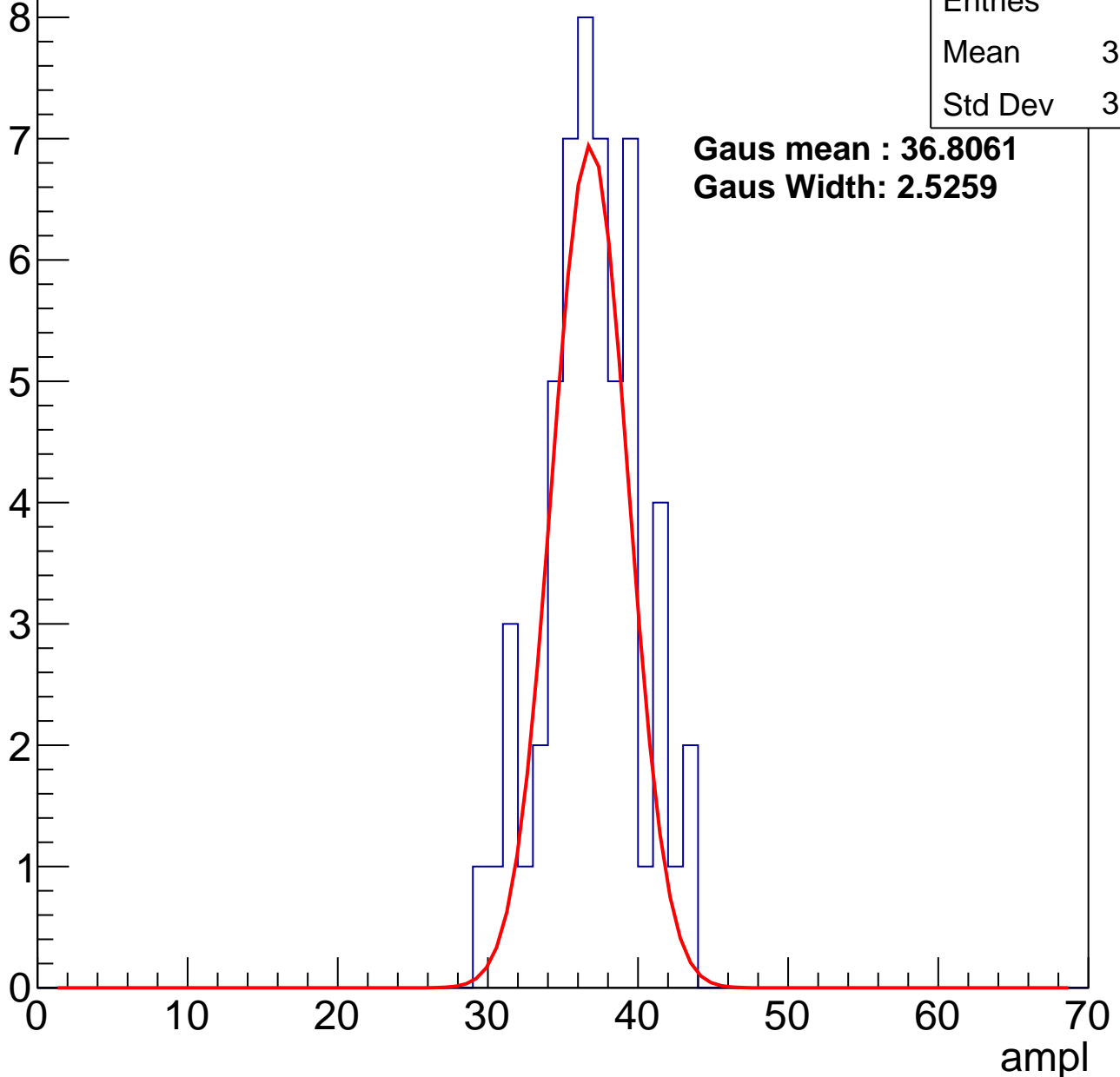
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	36.49
Std Dev	3.156

**Gaus mean : 36.8061**

**Gaus Width: 2.5259**



# B0L000S, U25-ch94, adc2

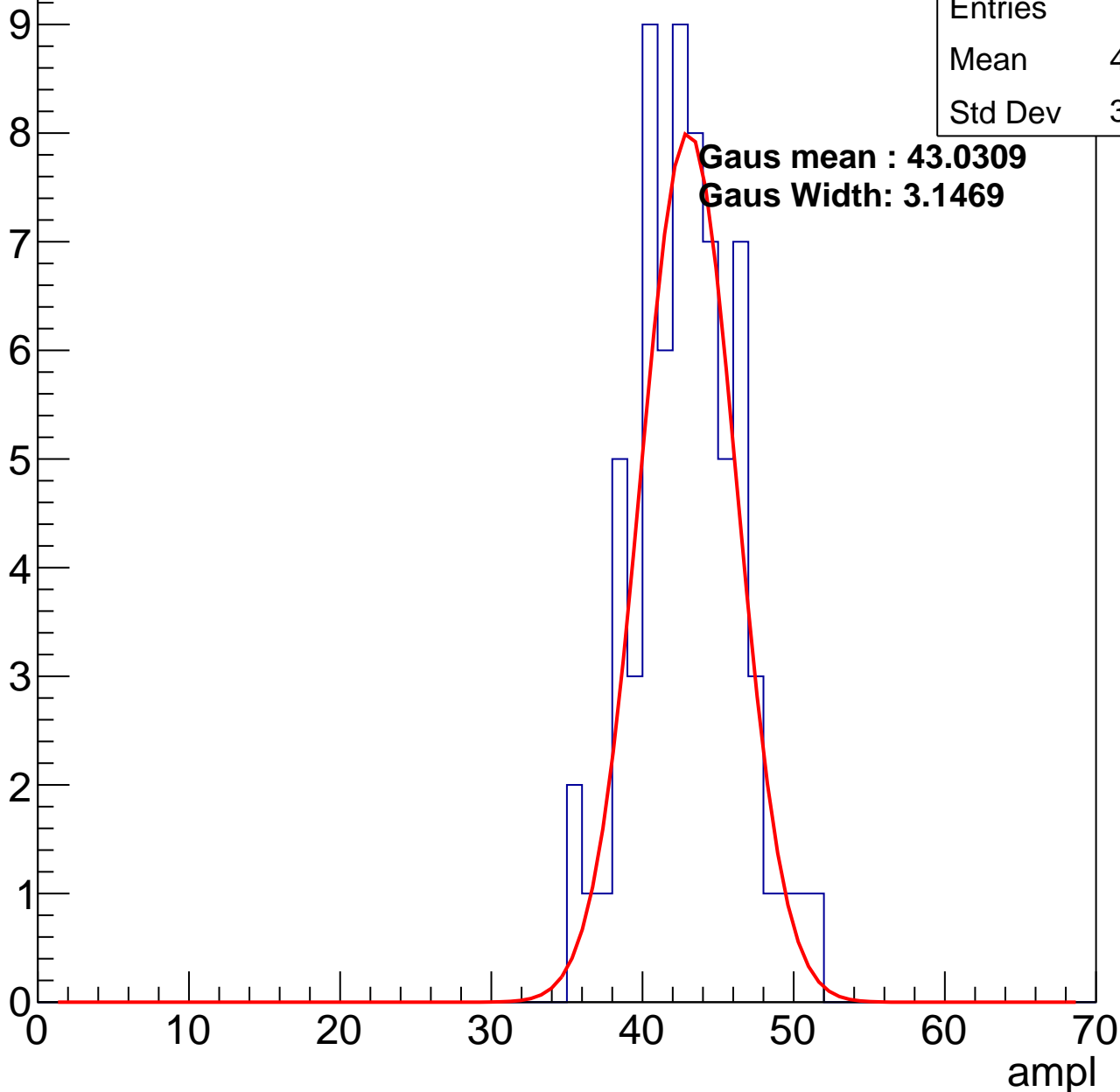
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	42.46
Std Dev	3.362

**Gaus mean : 43.0309**

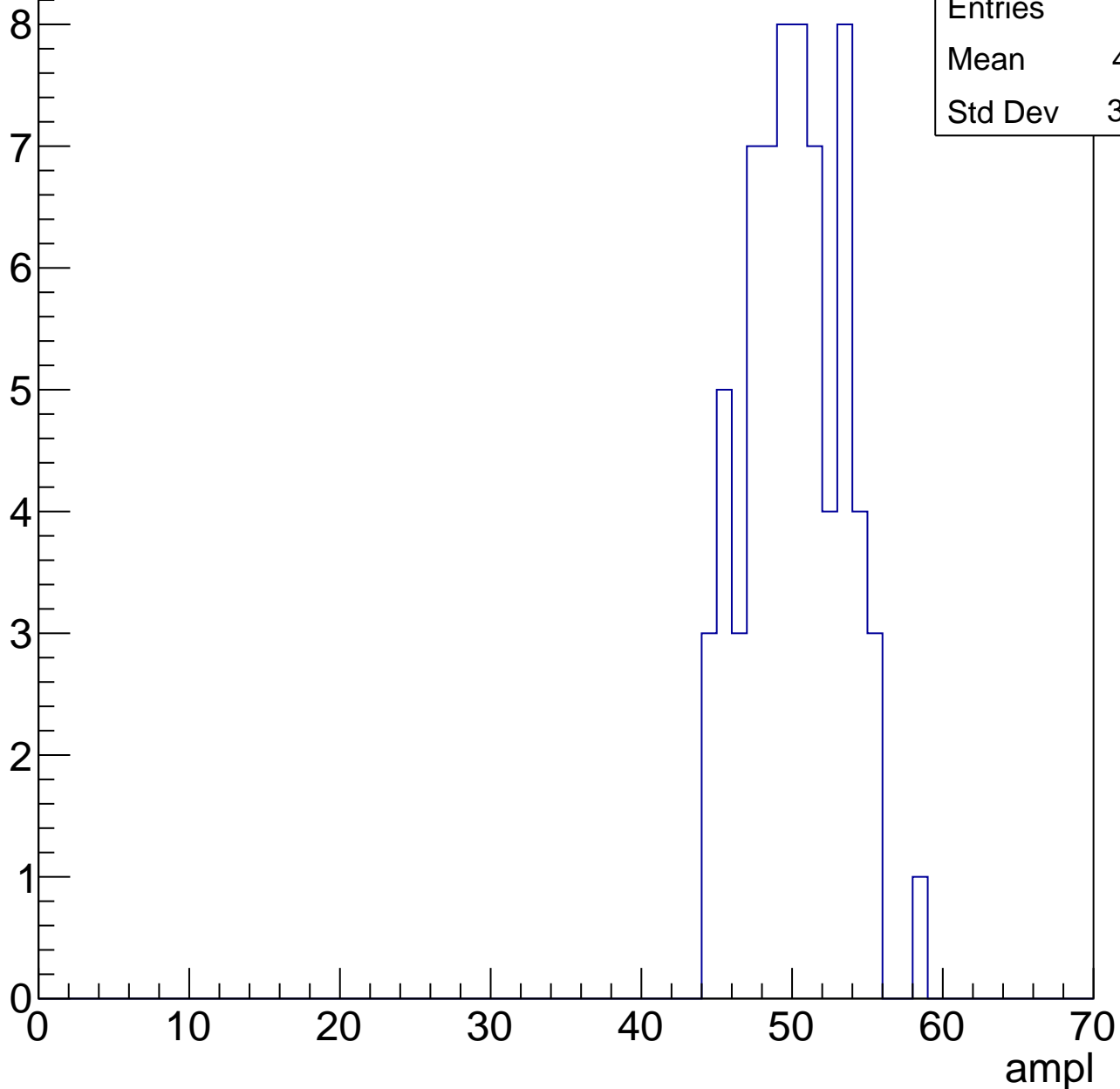
**Gaus Width: 3.1469**



# B0L000S, U25-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	56
Mean	55.39
Std Dev	3.177

Entry

10

8

6

4

2

0

0

10

20

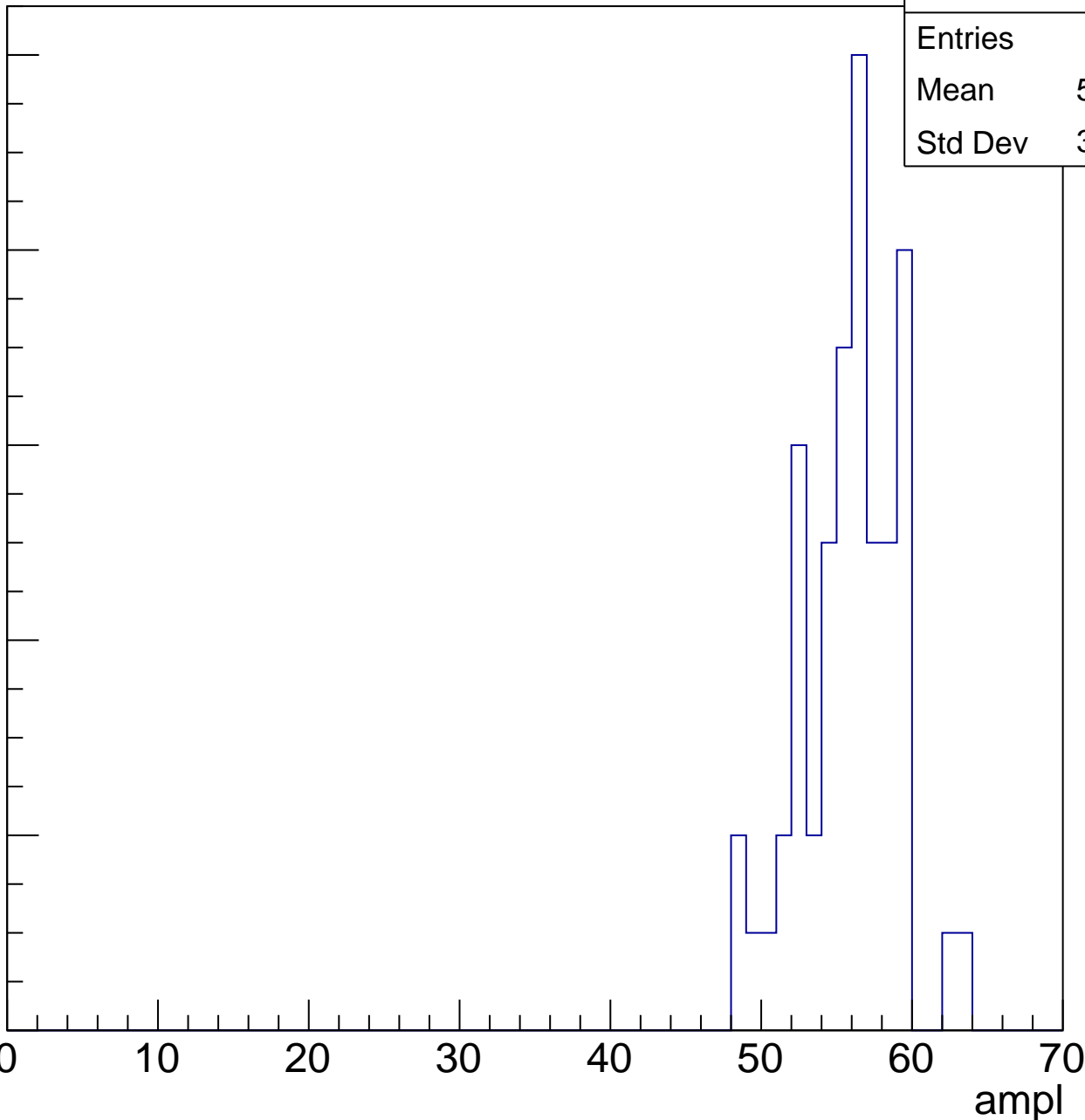
30

40

50

60

ampl

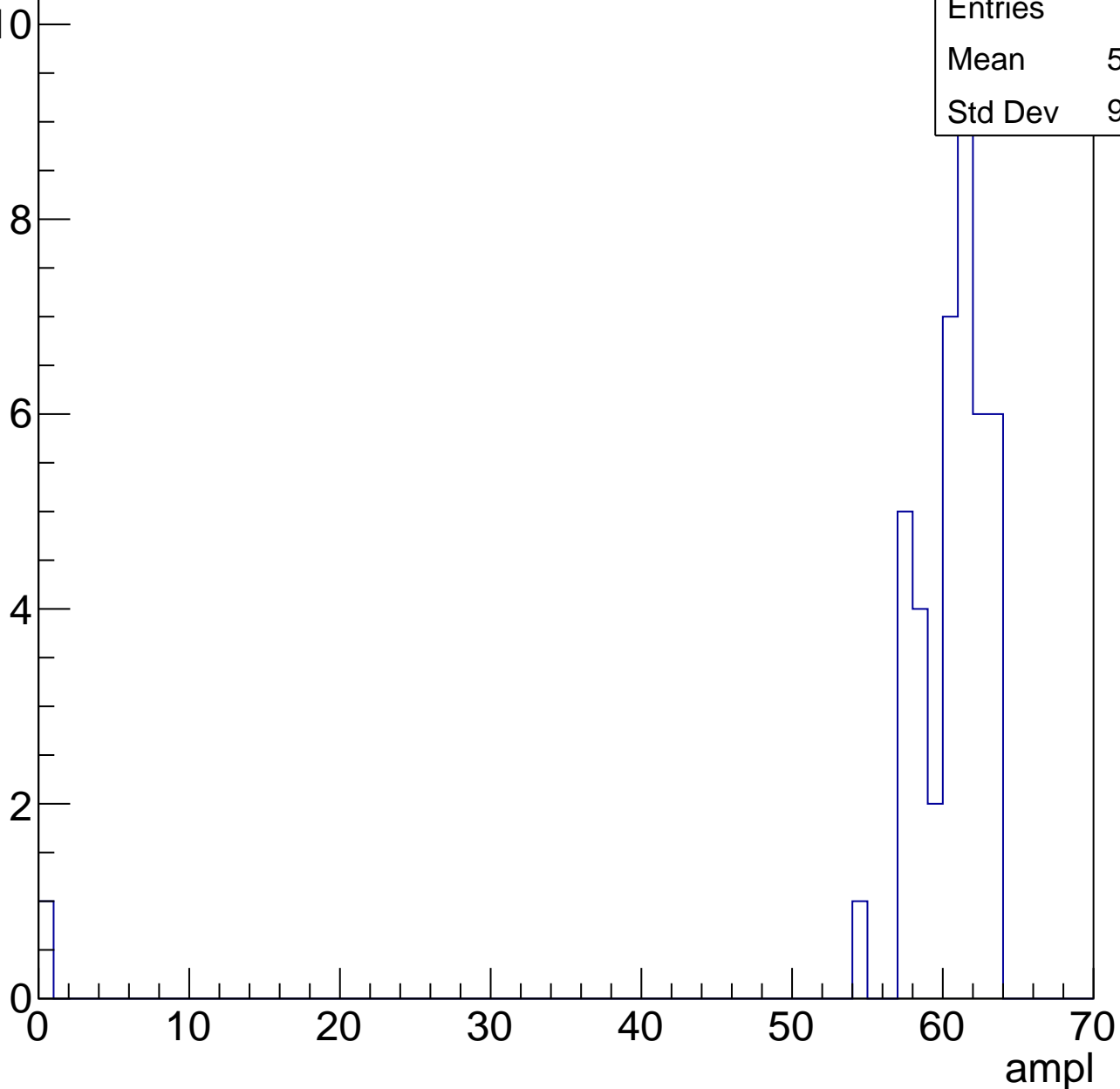


# B0L000S, U25-ch94, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

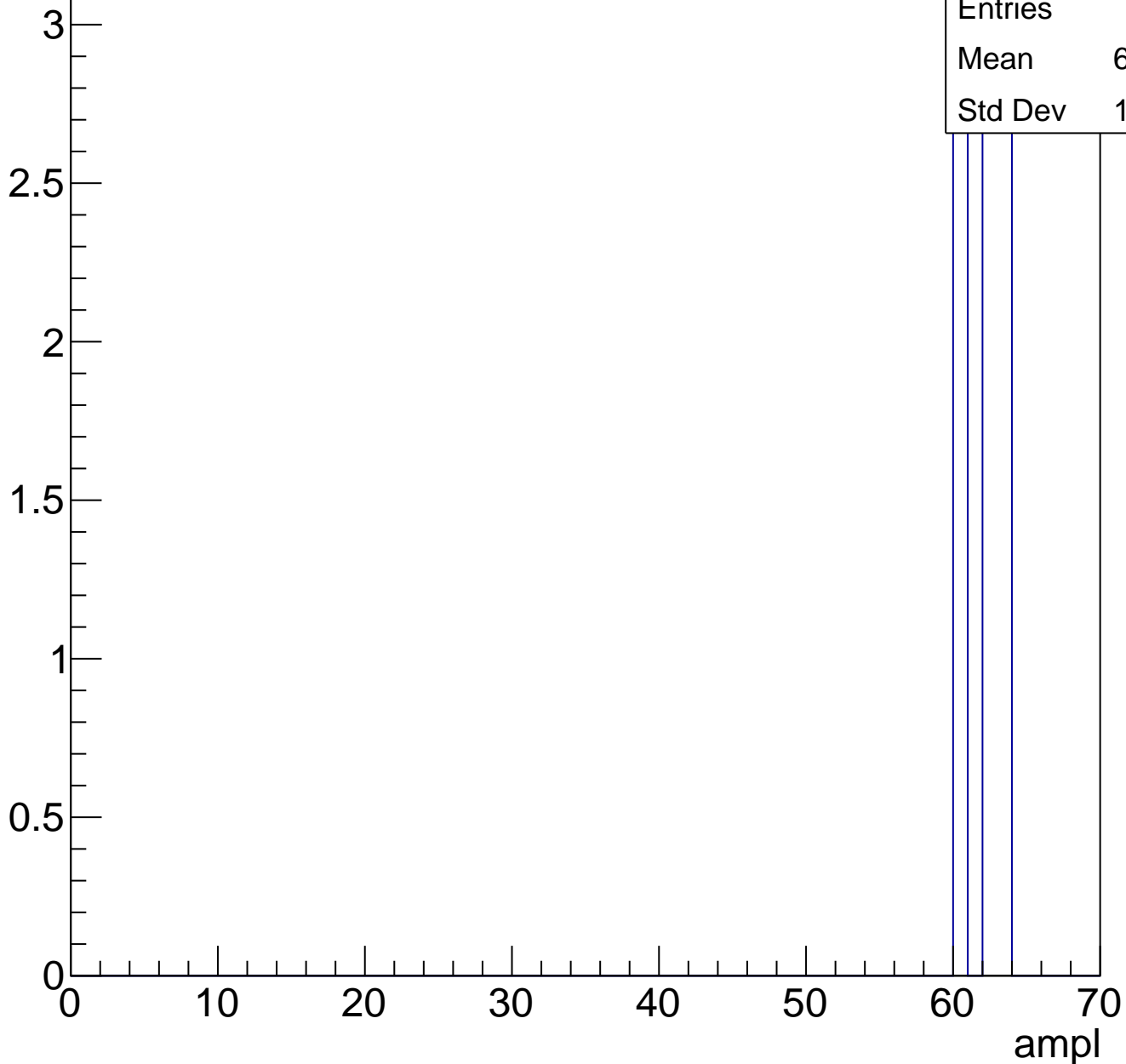
Entries	42
Mean	58.79
Std Dev	9.418



# B0L000S, U25-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch95, adc0

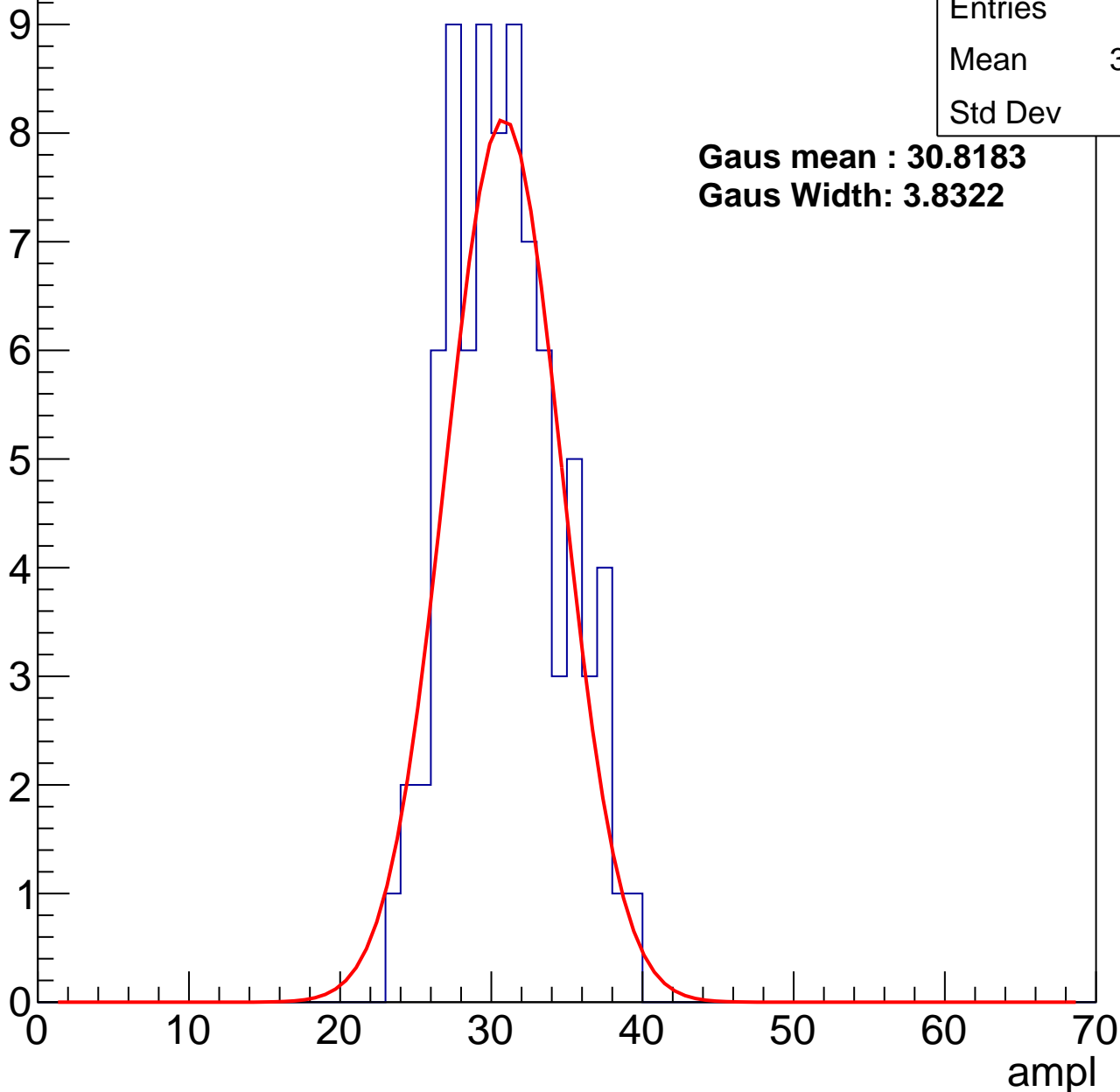
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	30.49
Std Dev	3.62

**Gaus mean : 30.8183**

**Gaus Width: 3.8322**



# B0L000S, U25-ch95, adc1

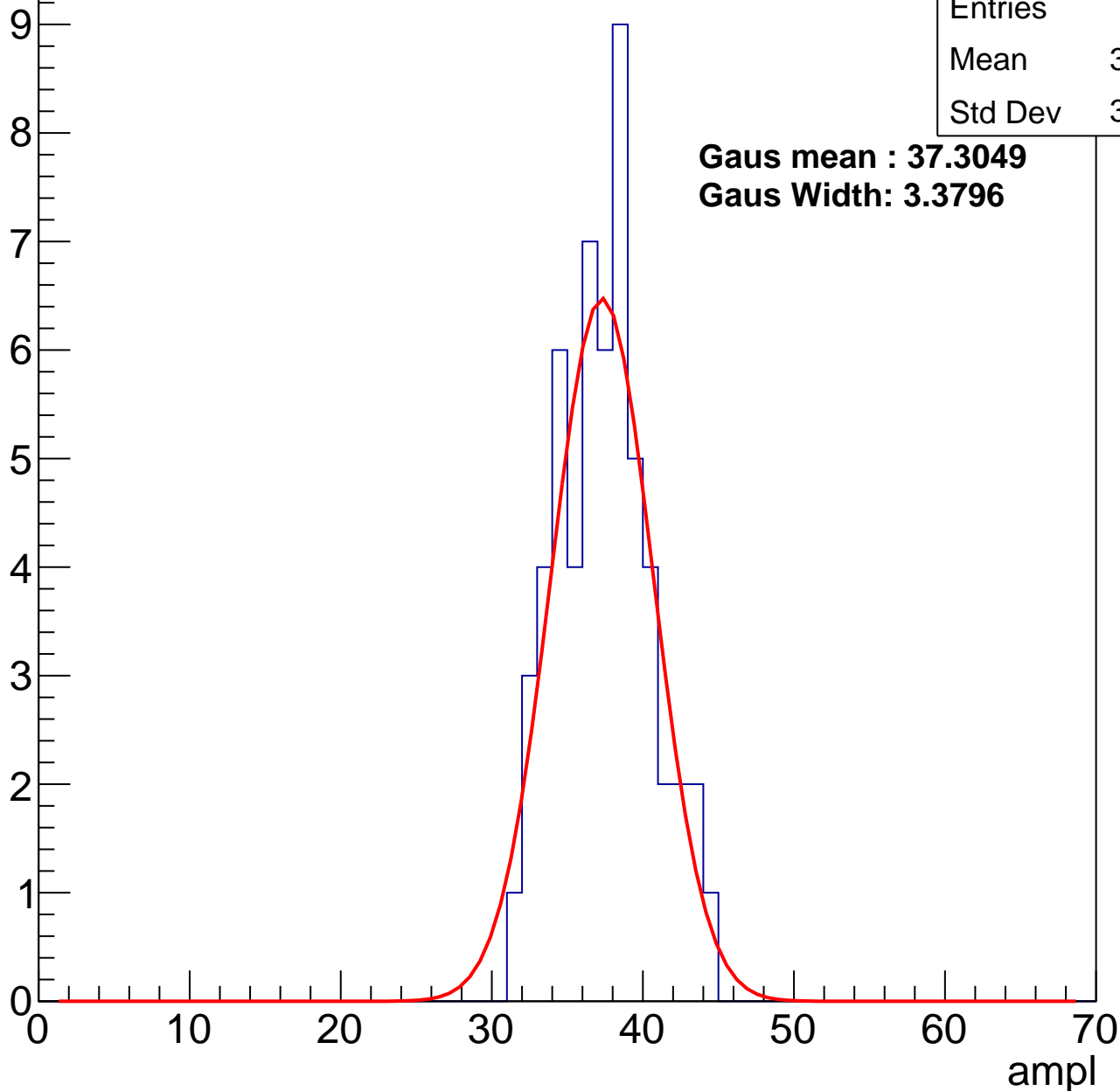
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.96
Std Dev	3.047

**Gaus mean : 37.3049**

**Gaus Width: 3.3796**



# B0L000S, U25-ch95, adc2

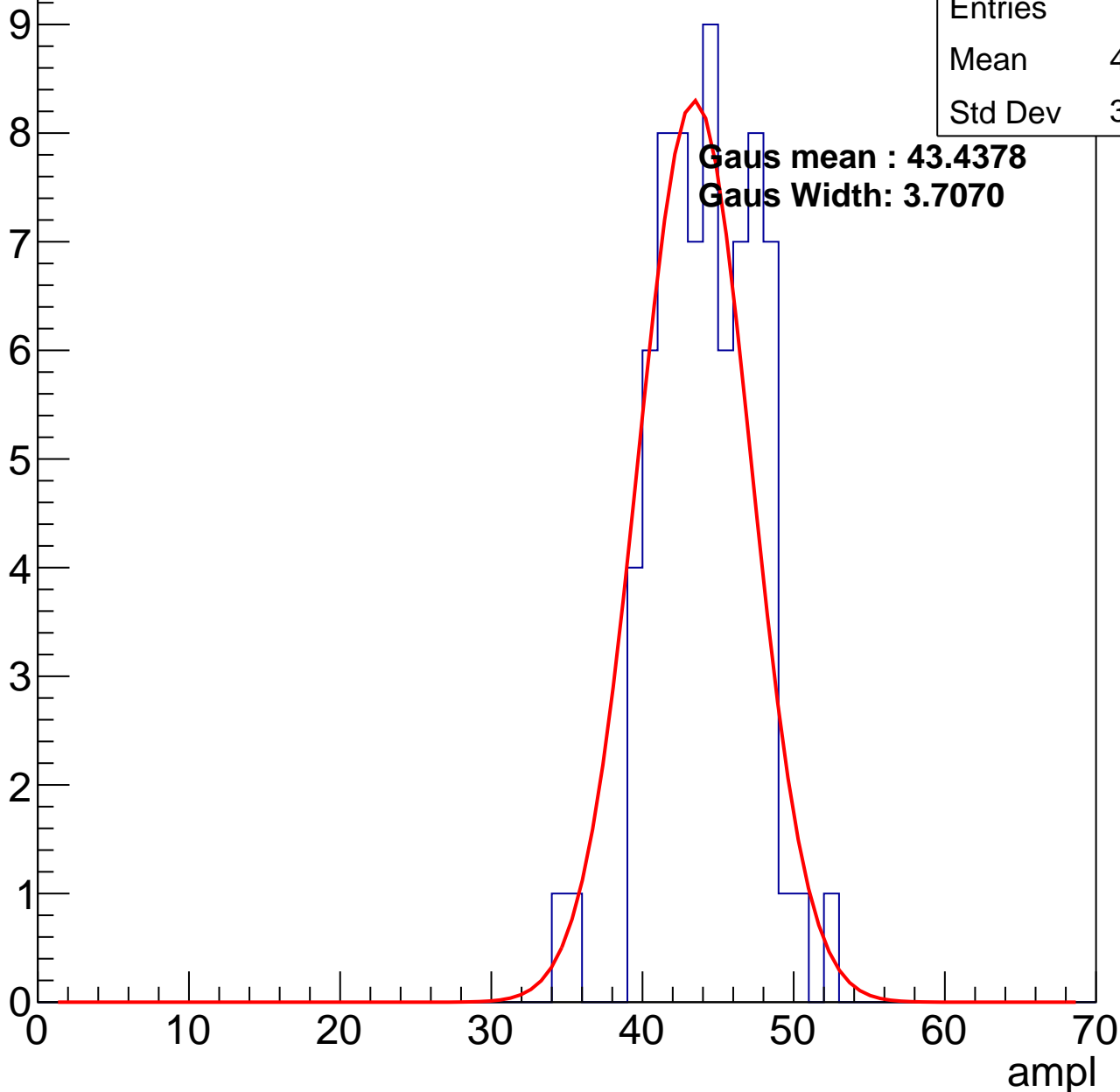
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	43.75
Std Dev	3.319

**Gaus mean : 43.4378**

**Gaus Width: 3.7070**

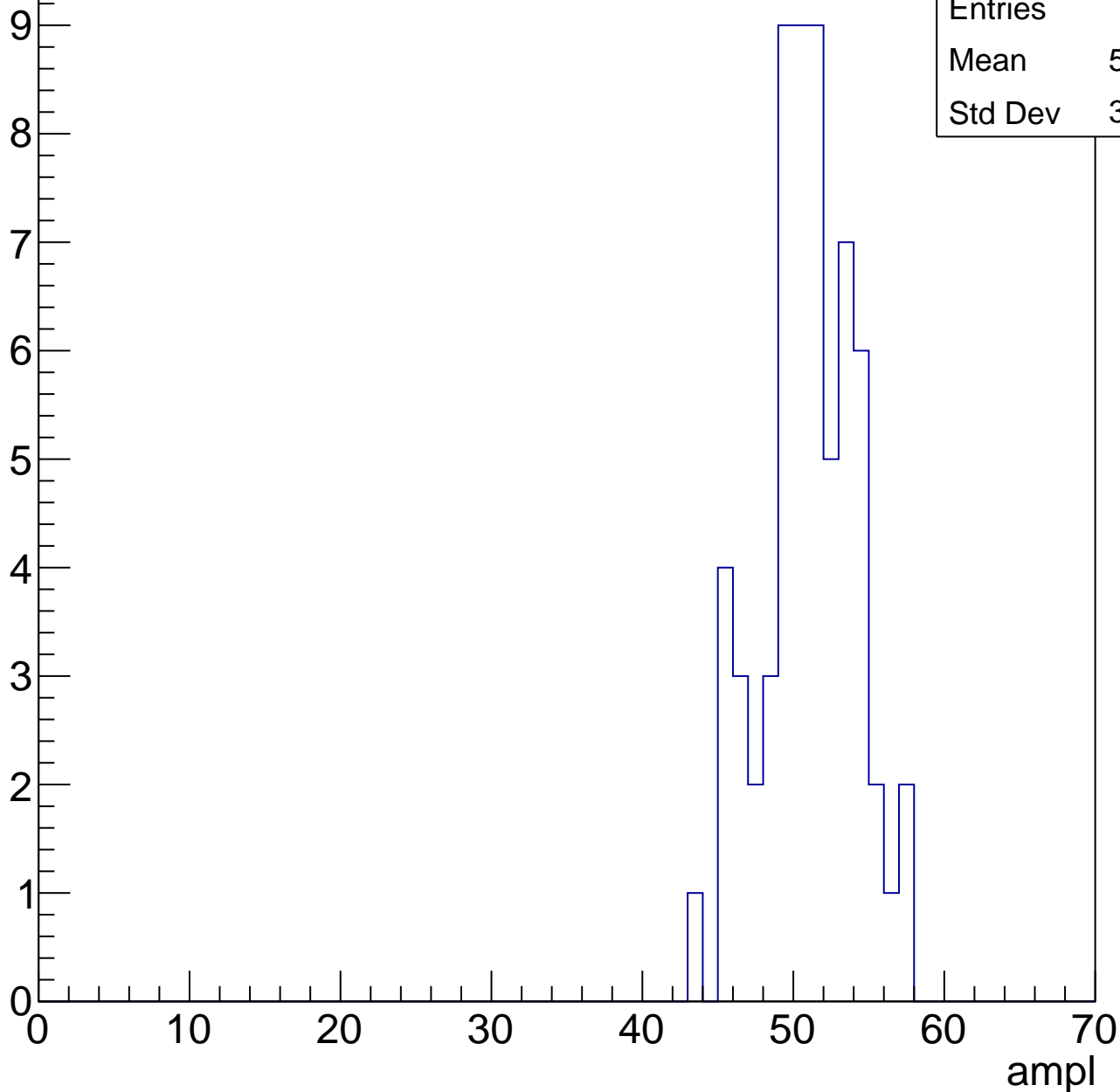


# B0L000S, U25-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

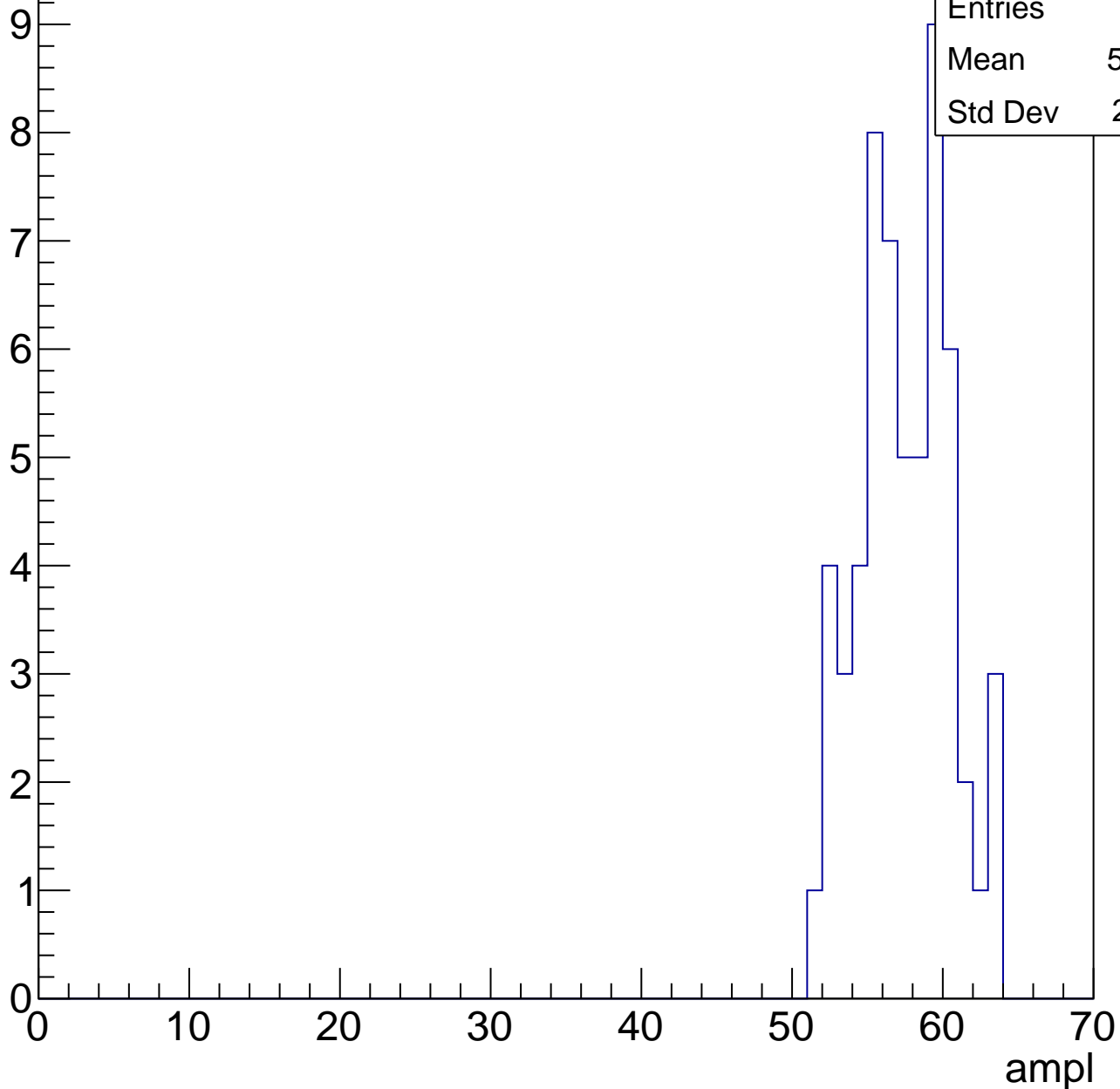
Entries	63
Mean	50.54
Std Dev	3.059



# B0L000S, U25-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



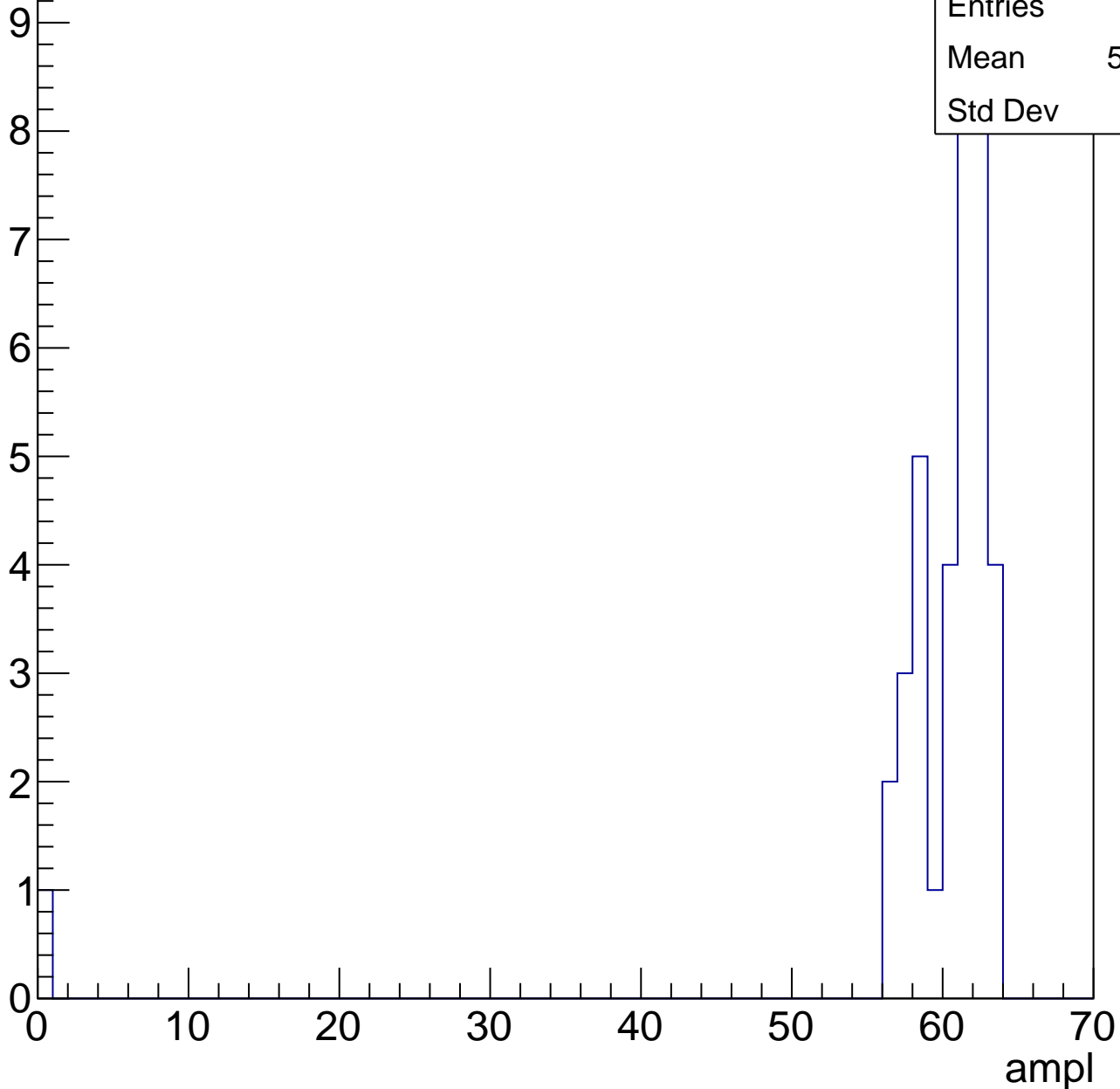
Entries	58
Mean	56.98
Std Dev	2.991

# B0L000S, U25-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	37
Mean	58.65
Std Dev	9.99



# B0L000S, U25-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	63
Std Dev	0



# B0L000S, U25-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch96, adc0

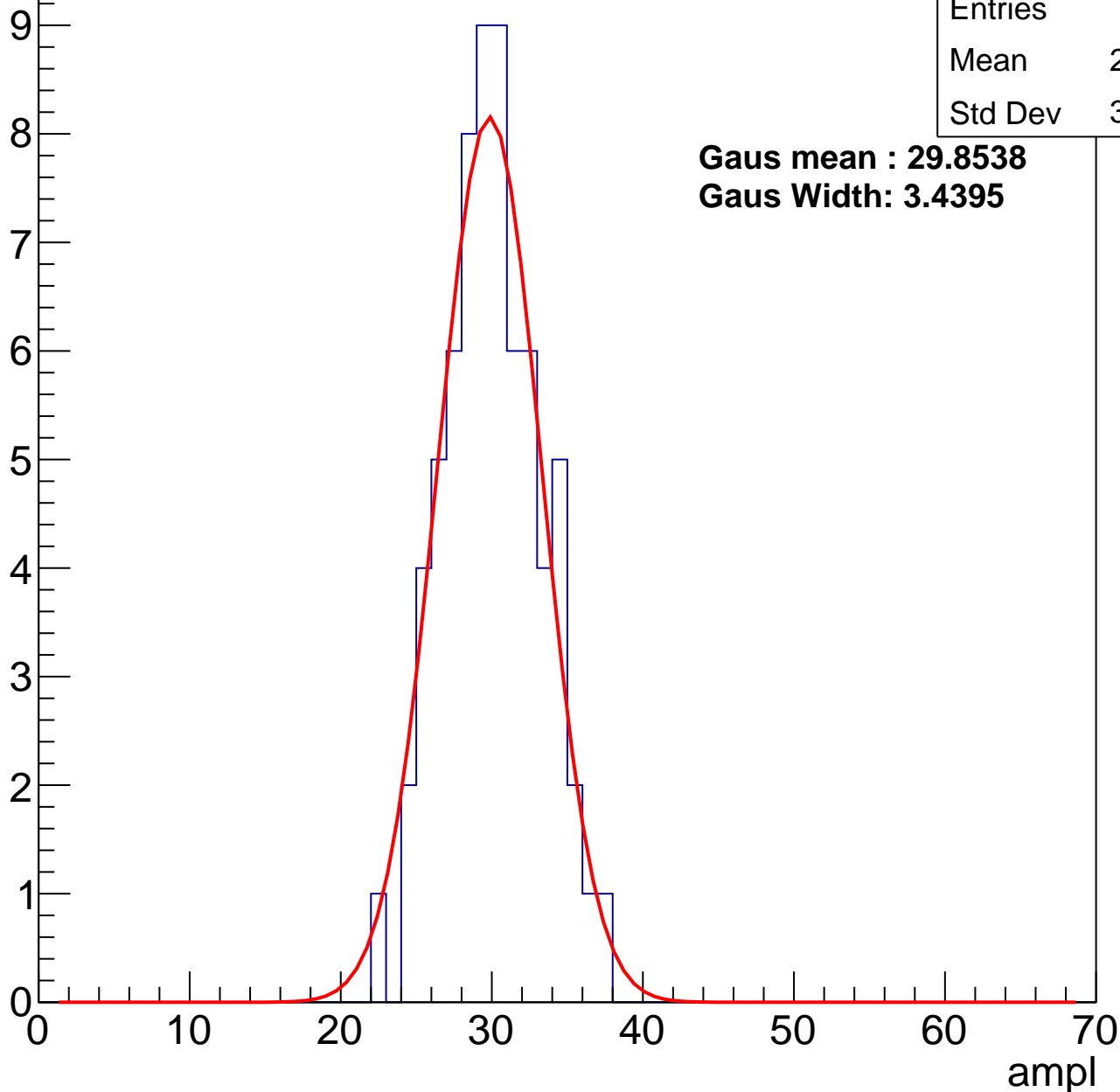
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.57
Std Dev	3.137

**Gaus mean : 29.8538**

**Gaus Width: 3.4395**



# B0L000S, U25-ch96, adc1

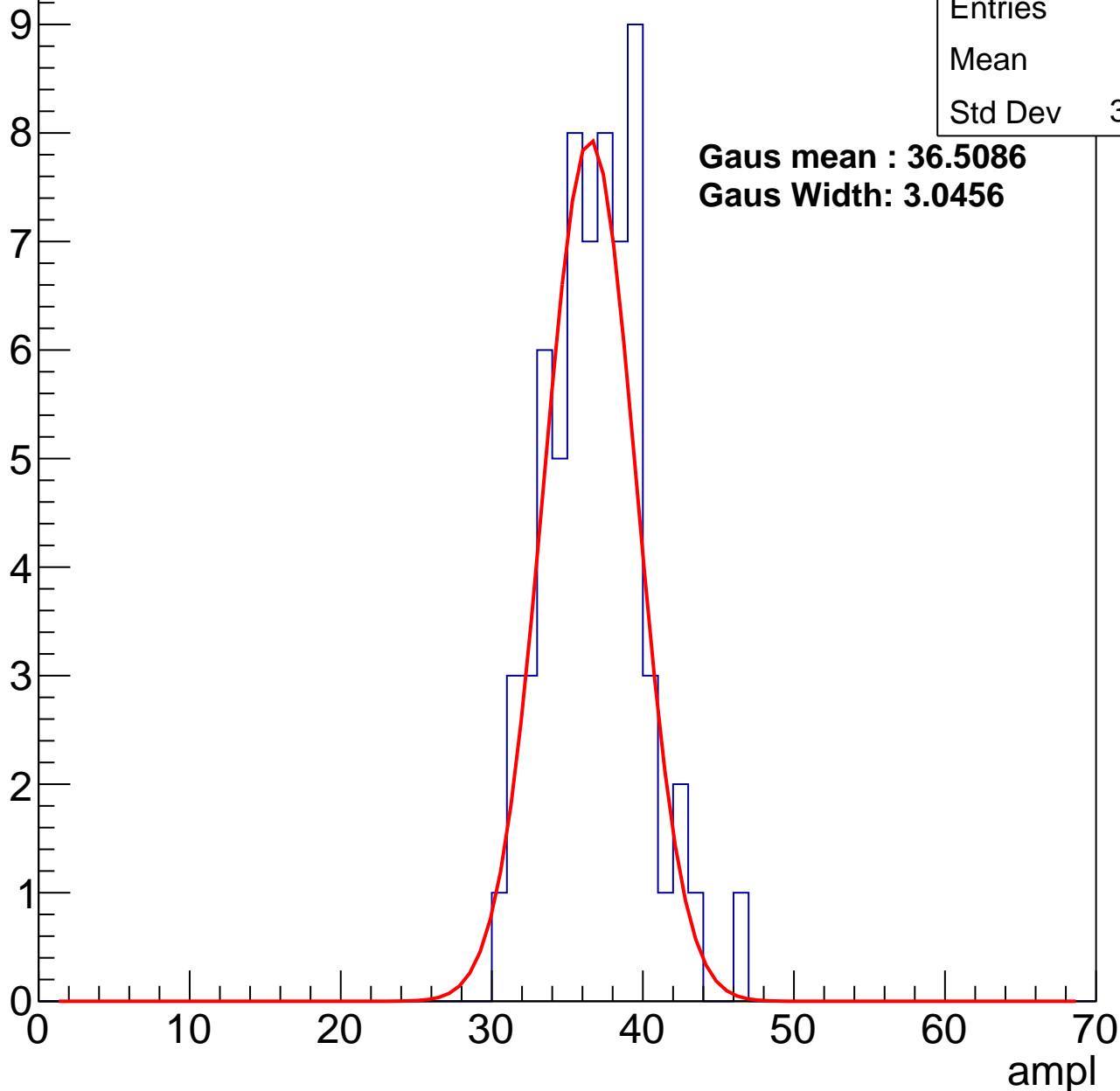
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	36.4
Std Dev	3.142

**Gaus mean : 36.5086**

**Gaus Width: 3.0456**



# B0L000S, U25-ch96, adc2

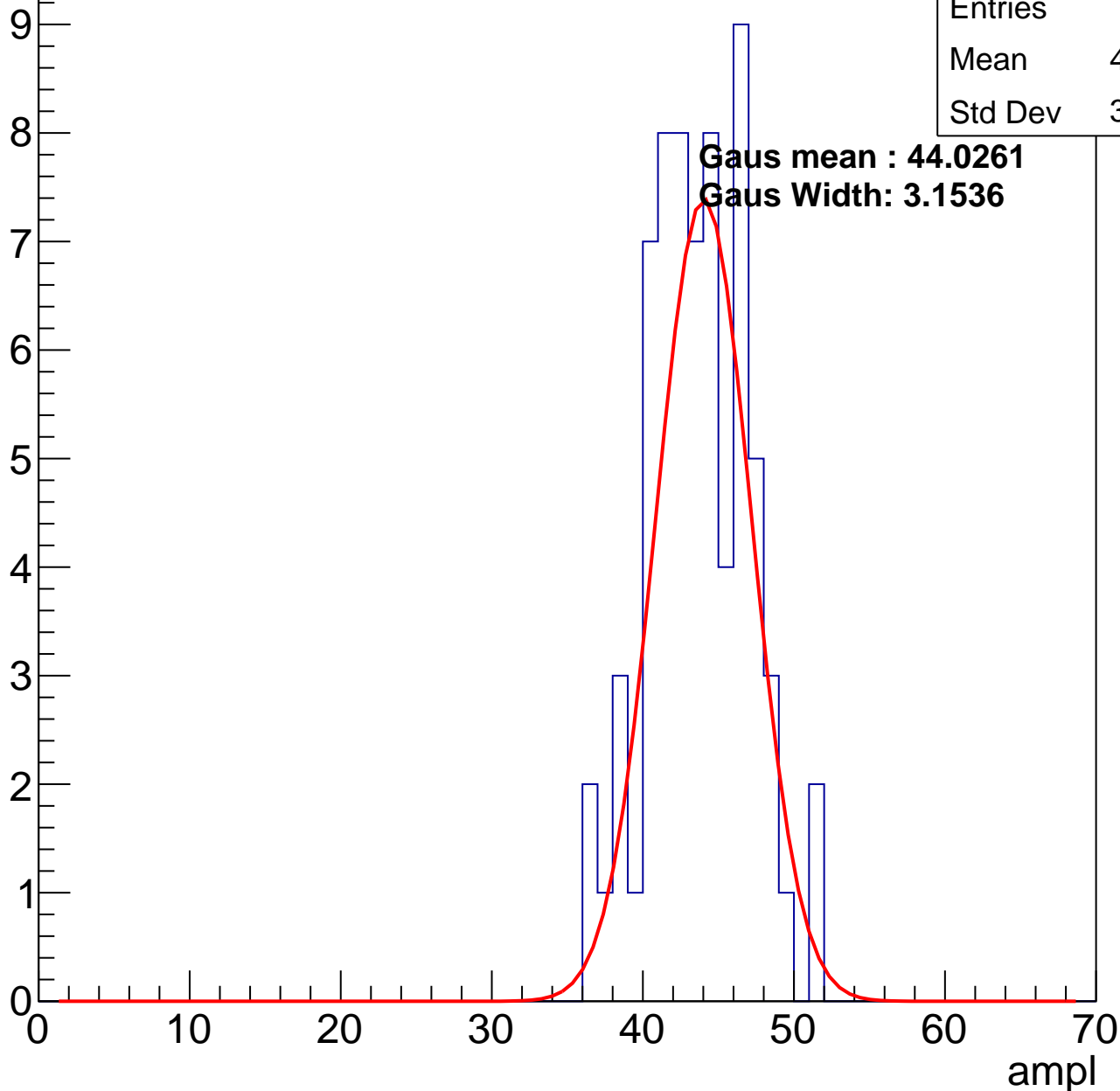
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	43.23
Std Dev	3.293

**Gaus mean : 44.0261**

**Gaus Width: 3.1536**

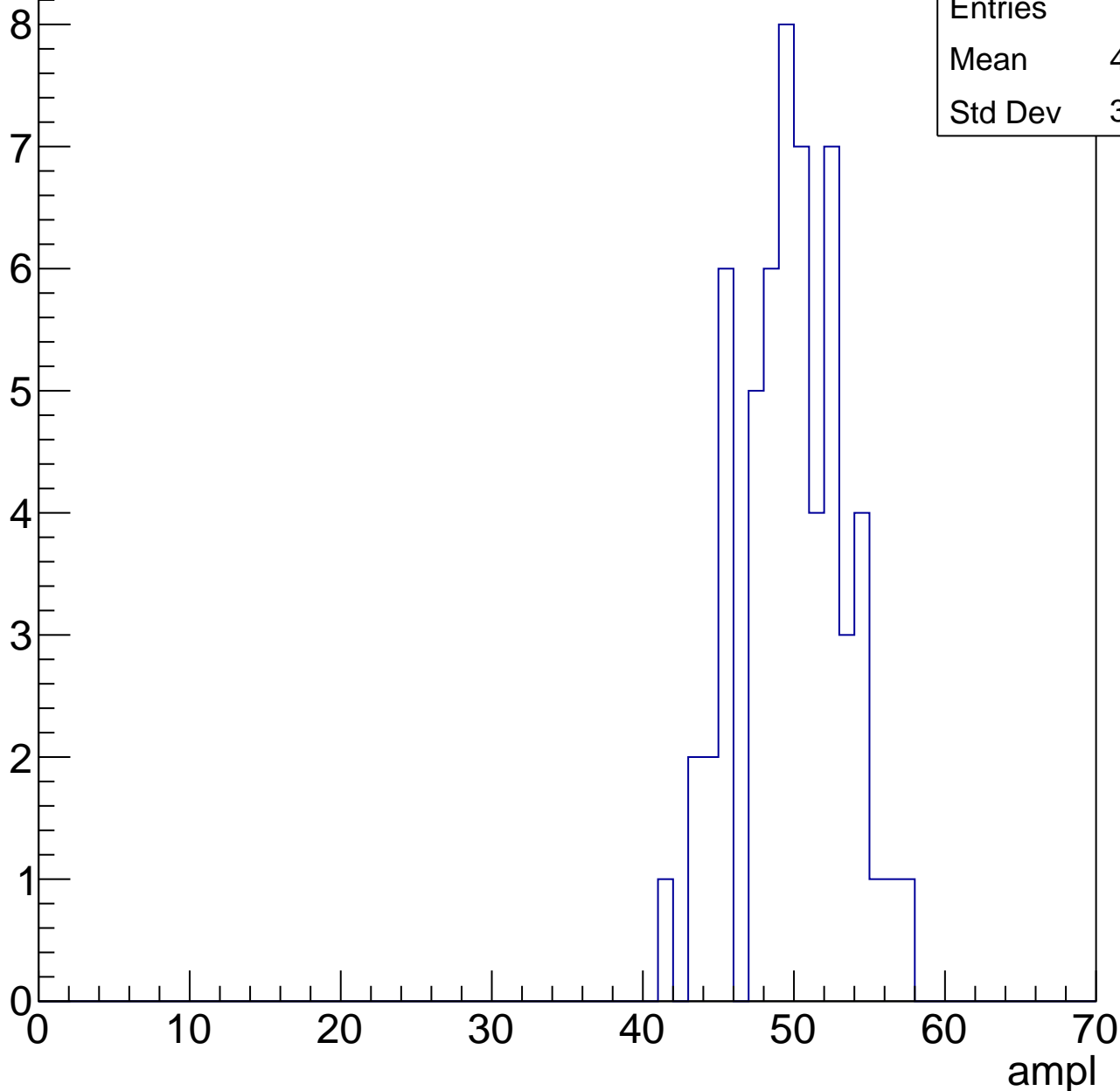


# B0L000S, U25-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	49.33
Std Dev	3.436



# B0L000S, U25-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

7

6

5

4

3

2

1

0

Entries 53

Mean 55.13

Std Dev 3.297

ampl

0

10

20

30

40

50

60

70

0

10

20

30

40

50

60

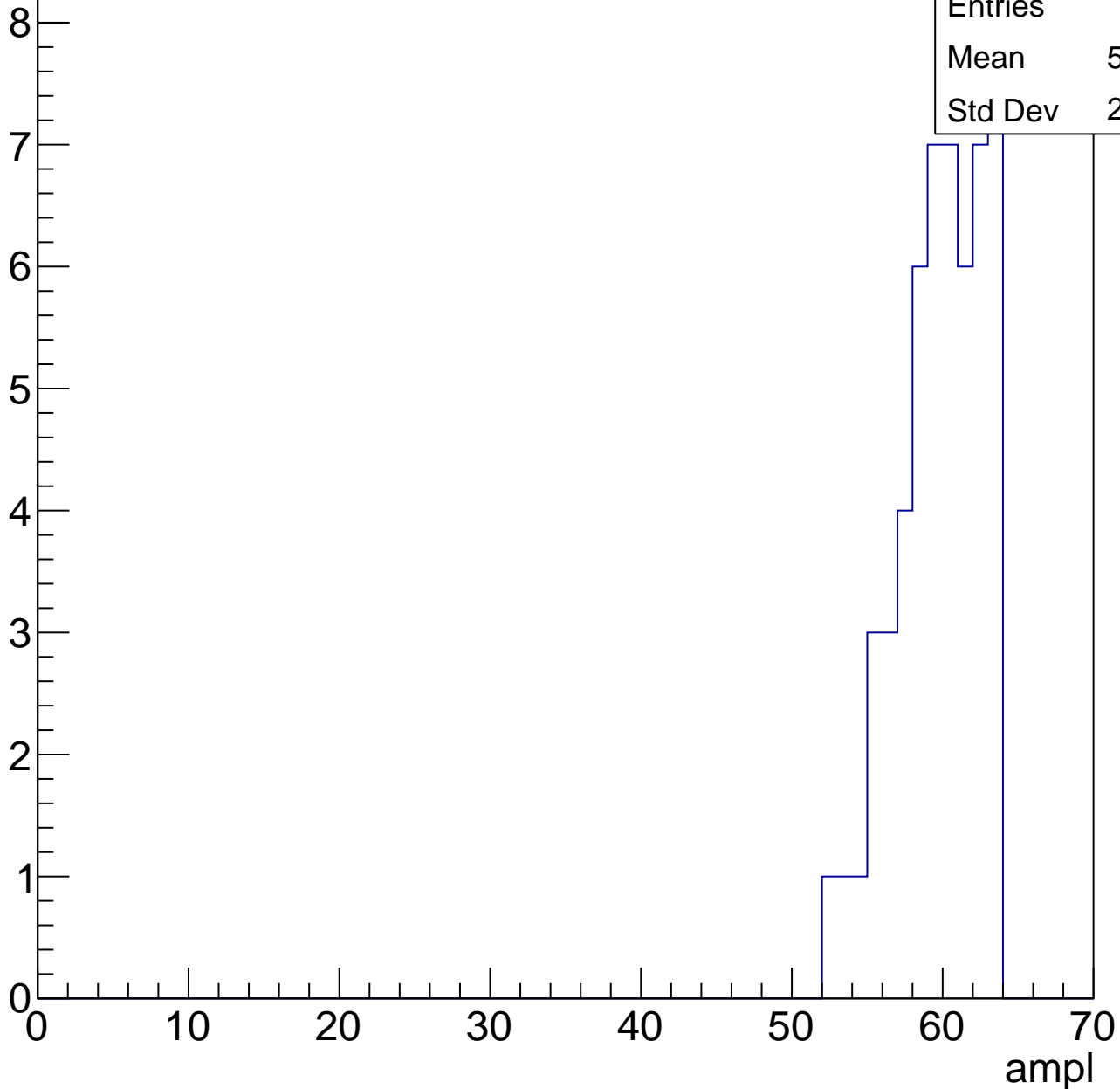
70

# B0L000S, U25-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

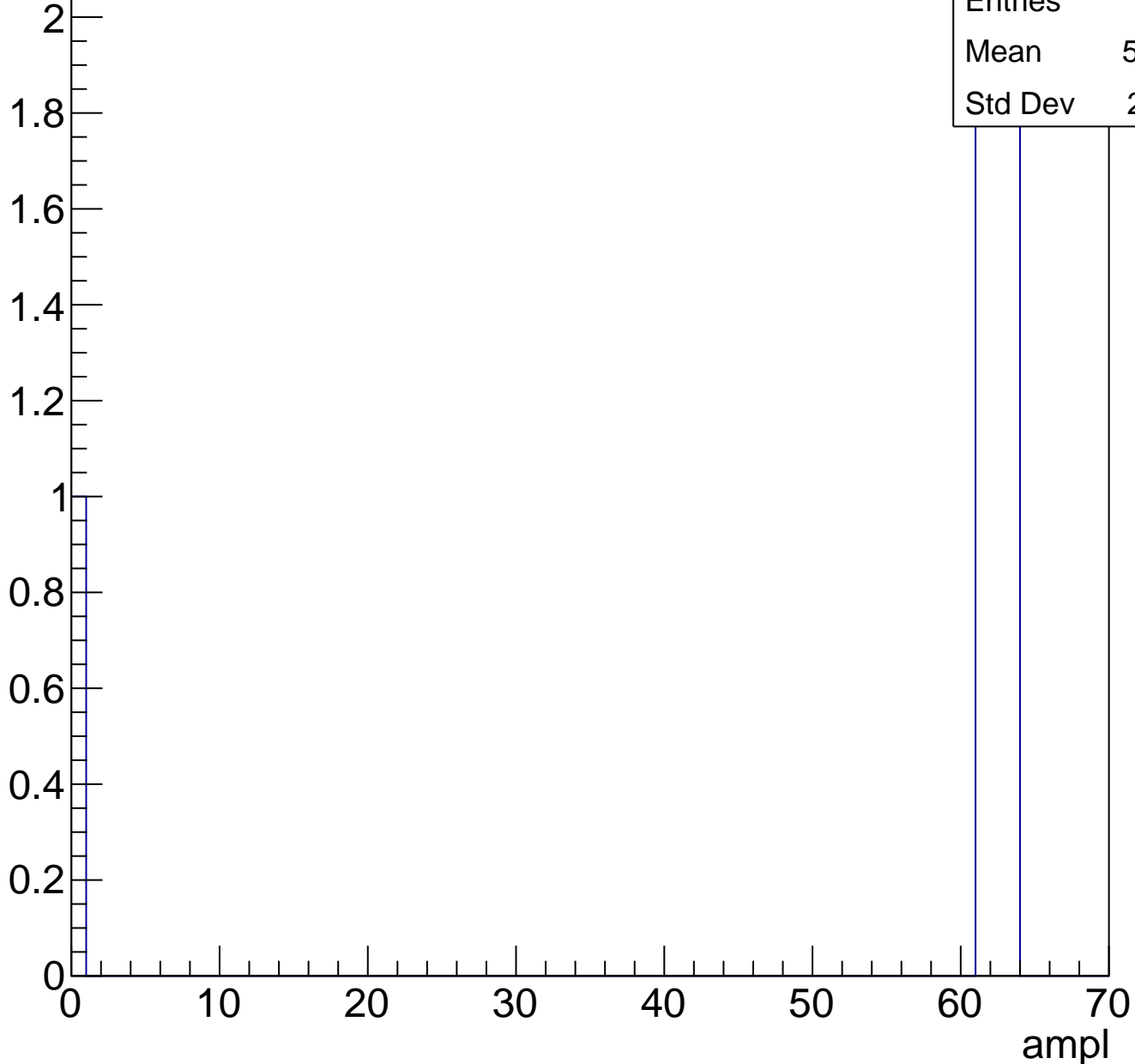
Entries	54
Mean	59.35
Std Dev	2.797



# B0L000S, U25-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch96, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch97, adc0

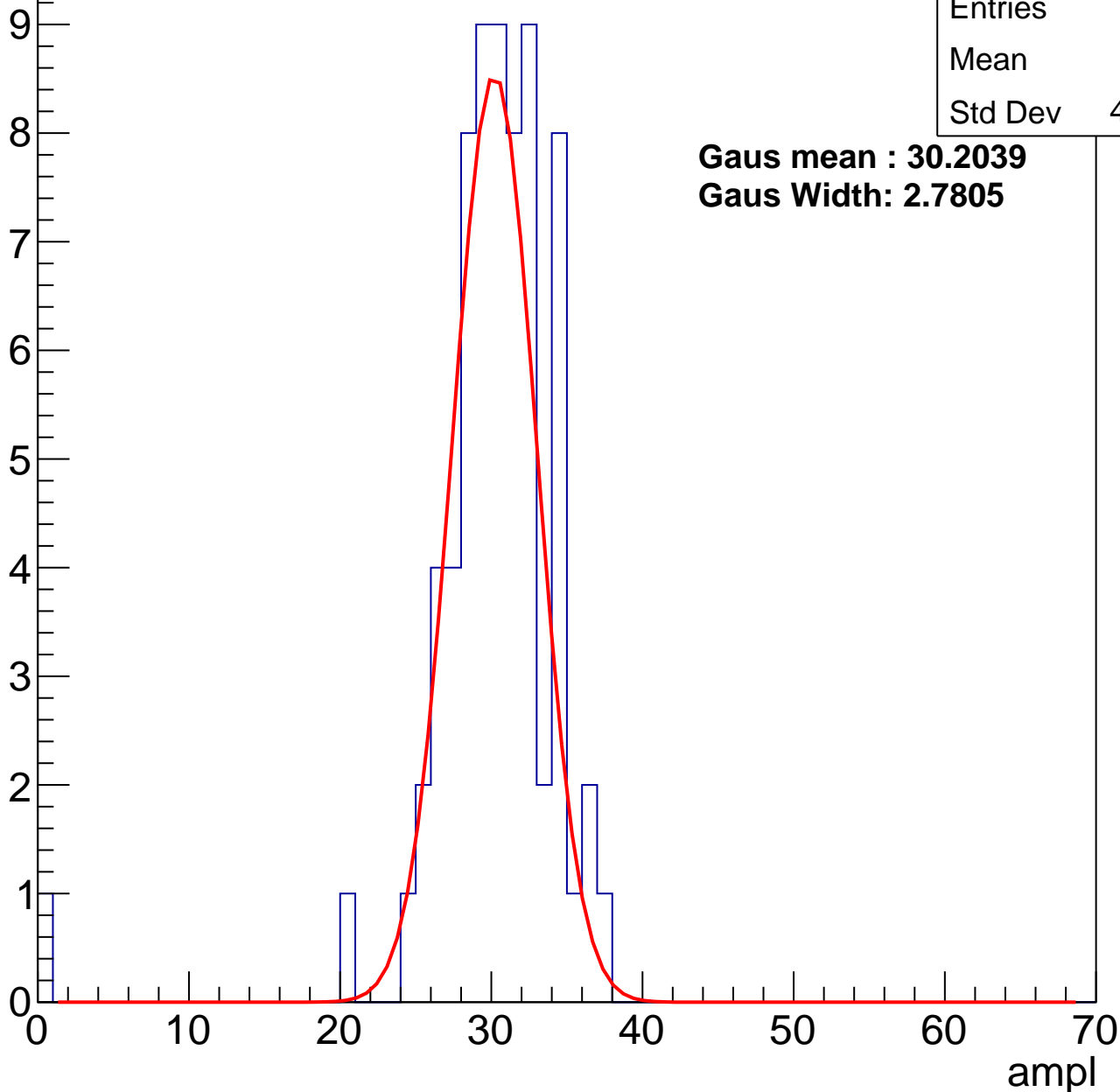
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.7
Std Dev	4.716

**Gaus mean : 30.2039**

**Gaus Width: 2.7805**



# B0L000S, U25-ch97, adc1

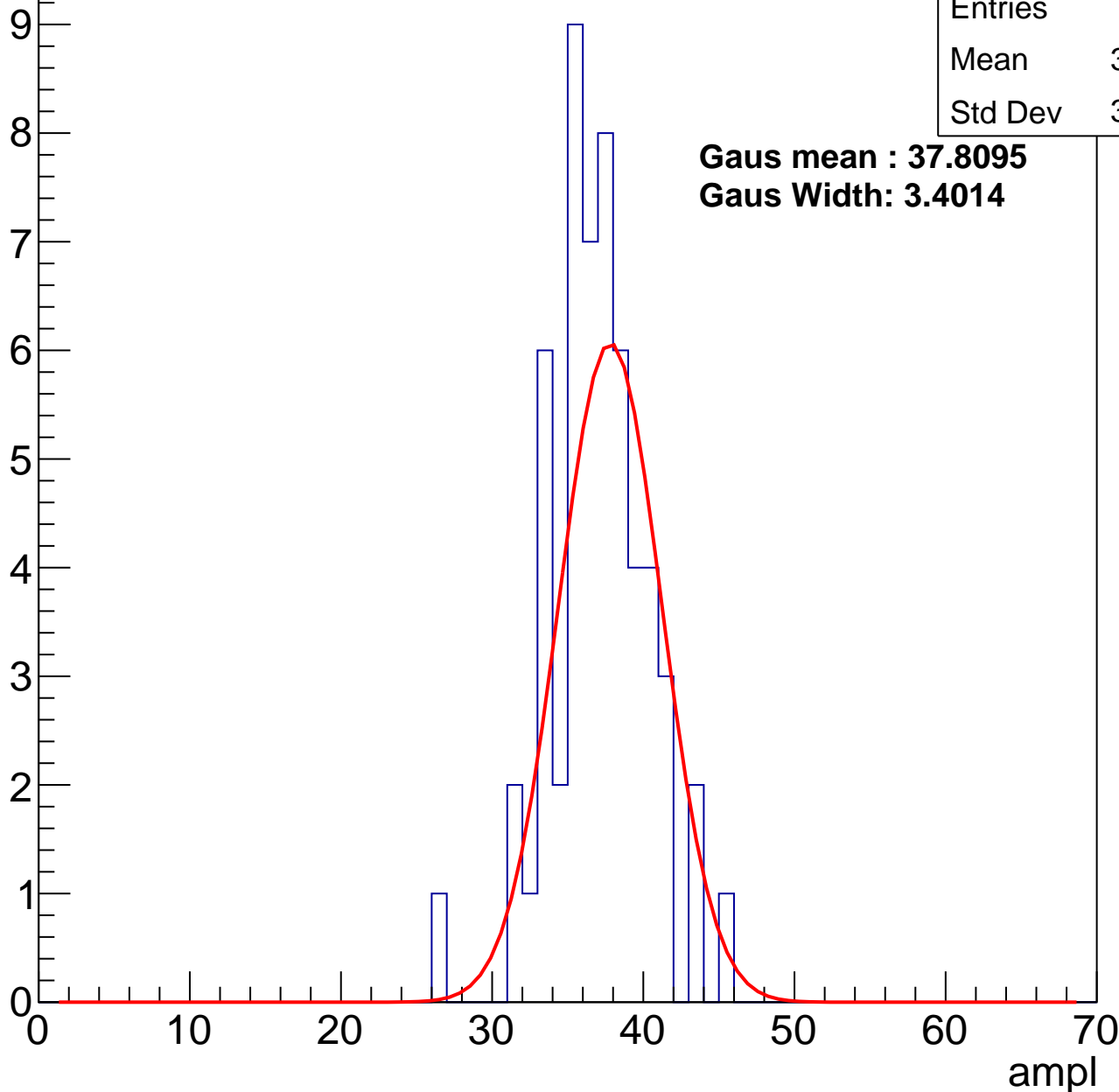
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.55
Std Dev	3.289

**Gaus mean : 37.8095**

**Gaus Width: 3.4014**



# B0L000S, U25-ch97, adc2

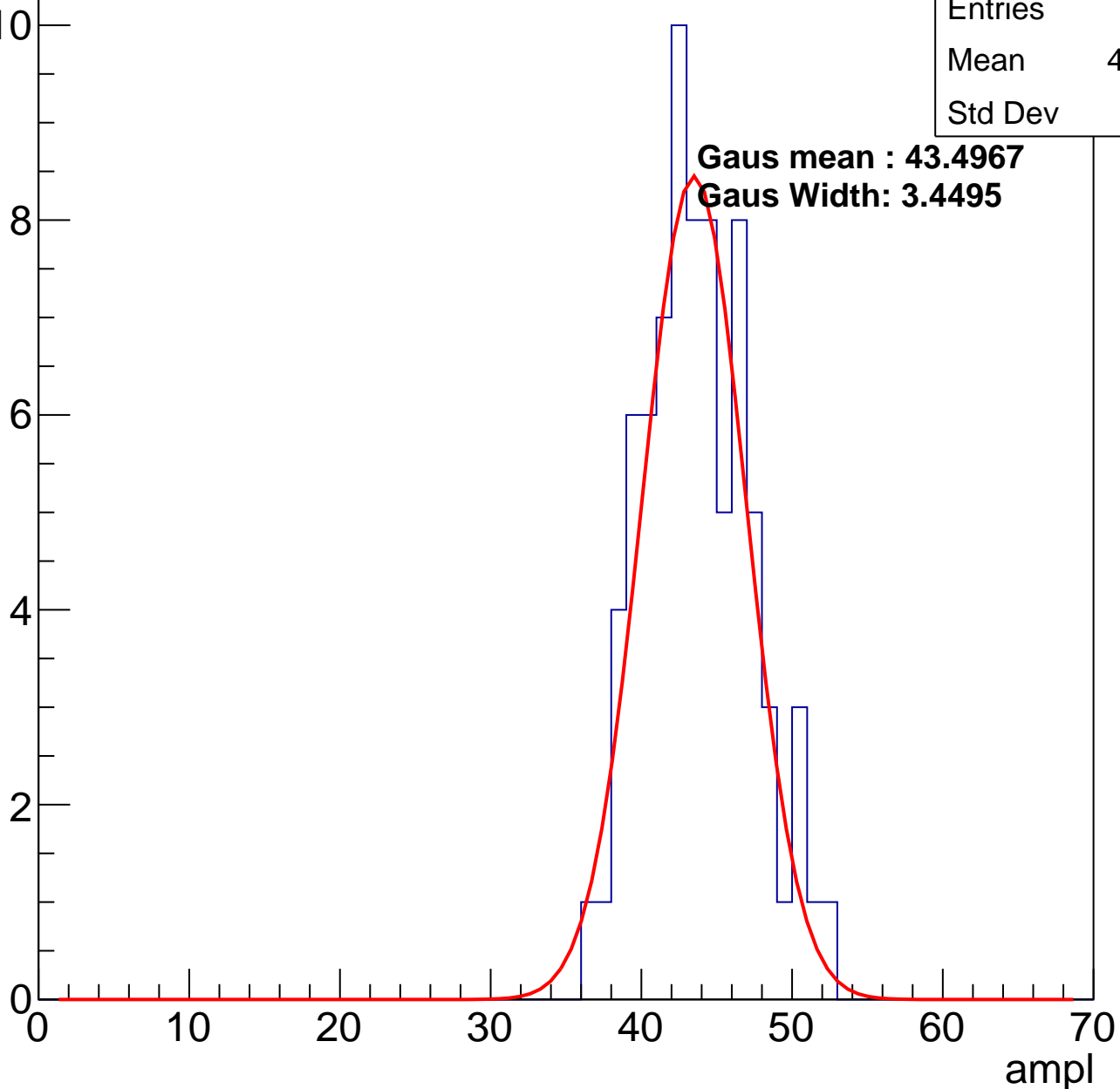
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	43.28
Std Dev	3.49

**Gaus mean : 43.4967**

**Gaus Width: 3.4495**

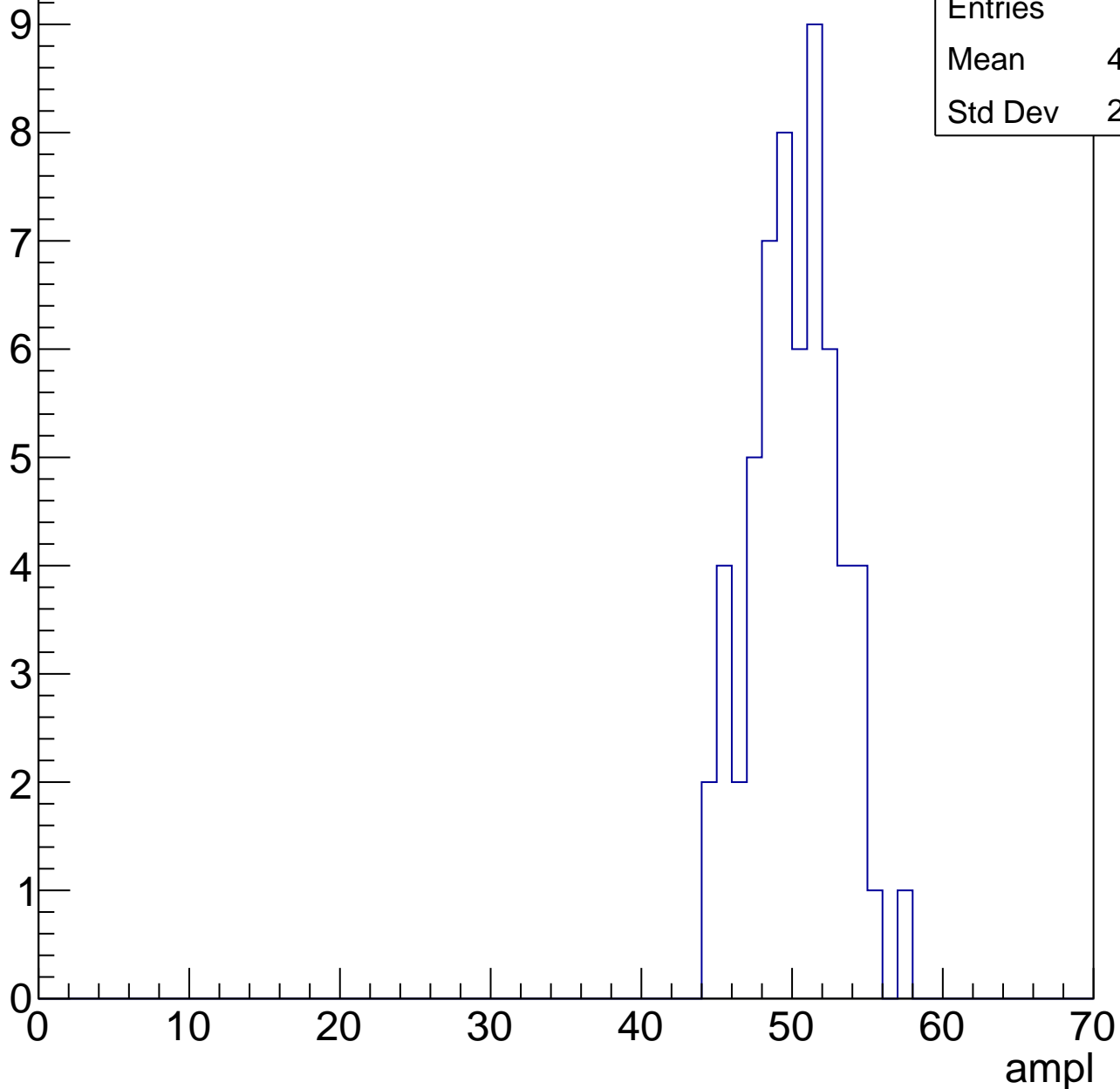


# B0L000S, U25-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	49.73
Std Dev	2.875



# B0L000S, U25-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

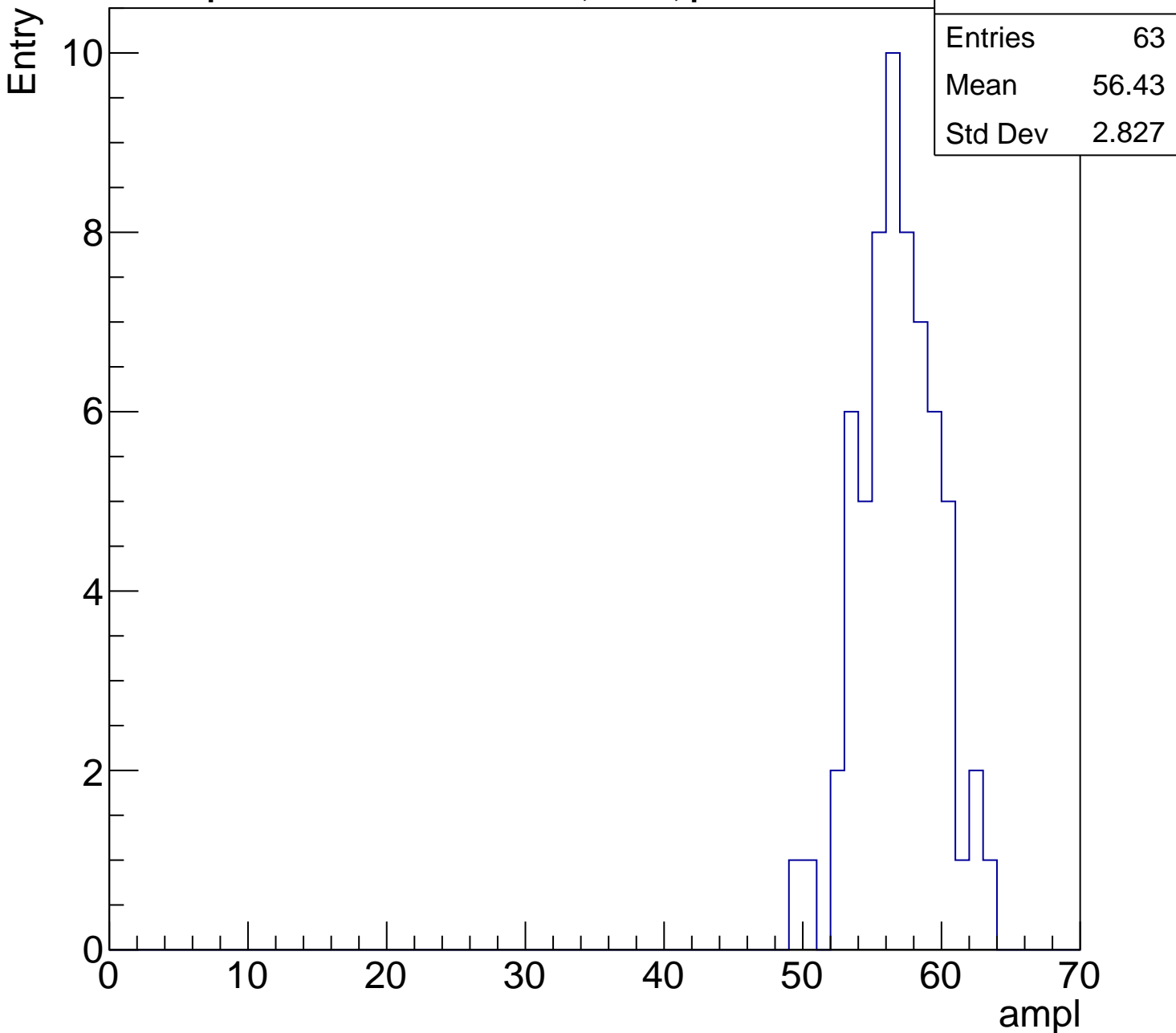
Entries	63
Mean	56.43
Std Dev	2.827

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

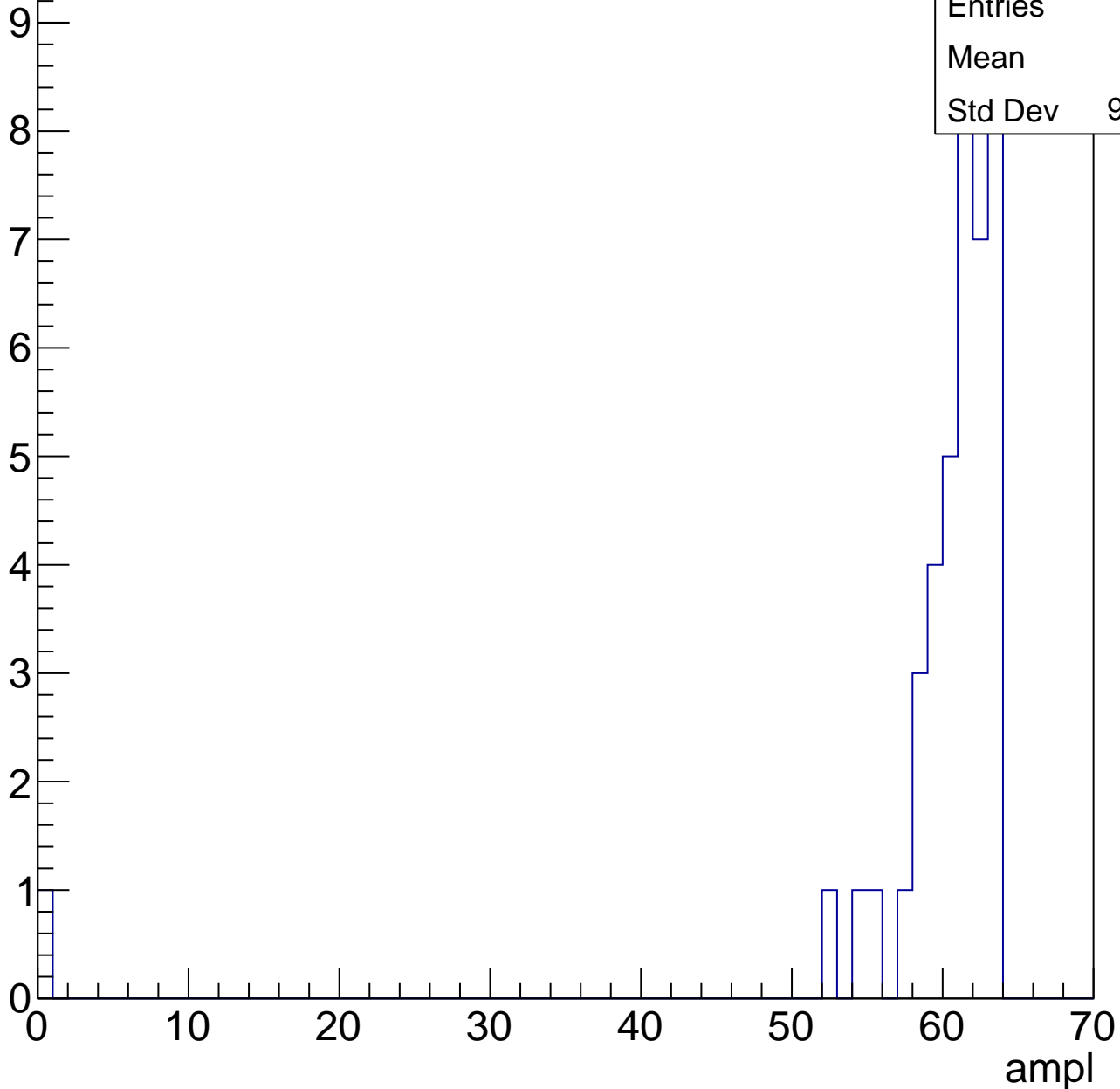


# B0L000S, U25-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	41
Mean	58.9
Std Dev	9.639



# B0L000S, U25-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch98, adc0

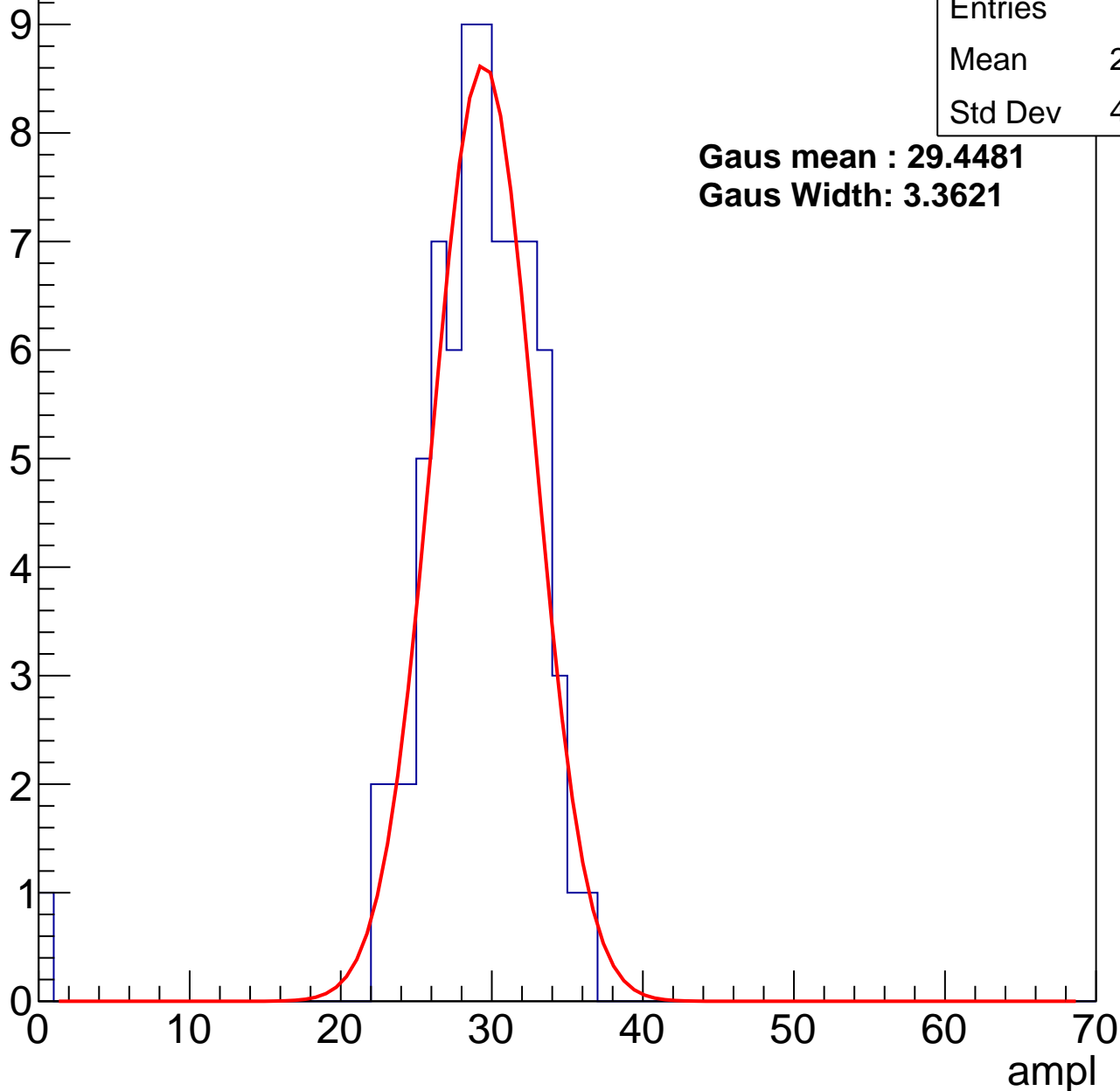
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	28.56
Std Dev	4.585

**Gaus mean : 29.4481**

**Gaus Width: 3.3621**



# B0L000S, U25-ch98, adc1

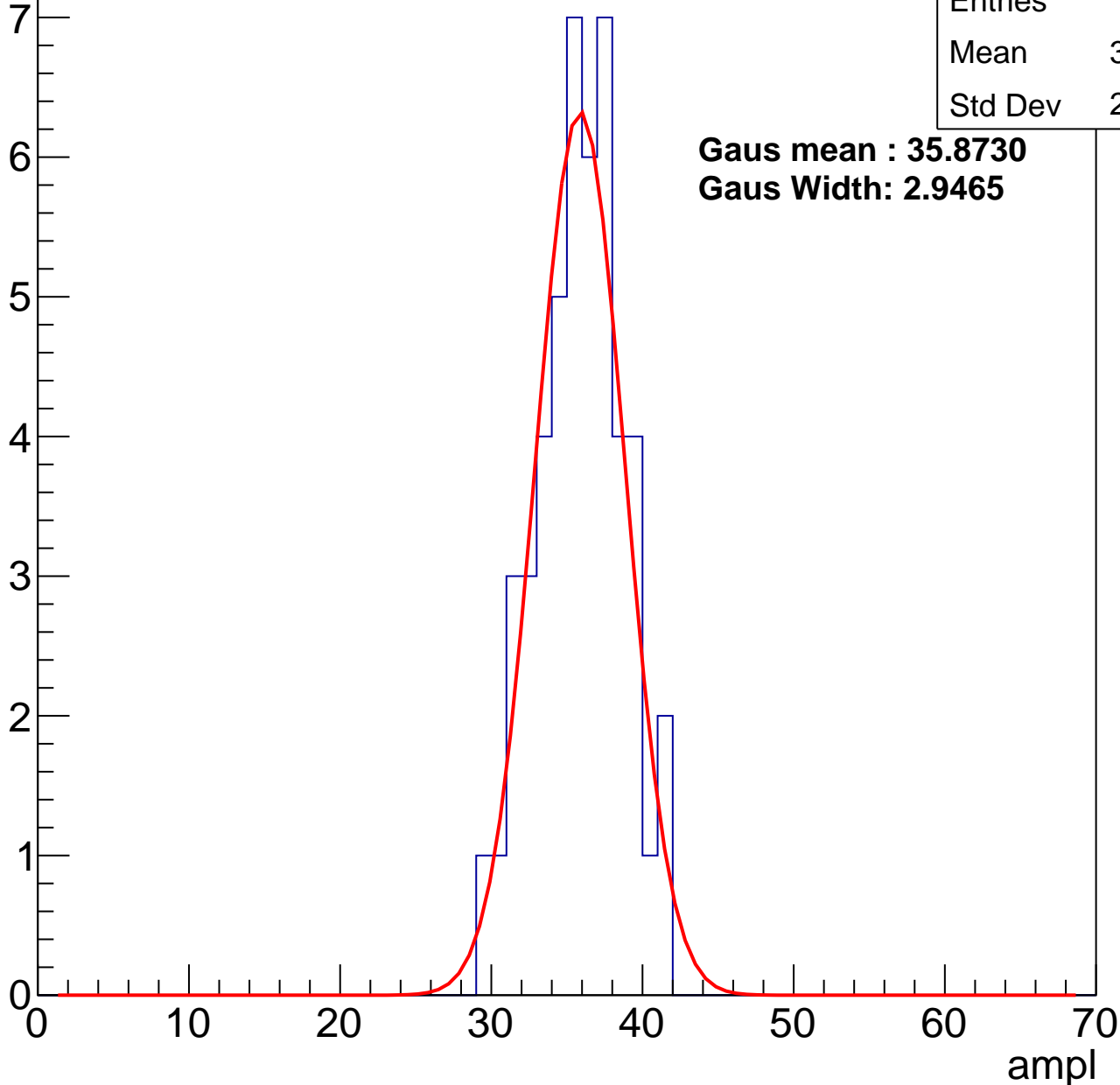
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	35.42
Std Dev	2.812

**Gaus mean : 35.8730**

**Gaus Width: 2.9465**



# B0L000S, U25-ch98, adc2

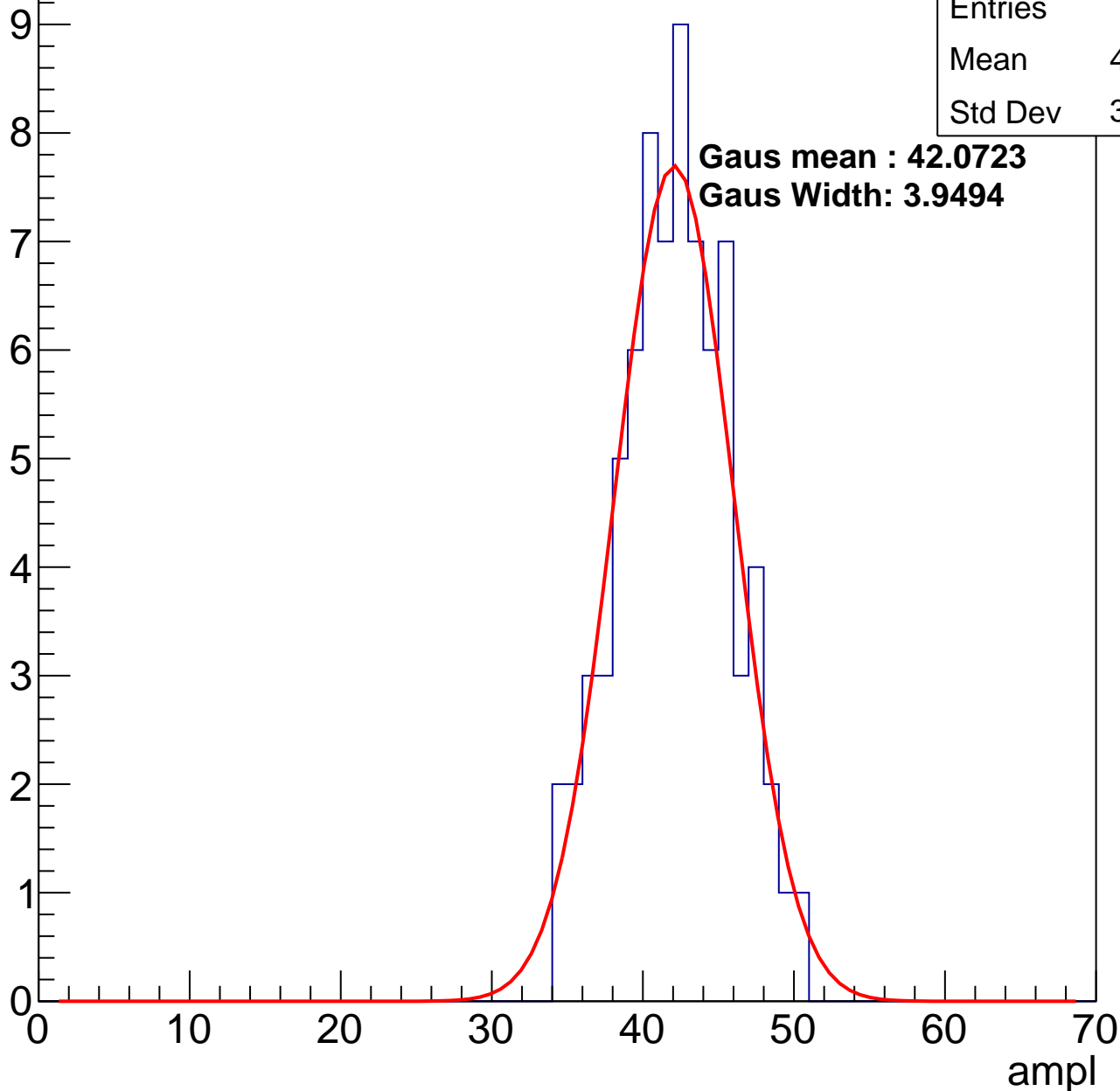
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	41.67
Std Dev	3.629

**Gaus mean : 42.0723**

**Gaus Width: 3.9494**

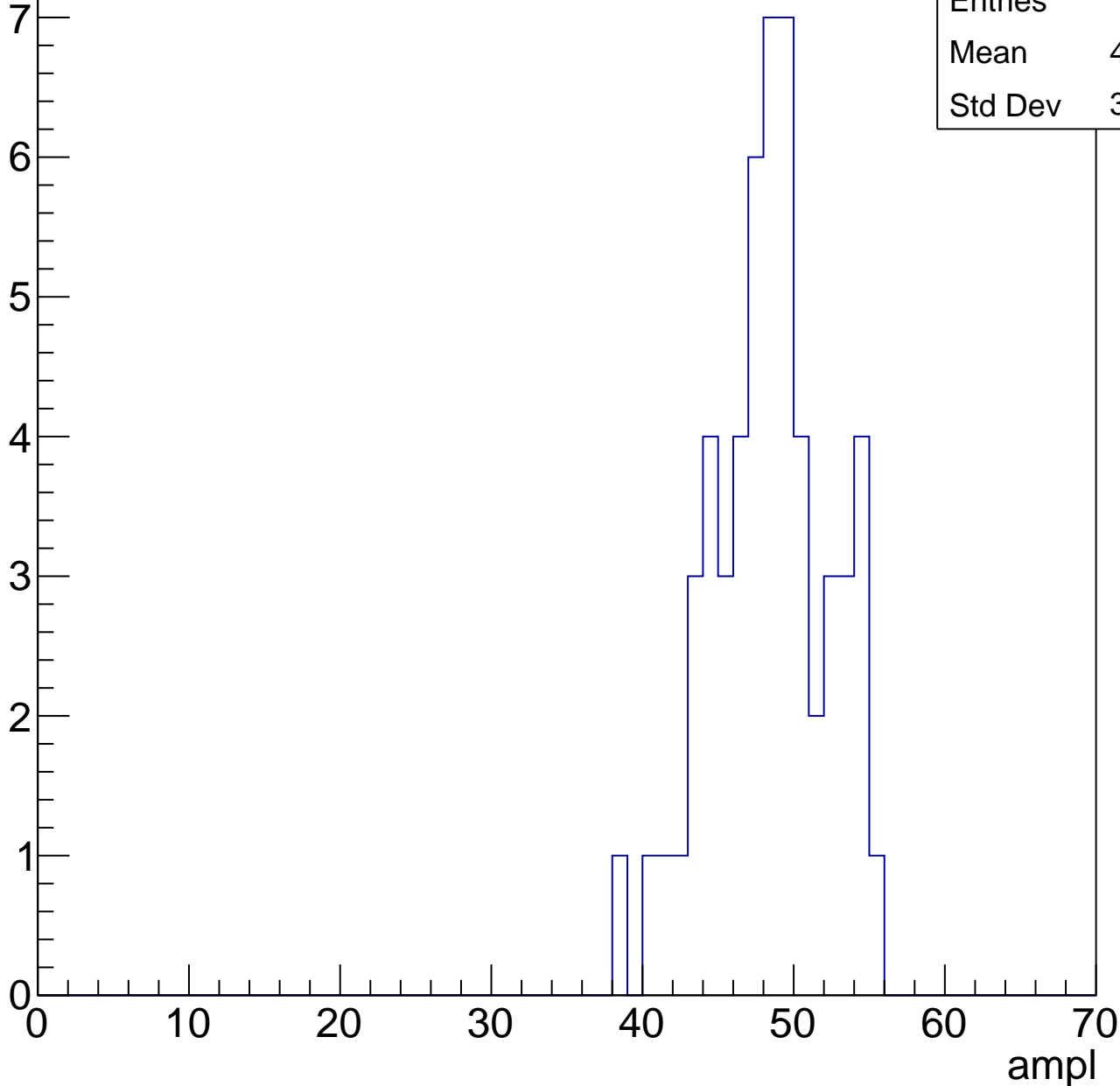


# B0L000S, U25-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	47.89
Std Dev	3.798

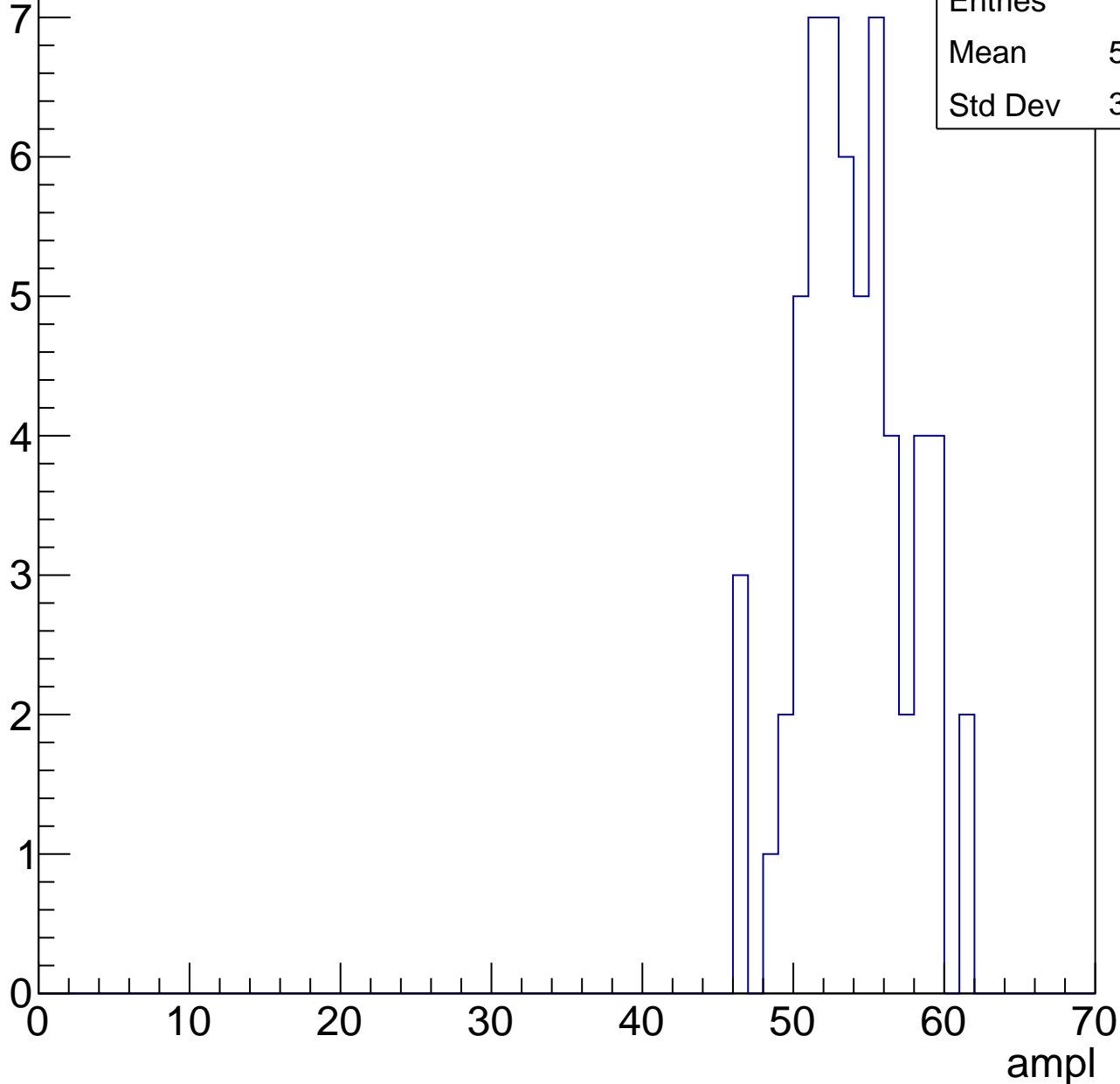


# B0L000S, U25-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

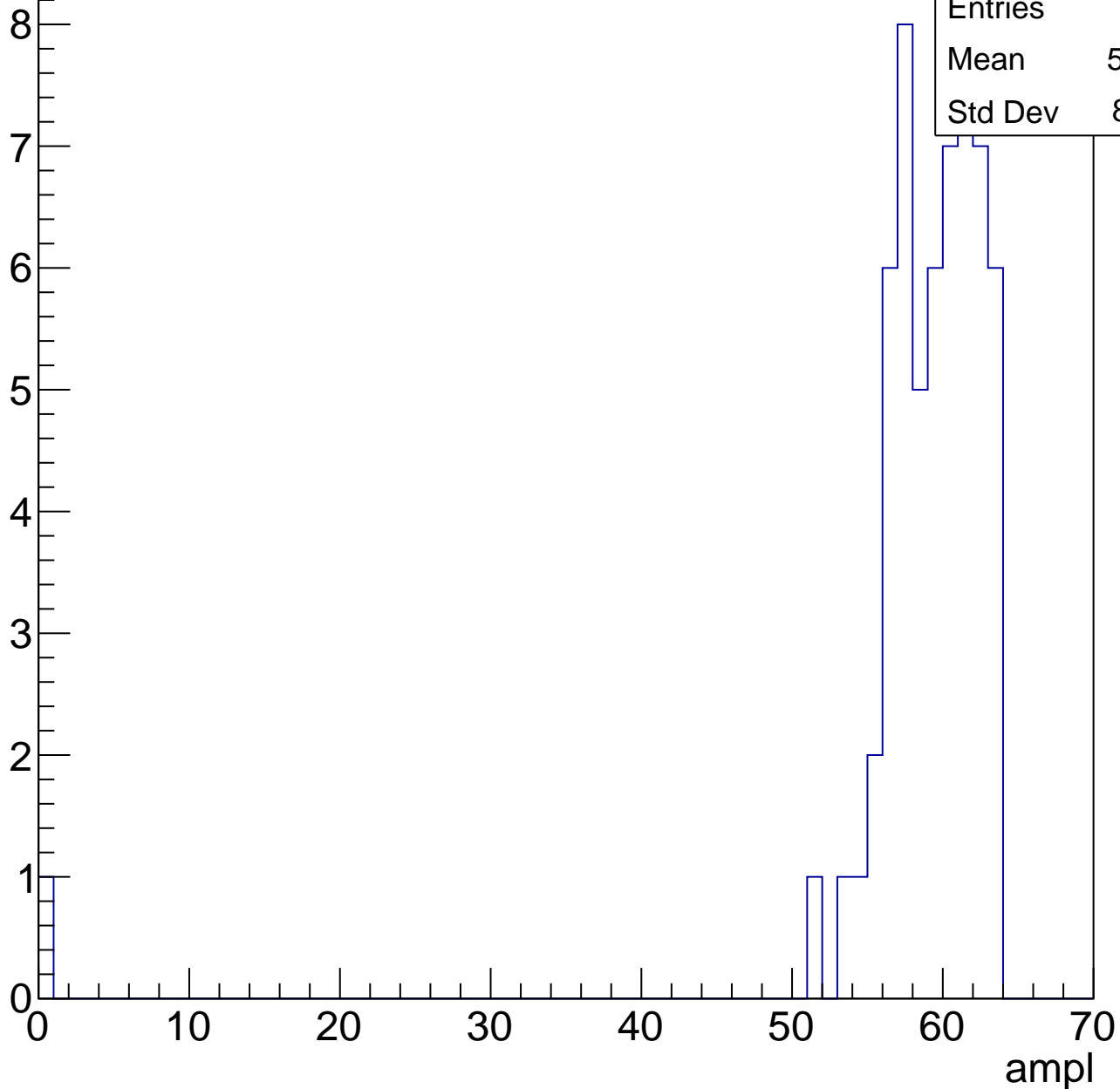
Entries	59
Mean	53.49
Std Dev	3.548



# B0L000S, U25-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

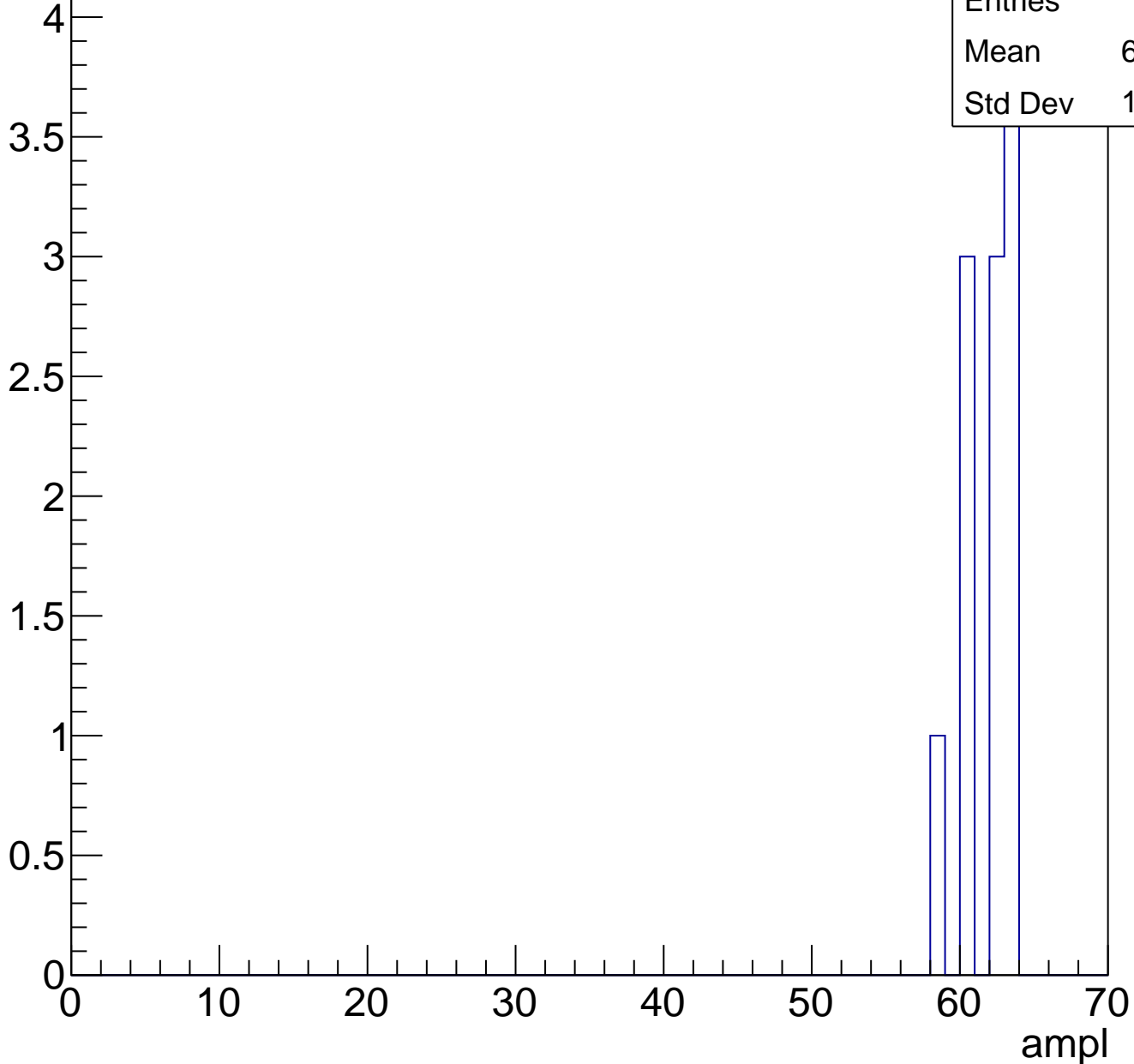
Entry



# B0L000S, U25-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch99, adc0

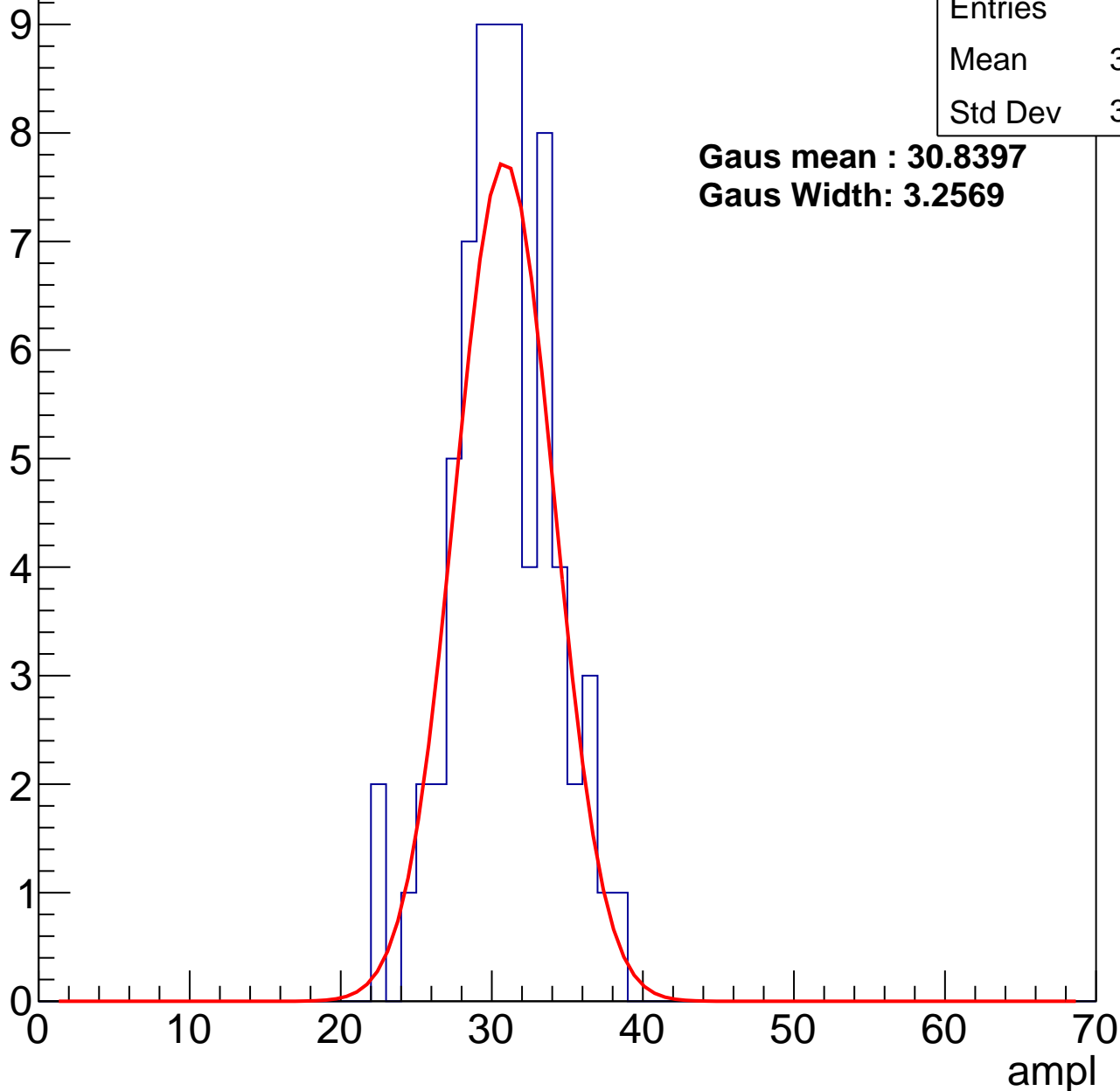
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	30.32
Std Dev	3.303

**Gaus mean : 30.8397**

**Gaus Width: 3.2569**



# B0L000S, U25-ch99, adc1

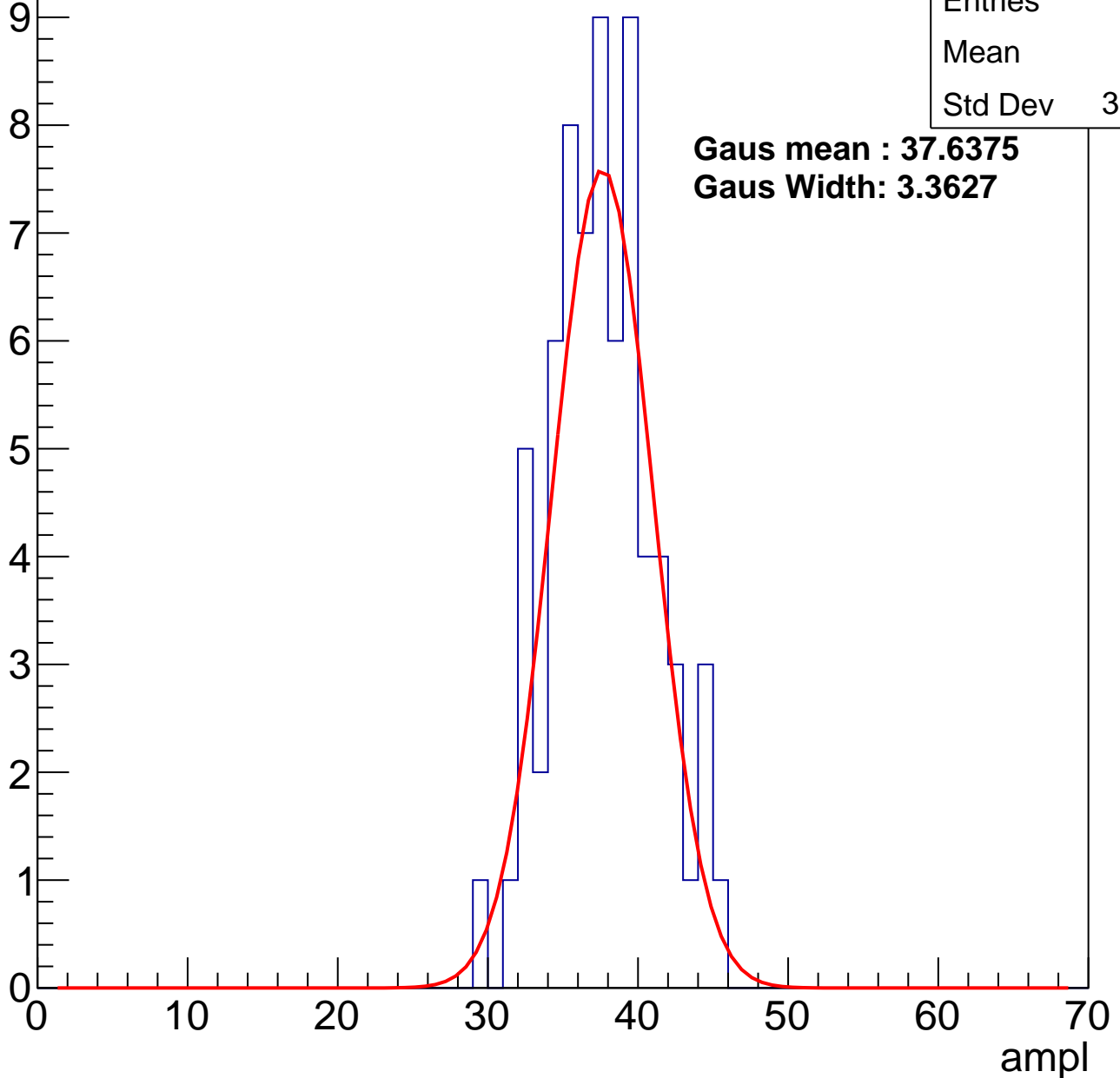
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.2
Std Dev	3.404

**Gaus mean : 37.6375**

**Gaus Width: 3.3627**



# B0L000S, U25-ch99, adc2

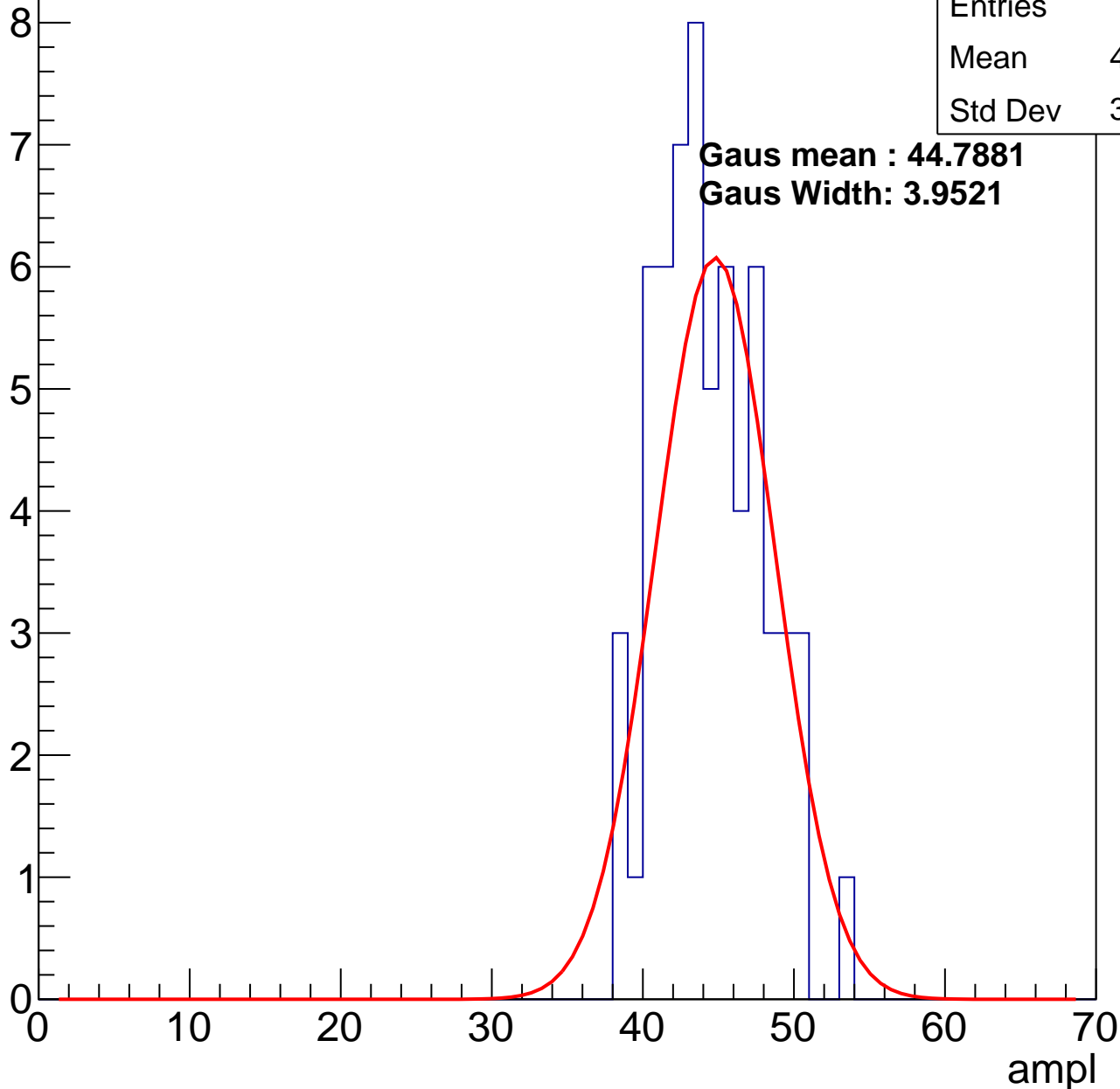
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.98
Std Dev	3.377

**Gaus mean : 44.7881**

**Gaus Width: 3.9521**



# B0L000S, U25-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

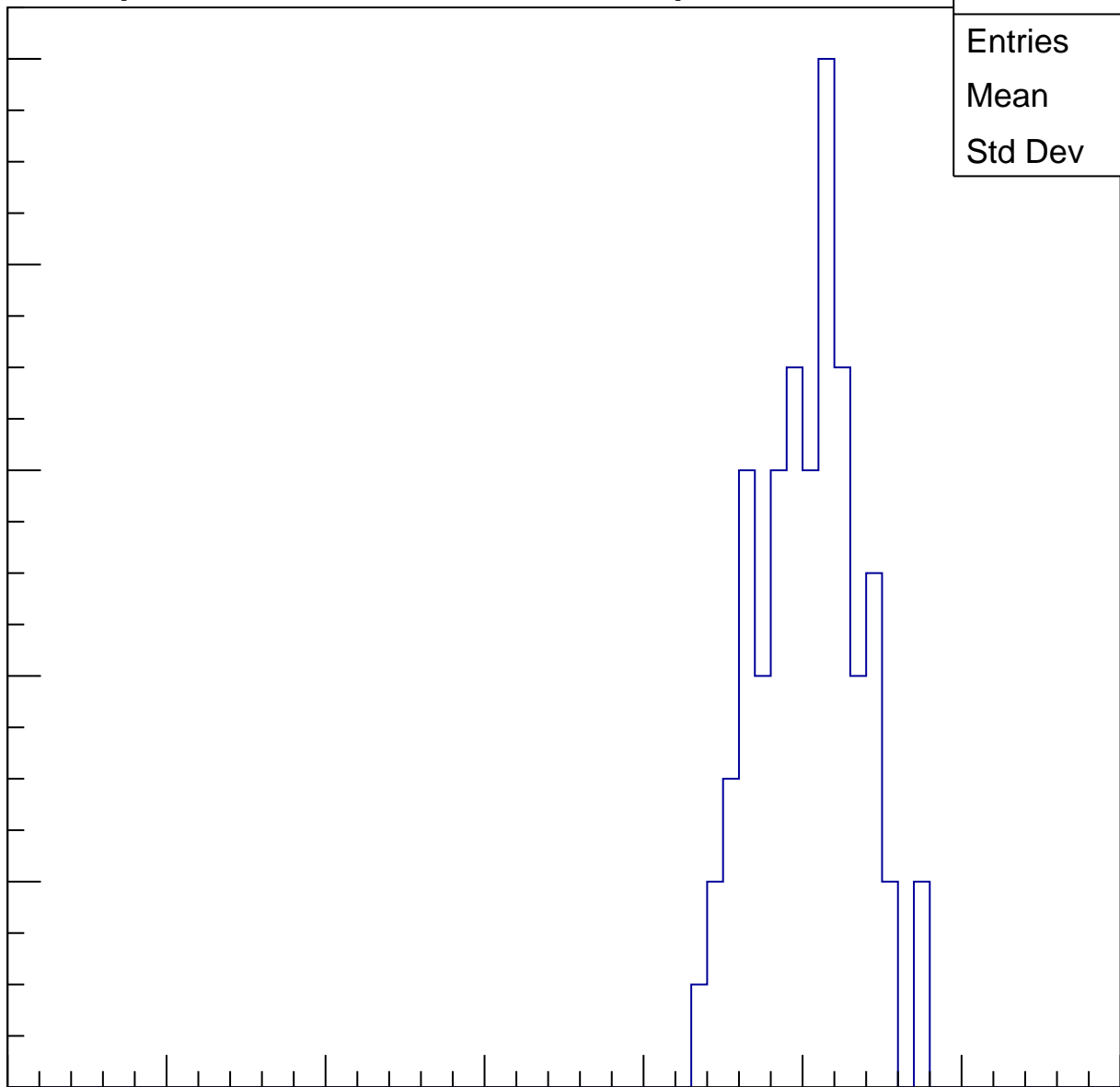
Entries	65
Mean	49.86
Std Dev	3.186

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

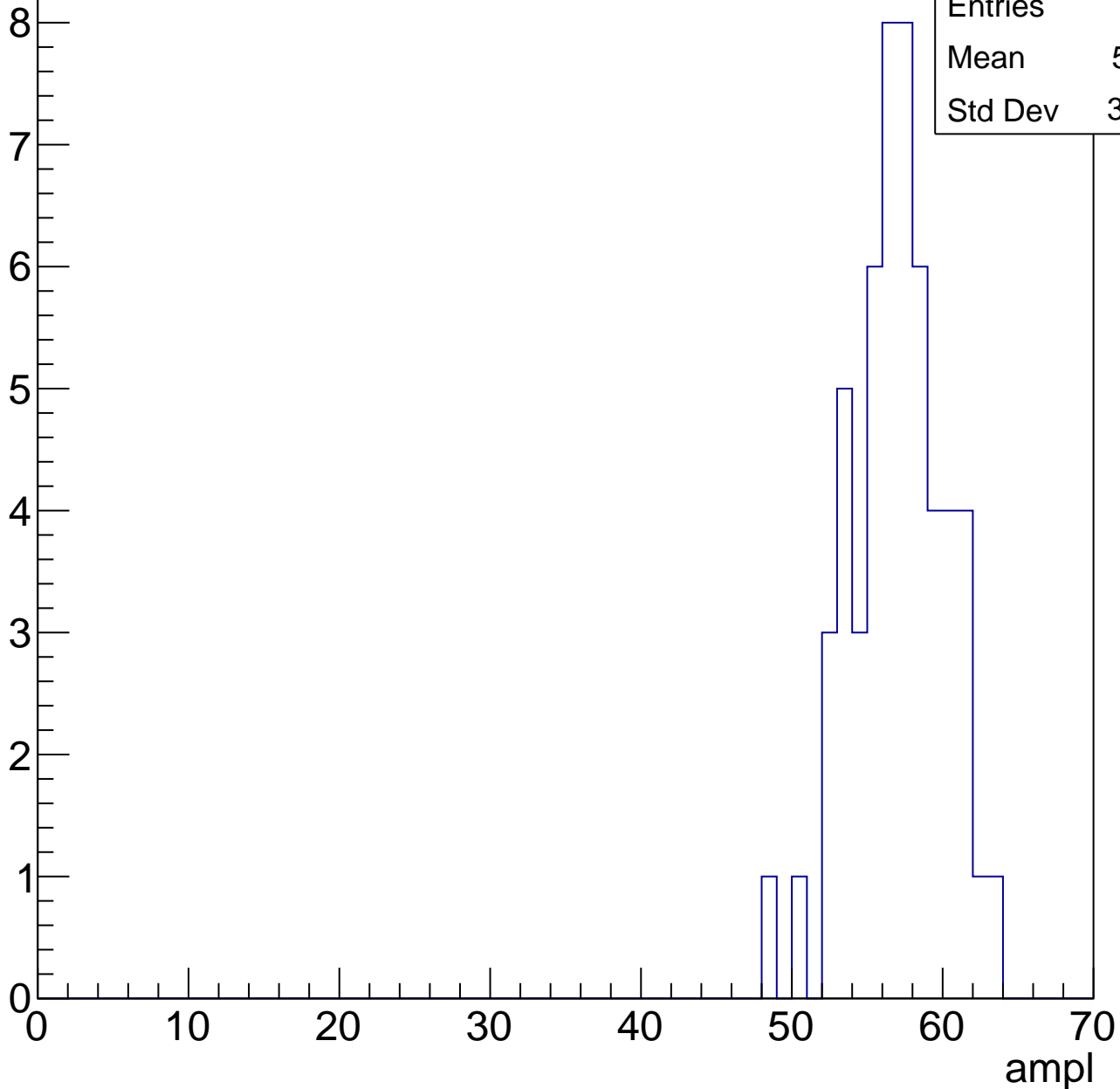


# B0L000S, U25-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	56.51
Std Dev	3.056

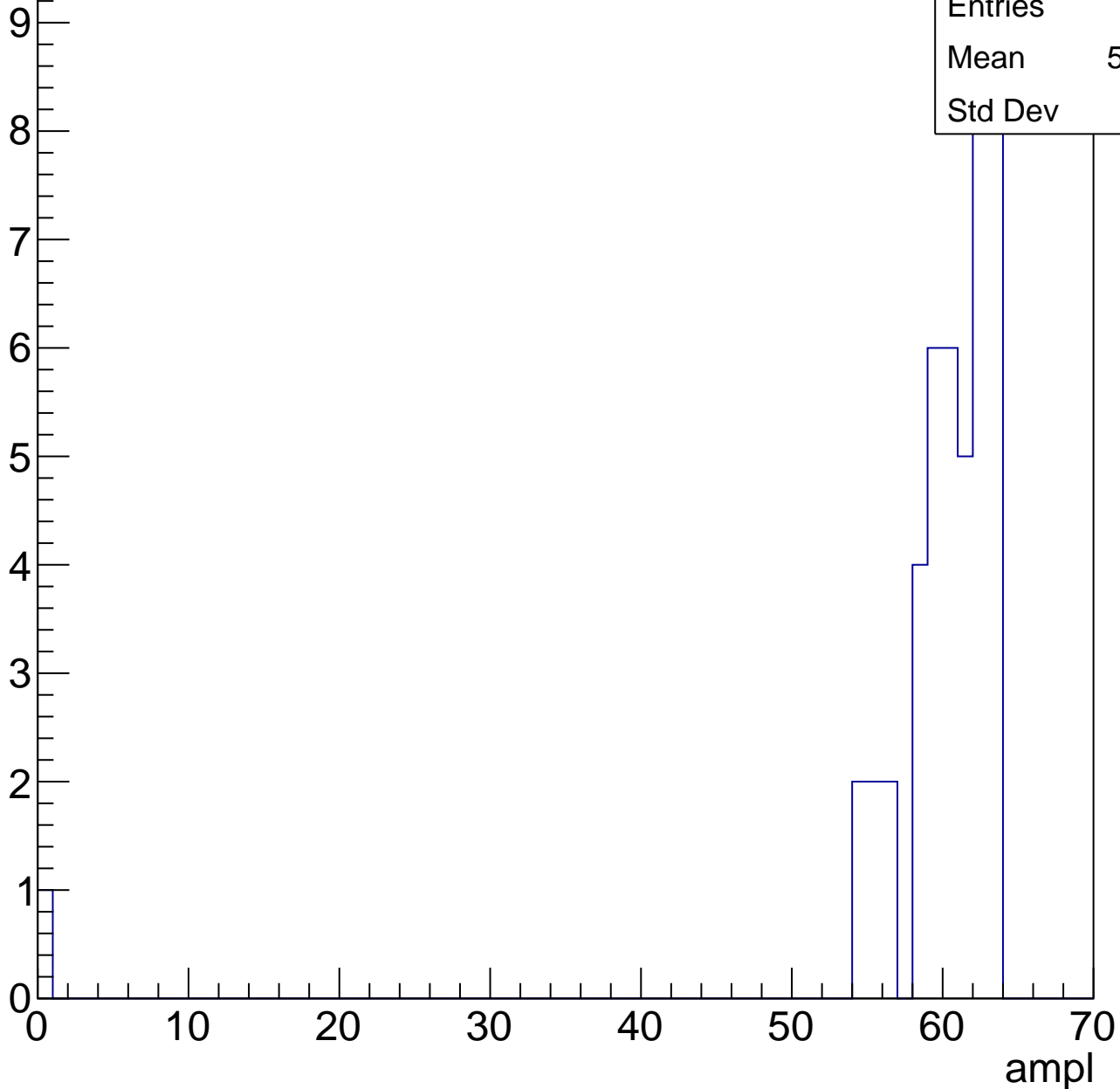


# B0L000S, U25-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	45
Mean	58.73
Std Dev	9.21



# B0L000S, U25-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch100, adc0

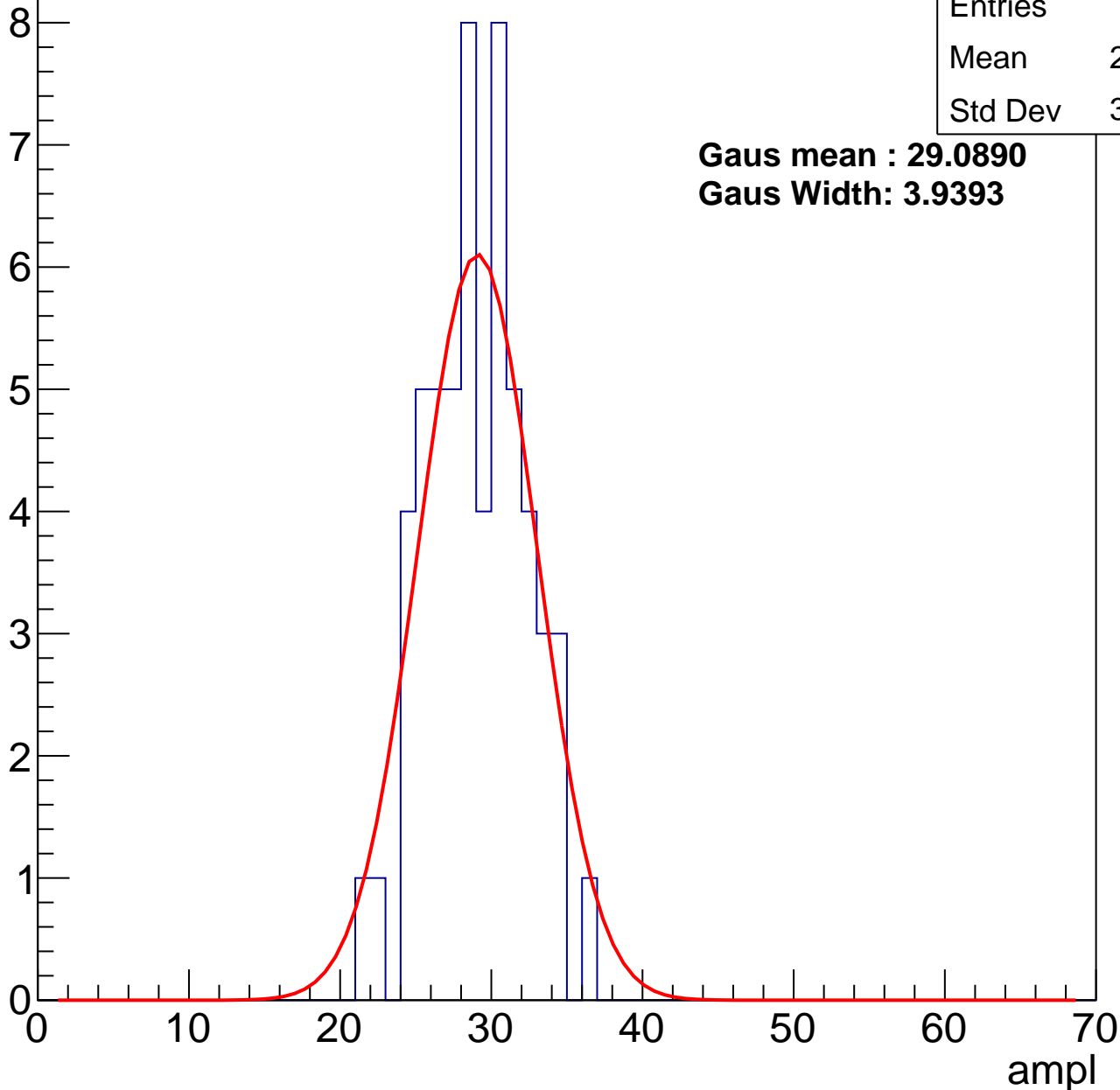
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	28.58
Std Dev	3.222

**Gaus mean : 29.0890**

**Gaus Width: 3.9393**



# B0L000S, U25-ch100, adc1

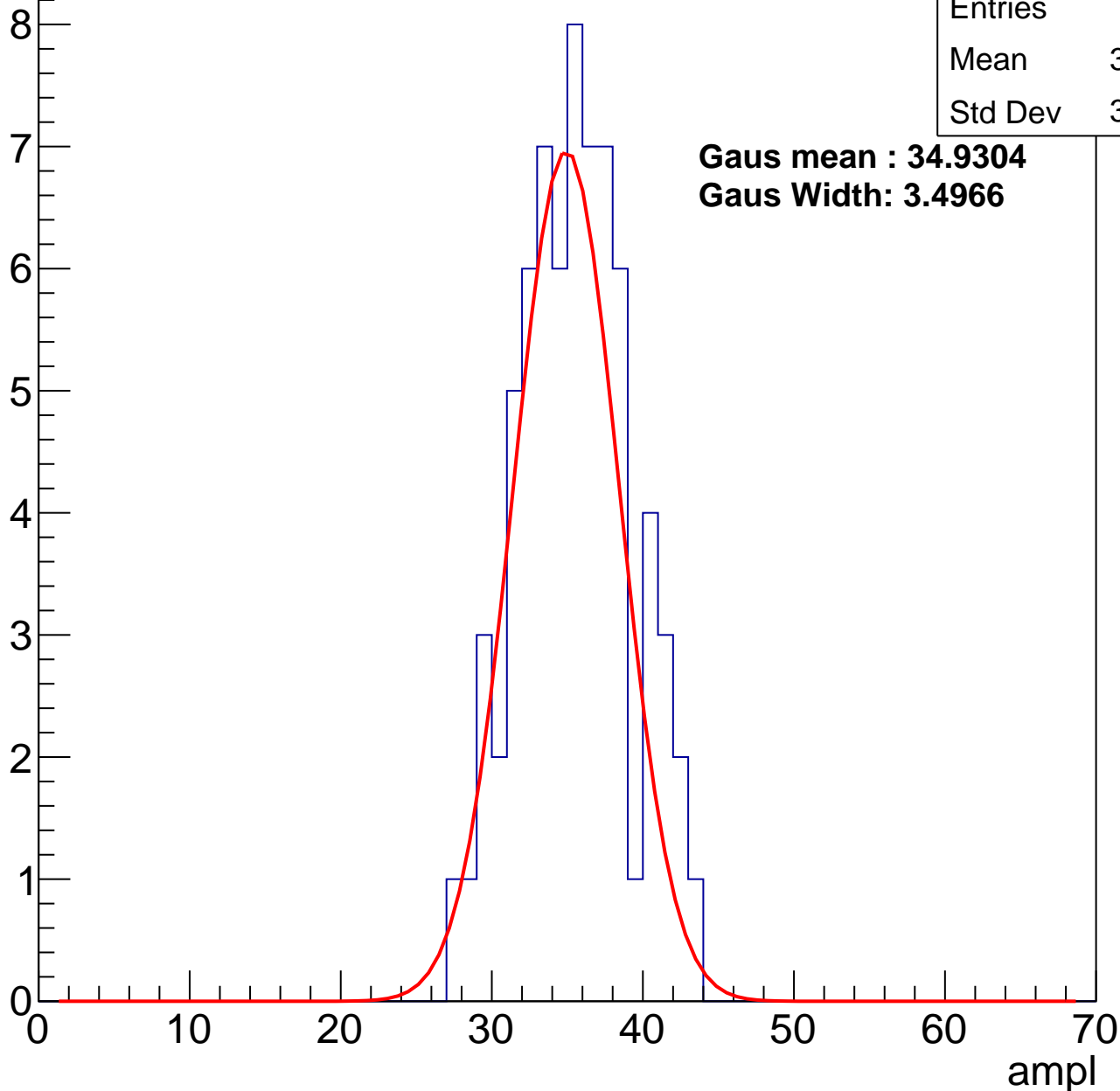
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	35.03
Std Dev	3.613

**Gaus mean : 34.9304**

**Gaus Width: 3.4966**



# B0L000S, U25-ch100, adc2

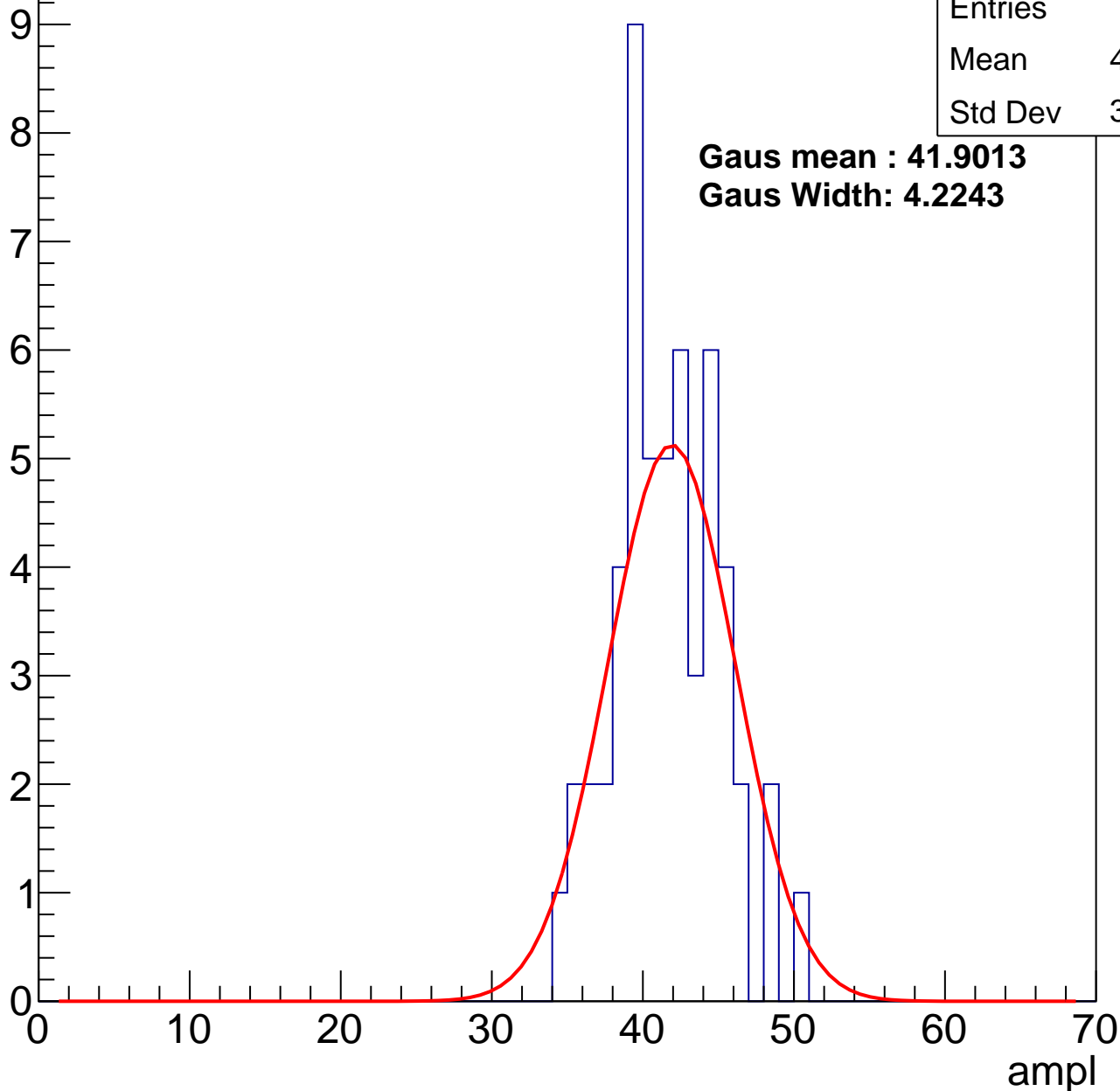
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	41.13
Std Dev	3.454

**Gaus mean : 41.9013**

**Gaus Width: 4.2243**

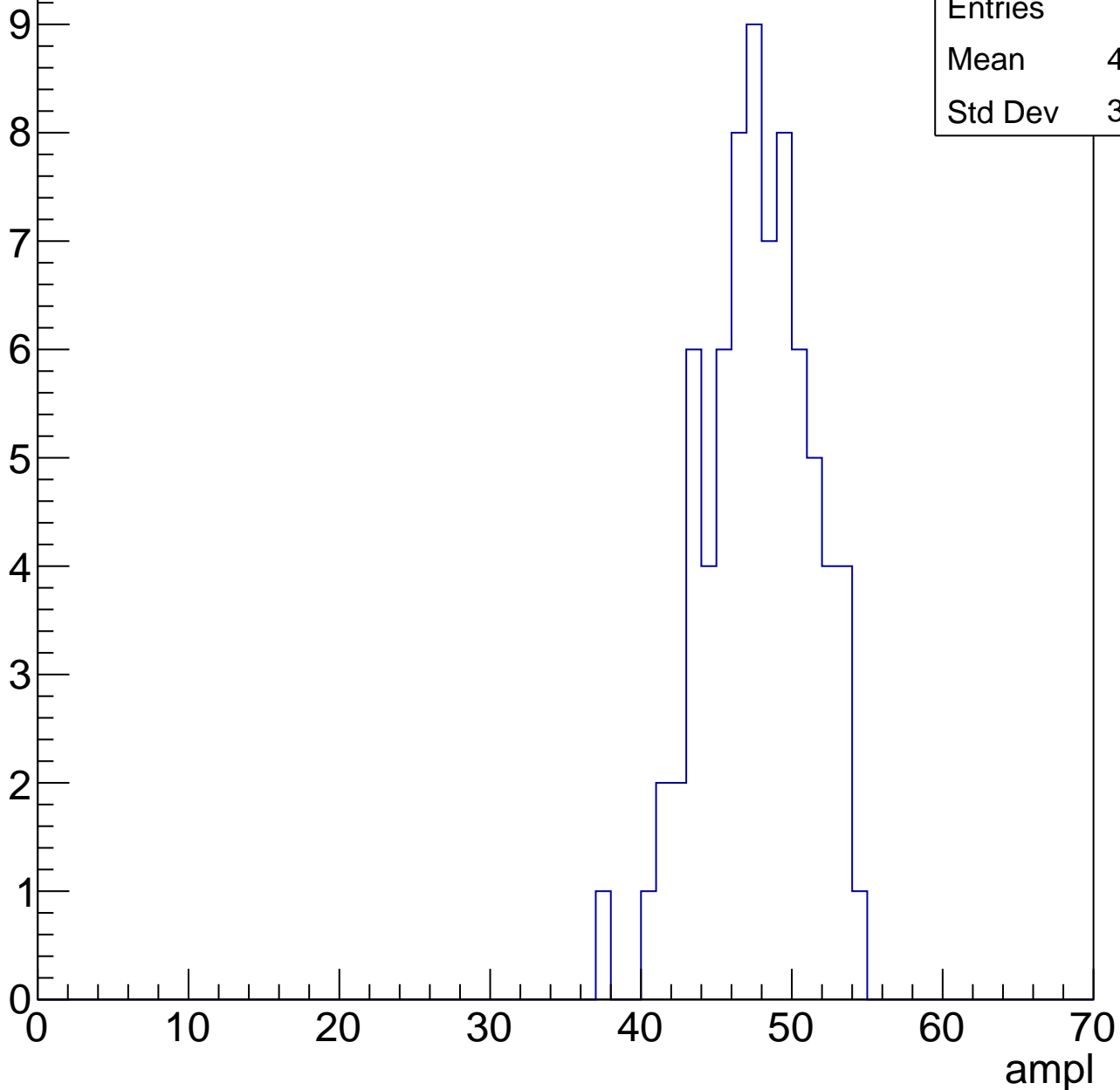


# B0L000S, U25-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	47.23
Std Dev	3.482

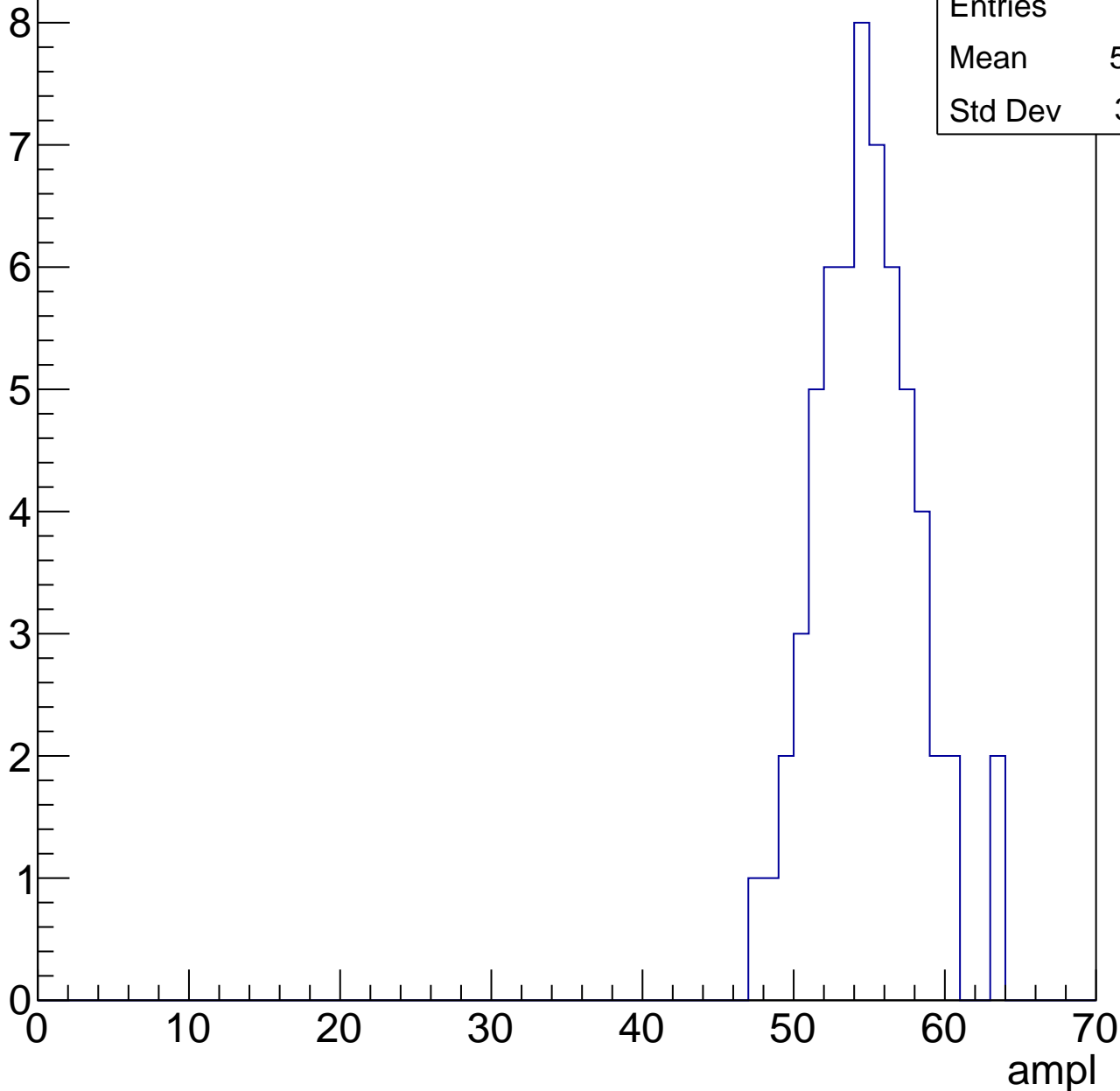


# B0L000S, U25-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

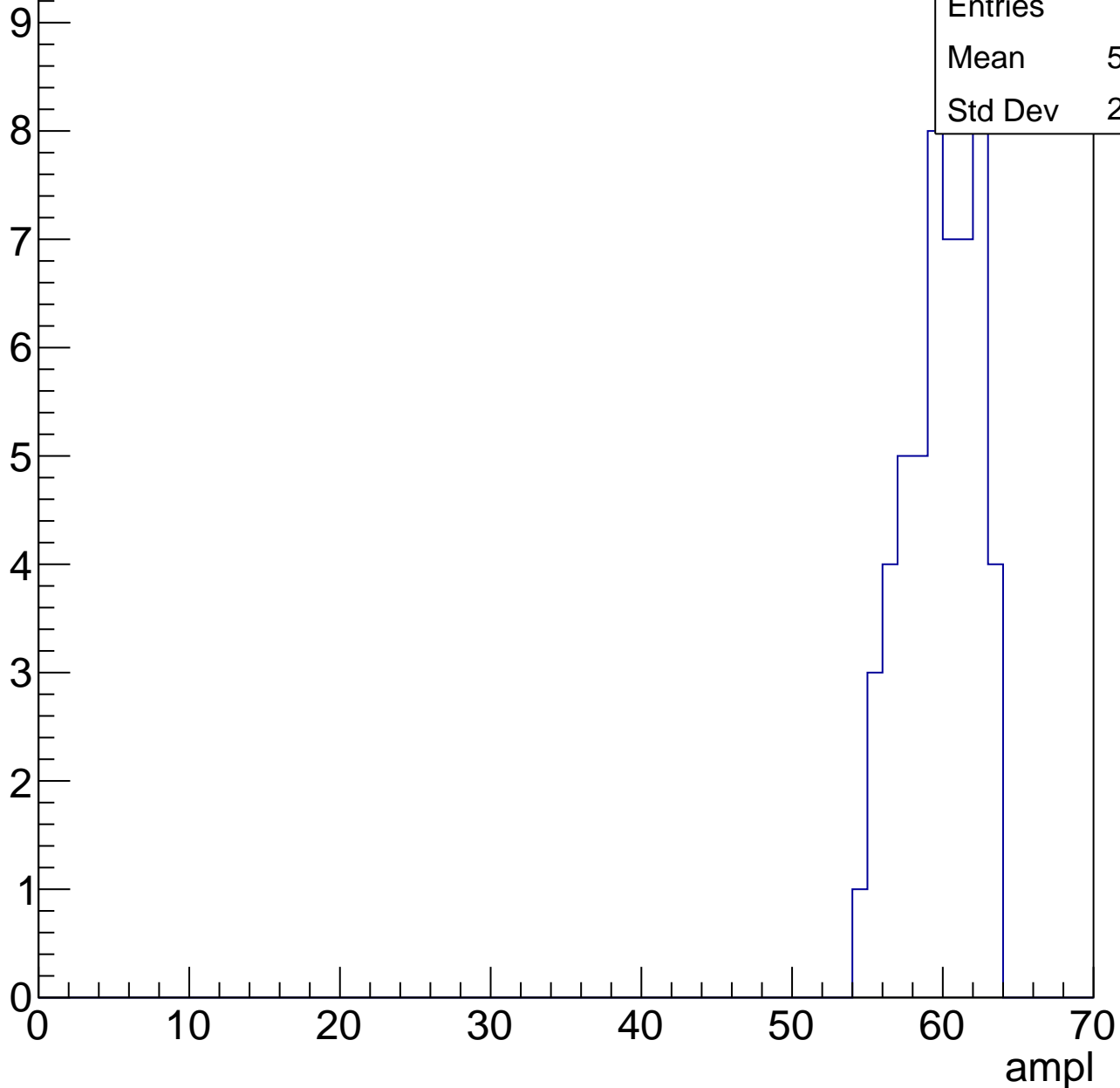
Entries	60
Mean	54.37
Std Dev	3.351



# B0L000S, U25-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	53
Mean	59.38
Std Dev	2.405

# B0L000S, U25-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

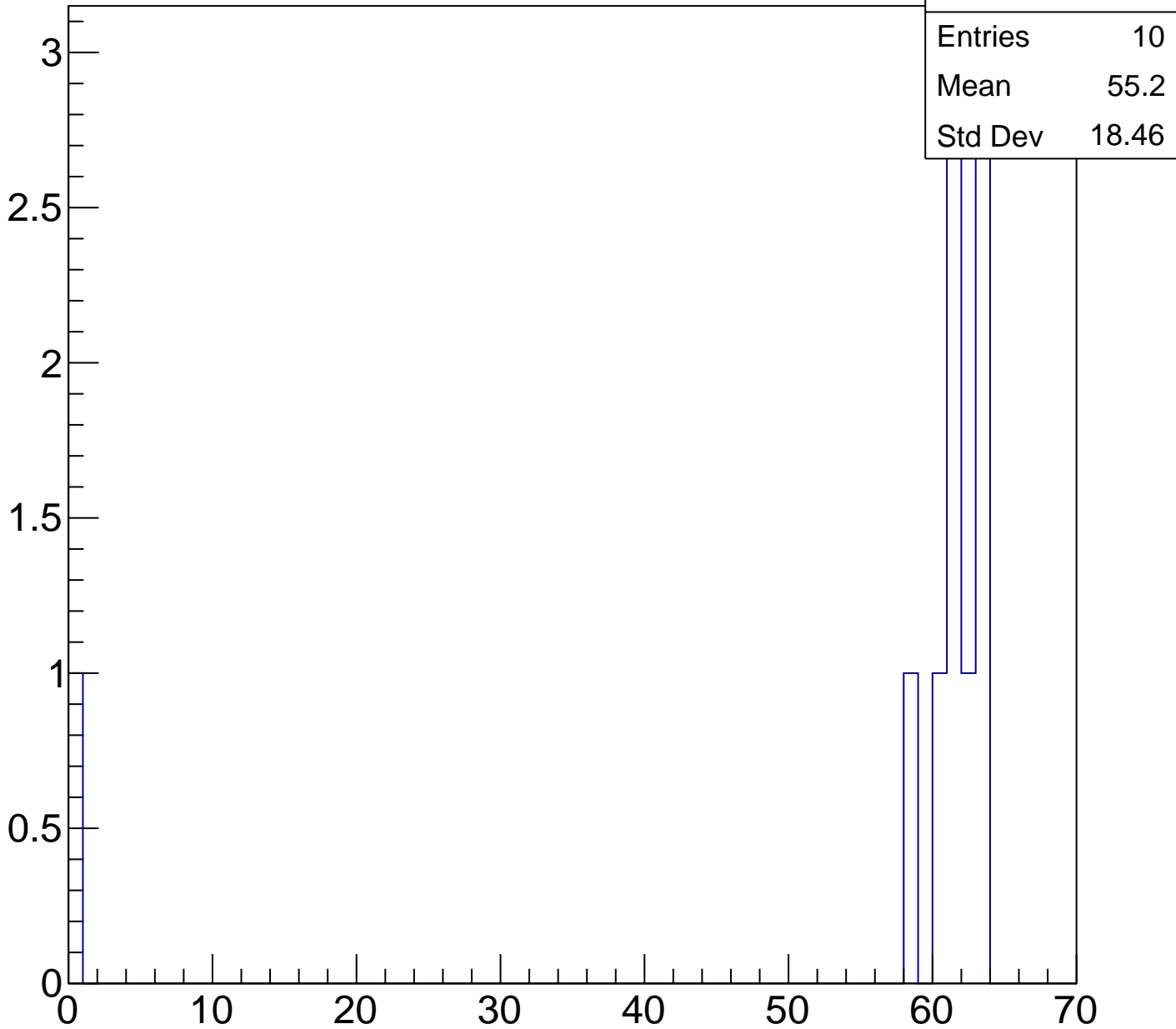
1

0.5

0

ampl

Entries	10
Mean	55.2
Std Dev	18.46

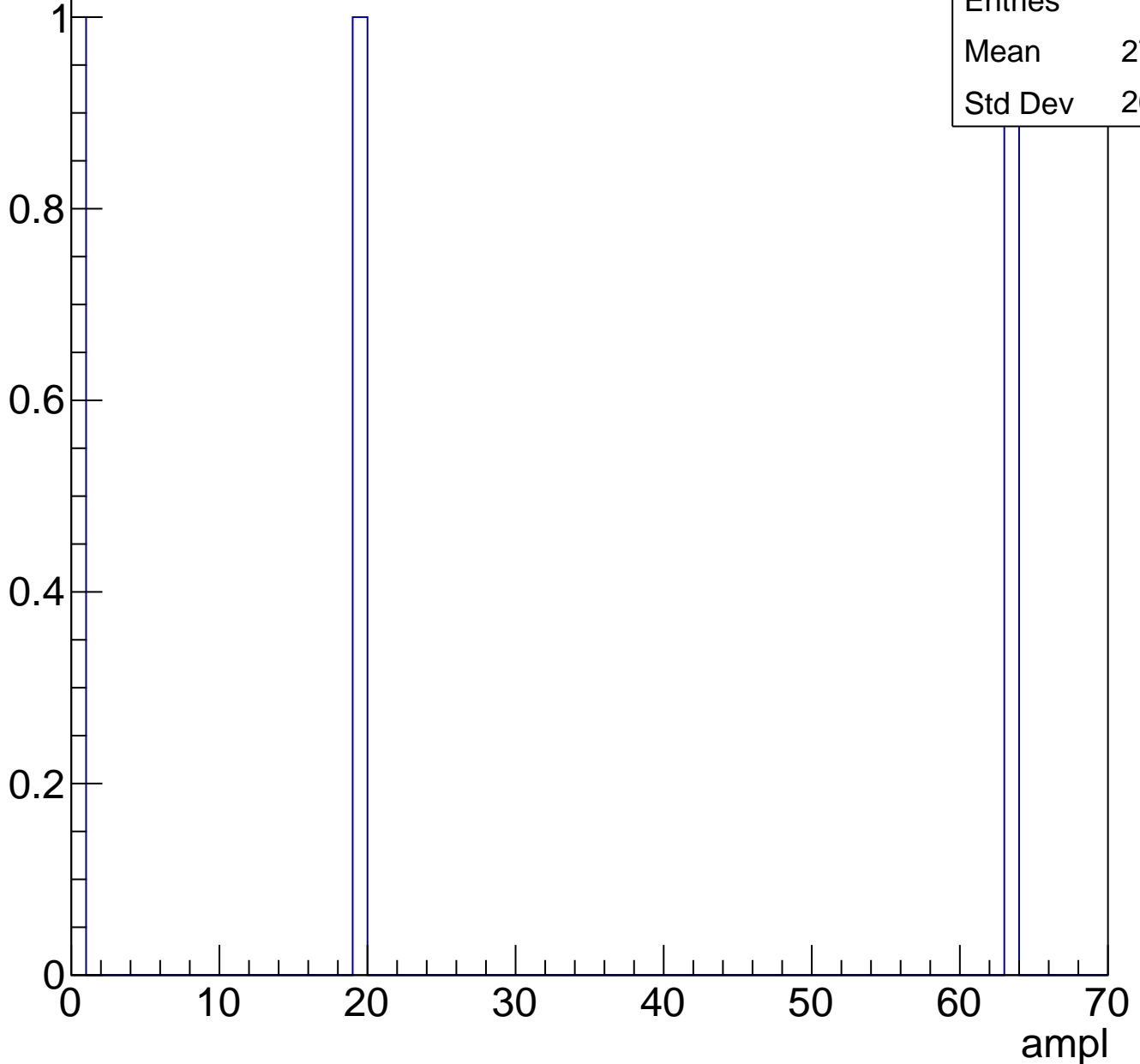




# B0L000S, U25-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	27.33
Std Dev	26.39

# B0L000S, U25-ch101, adc0

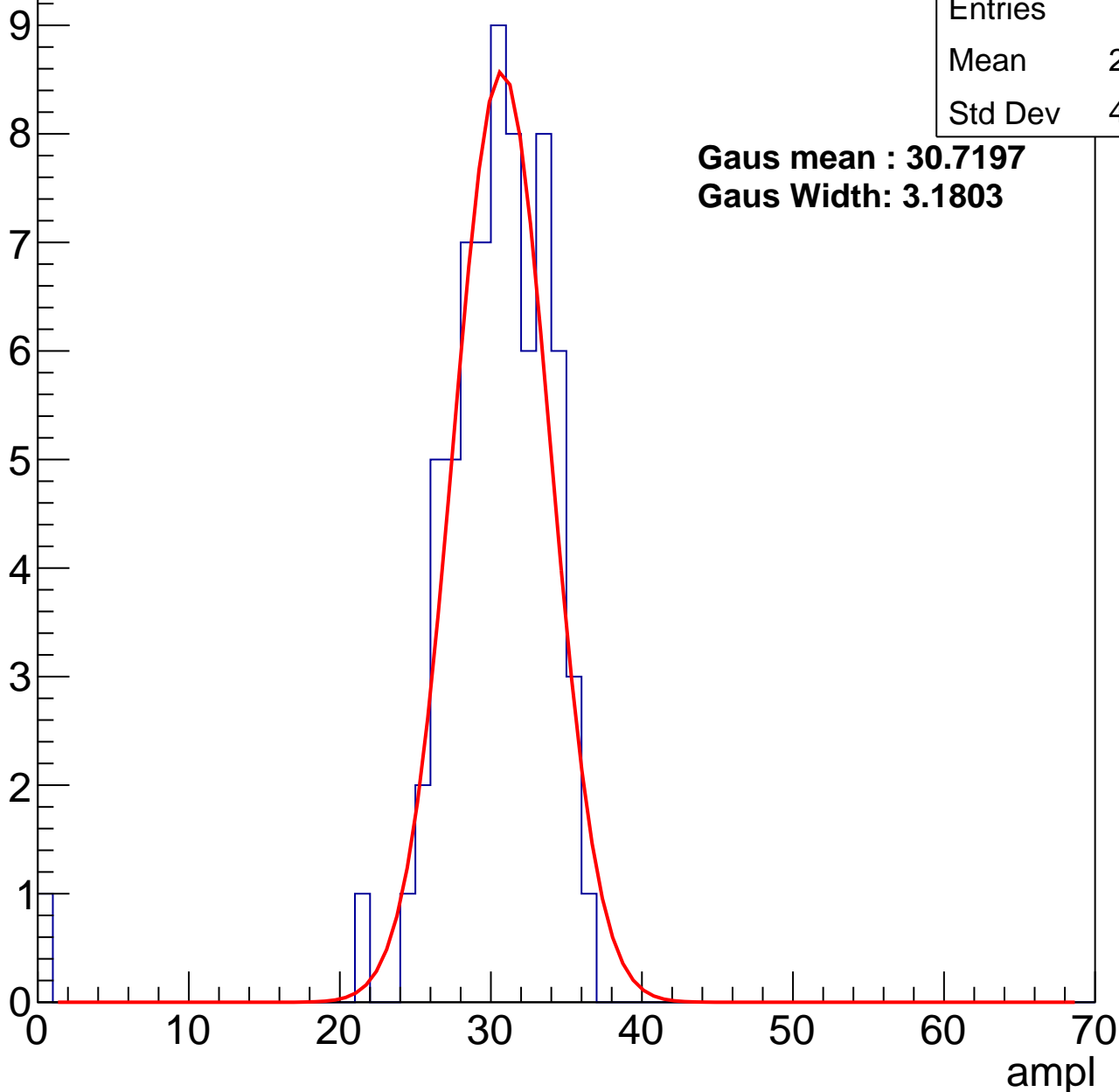
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	29.69
Std Dev	4.677

**Gaus mean : 30.7197**

**Gaus Width: 3.1803**



# B0L000S, U25-ch101, adc1

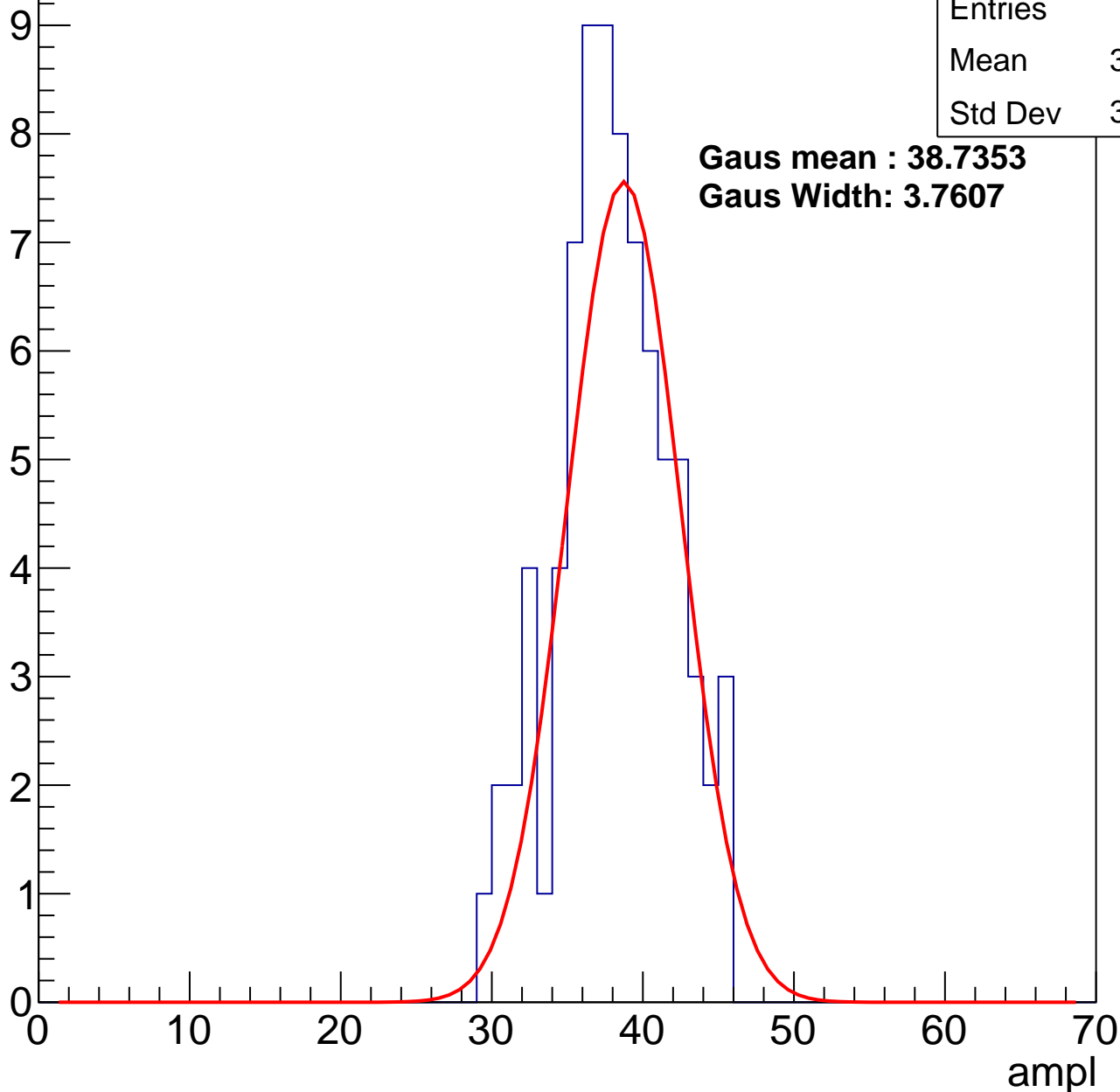
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	37.62
Std Dev	3.732

**Gaus mean : 38.7353**

**Gaus Width: 3.7607**



# B0L000S, U25-ch101, adc2

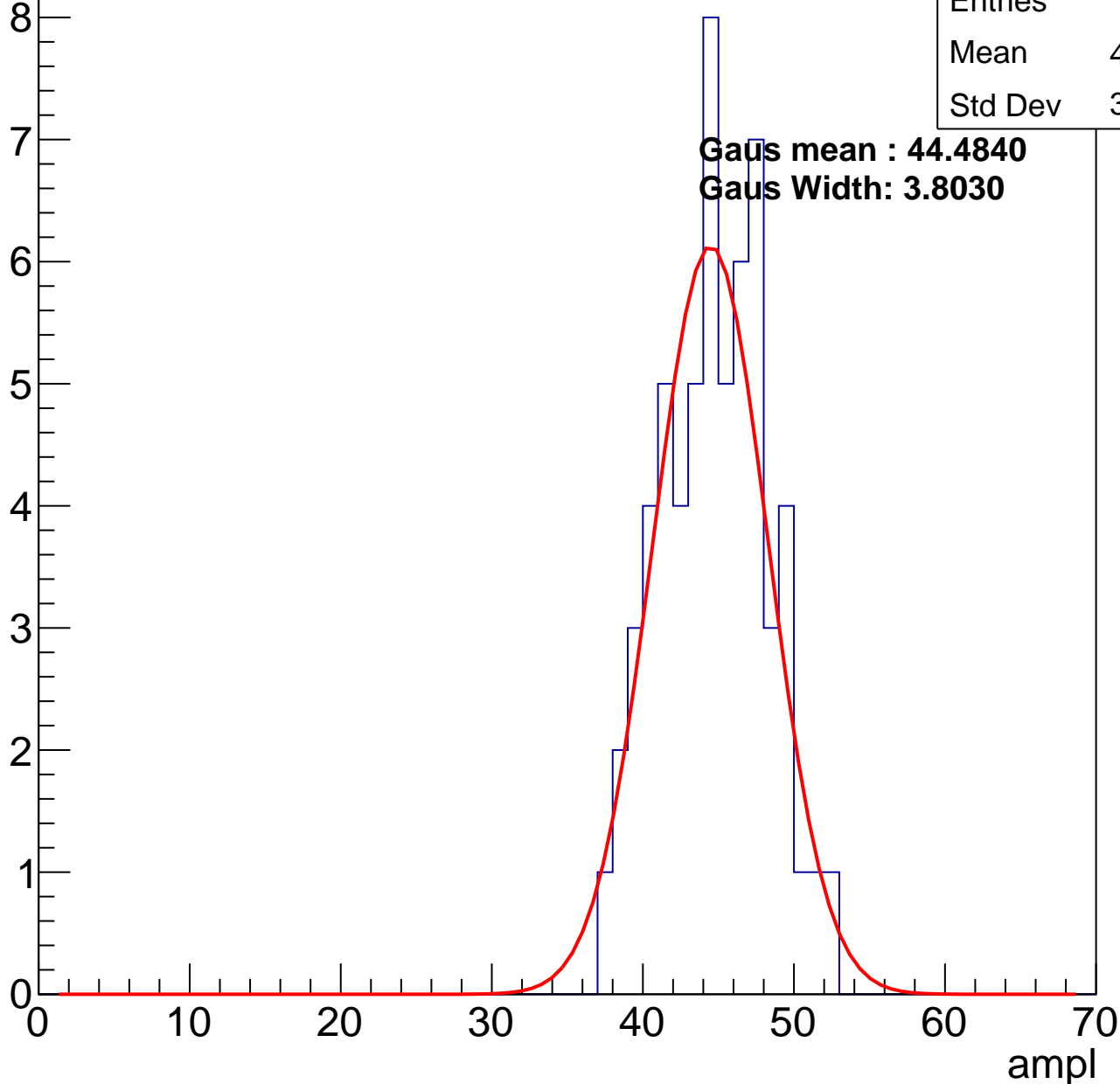
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.22
Std Dev	3.445

**Gaus mean : 44.4840**

**Gaus Width: 3.8030**

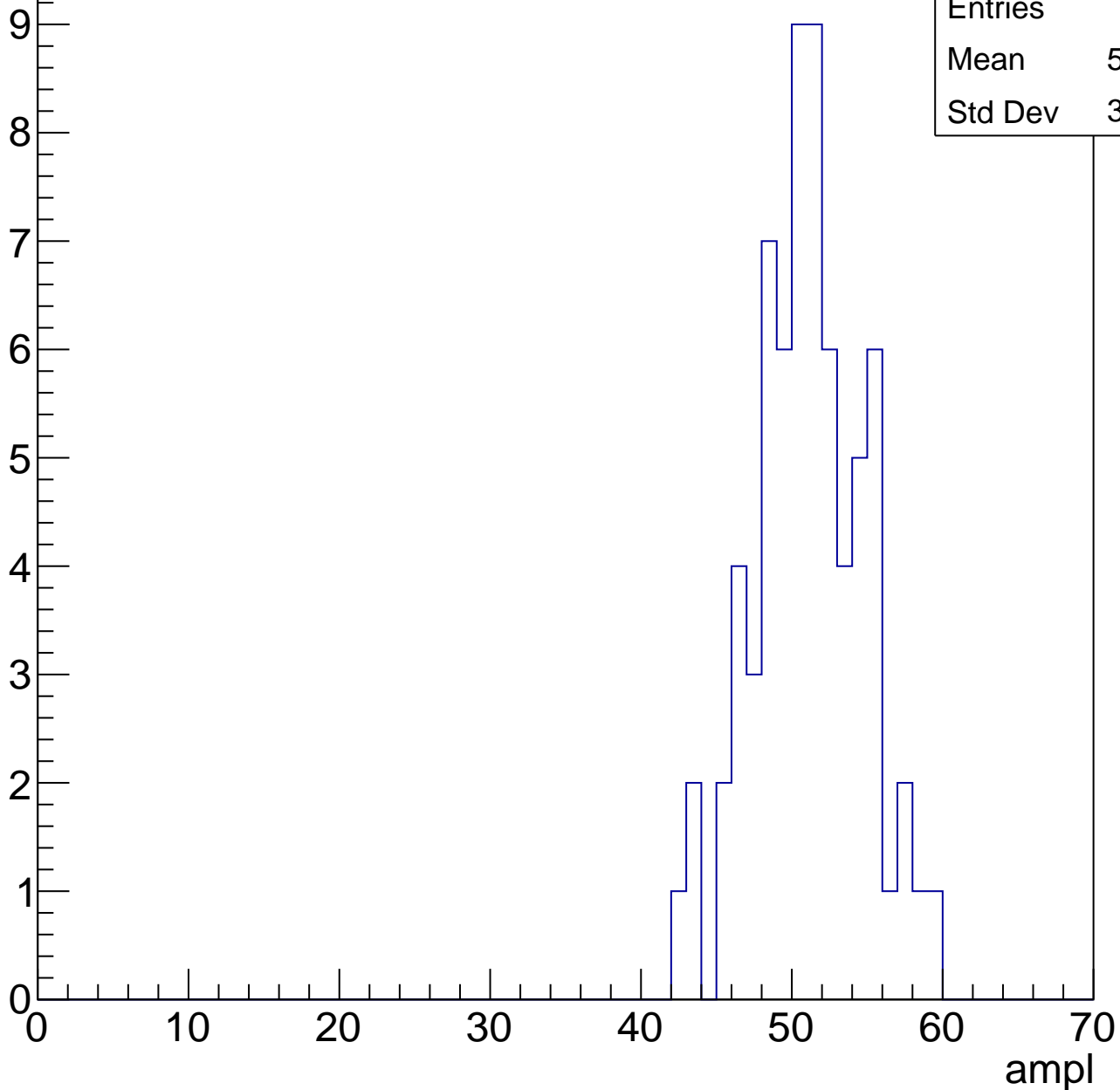


# B0L000S, U25-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

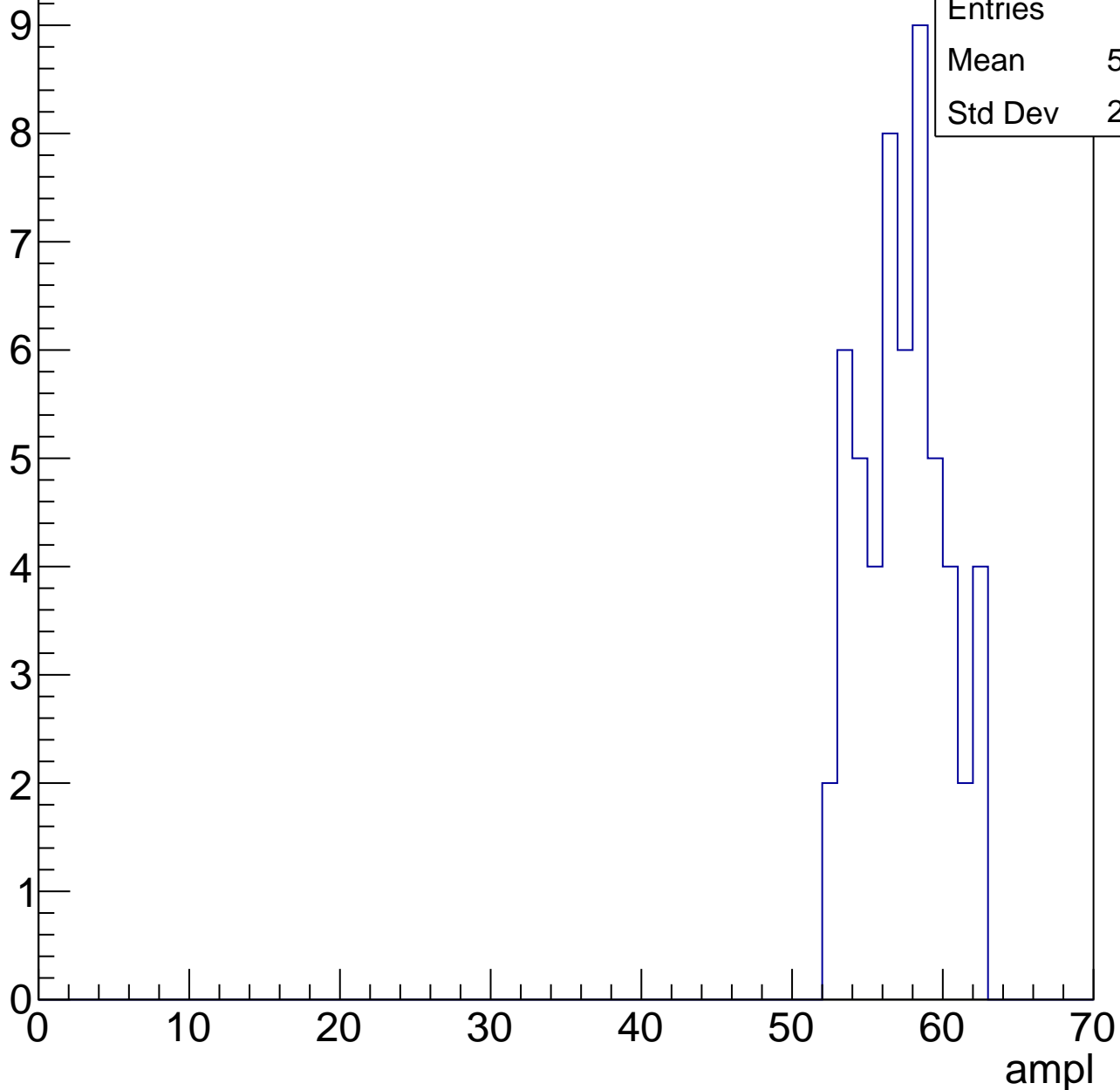
Entries	69
Mean	50.62
Std Dev	3.588



# B0L000S, U25-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

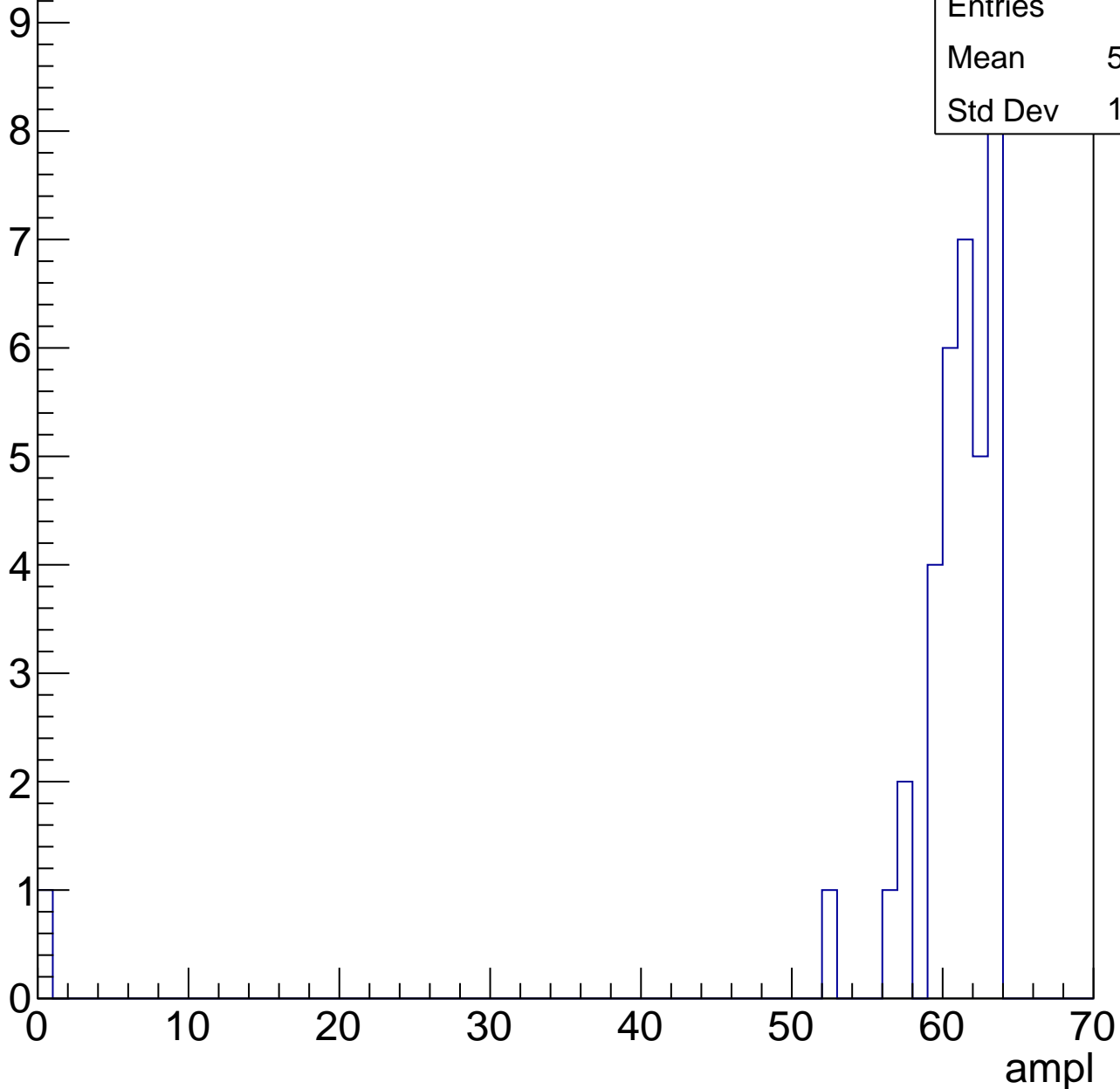


# B0L000S, U25-ch101, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	58.94
Std Dev	10.23



# B0L000S, U25-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch102, adc0

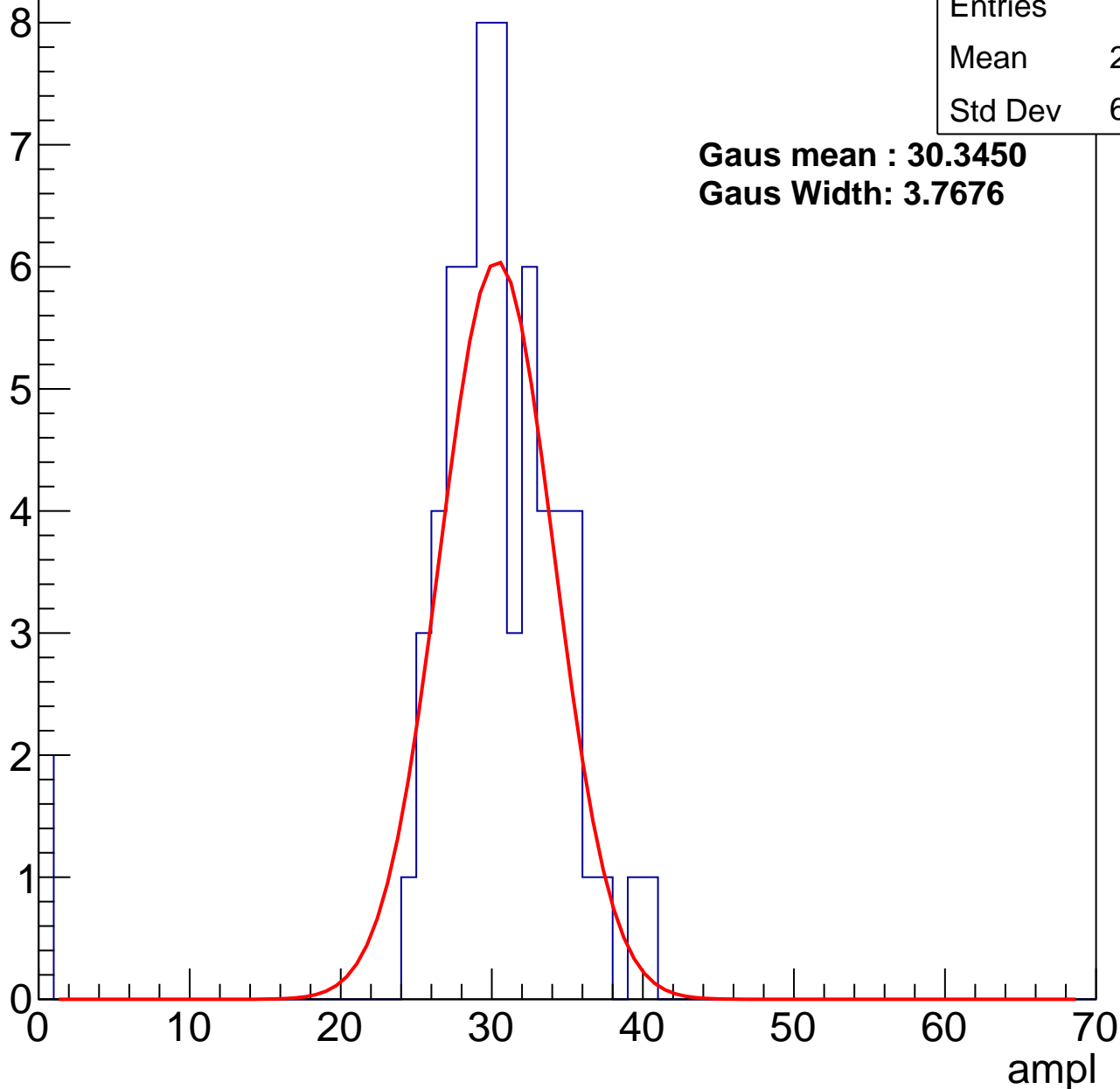
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	29.37
Std Dev	6.335

**Gaus mean : 30.3450**

**Gaus Width: 3.7676**



# B0L000S, U25-ch102, adc1

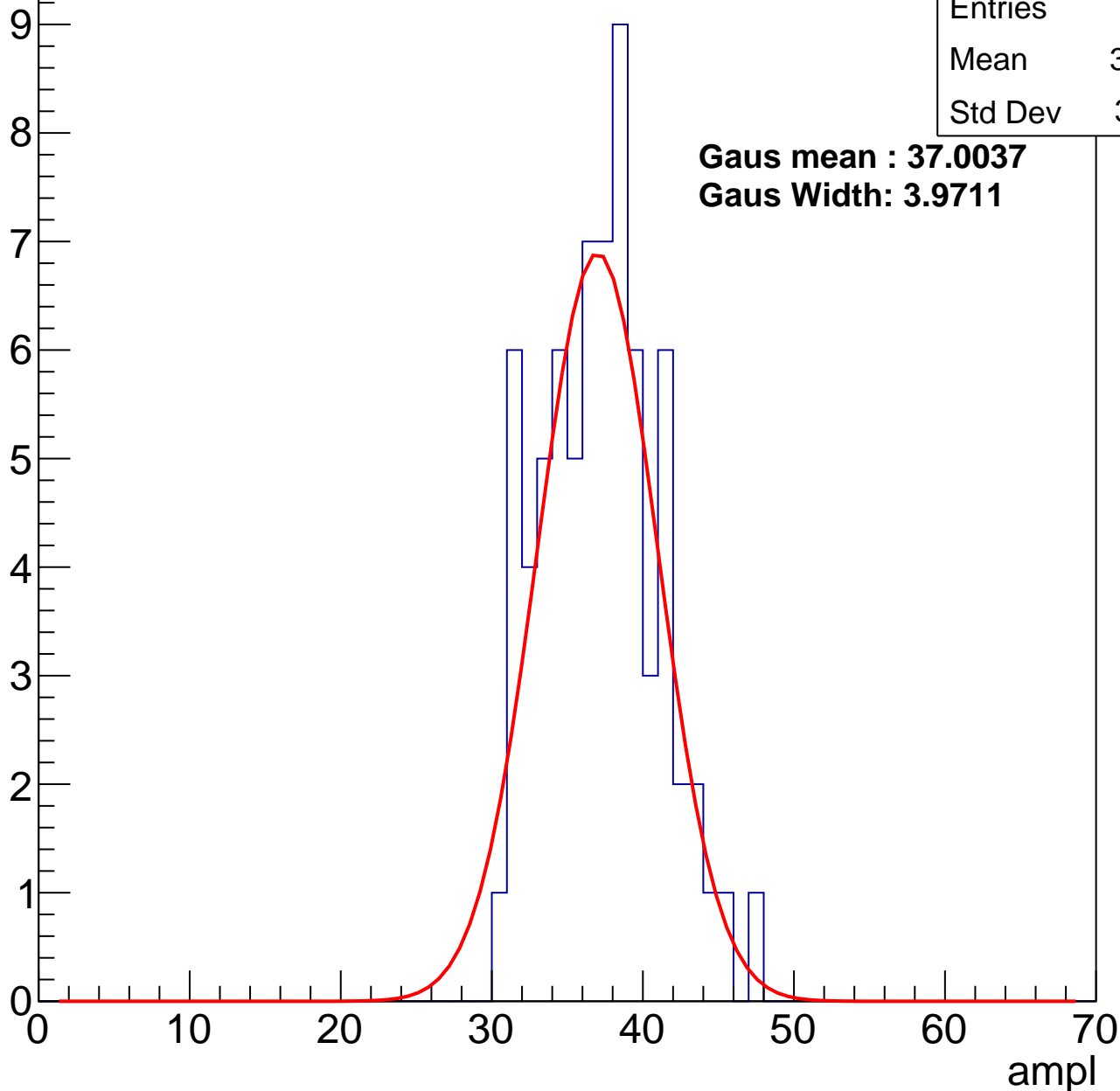
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	36.76
Std Dev	3.751

**Gaus mean : 37.0037**

**Gaus Width: 3.9711**



# B0L000S, U25-ch102, adc2

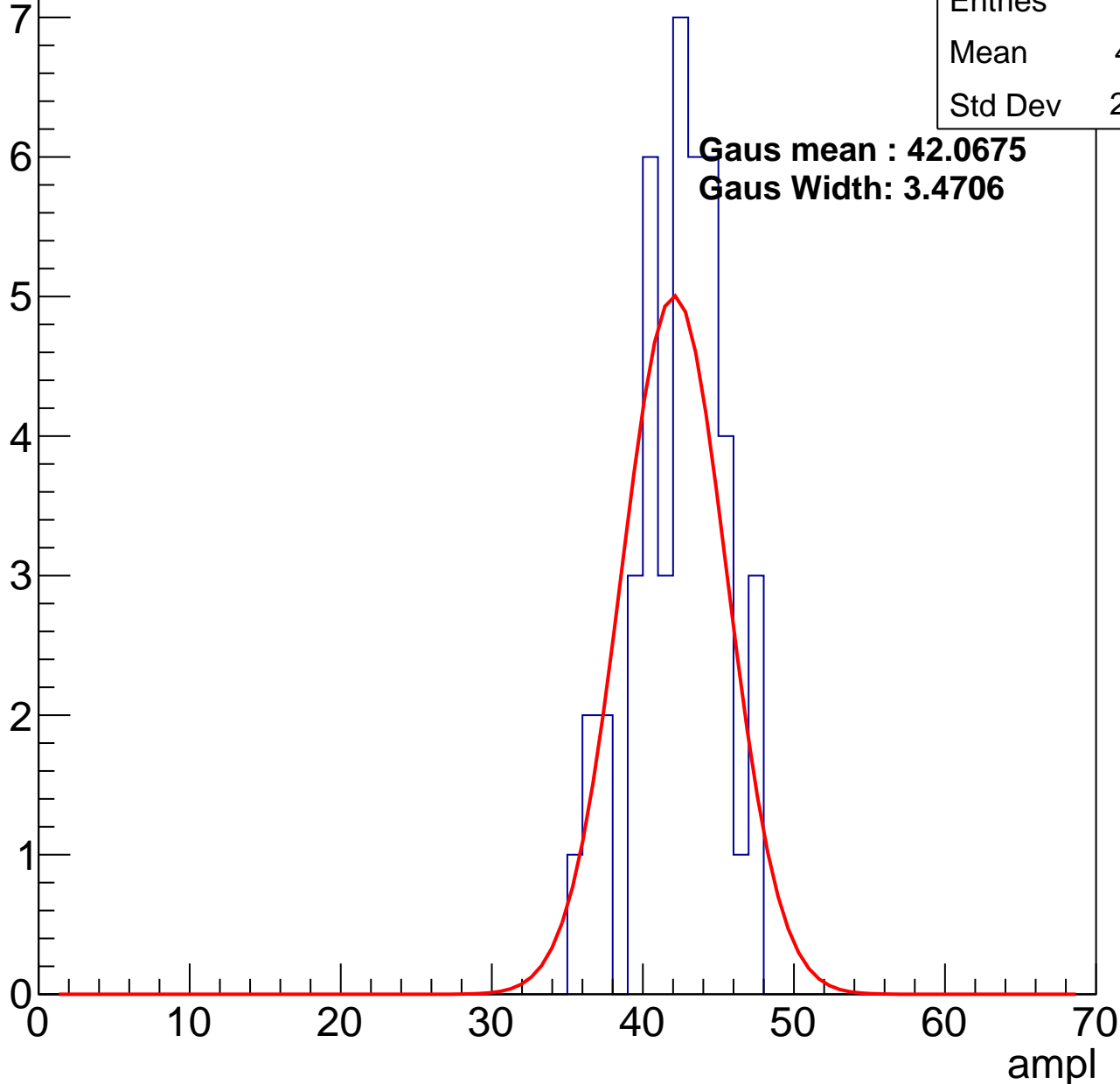
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	41.91
Std Dev	2.945

**Gaus mean : 42.0675**

**Gaus Width: 3.4706**

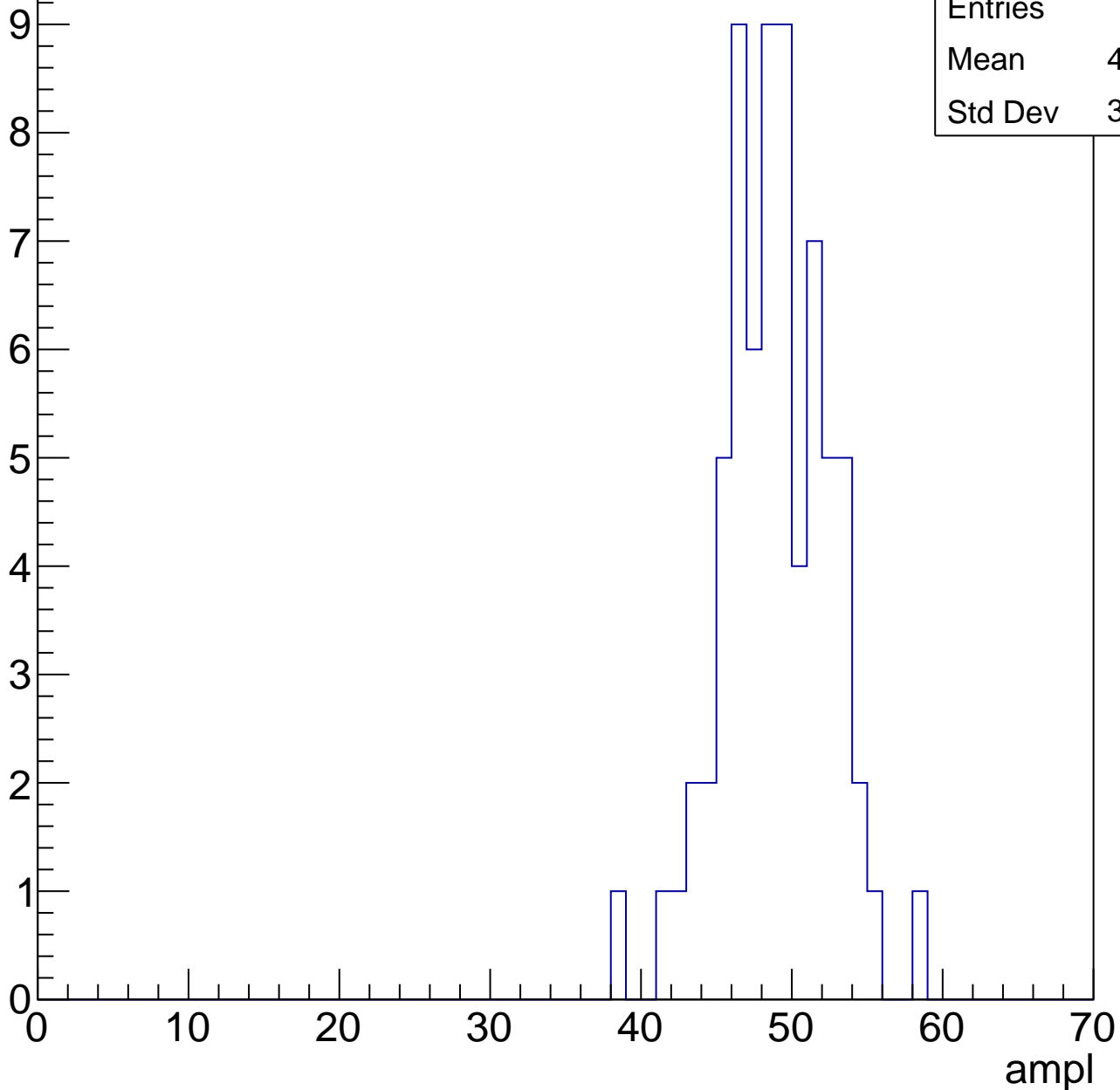


# B0L000S, U25-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	48.46
Std Dev	3.492

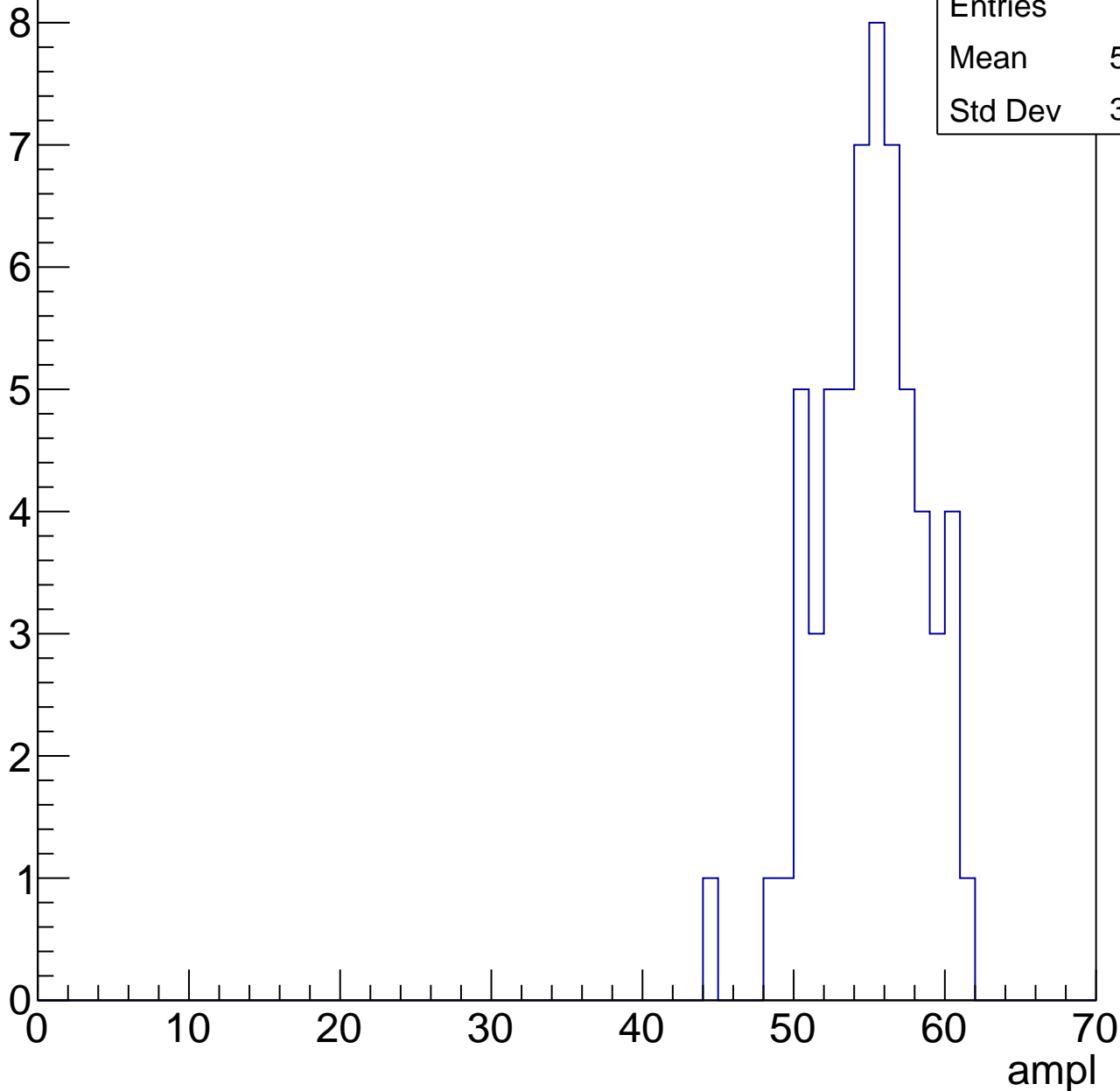


# B0L000S, U25-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	54.57
Std Dev	3.383

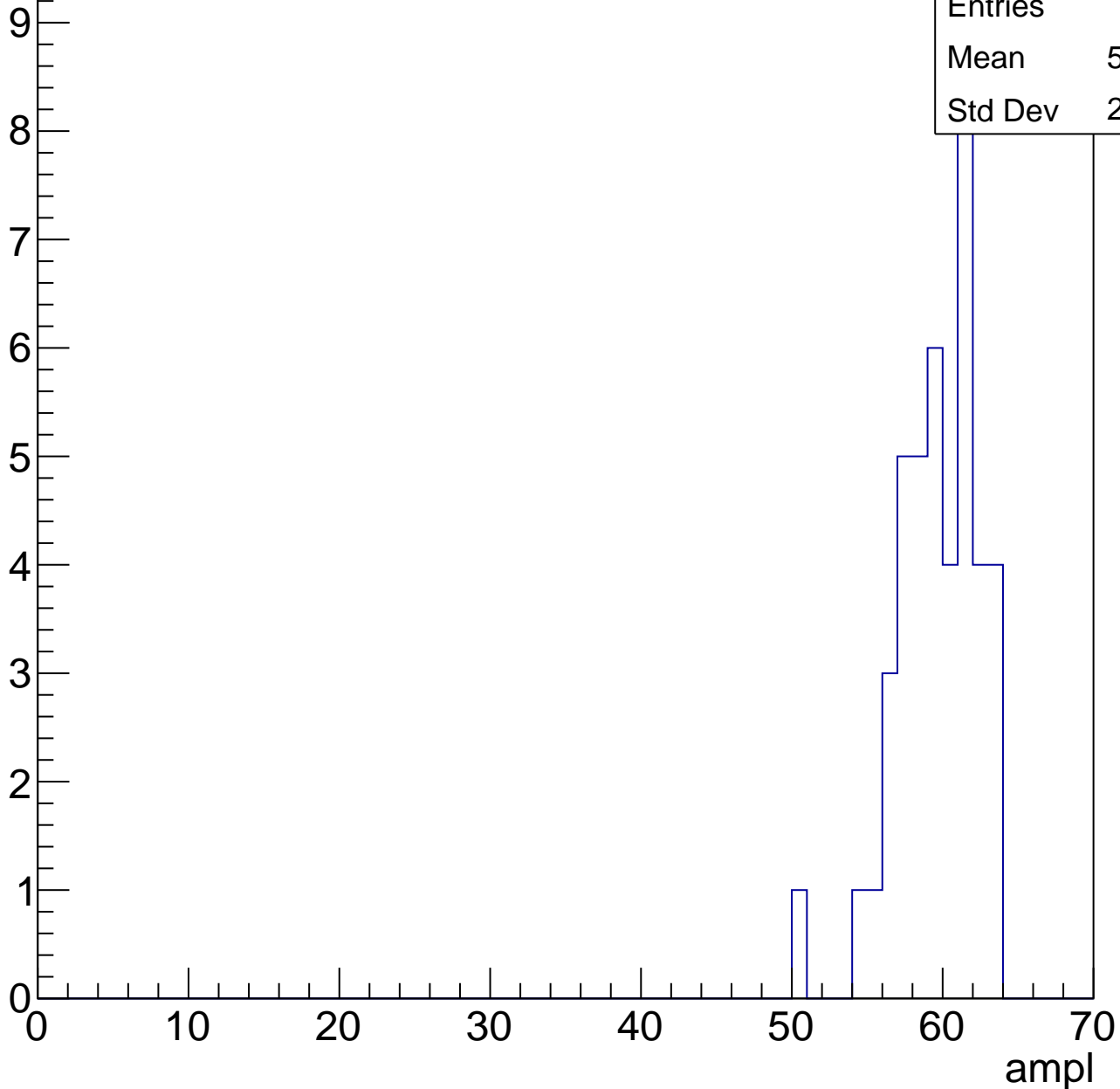


# B0L000S, U25-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	43
Mean	59.19
Std Dev	2.696

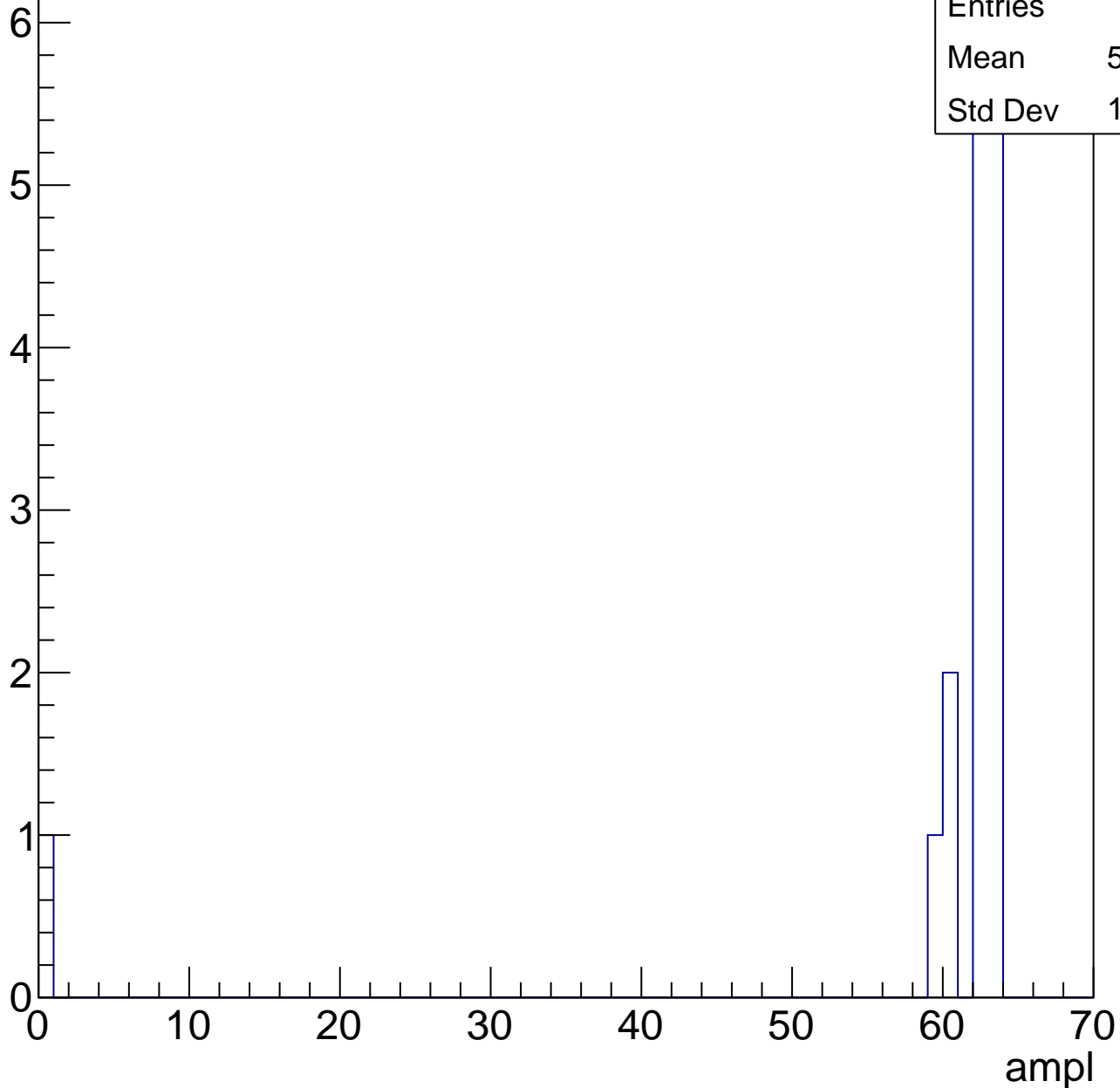


# B0L000S, U25-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	58.06
Std Dev	15.04





# B0L000S, U25-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch103, adc0

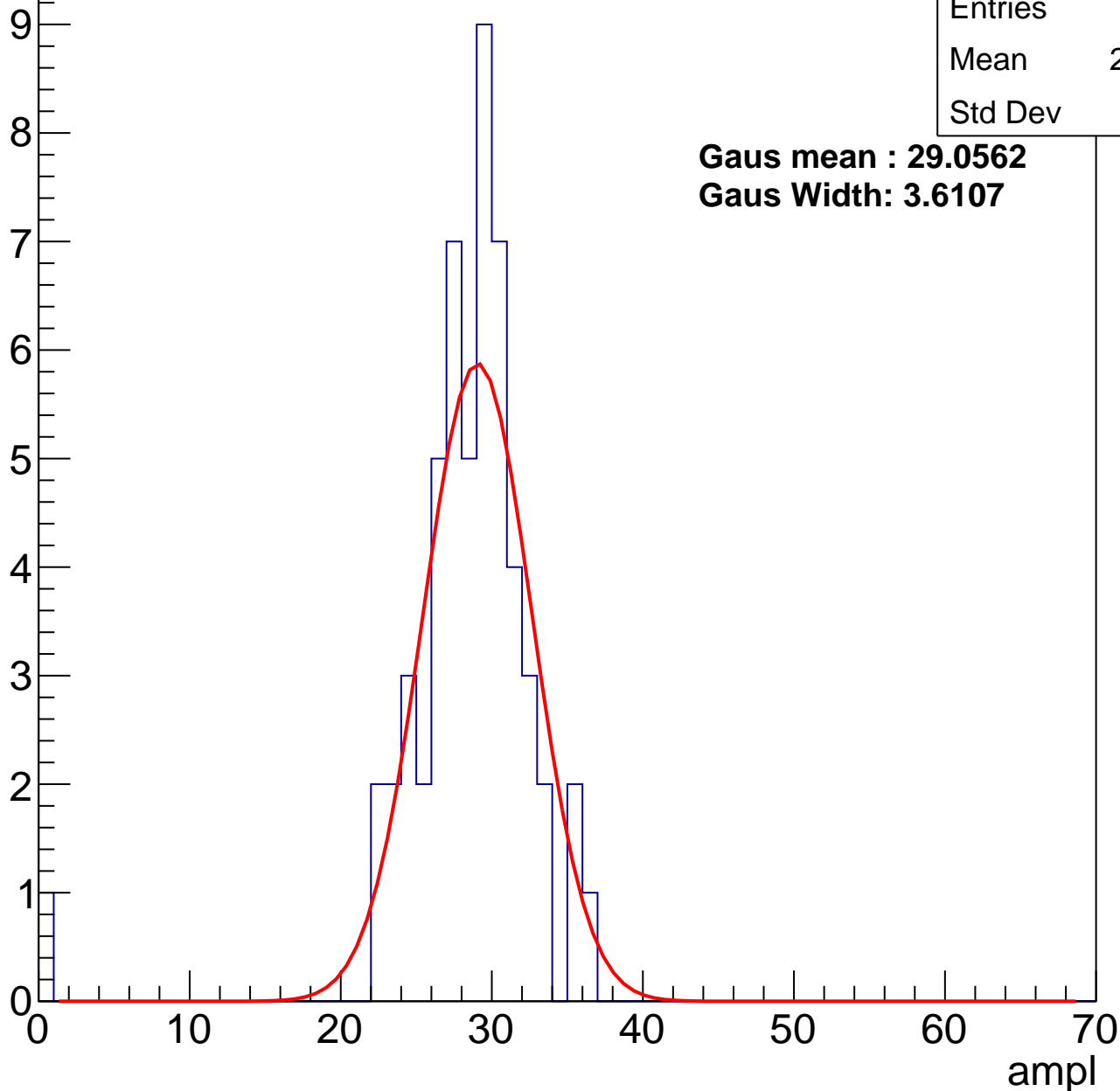
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	27.89
Std Dev	4.92

**Gaus mean : 29.0562**

**Gaus Width: 3.6107**



# B0L000S, U25-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	93
Mean	36.01
Std Dev	3.591

**Gaus mean : 36.6110**

**Gaus Width: 3.6017**

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

10

# B0L000S, U25-ch103, adc2

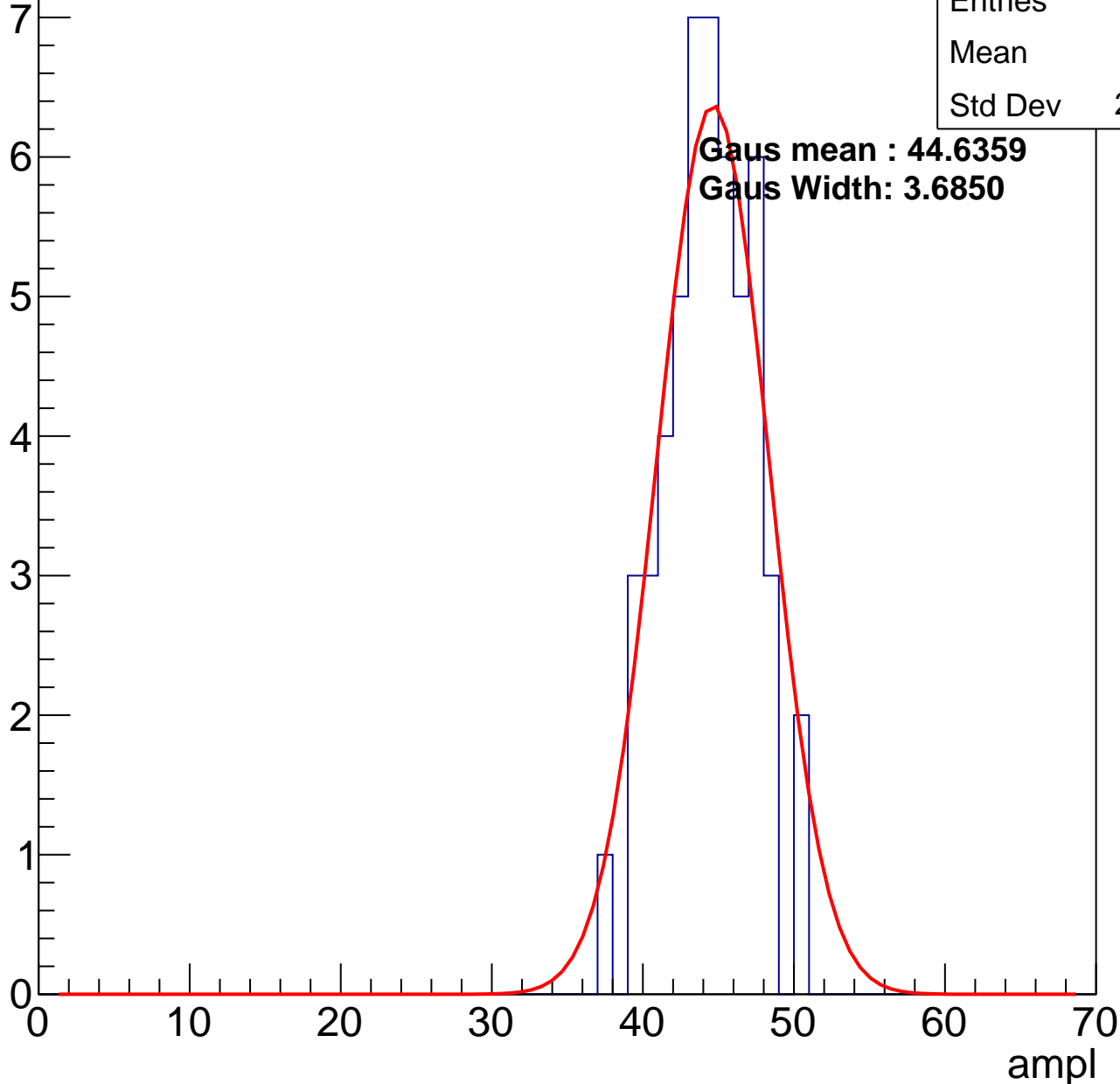
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	43.9
Std Dev	2.891

**Gaus mean : 44.6359**

**Gaus Width: 3.6850**



# B0L000S, U25-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

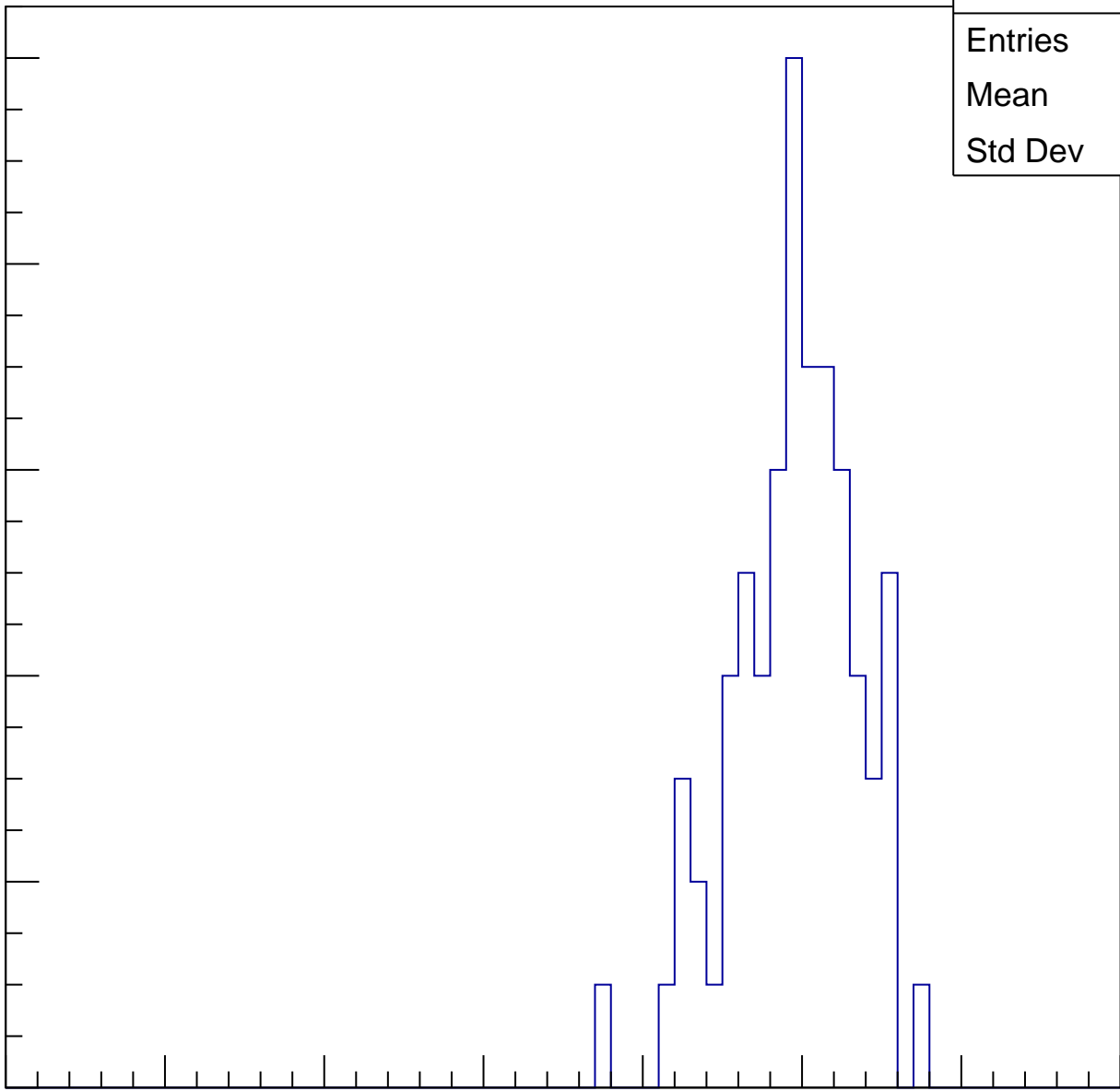
Entries	70
Mean	49.07
Std Dev	3.882

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

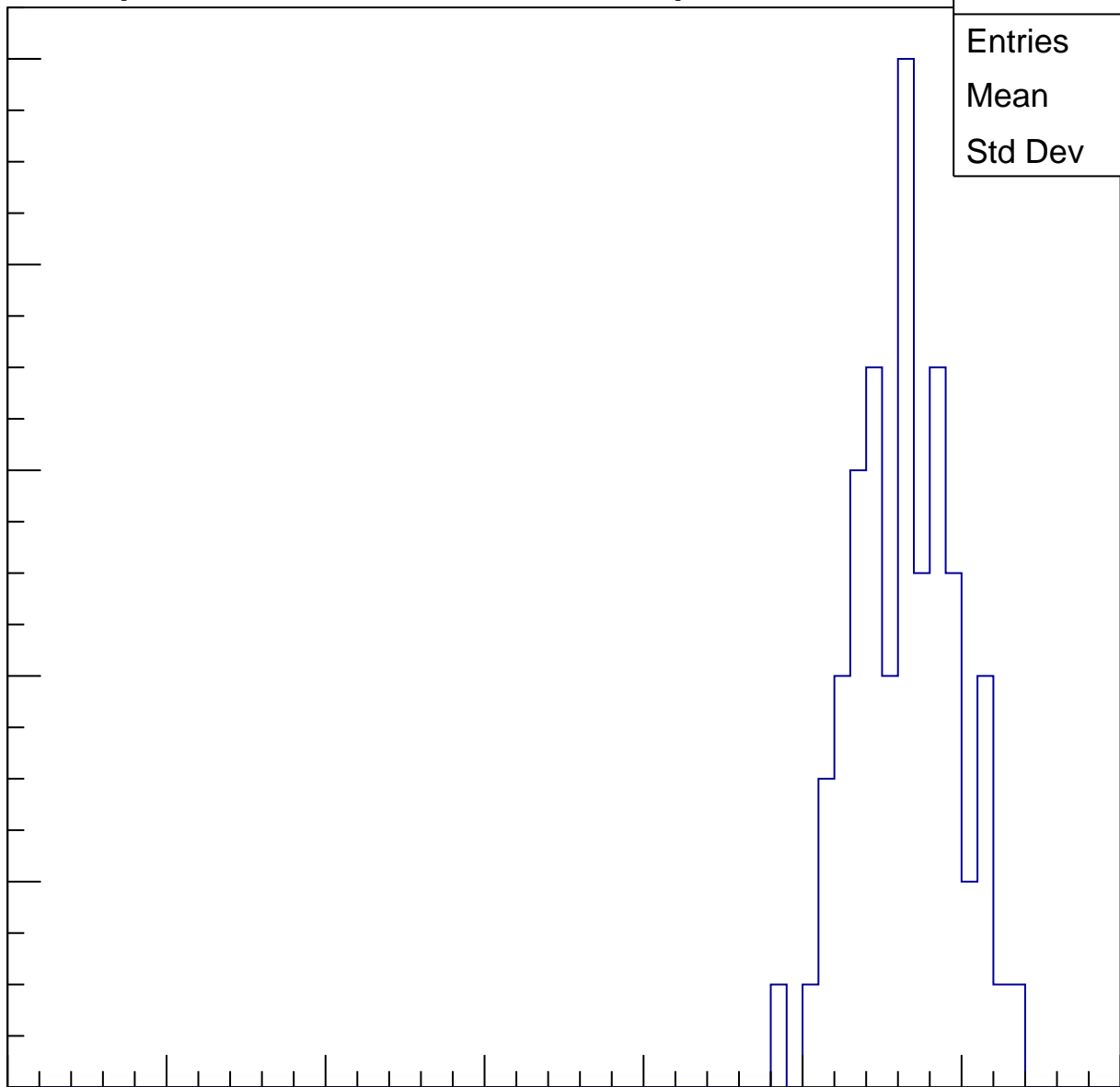
Entries	61
Mean	55.9
Std Dev	3.187

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

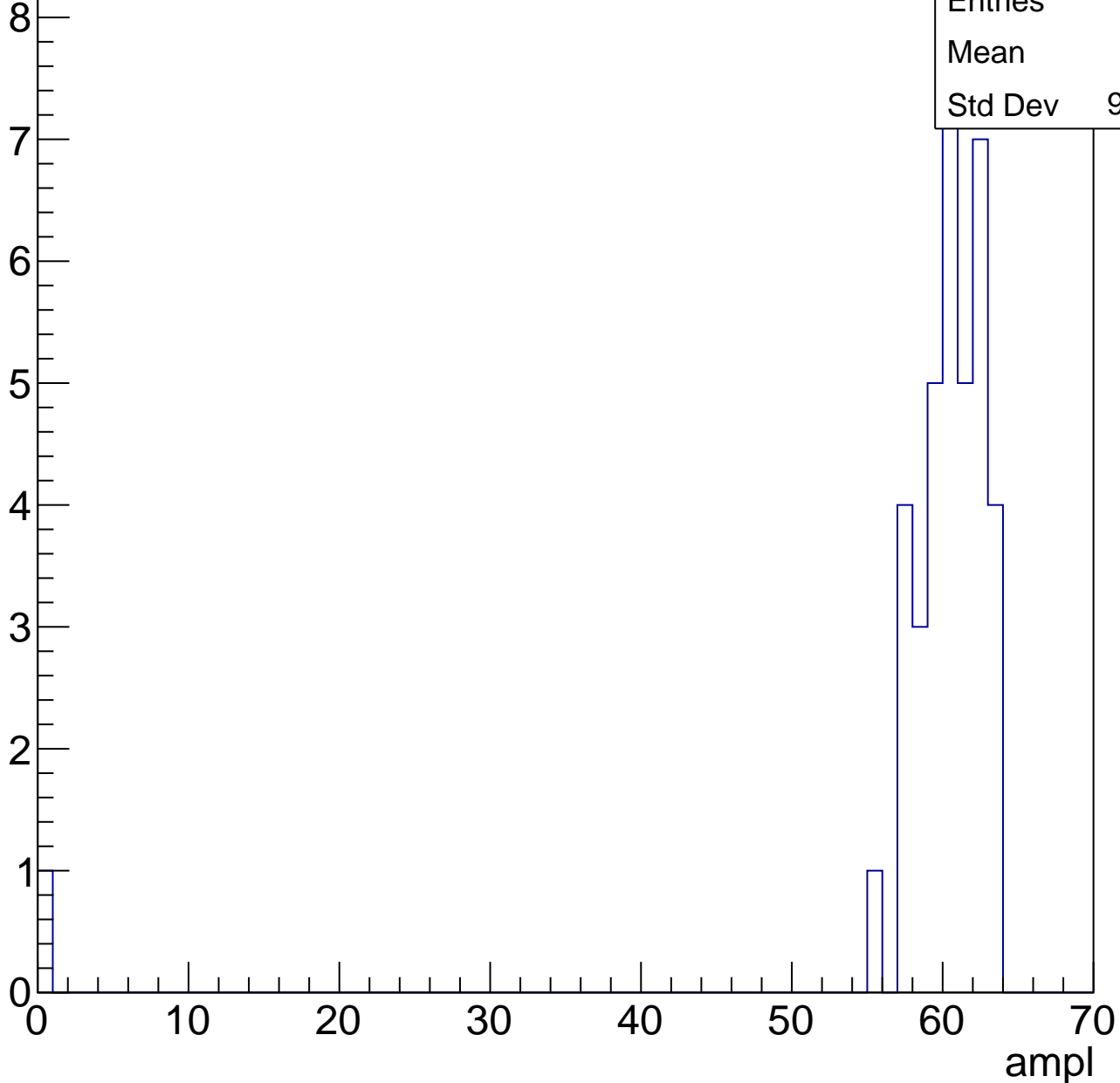


# B0L000S, U25-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.5
Std Dev	9.816

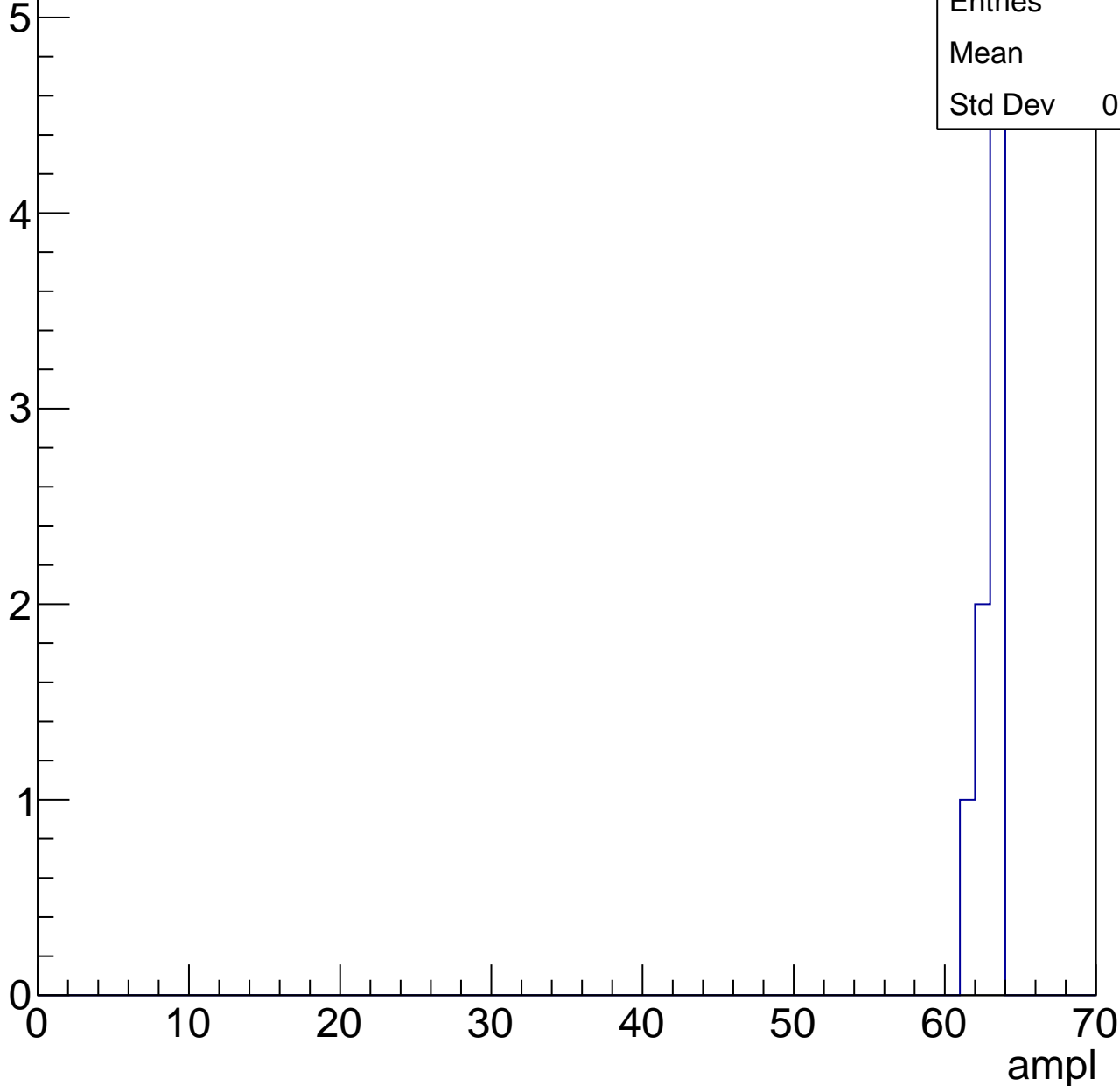


# B0L000S, U25-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	8
Mean	62.5
Std Dev	0.7071





# B0L000S, U25-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch104, adc0

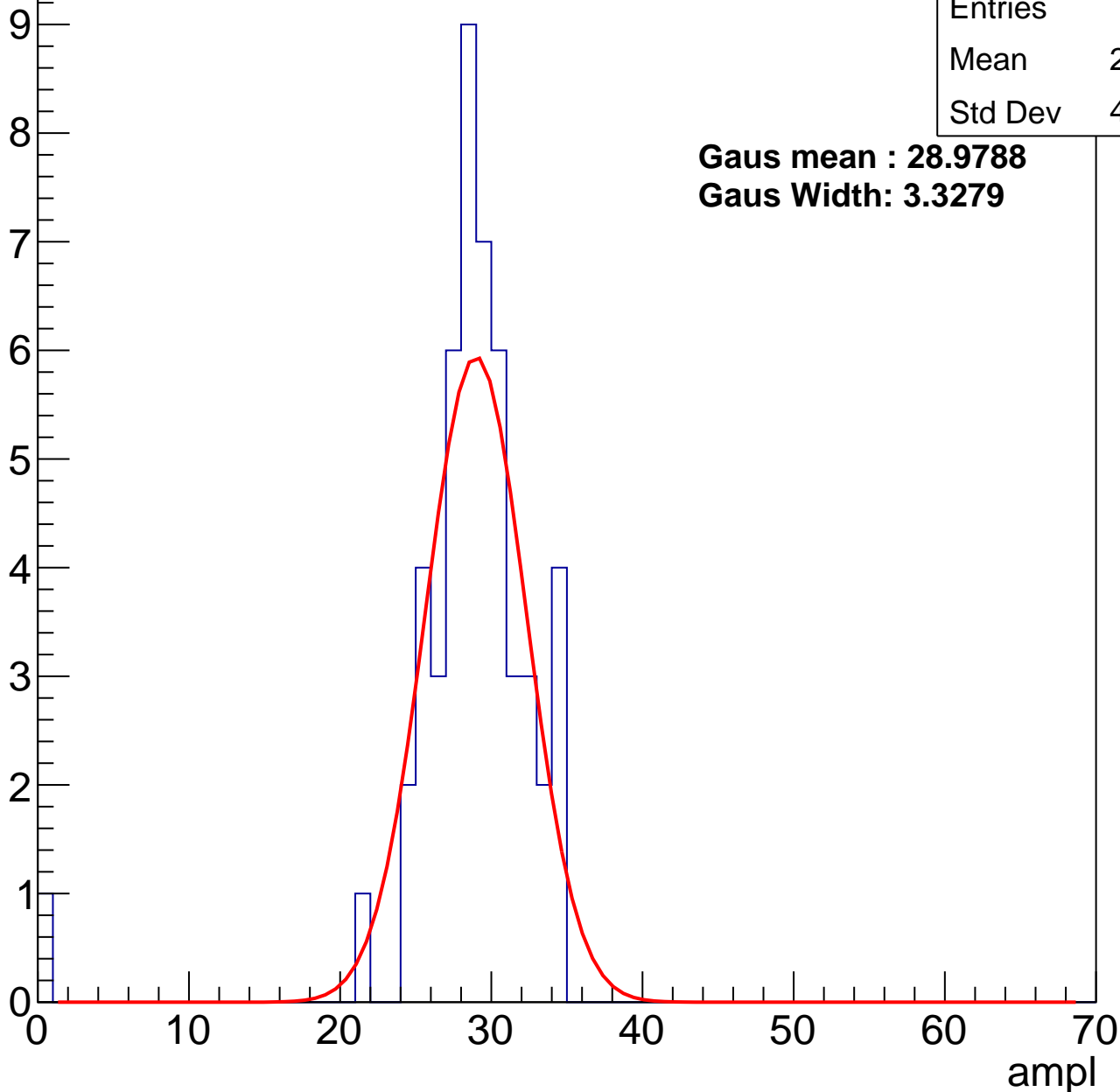
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	28.14
Std Dev	4.887

**Gaus mean : 28.9788**

**Gaus Width: 3.3279**



# B0L000S, U25-ch104, adc1

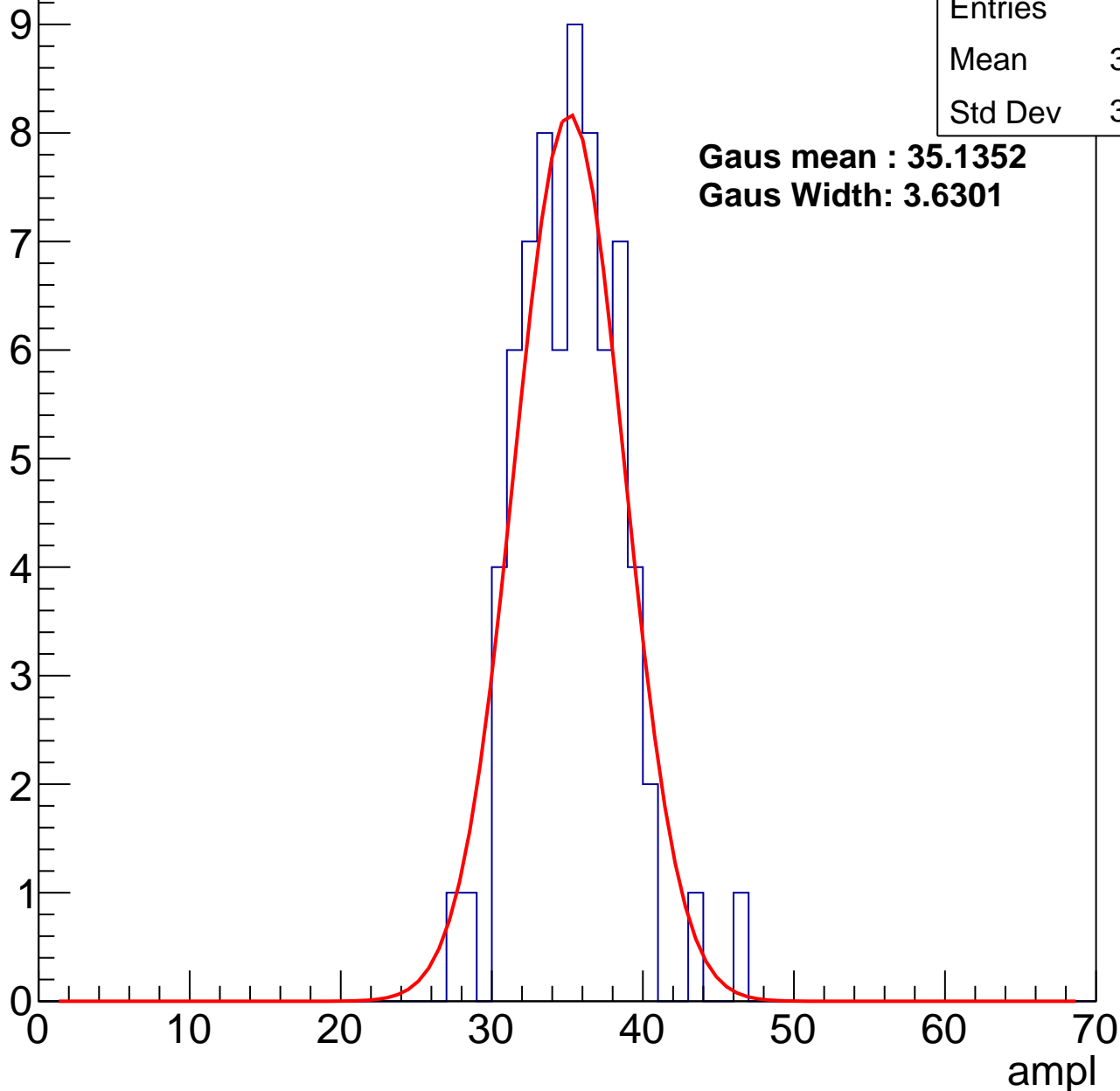
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	34.77
Std Dev	3.358

**Gaus mean : 35.1352**

**Gaus Width: 3.6301**



# B0L000S, U25-ch104, adc2

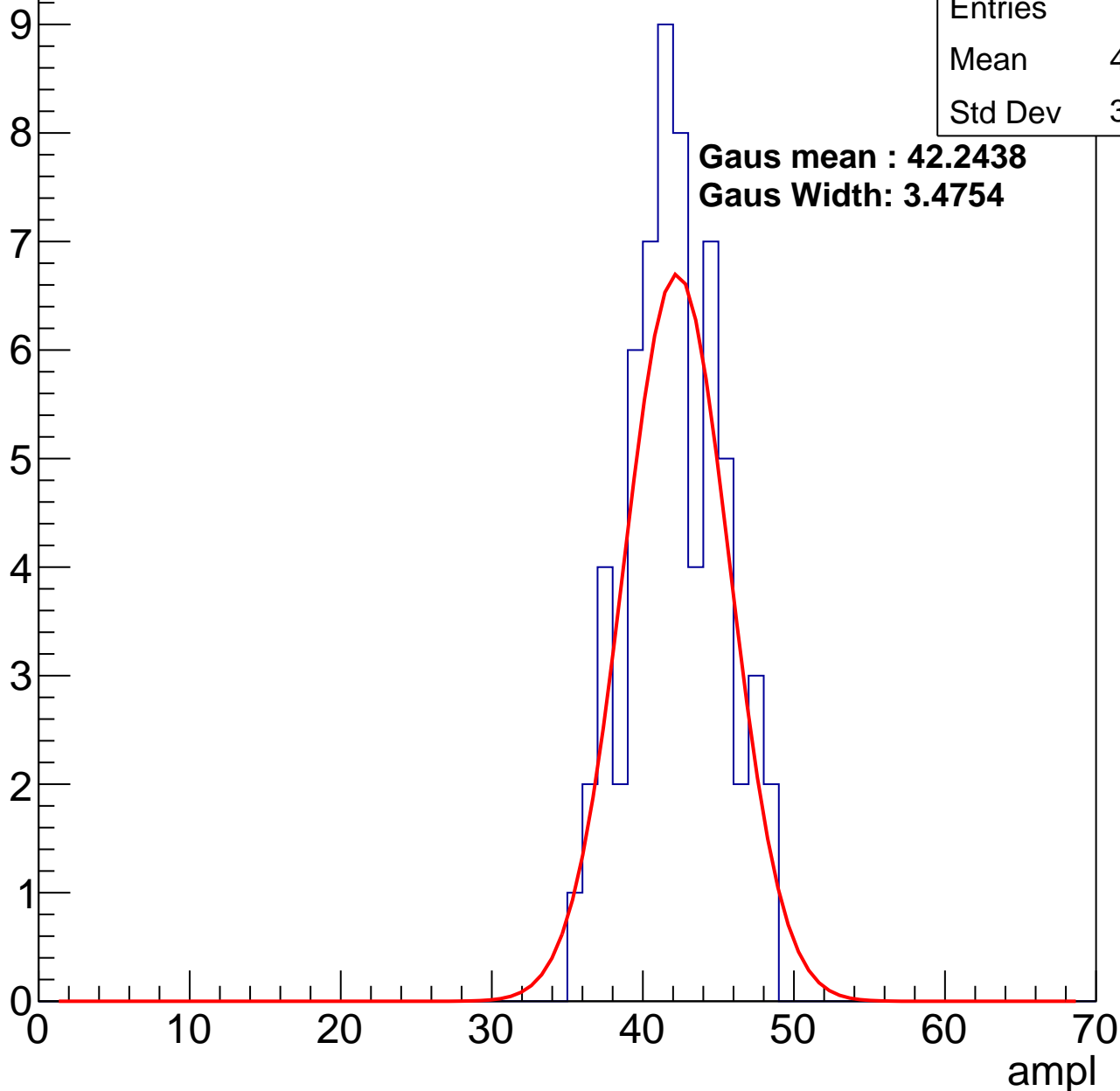
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	41.68
Std Dev	3.094

**Gaus mean : 42.2438**

**Gaus Width: 3.4754**

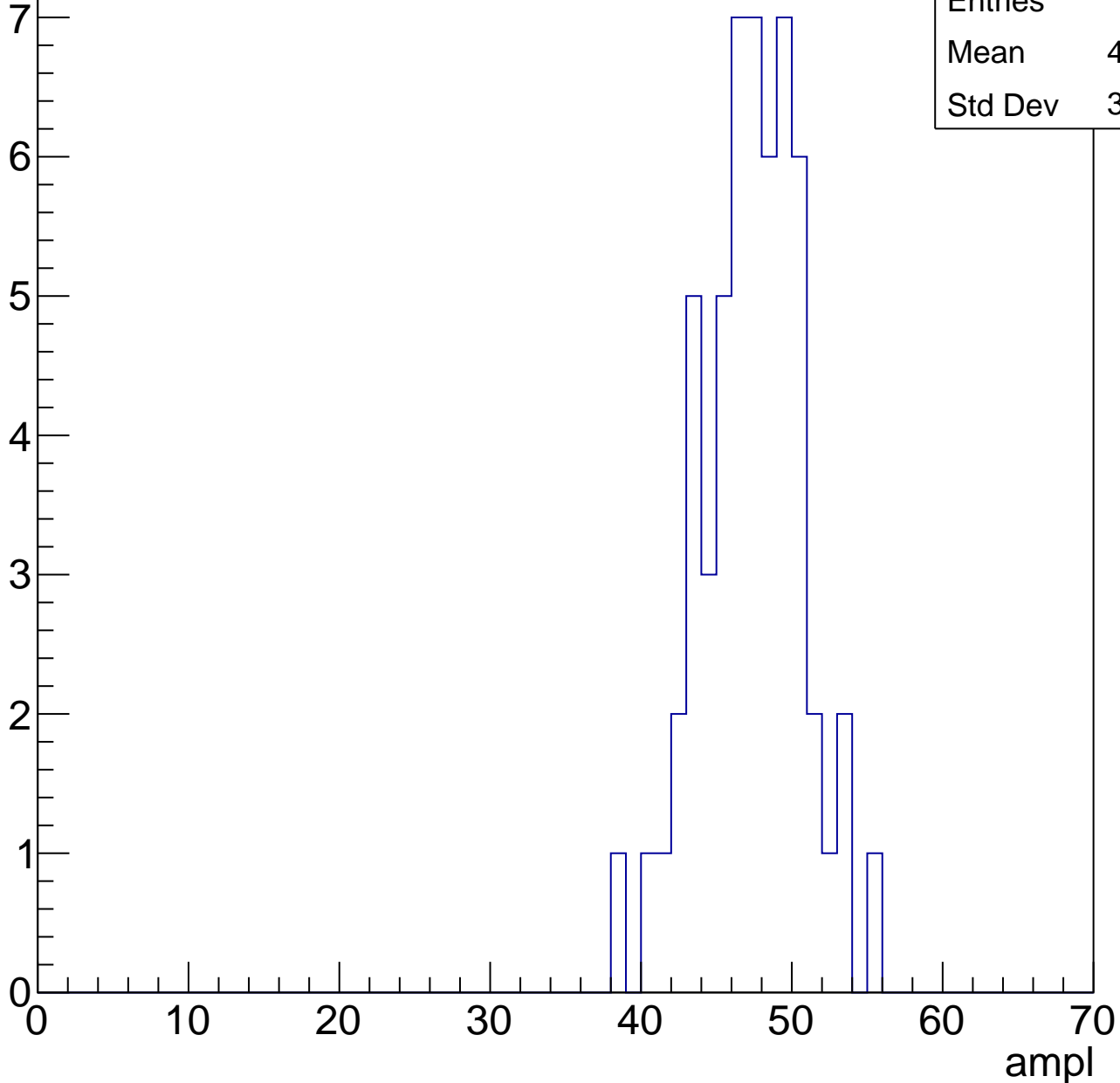


# B0L000S, U25-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	46.88
Std Dev	3.325

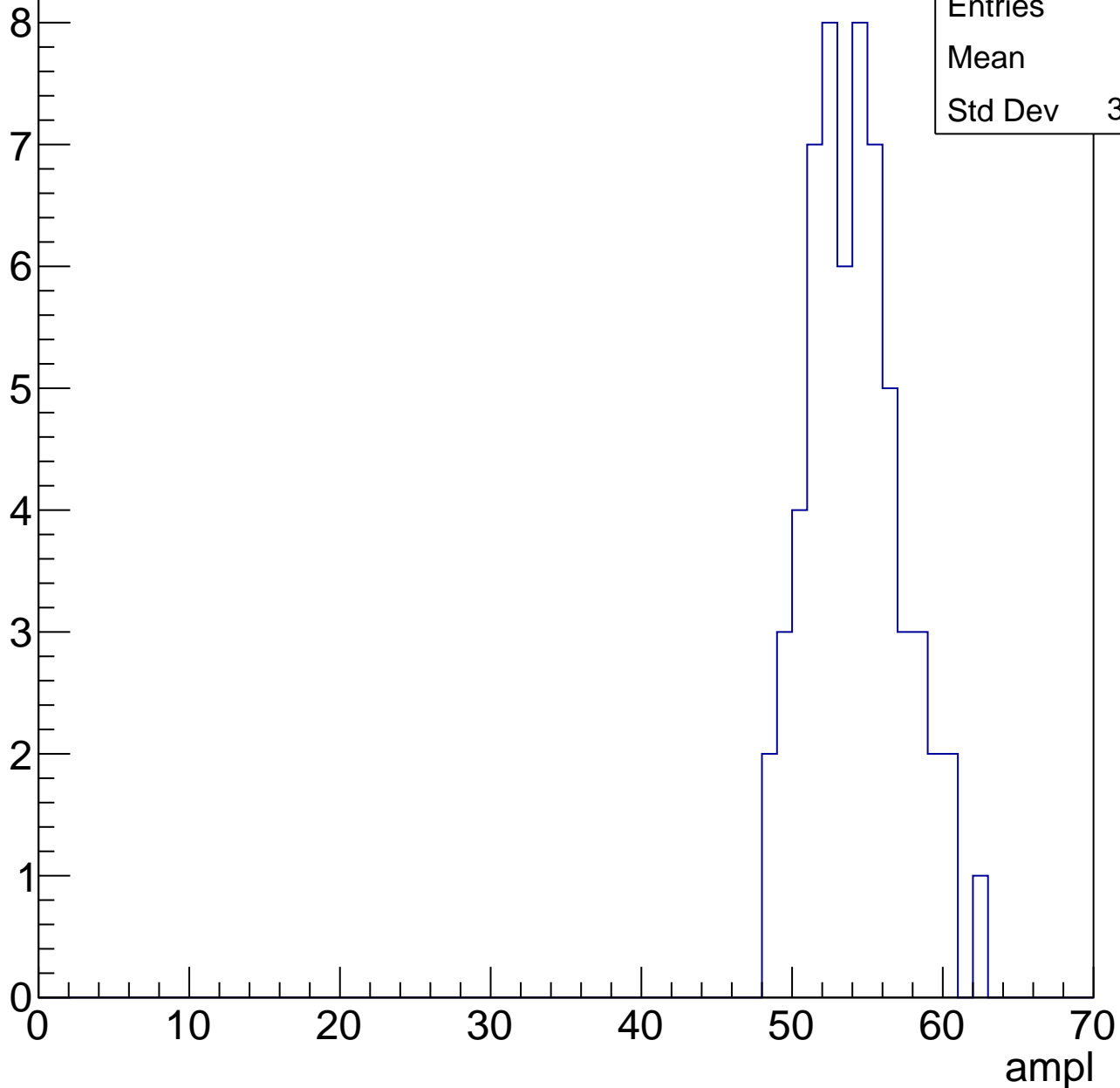


# B0L000S, U25-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	53.7
Std Dev	3.122

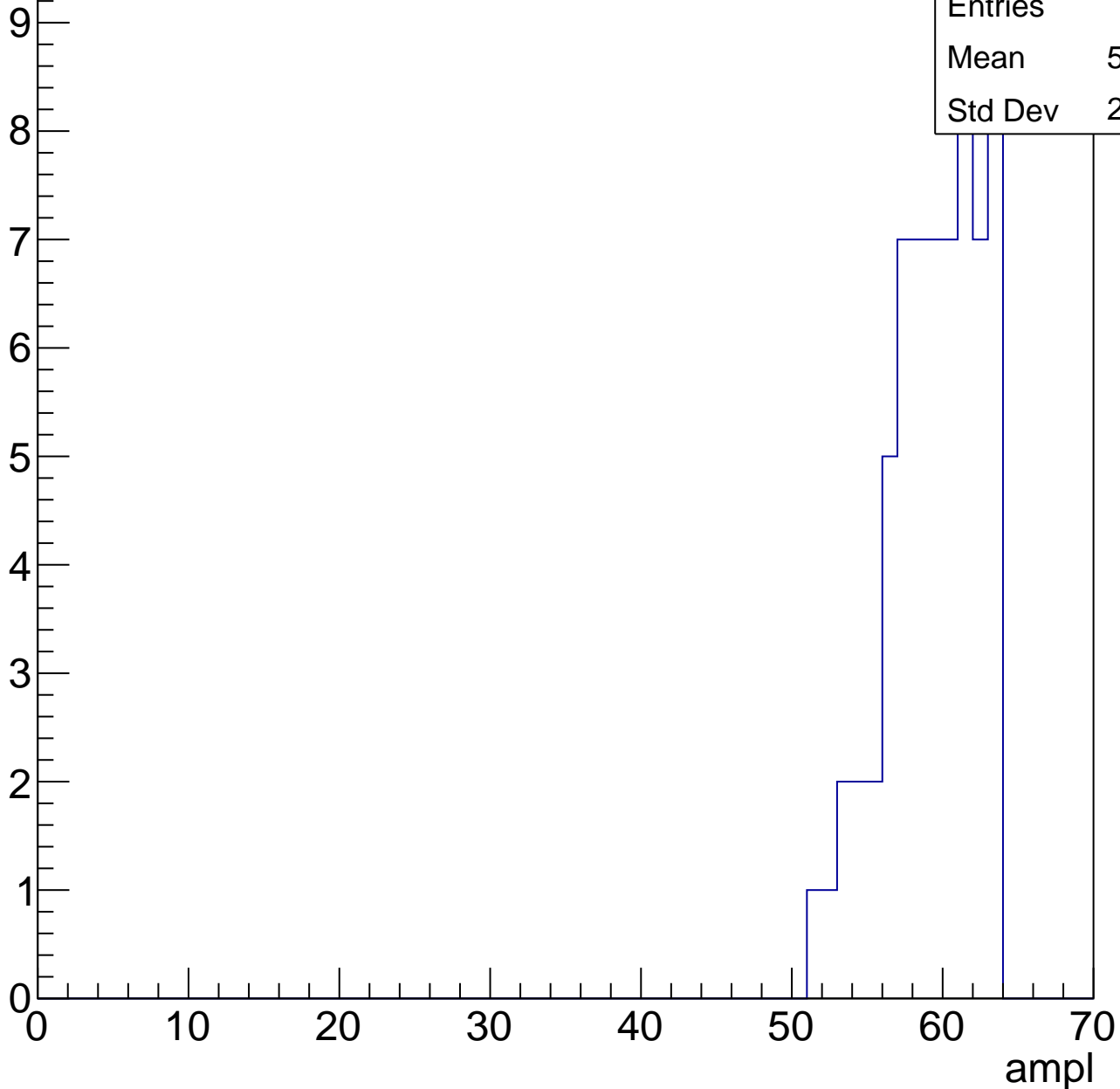


# B0L000S, U25-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	58.95
Std Dev	2.989



# B0L000S, U25-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

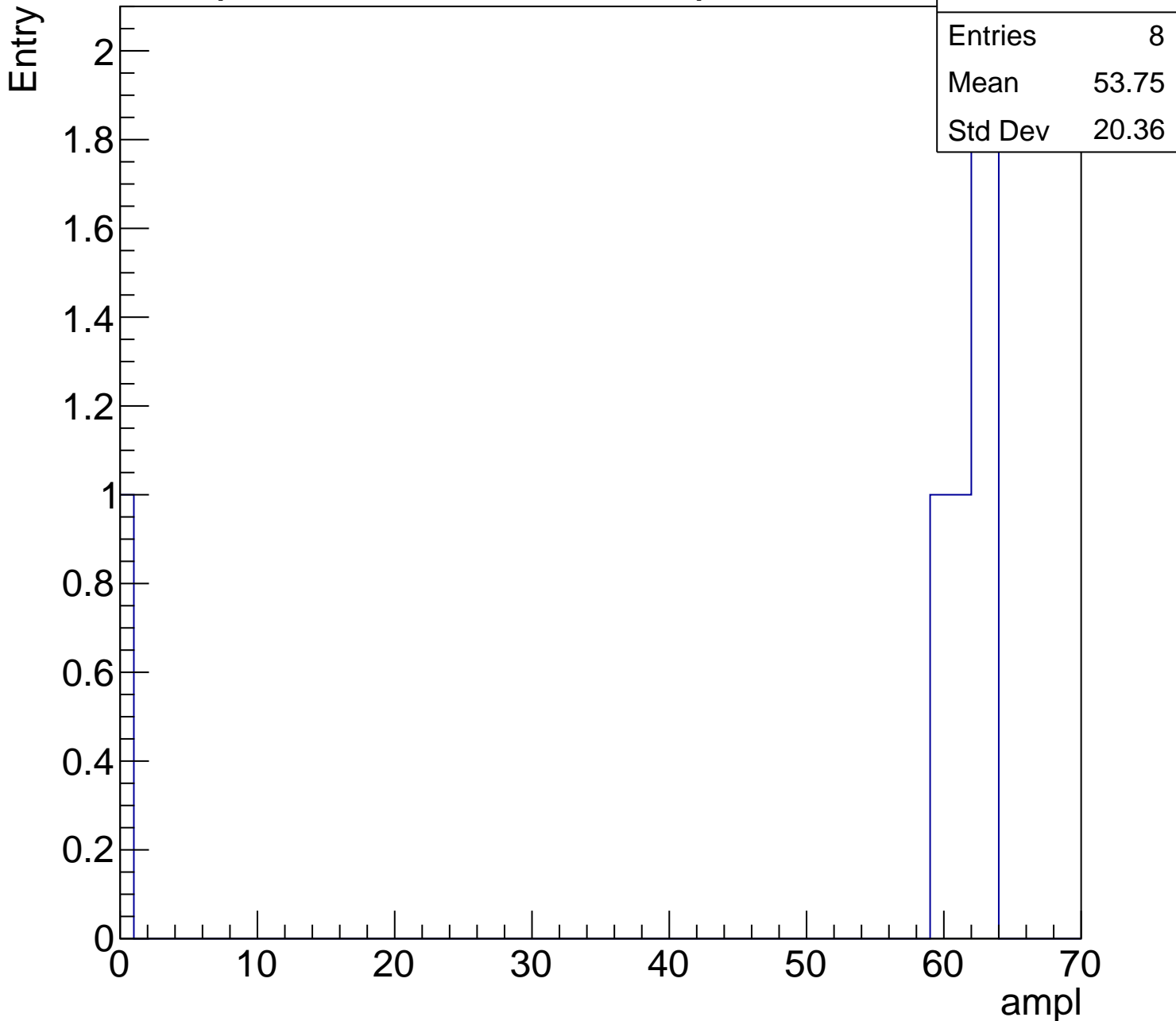
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	8
Mean	53.75
Std Dev	20.36

0 10 20 30 40 50 60 70

ampl





# B0L000S, U25-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch105, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	92
Mean	28.52
Std Dev	5.698

**Gaus mean : 29.9070**

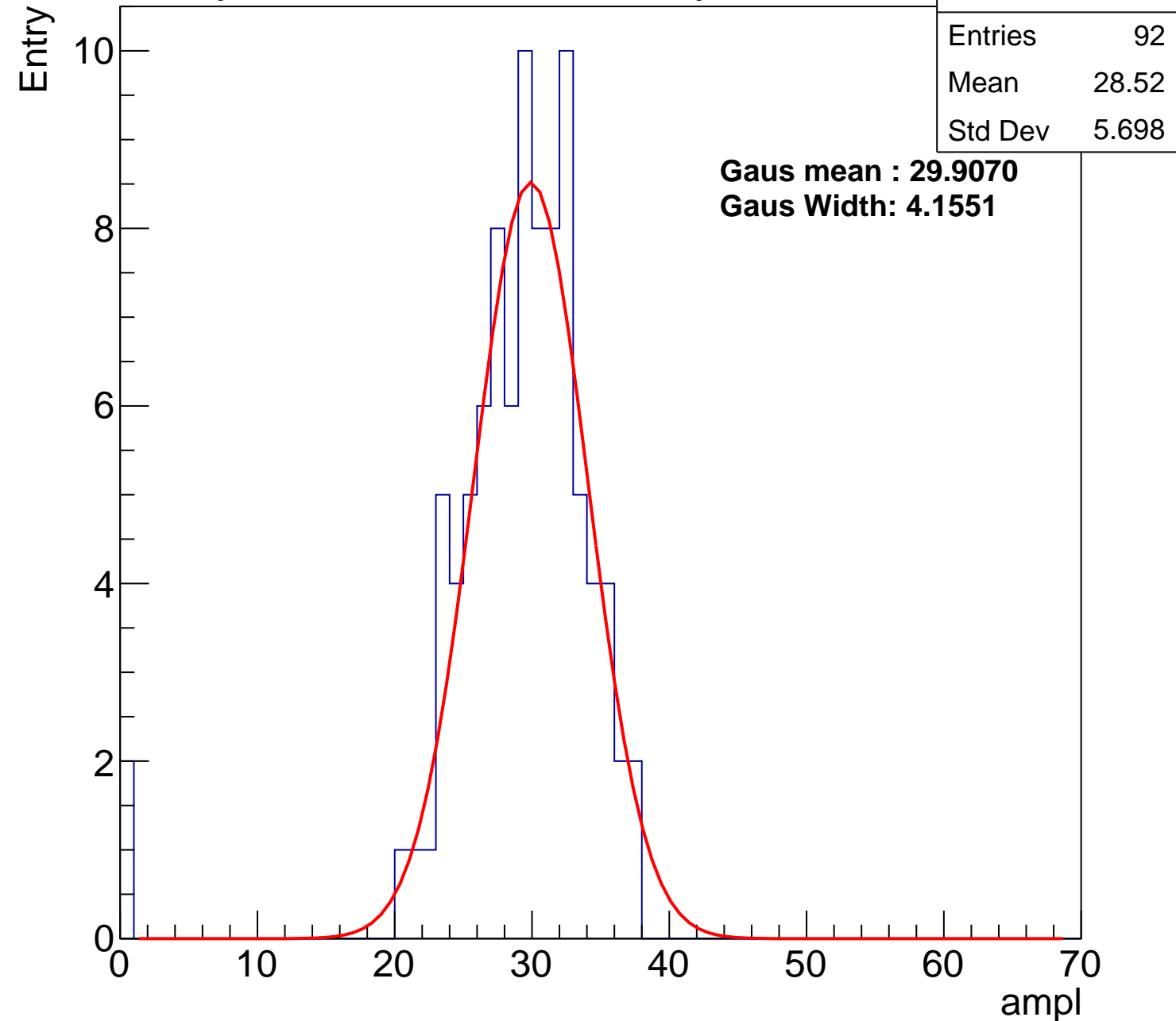
**Gaus Width: 4.1551**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch105, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	37.49
Std Dev	3.227

**Gaus mean : 37.6017**

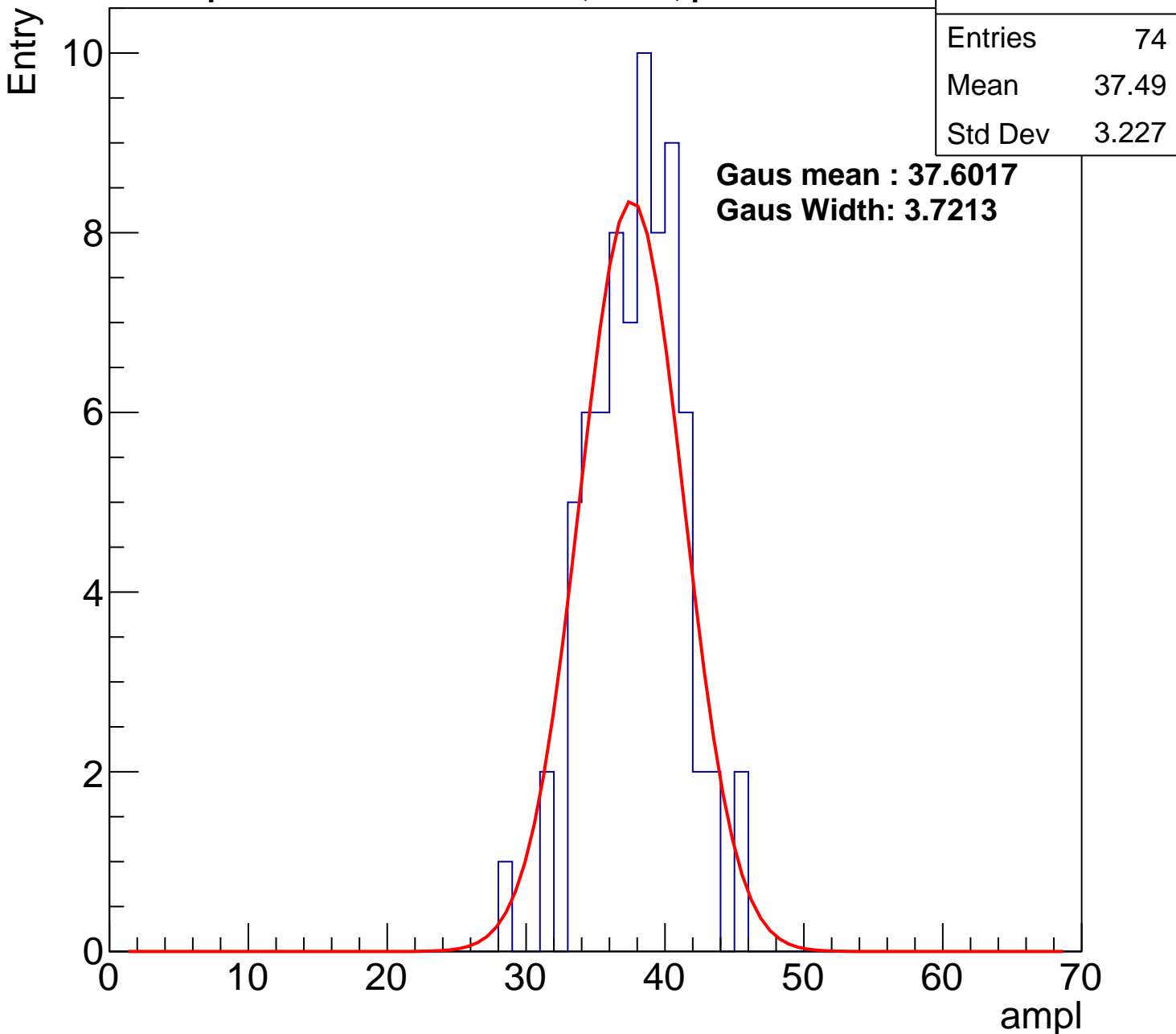
**Gaus Width: 3.7213**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch105, adc2

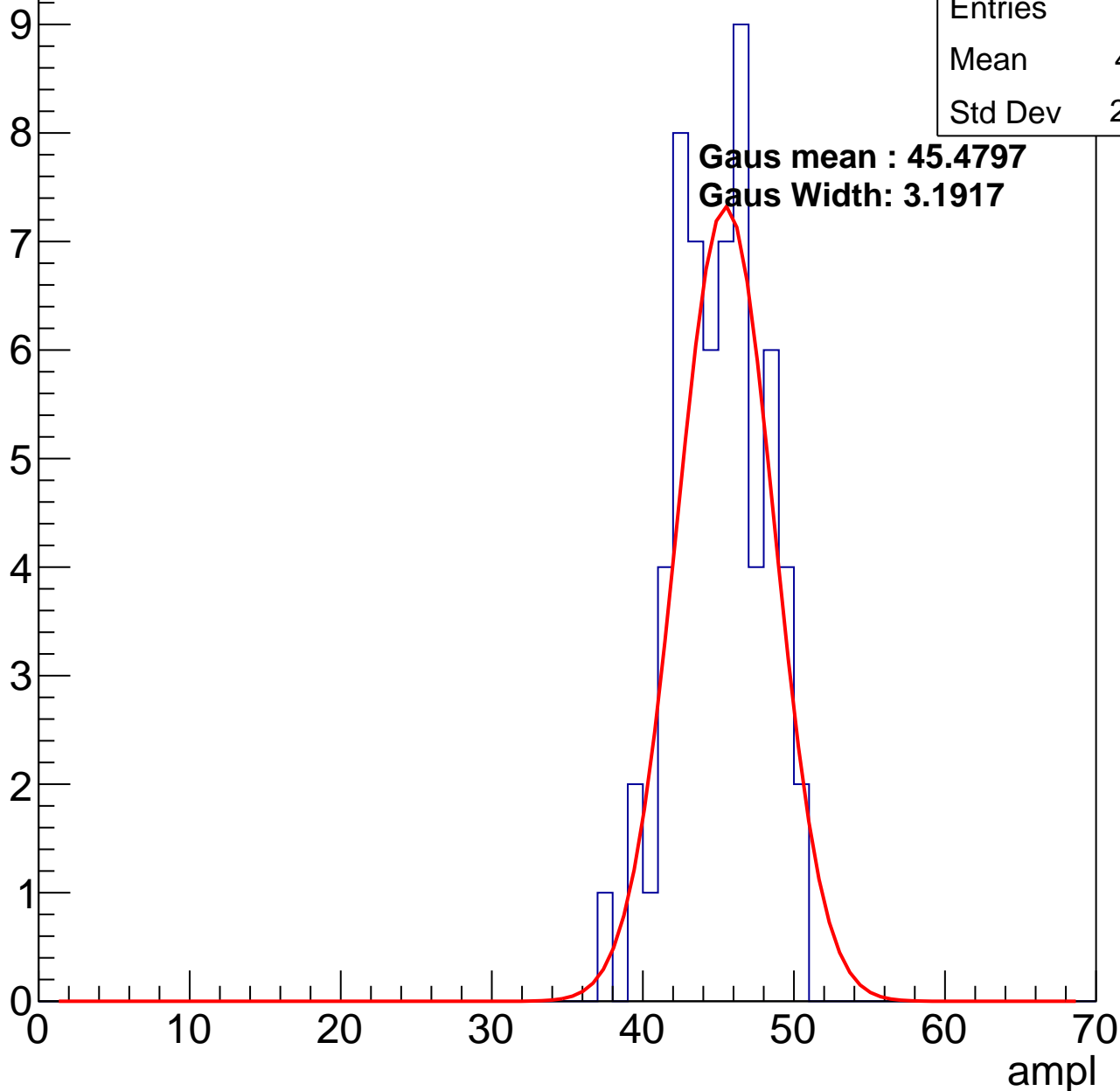
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	44.61
Std Dev	2.904

**Gaus mean : 45.4797**

**Gaus Width: 3.1917**

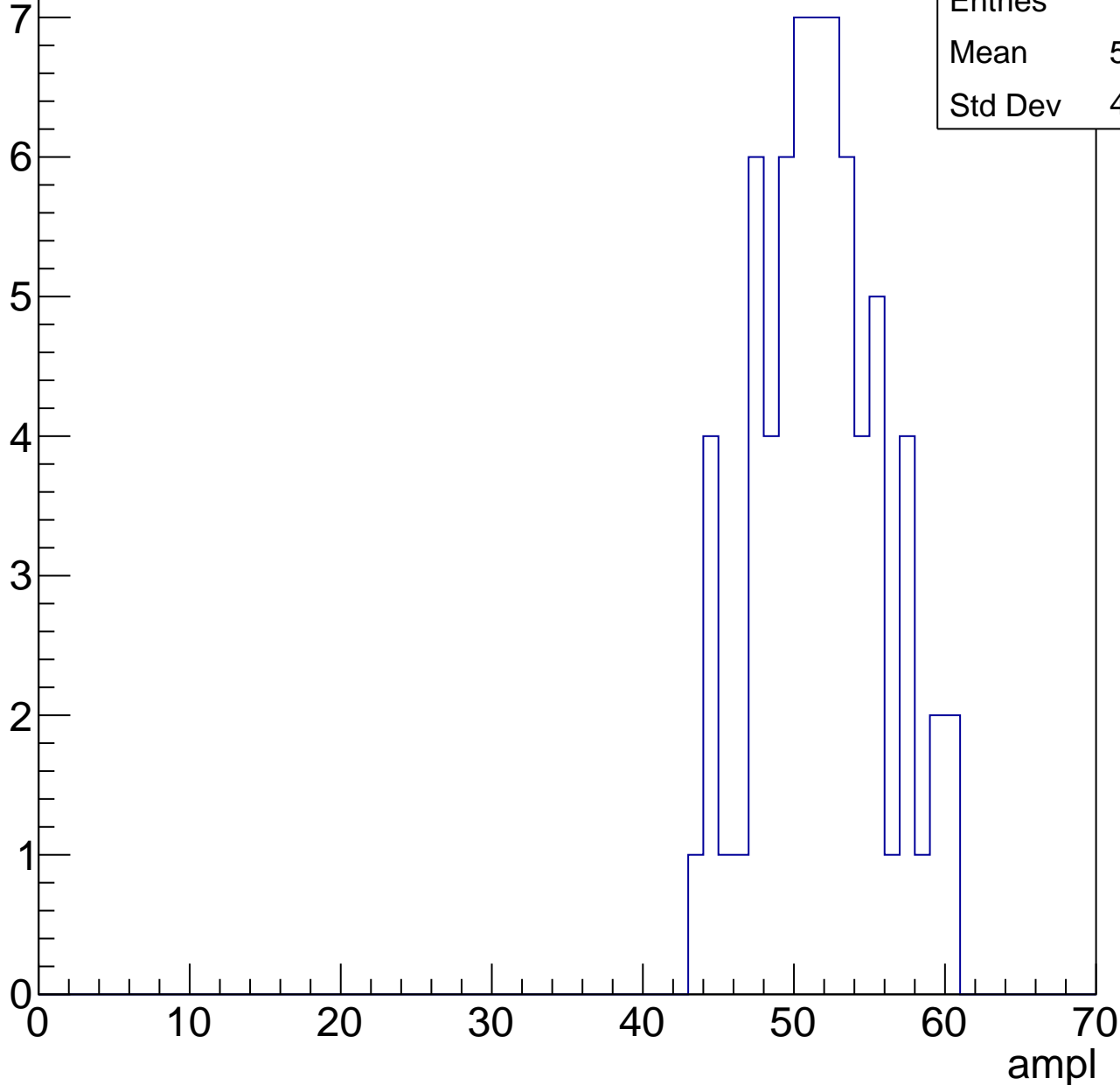


# B0L000S, U25-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

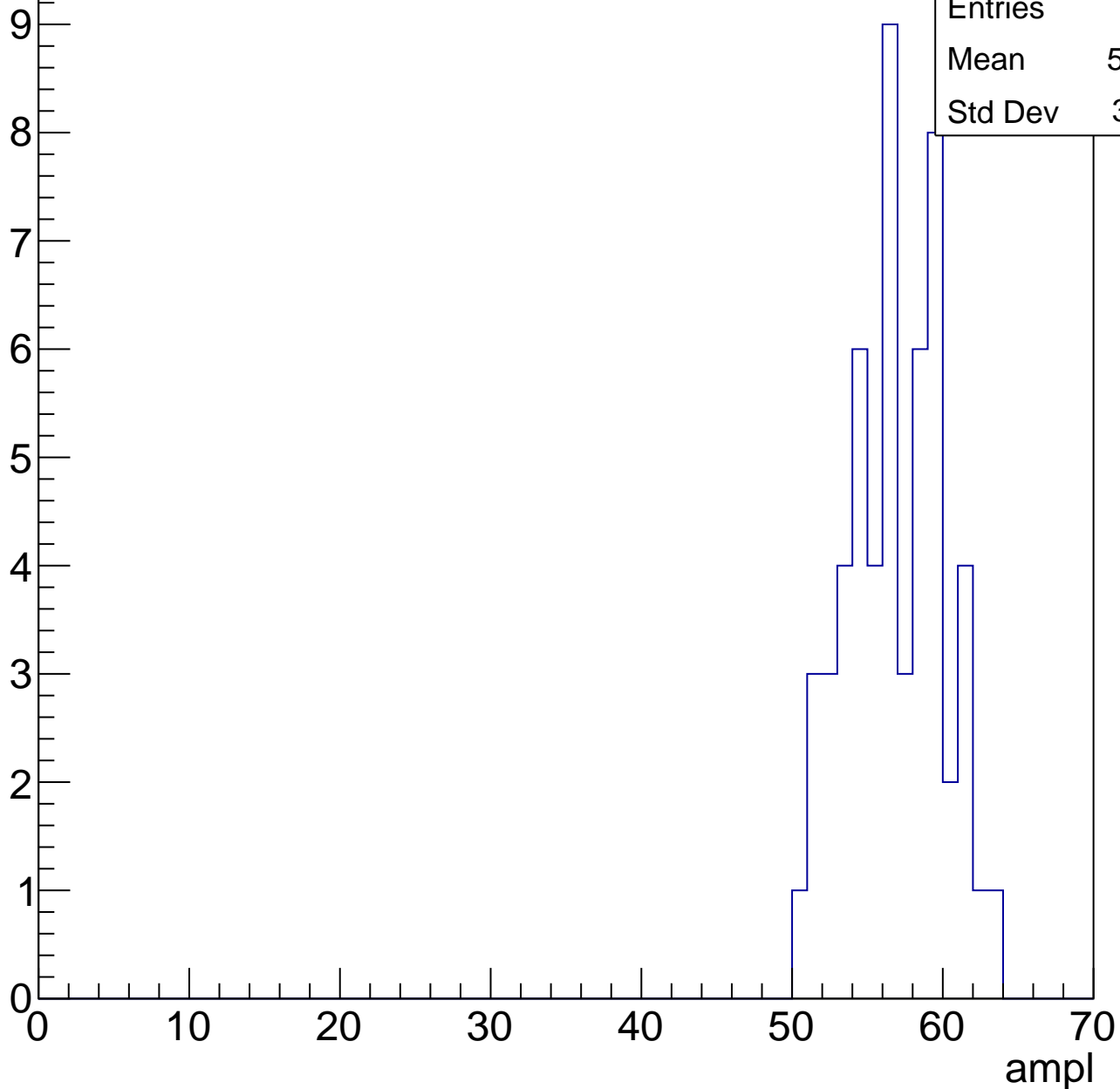
Entries	69
Mean	51.28
Std Dev	4.054



# B0L000S, U25-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



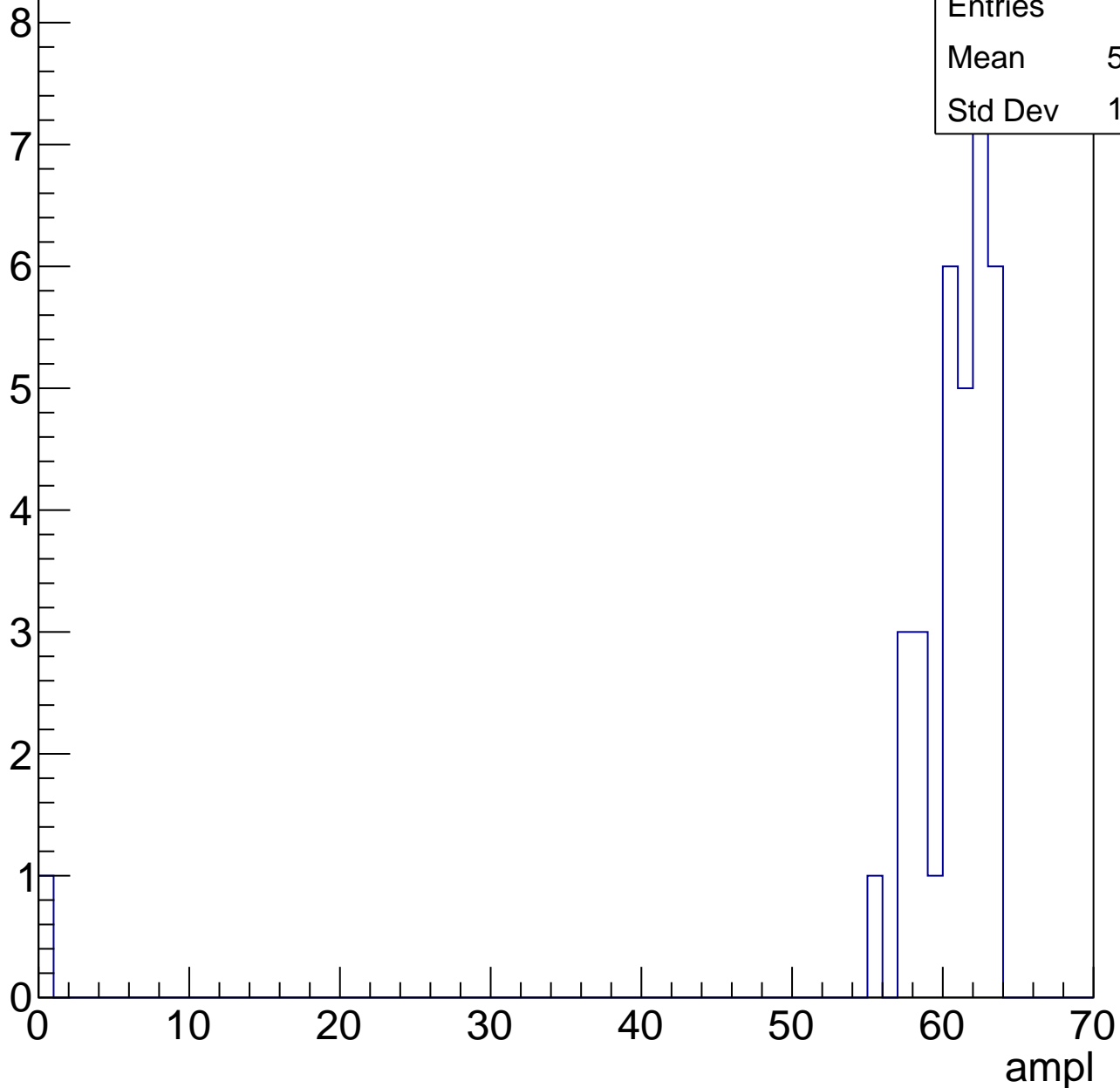
Entries	55
Mean	56.35
Std Dev	3.111

# B0L000S, U25-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

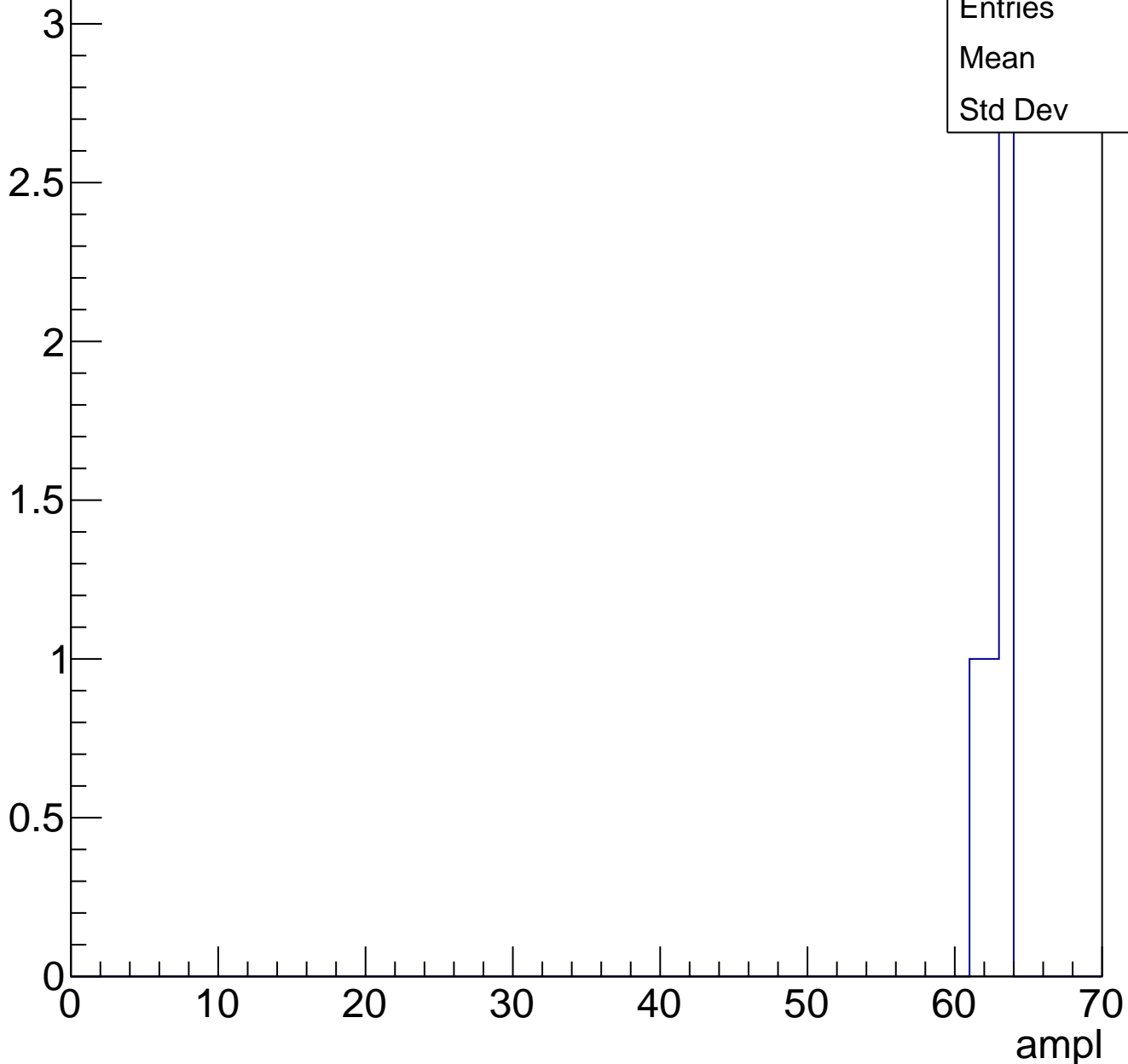
Entries	34
Mean	58.76
Std Dev	10.44



# B0L000S, U25-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	5
Mean	62.4
Std Dev	0.8



# B0L000S, U25-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch106, adc0

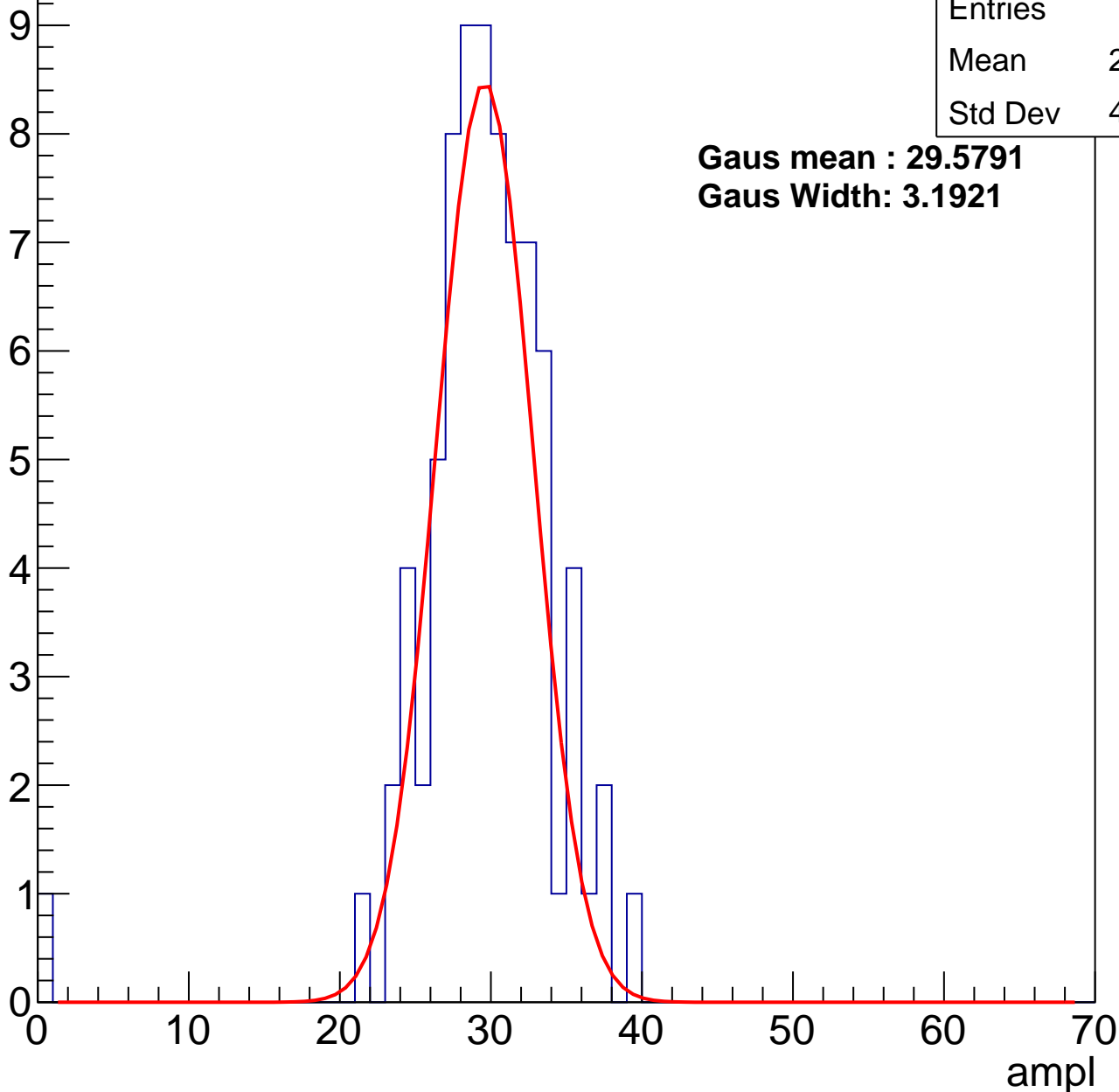
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	29.15
Std Dev	4.849

**Gaus mean : 29.5791**

**Gaus Width: 3.1921**



# B0L000S, U25-ch106, adc1

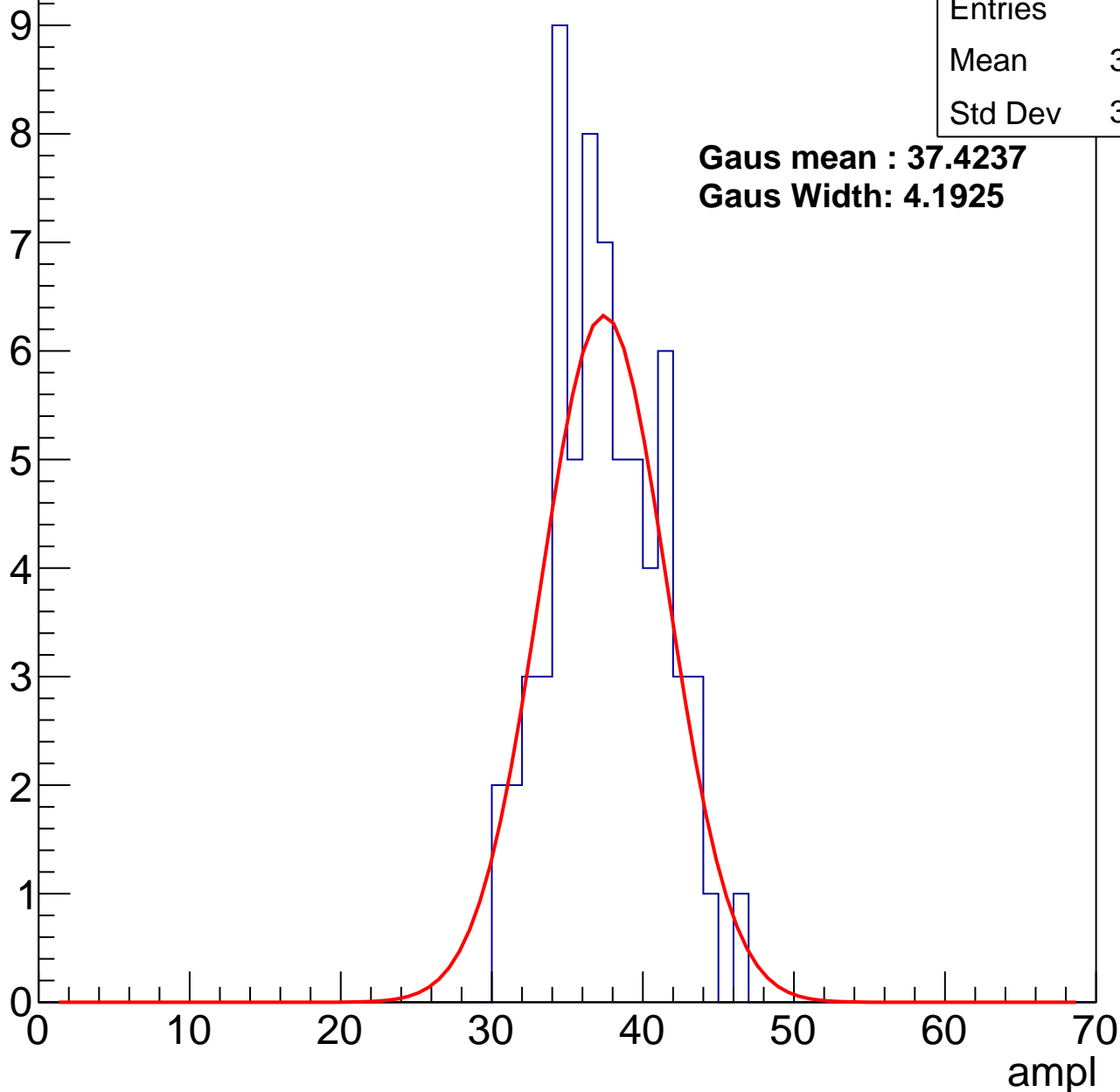
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.03
Std Dev	3.607

**Gaus mean : 37.4237**

**Gaus Width: 4.1925**



# B0L000S, U25-ch106, adc2

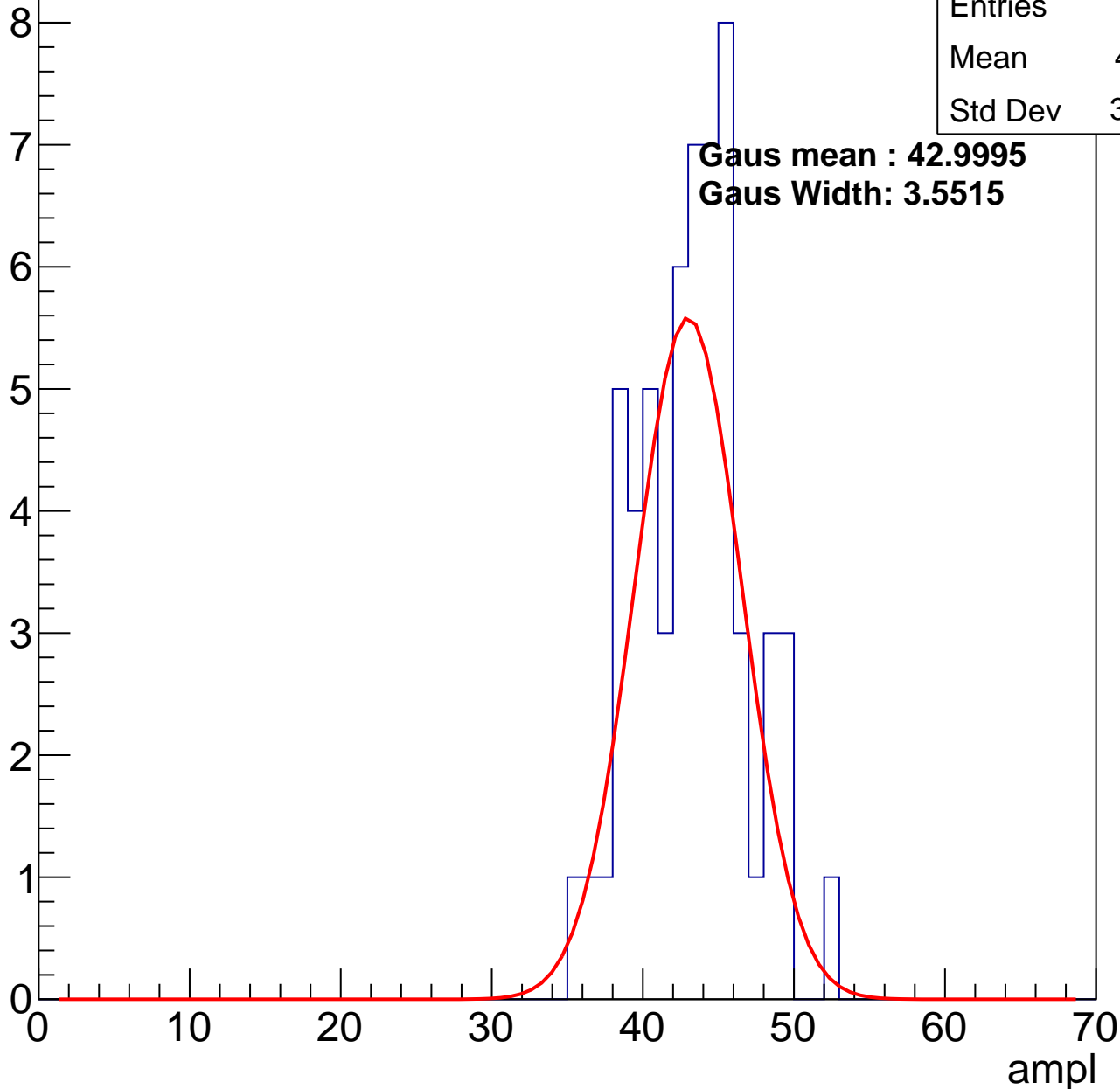
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	42.81
Std Dev	3.553

**Gaus mean : 42.9995**

**Gaus Width: 3.5515**



# B0L000S, U25-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	49.23
Std Dev	3.461

Entry

10

8

6

4

2

0

0

10

20

30

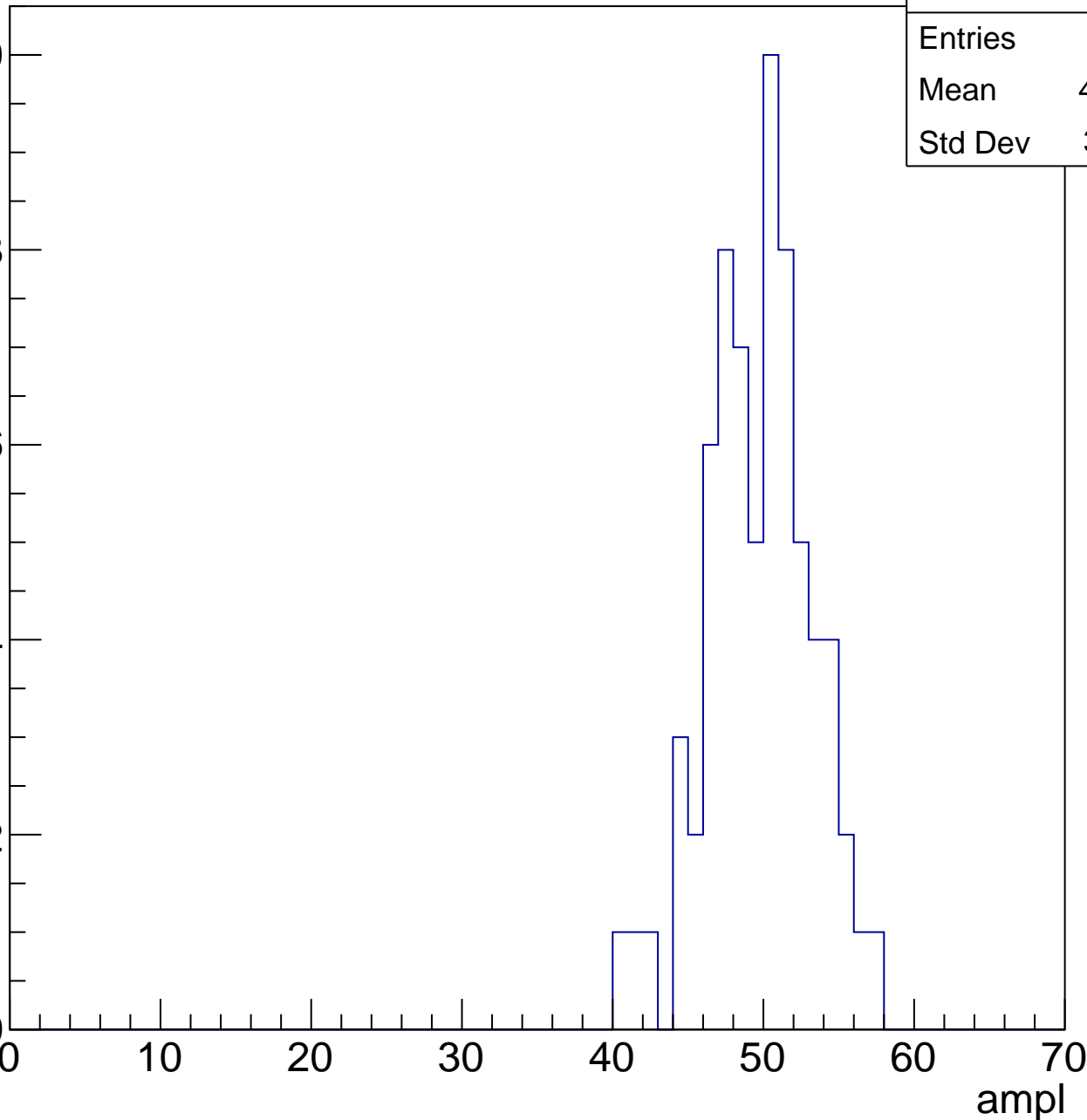
40

50

60

70

ampl

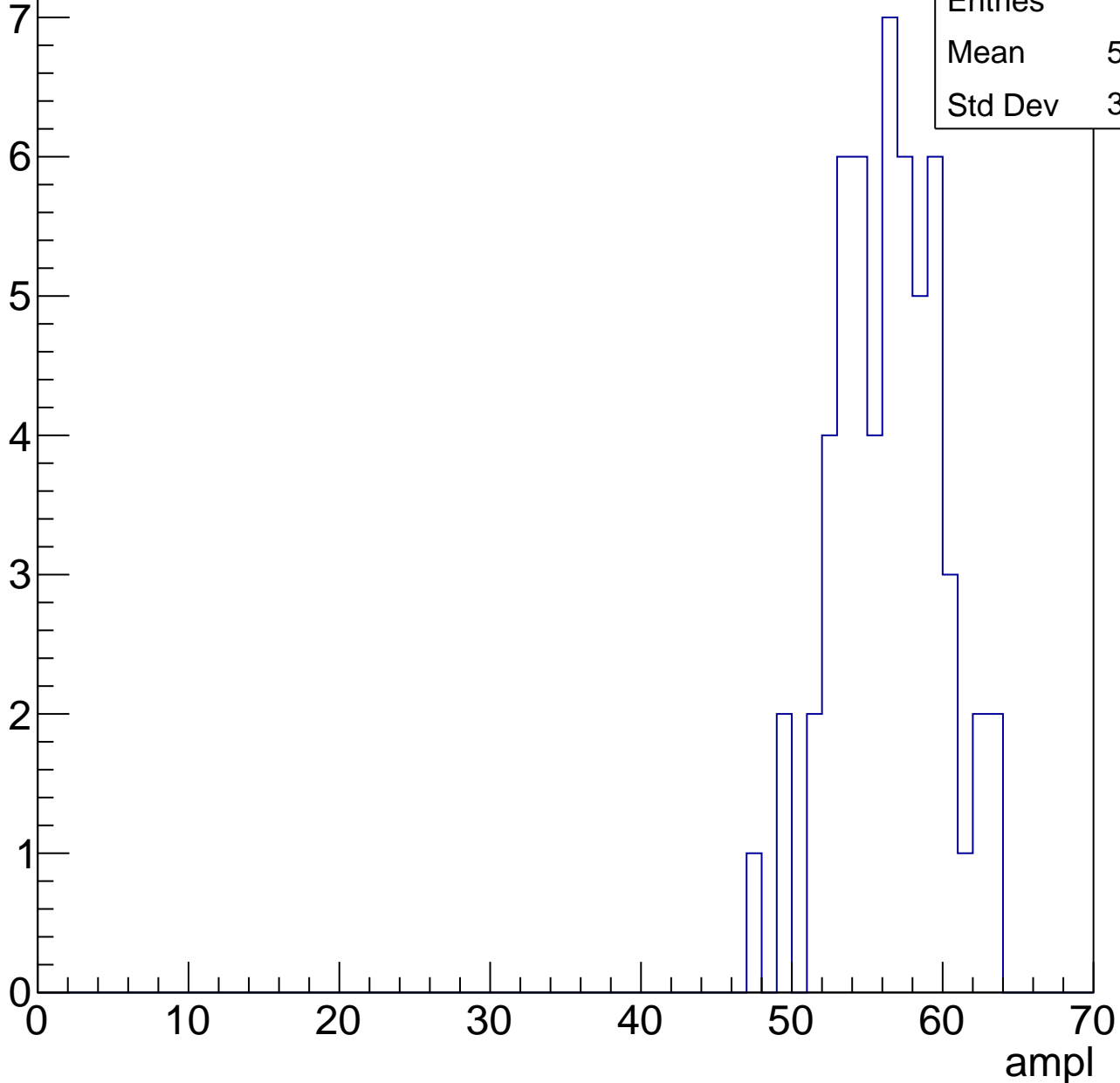


# B0L000S, U25-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	55.89
Std Dev	3.498

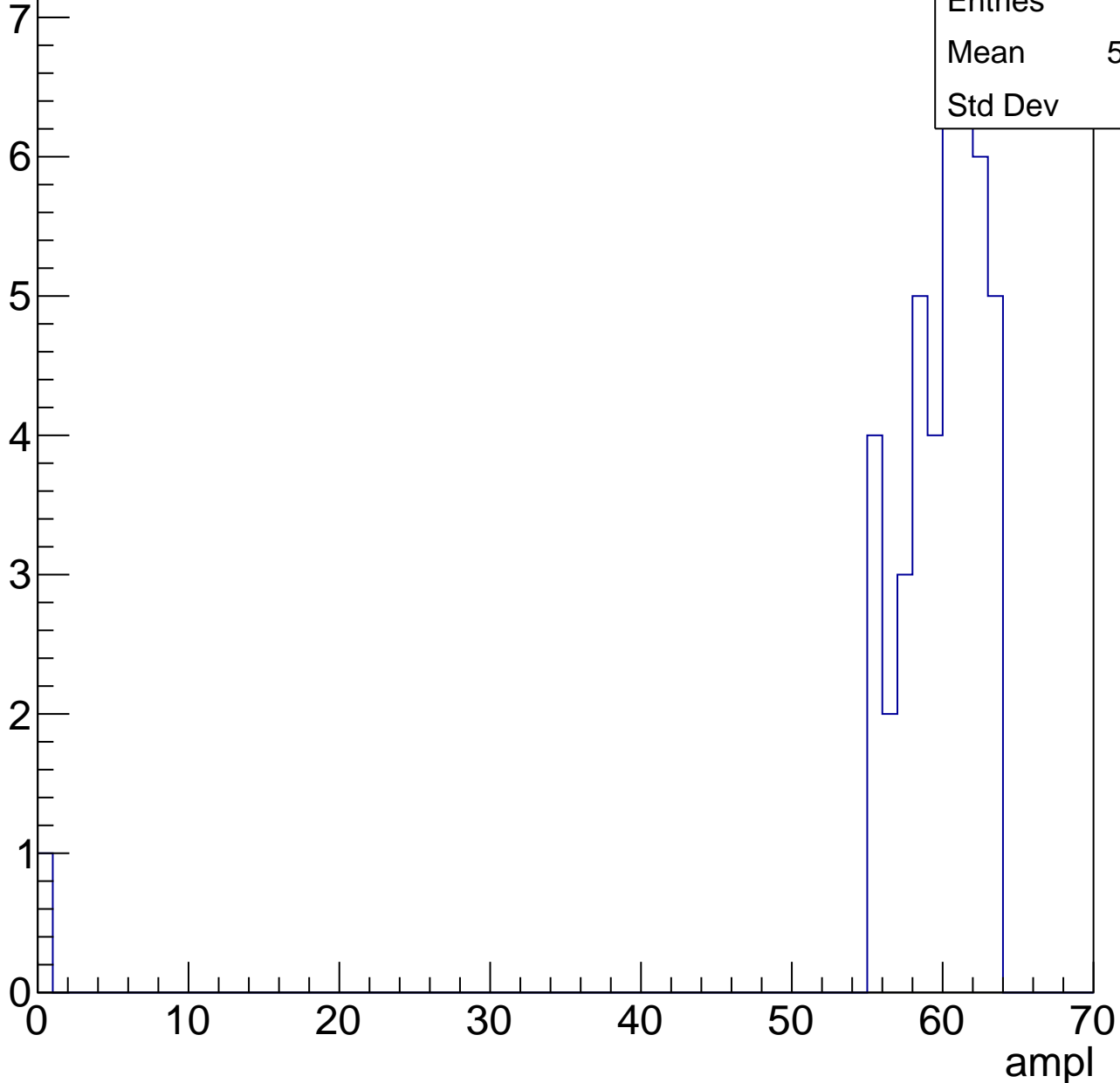


# B0L000S, U25-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.25
Std Dev	9.2



# B0L000S, U25-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	68
Mean	28.54
Std Dev	3.747

**Gaus mean : 28.7835**

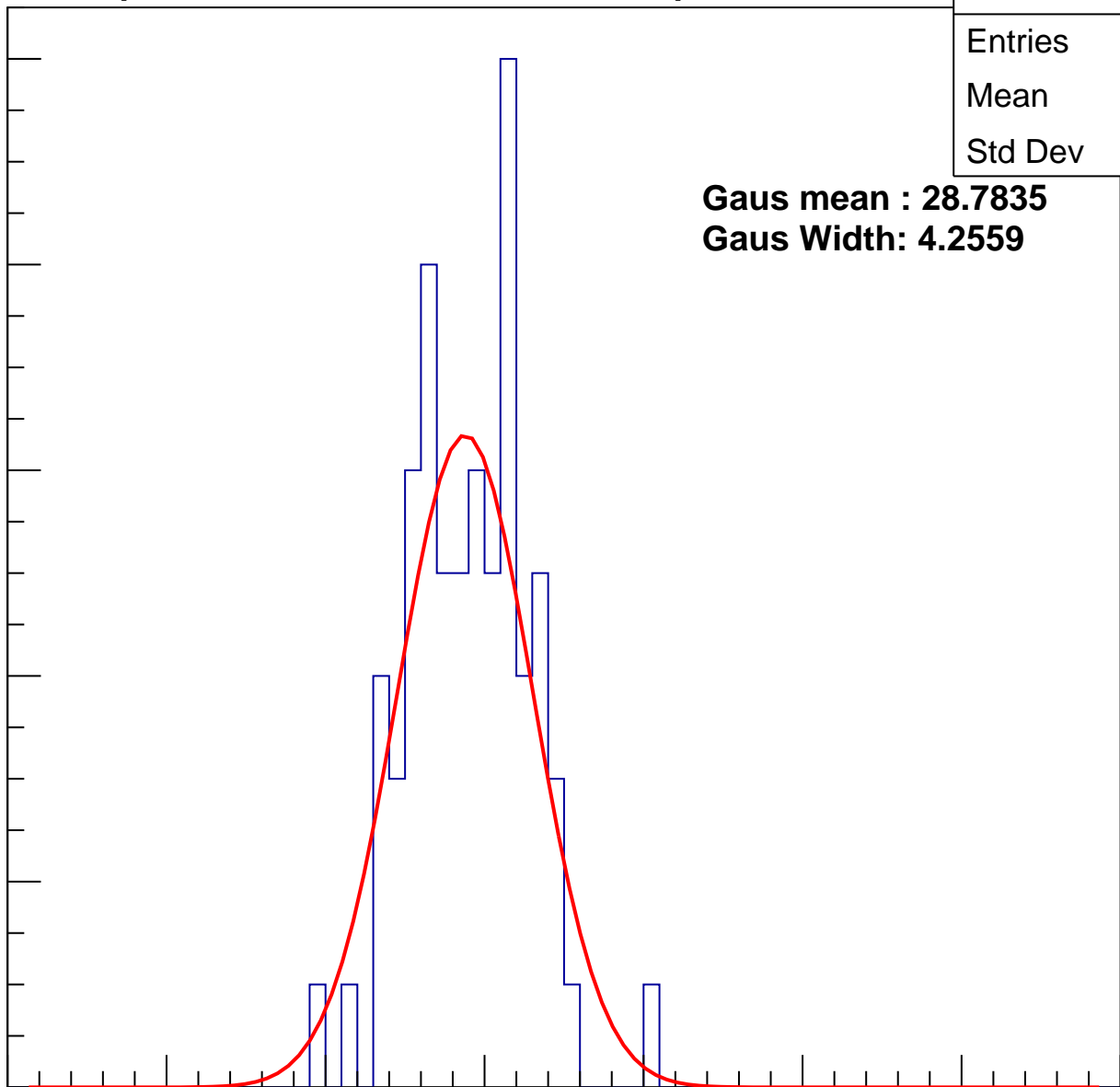
**Gaus Width: 4.2559**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch107, adc1

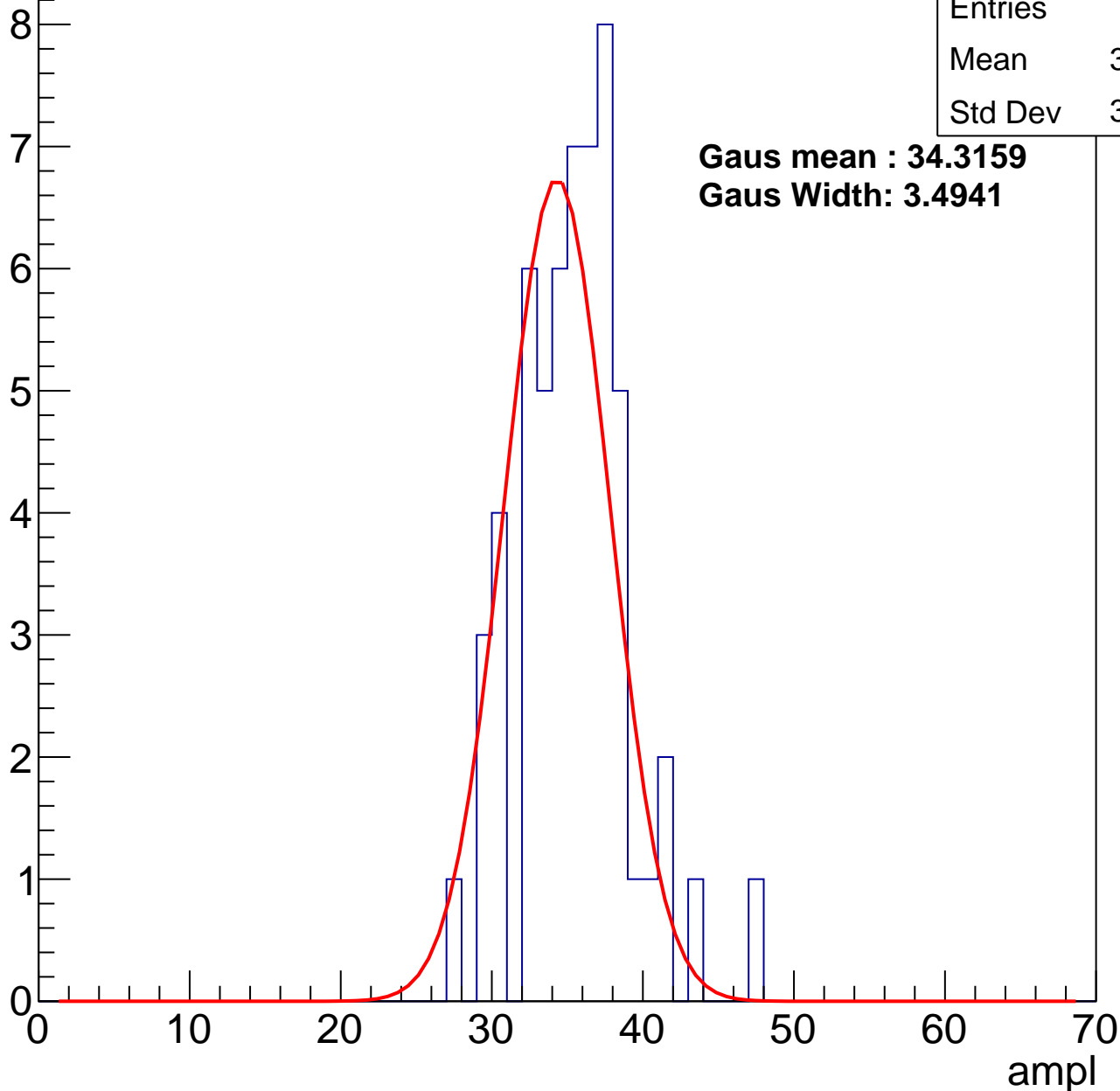
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	34.98
Std Dev	3.613

**Gaus mean : 34.3159**

**Gaus Width: 3.4941**



# B0L000S, U25-ch107, adc2

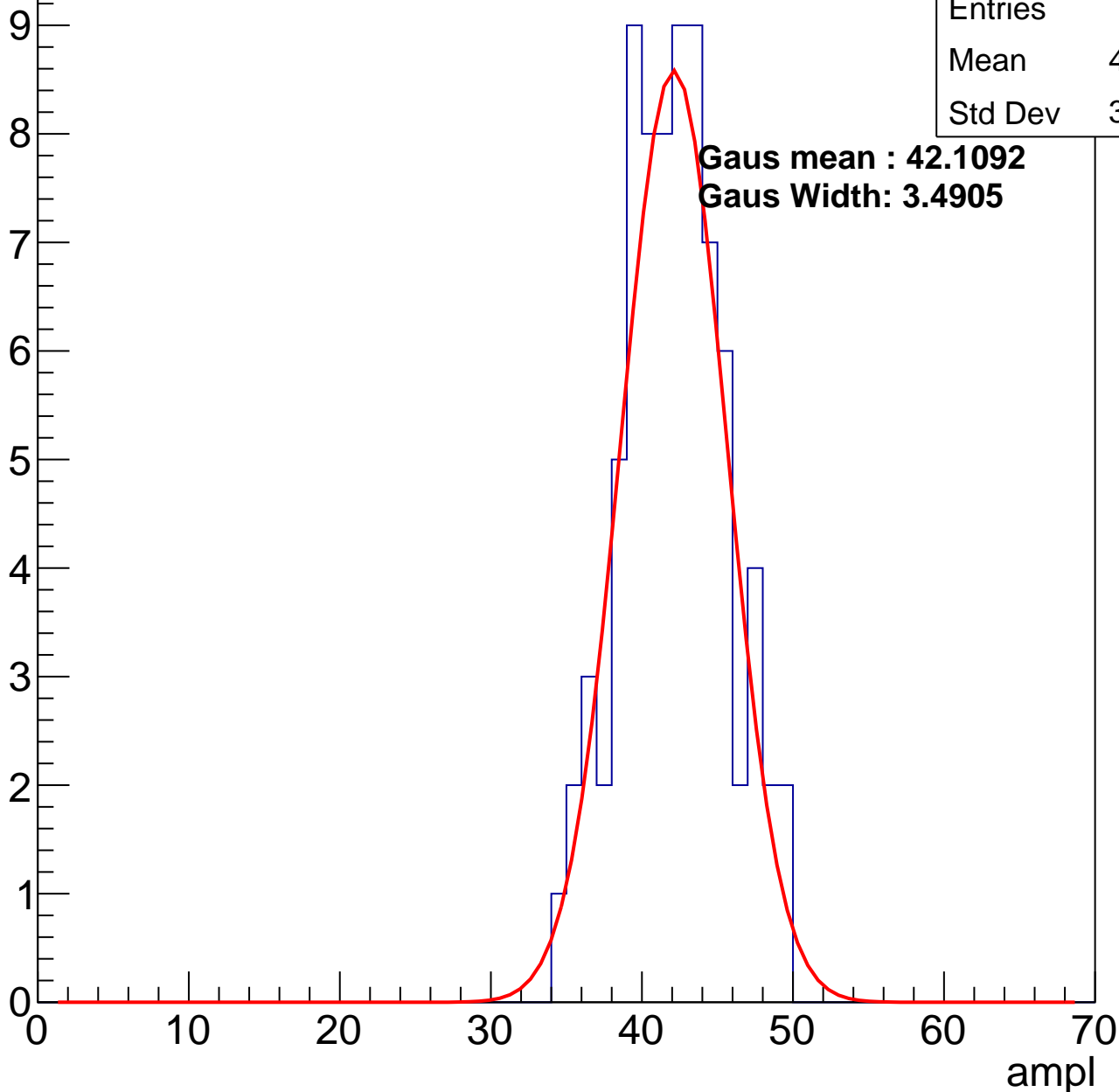
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	79
Mean	41.67
Std Dev	3.385

**Gaus mean : 42.1092**

**Gaus Width: 3.4905**

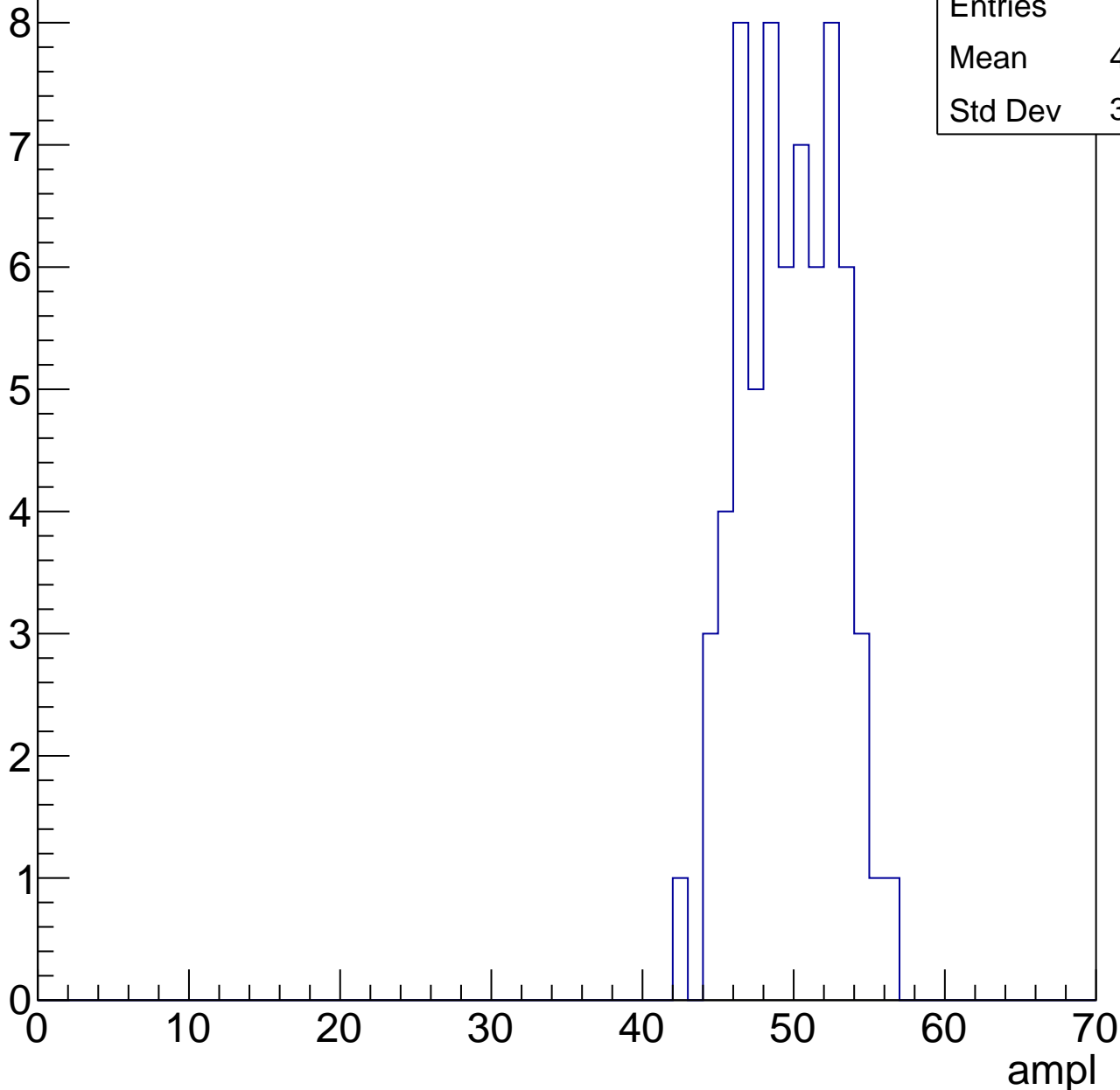


# B0L000S, U25-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	49.22
Std Dev	3.099

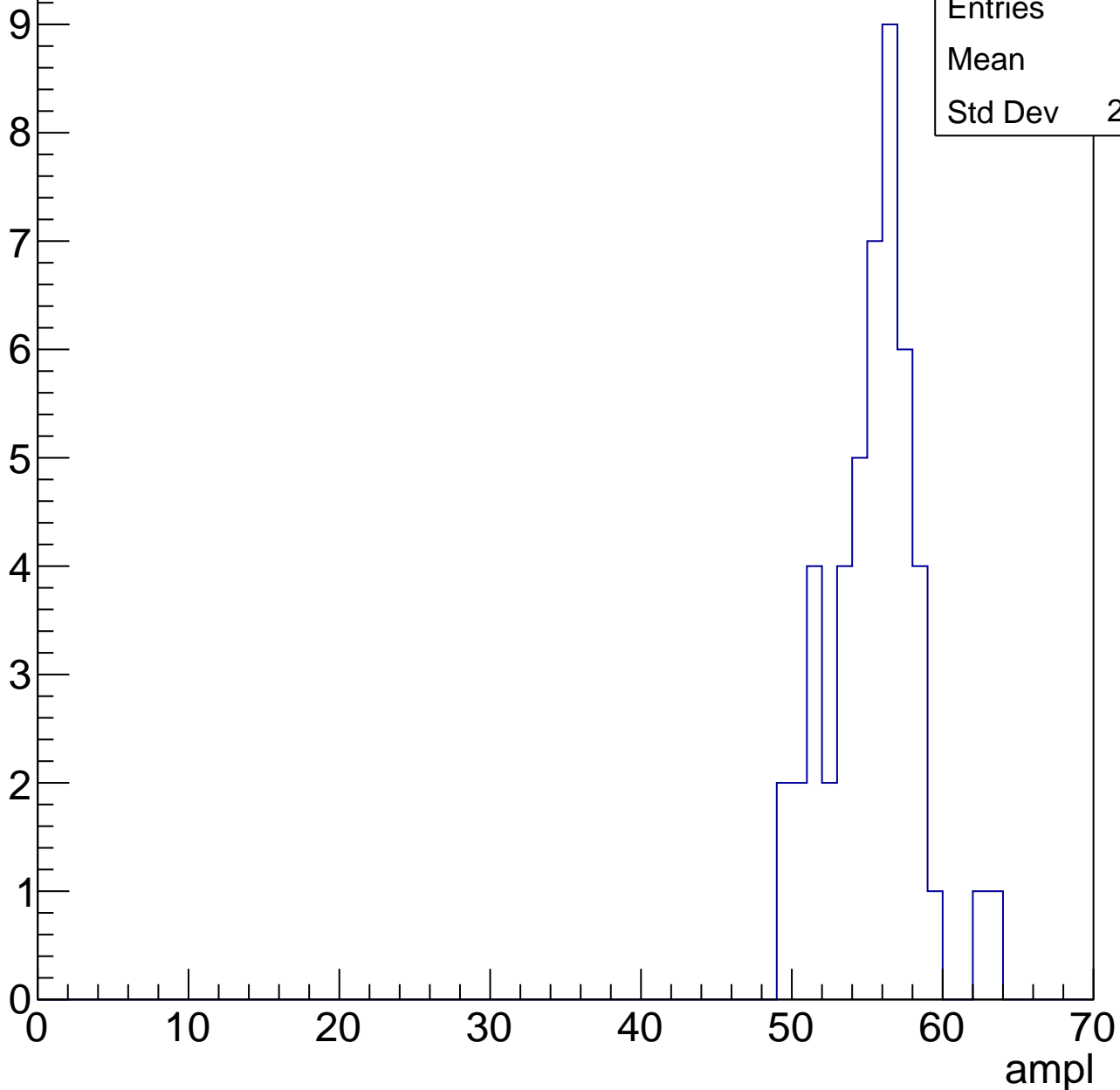


# B0L000S, U25-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	48
Mean	54.9
Std Dev	2.967

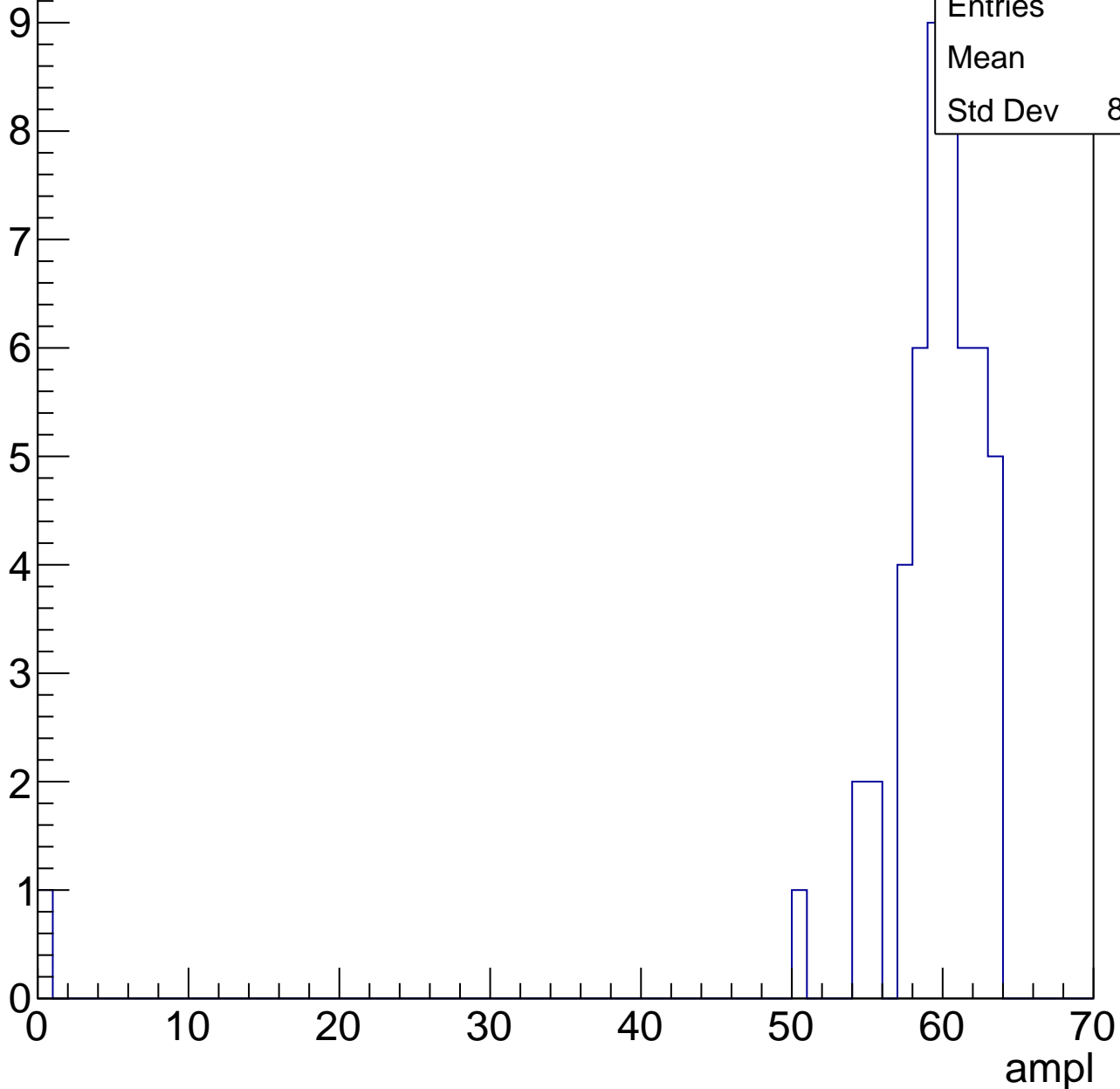


# B0L000S, U25-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

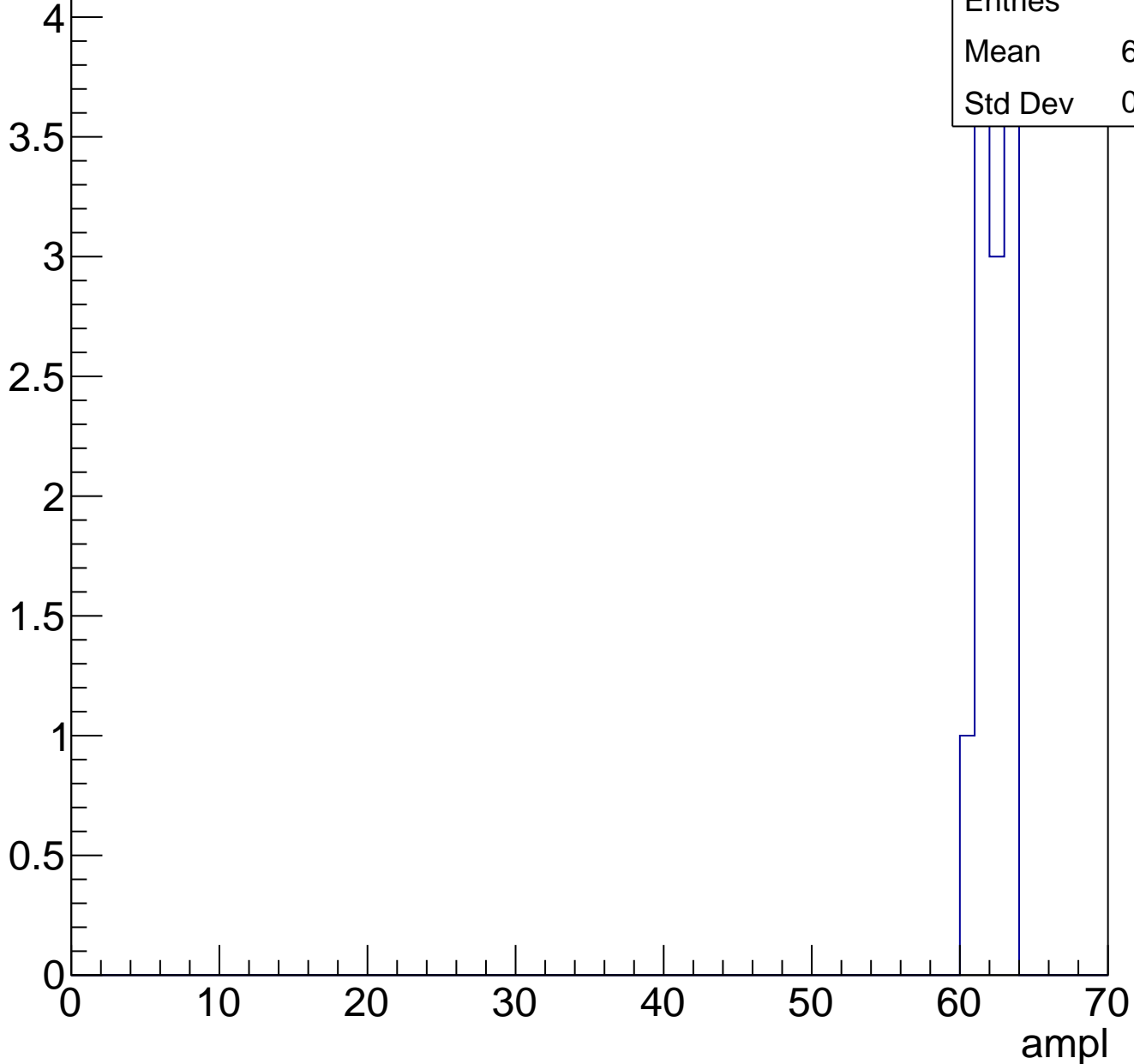
Entries	51
Mean	58.2
Std Dev	8.632



# B0L000S, U25-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch108, adc0

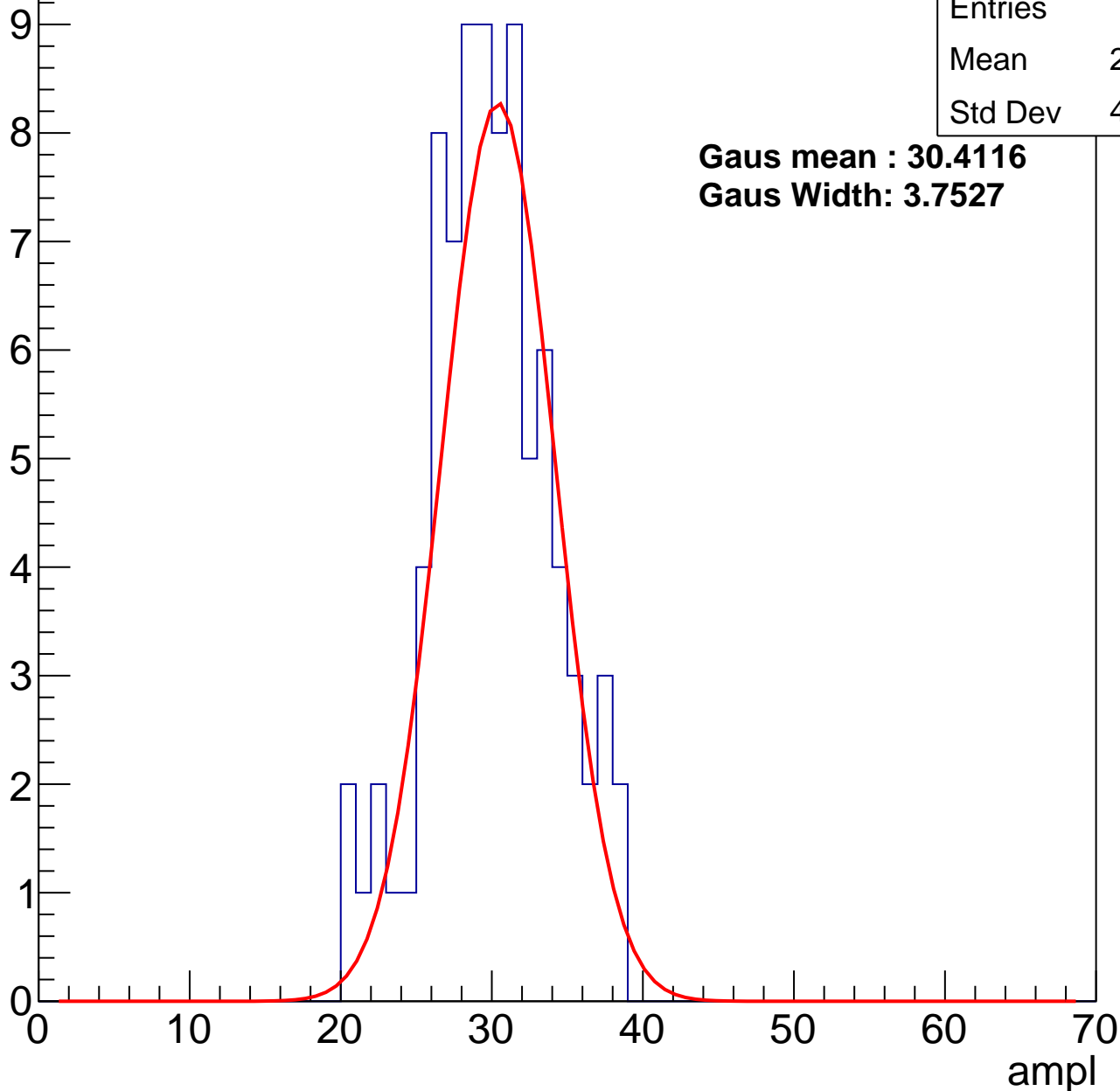
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	86
Mean	29.52
Std Dev	4.005

**Gaus mean : 30.4116**

**Gaus Width: 3.7527**



# B0L000S, U25-ch108, adc1

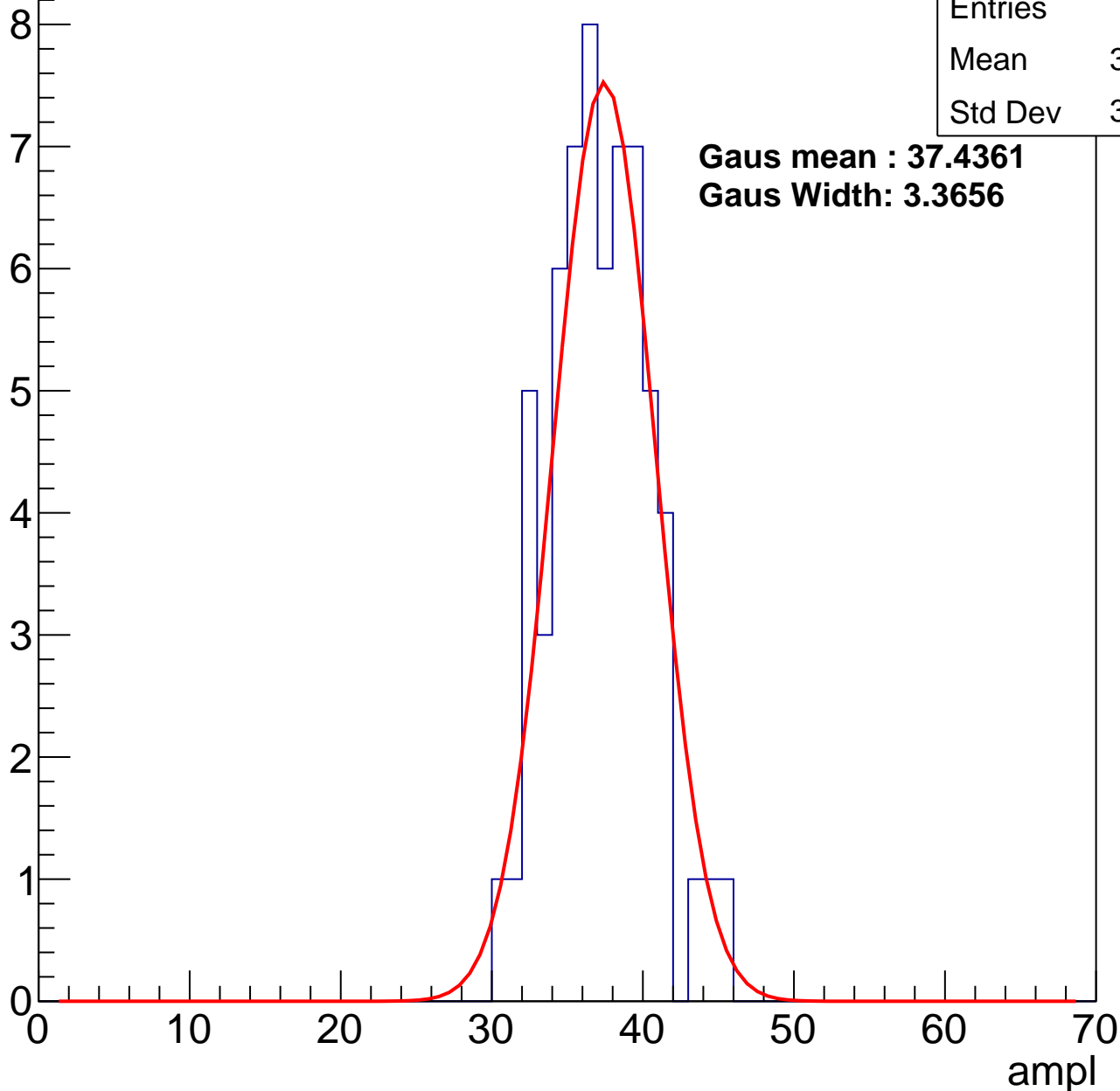
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.73
Std Dev	3.178

**Gaus mean : 37.4361**

**Gaus Width: 3.3656**



# B0L000S, U25-ch108, adc2

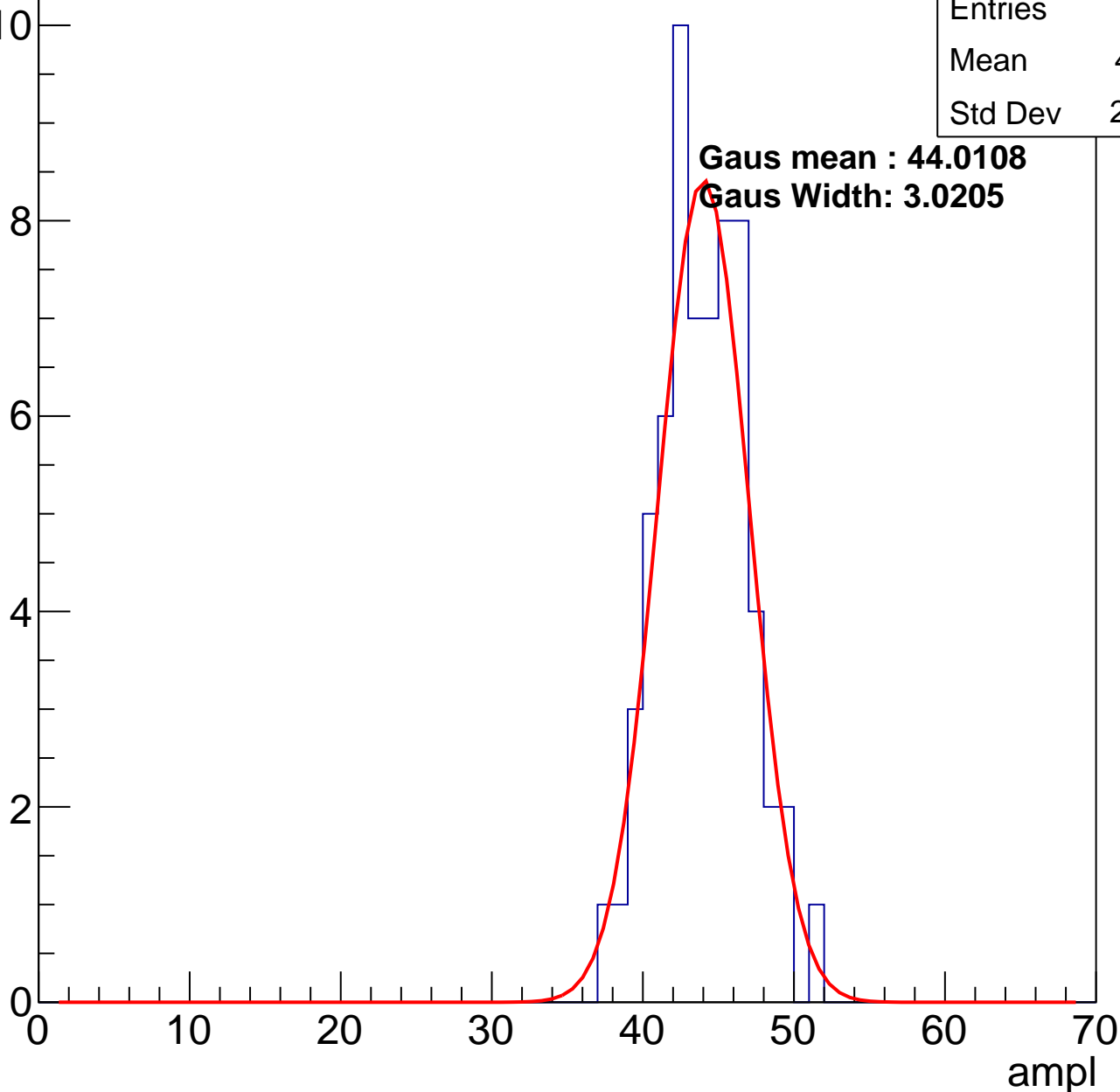
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.51
Std Dev	2.862

**Gaus mean : 44.0108**

**Gaus Width: 3.0205**



# B0L000S, U25-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

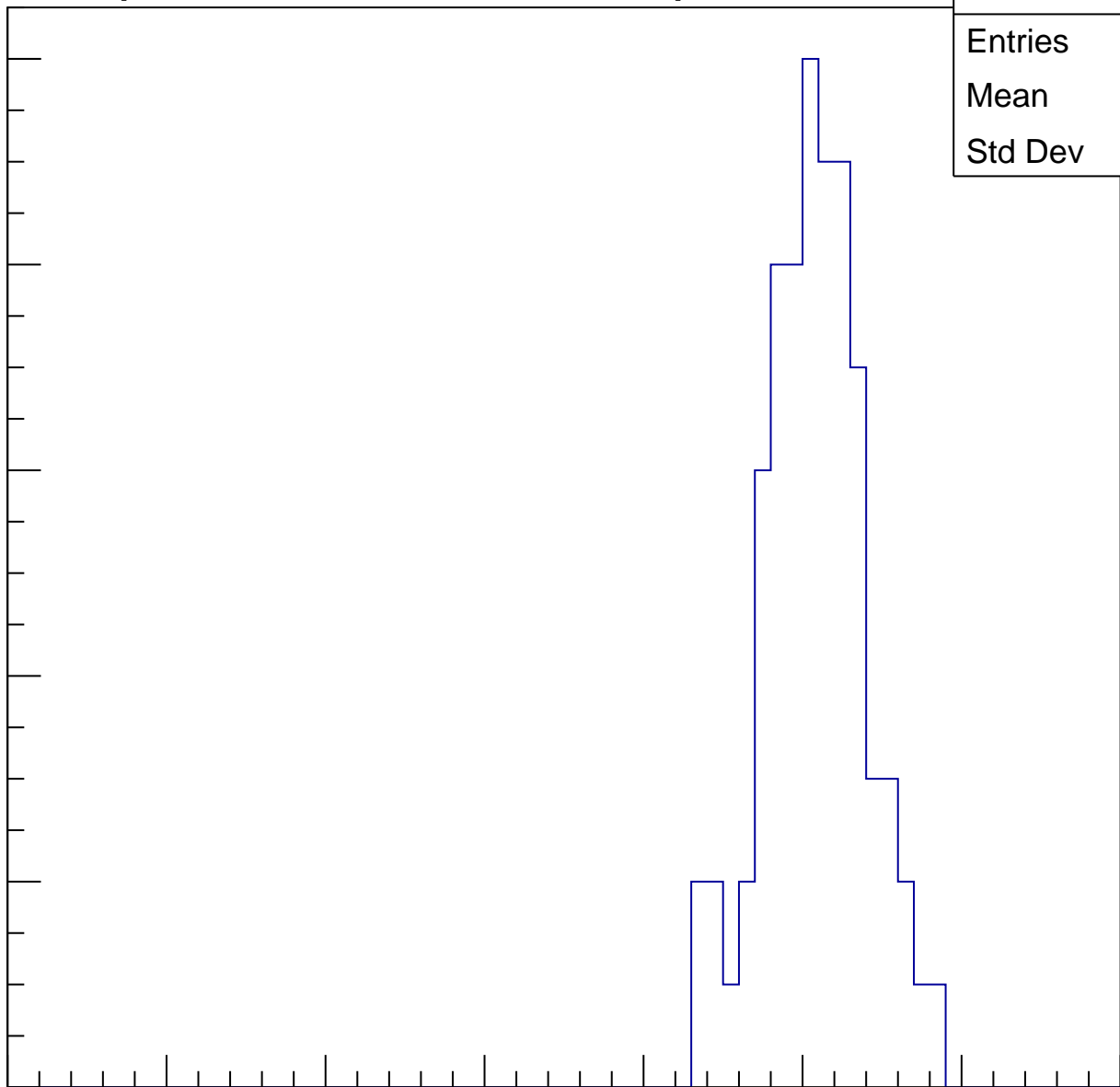
Entries	74
Mean	50.28
Std Dev	3.143

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

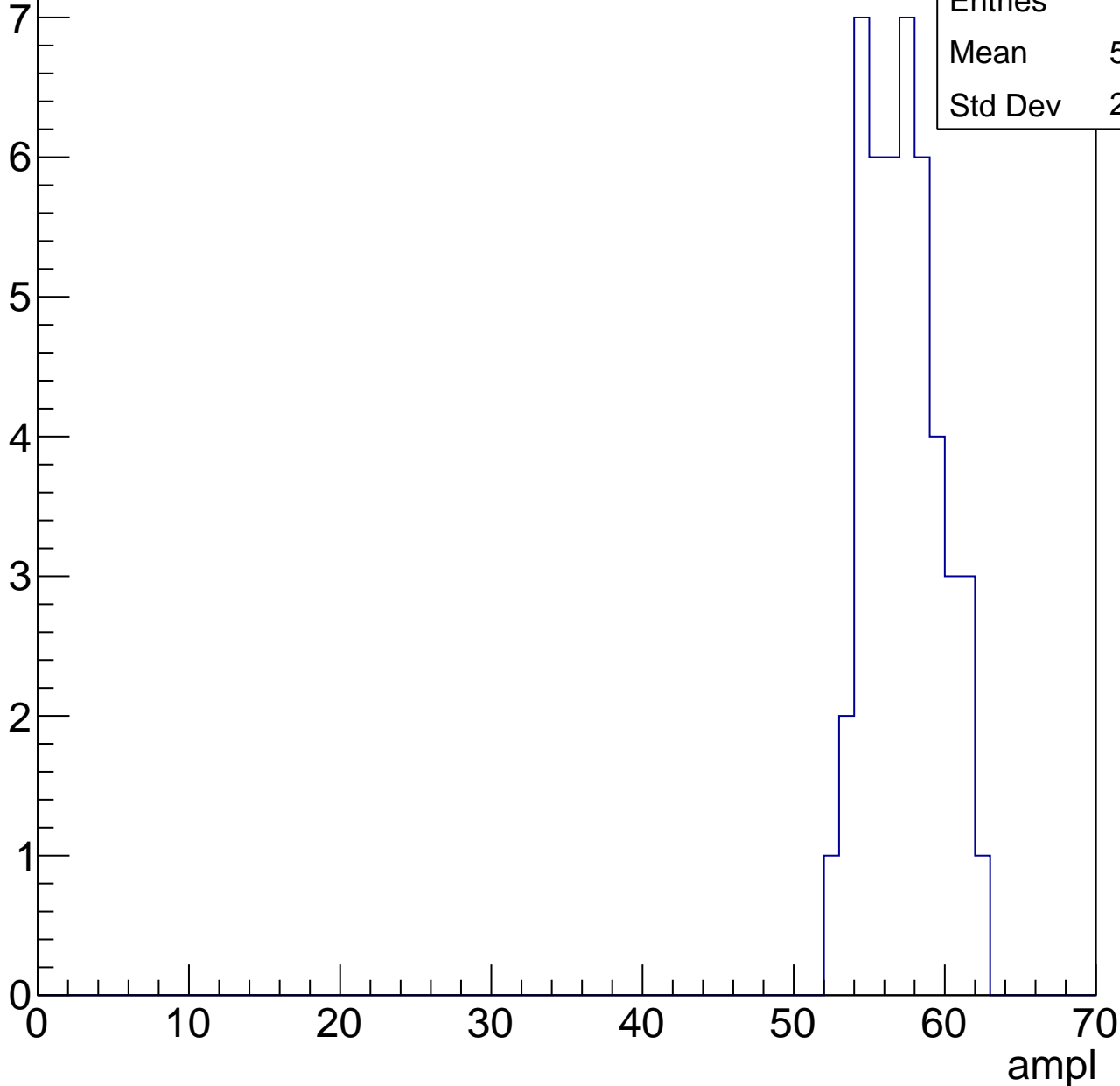


# B0L000S, U25-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	56.74
Std Dev	2.418

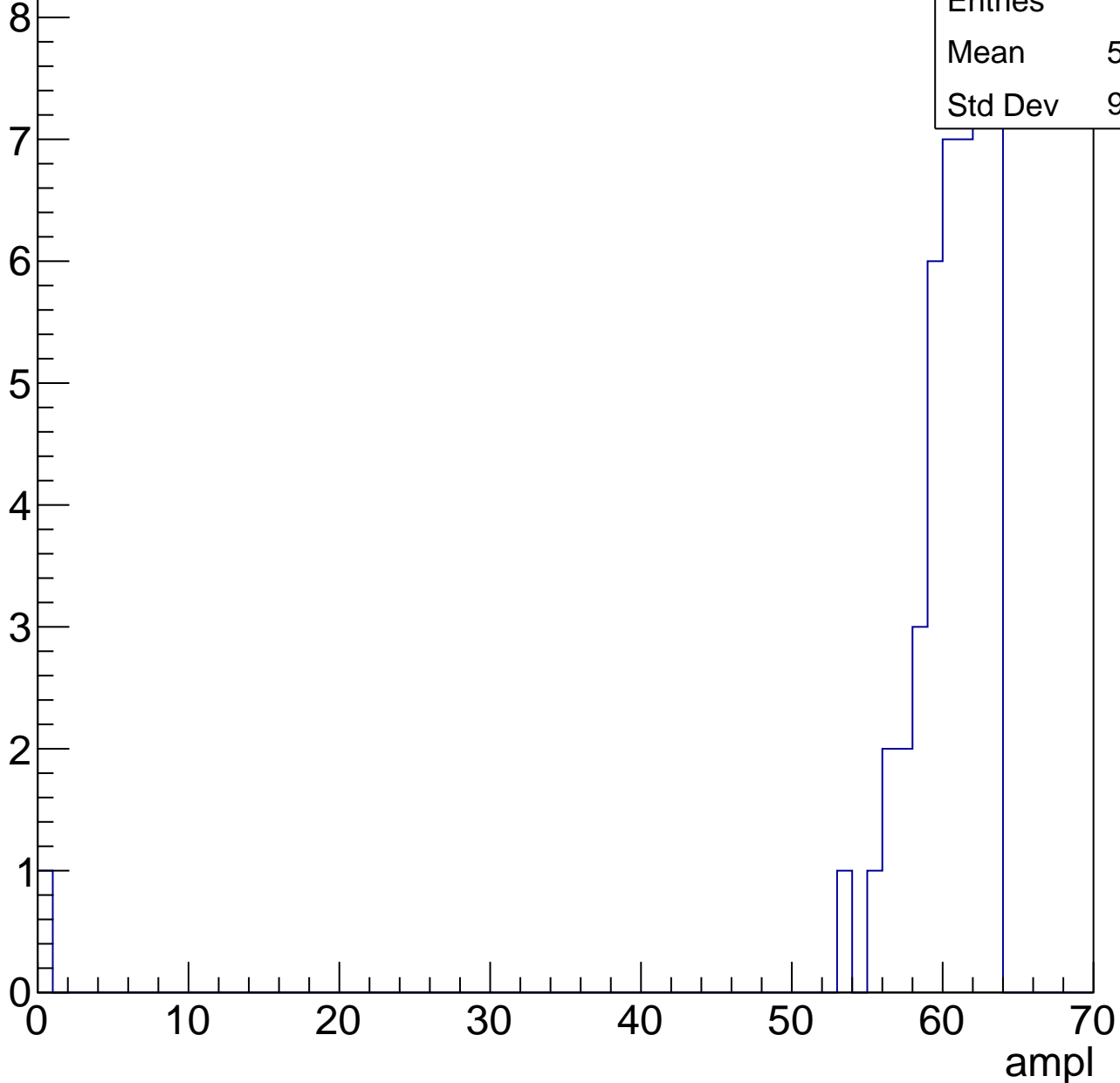


# B0L000S, U25-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	58.89
Std Dev	9.085



# B0L000S, U25-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	8.5
Std Dev	8.5

# B0L000S, U25-ch109, adc0

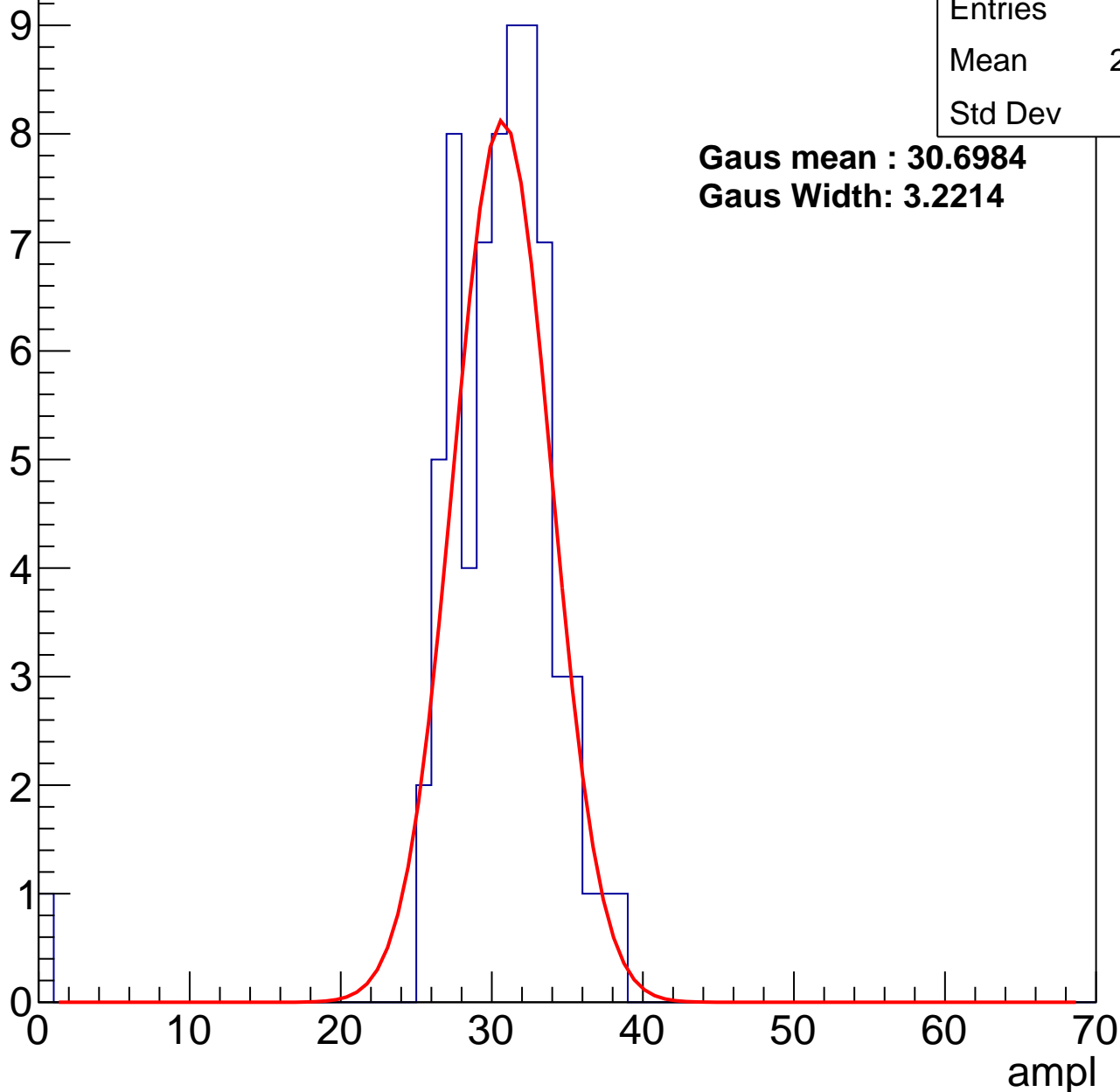
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	29.96
Std Dev	4.67

**Gaus mean : 30.6984**

**Gaus Width: 3.2214**



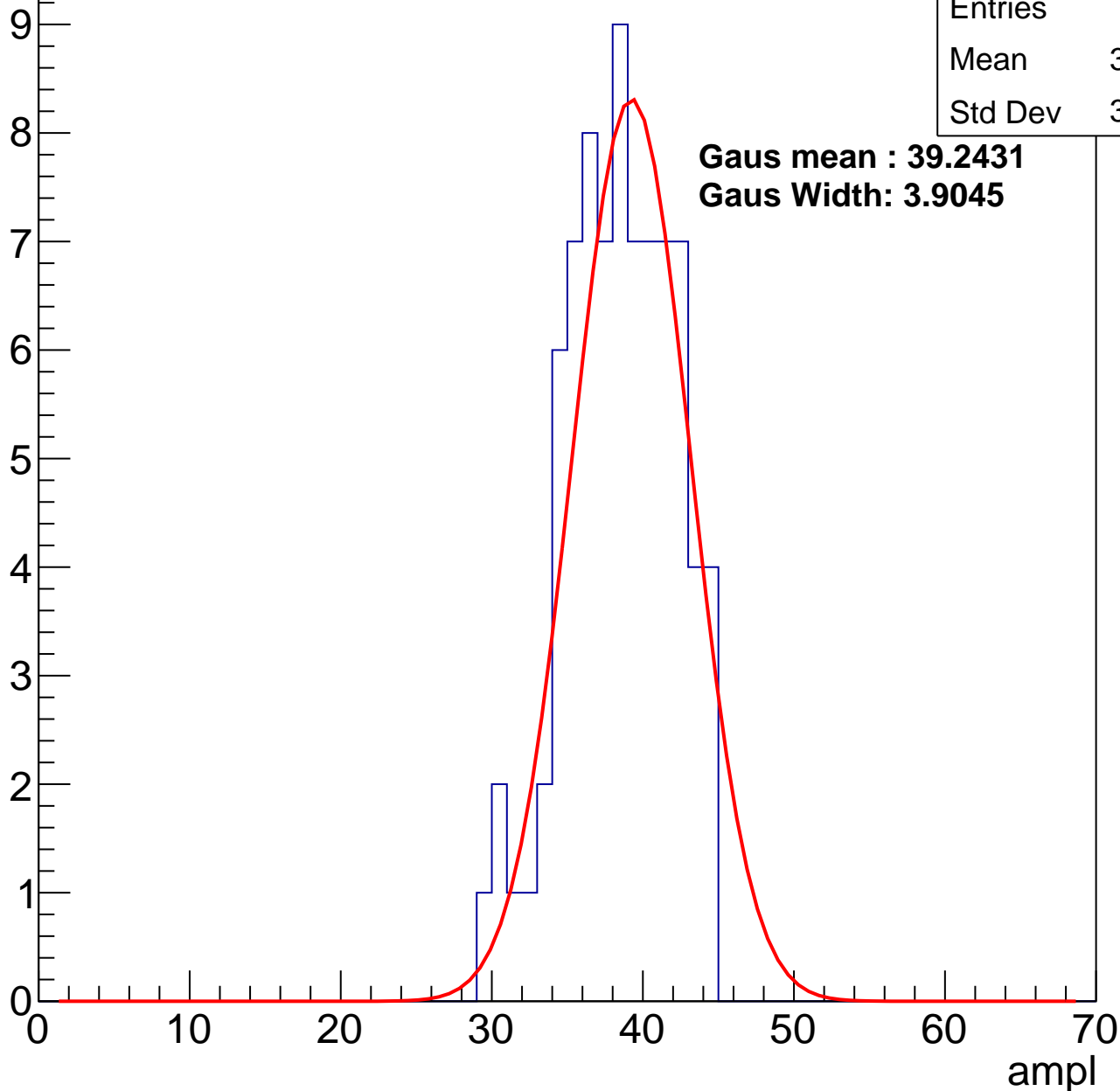
# B0L000S, U25-ch109, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	80
Mean	37.98
Std Dev	3.528

**Gaus mean : 39.2431**  
**Gaus Width: 3.9045**



# B0L000S, U25-ch109, adc2

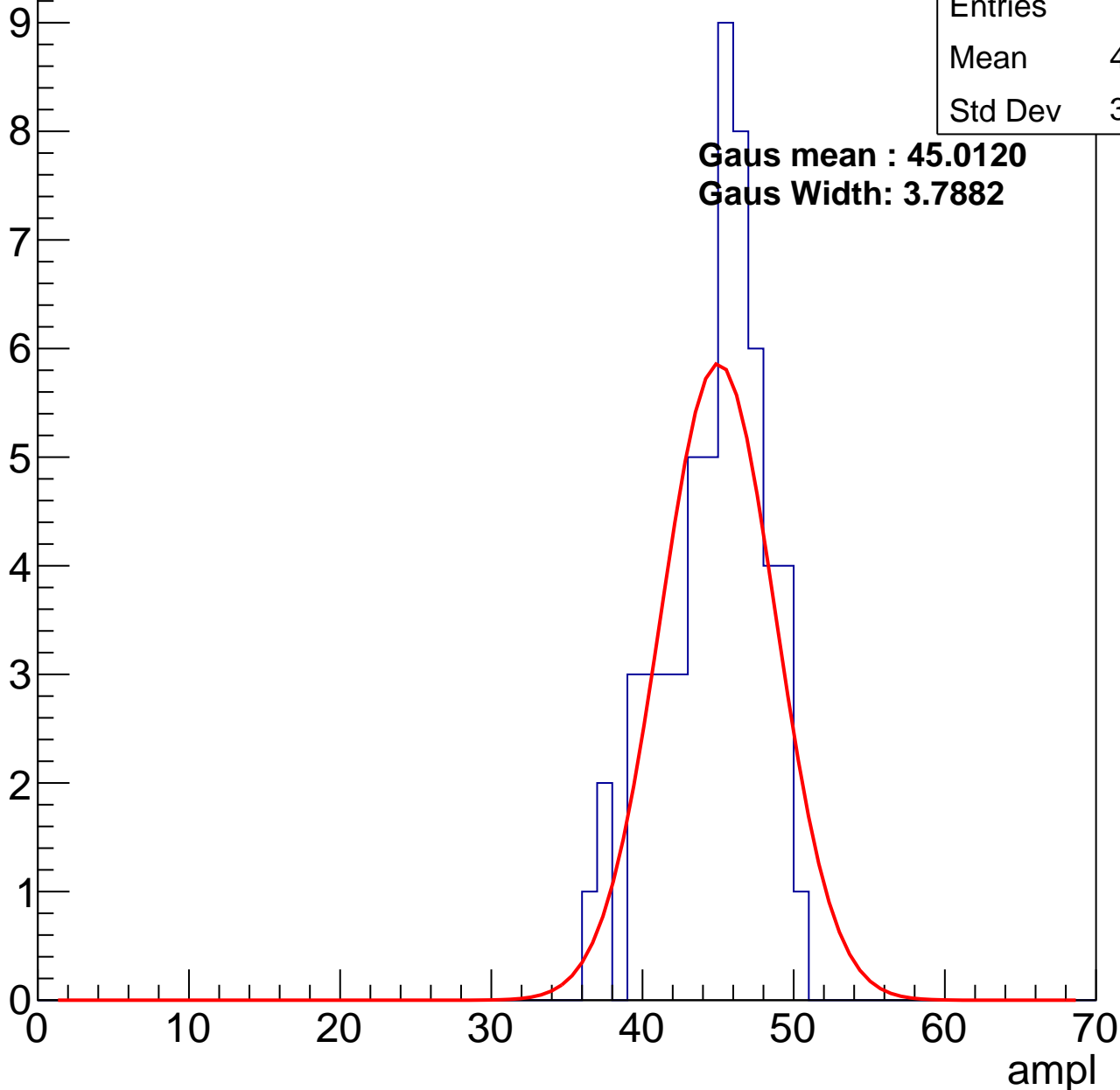
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	44.28
Std Dev	3.297

**Gaus mean : 45.0120**

**Gaus Width: 3.7882**

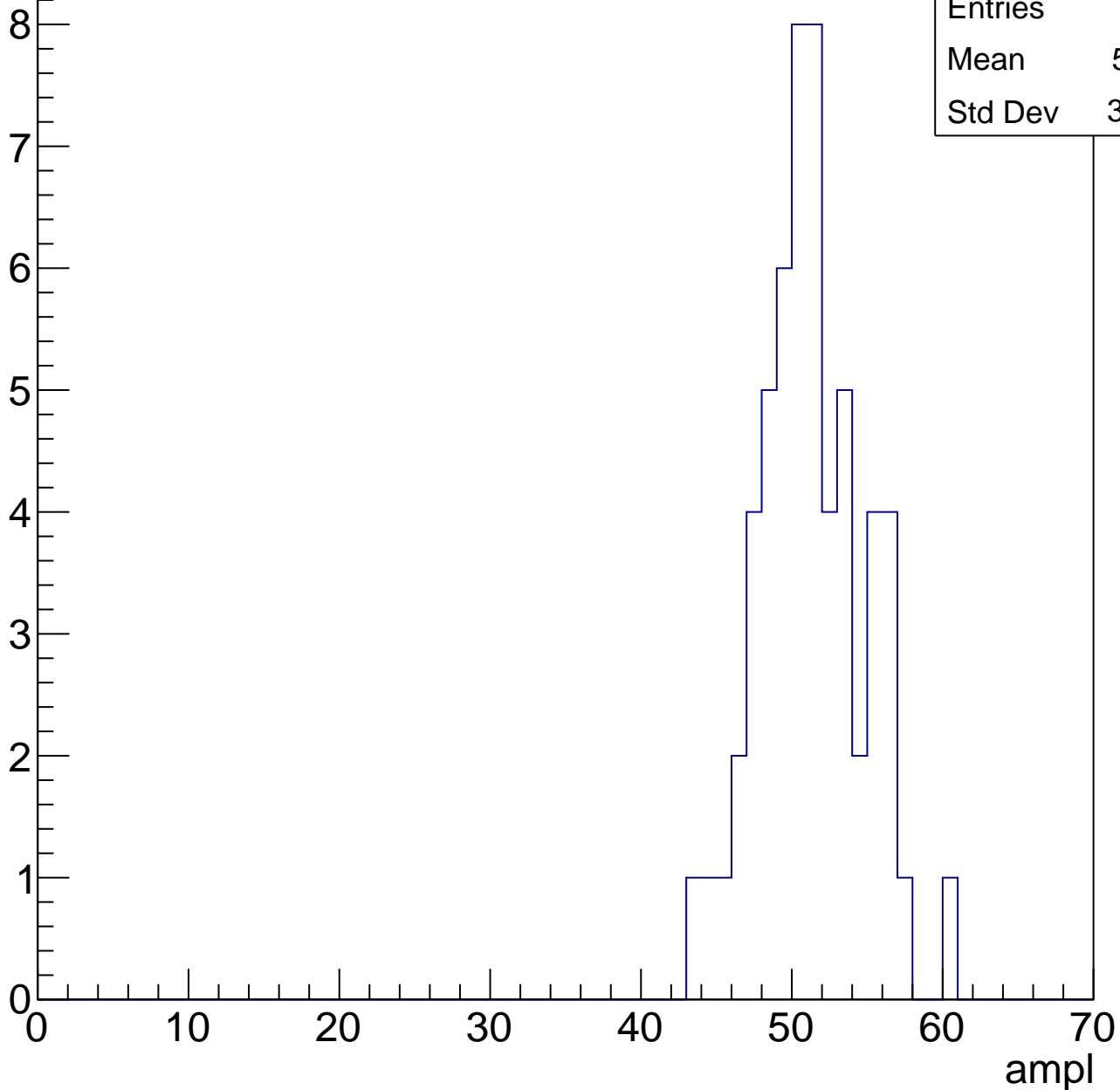


# B0L000S, U25-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	50.81
Std Dev	3.405

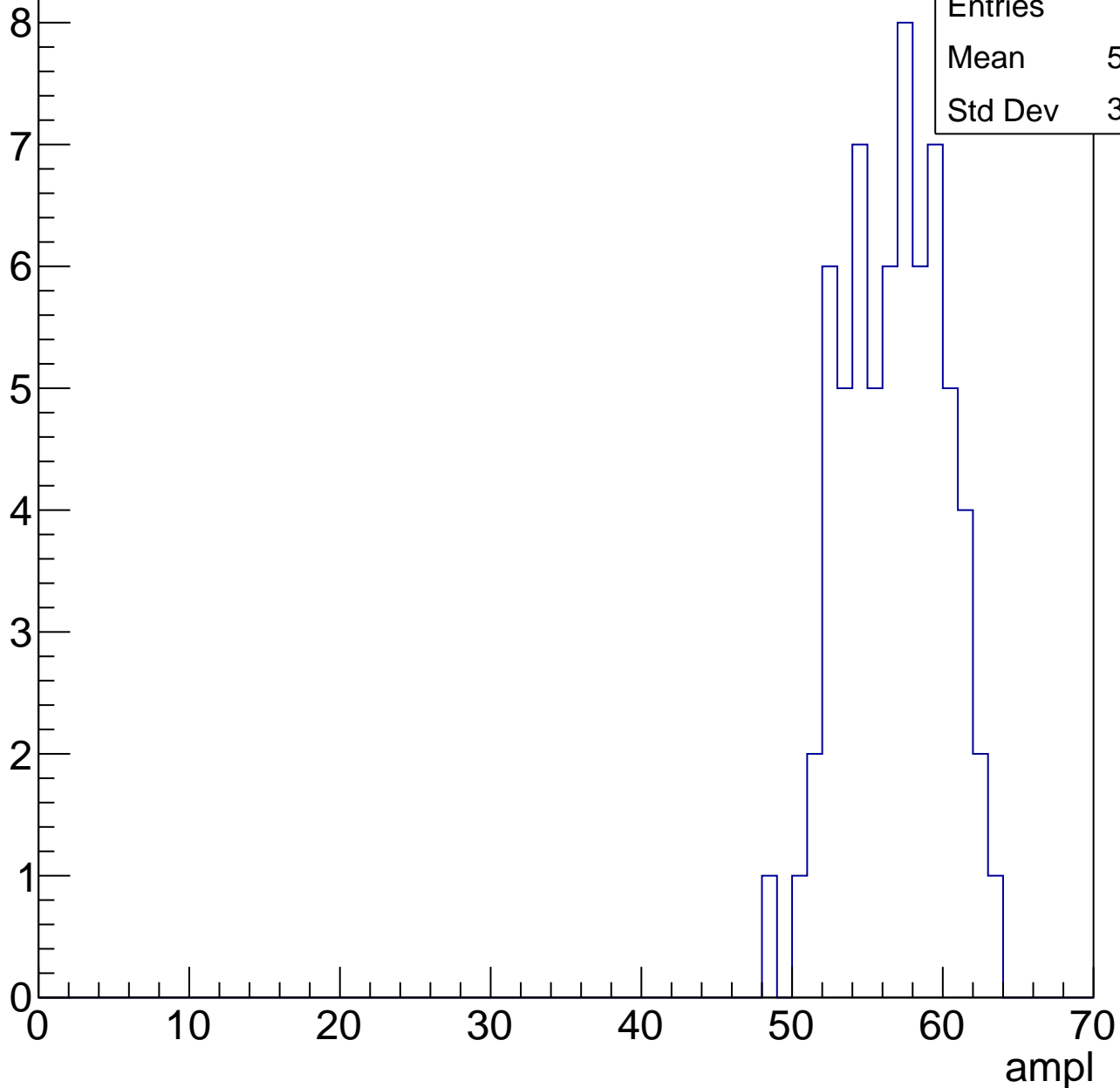


# B0L000S, U25-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	66
Mean	56.27
Std Dev	3.292

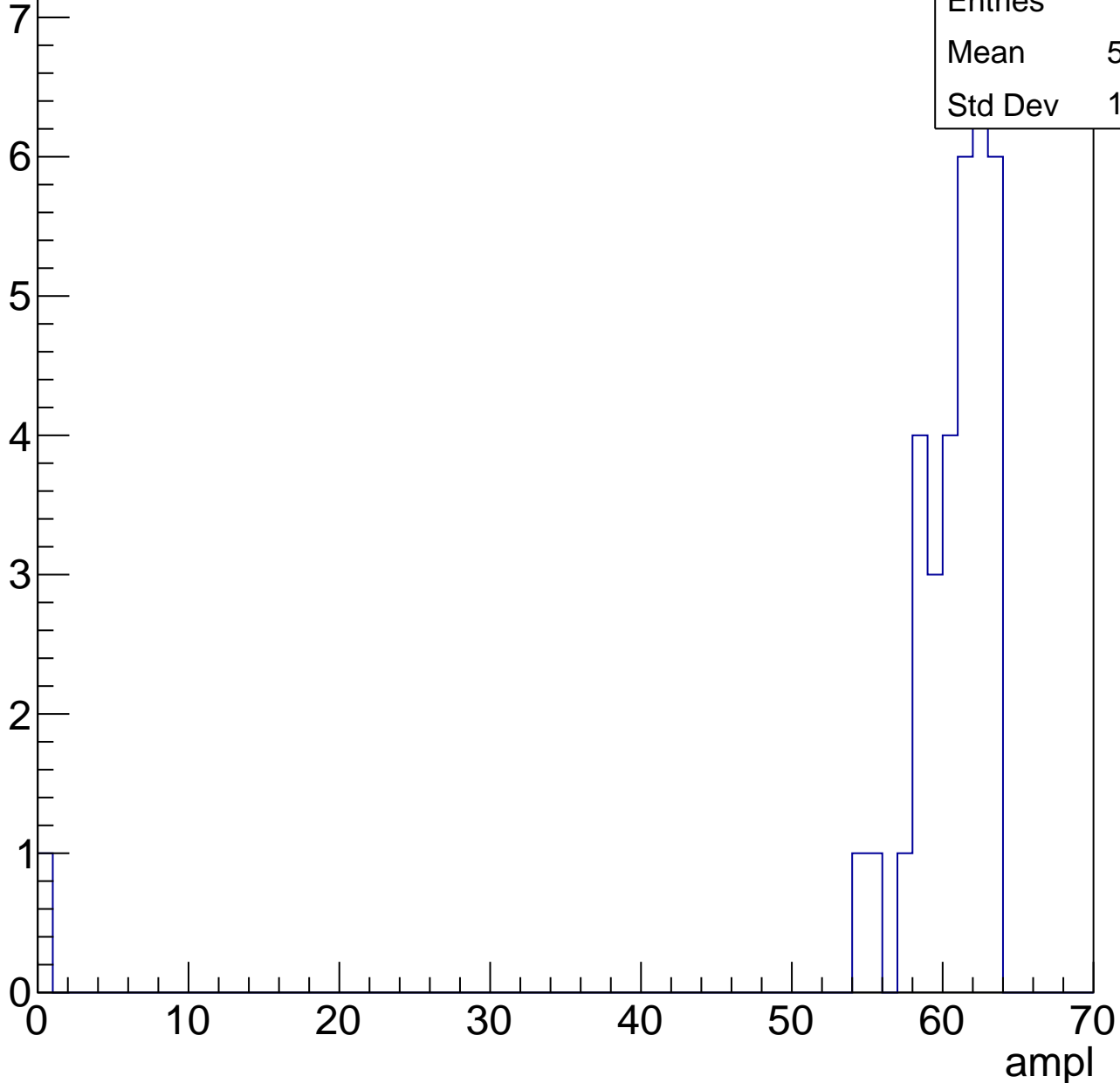


# B0L000S, U25-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	34
Mean	58.62
Std Dev	10.45



# B0L000S, U25-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch110, adc0

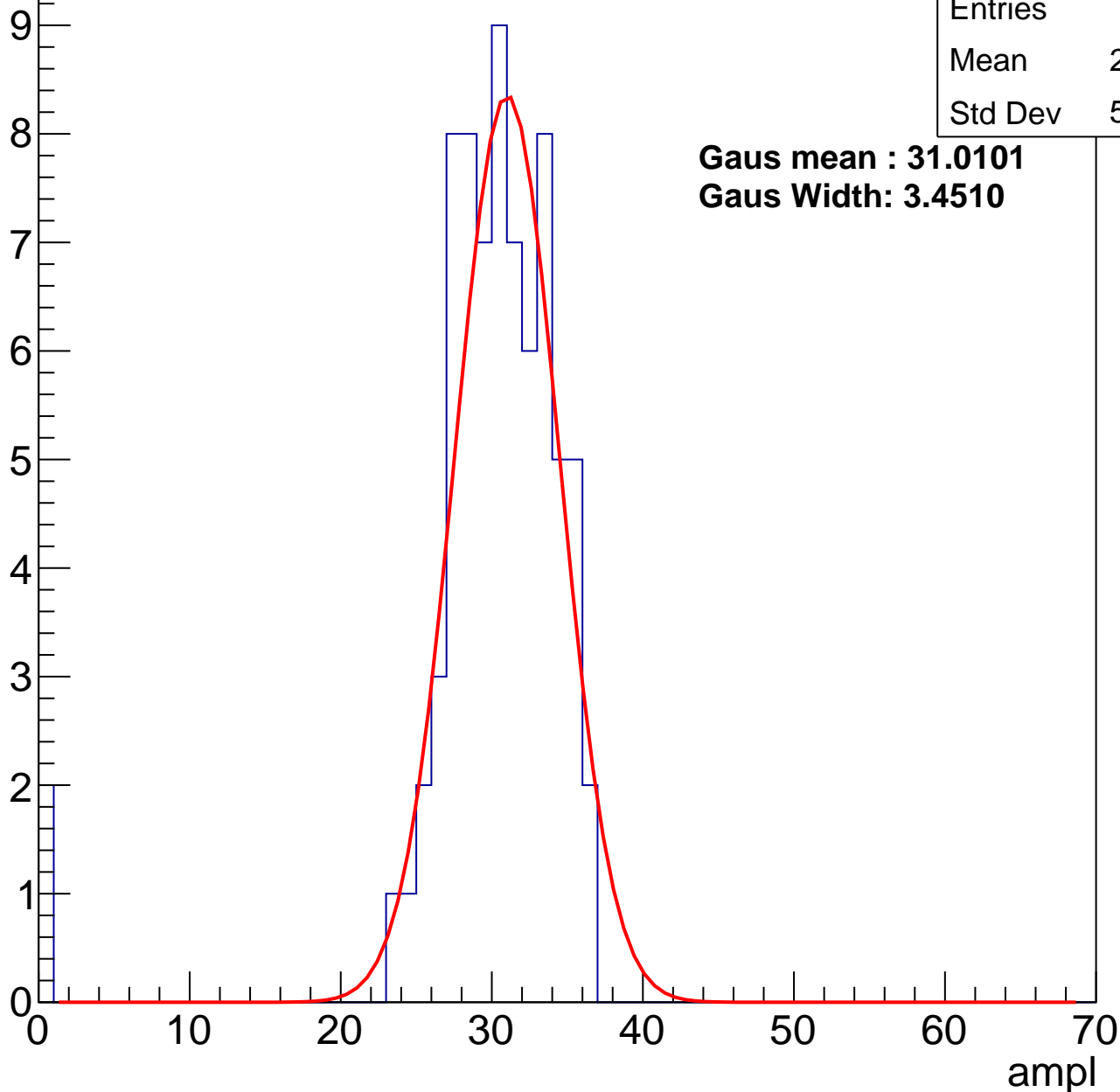
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	74
Mean	29.43
Std Dev	5.754

**Gaus mean : 31.0101**

**Gaus Width: 3.4510**



# B0L000S, U25-ch110, adc1

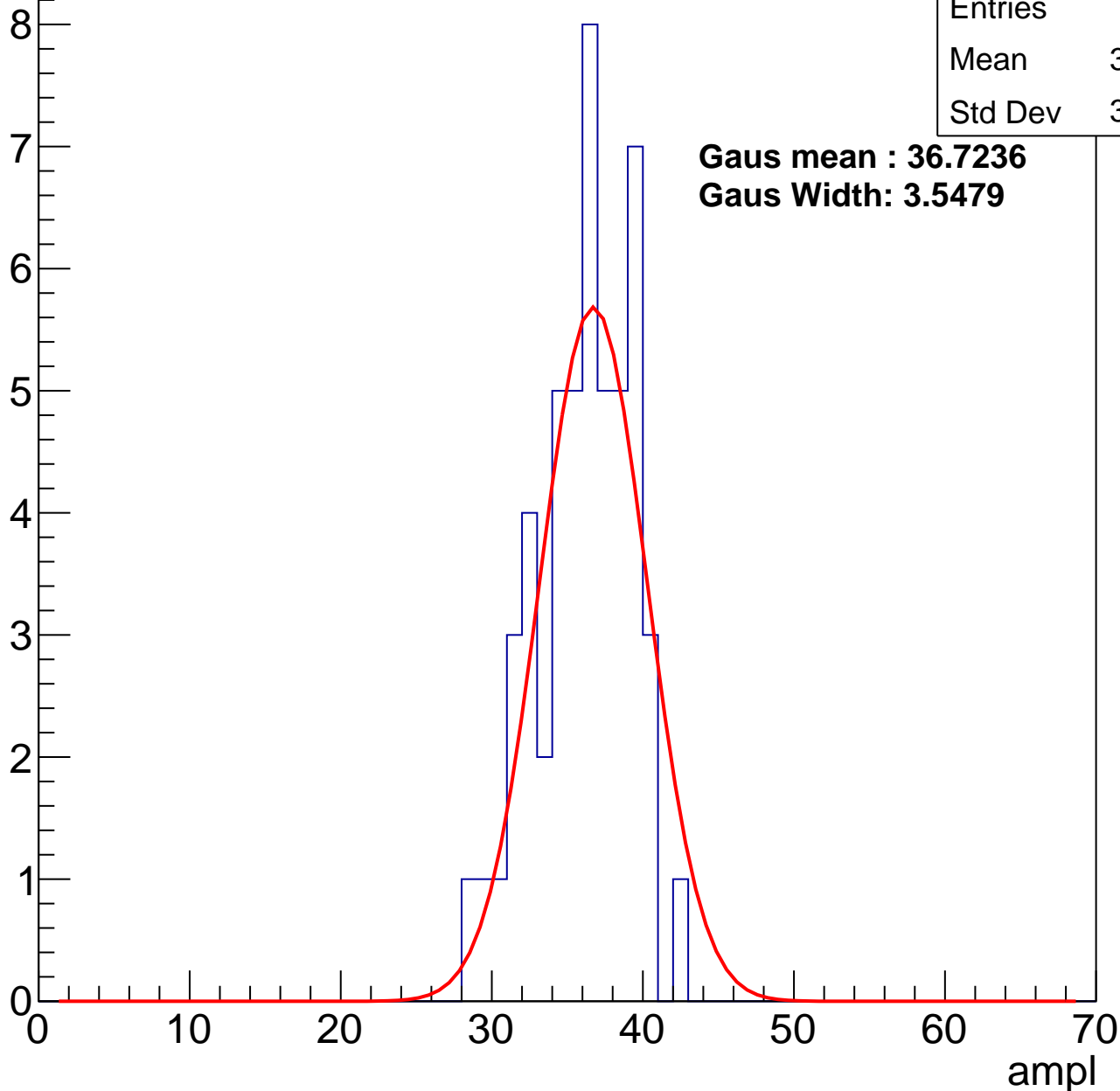
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	35.63
Std Dev	3.118

**Gaus mean : 36.7236**

**Gaus Width: 3.5479**



# B0L000S, U25-ch110, adc2

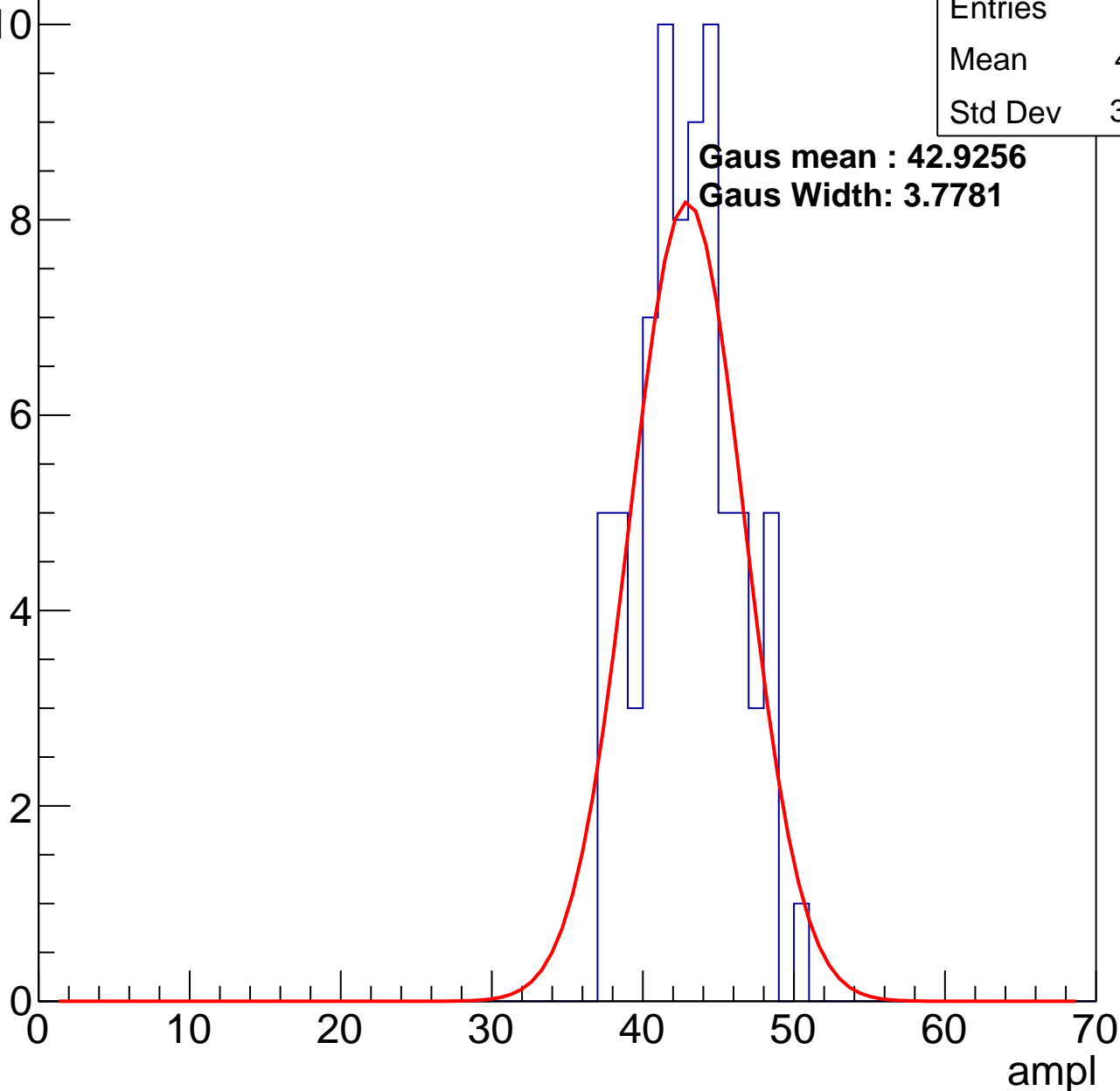
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	76
Mean	42.51
Std Dev	3.127

**Gaus mean : 42.9256**

**Gaus Width: 3.7781**

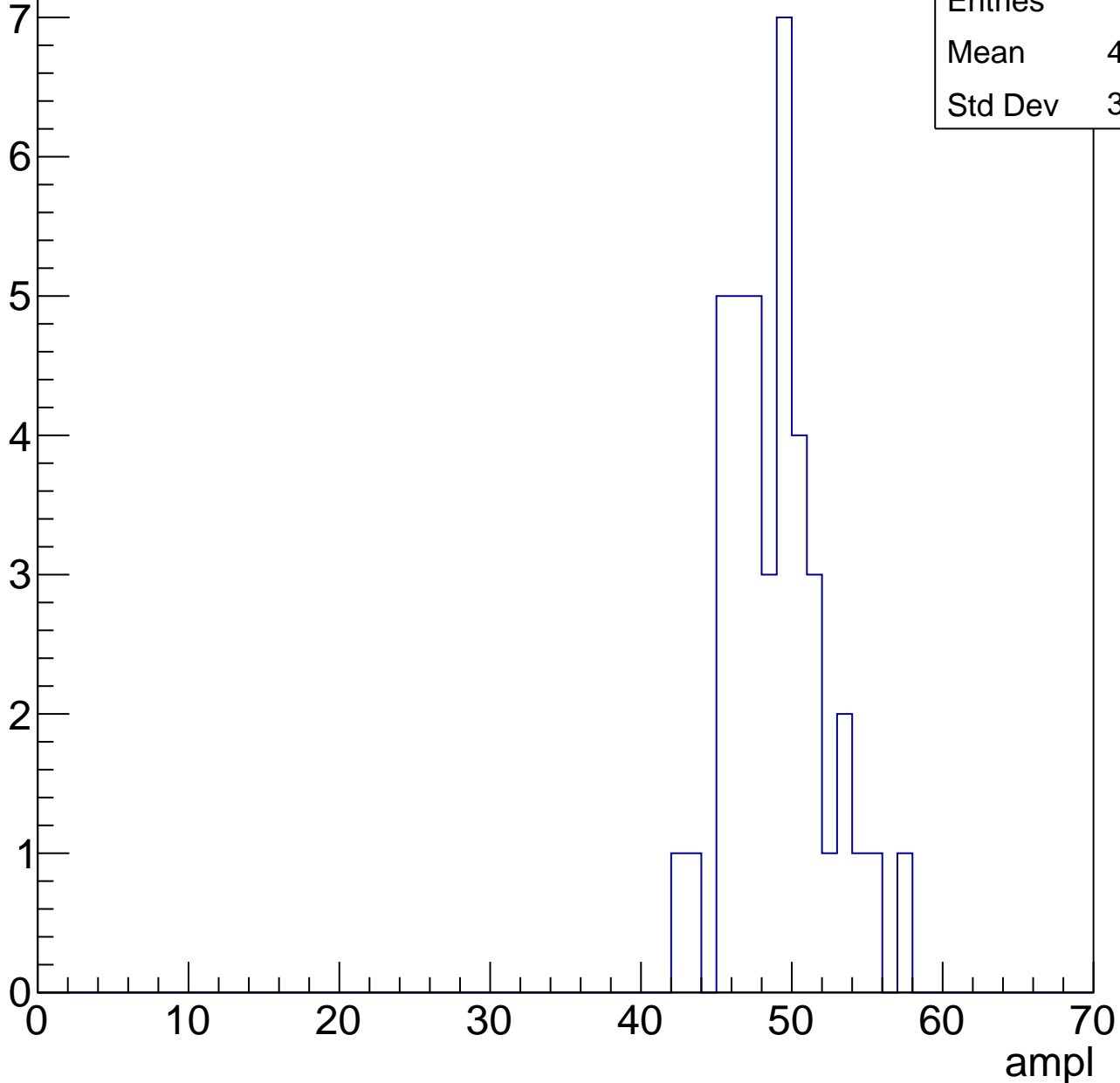


# B0L000S, U25-ch110, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

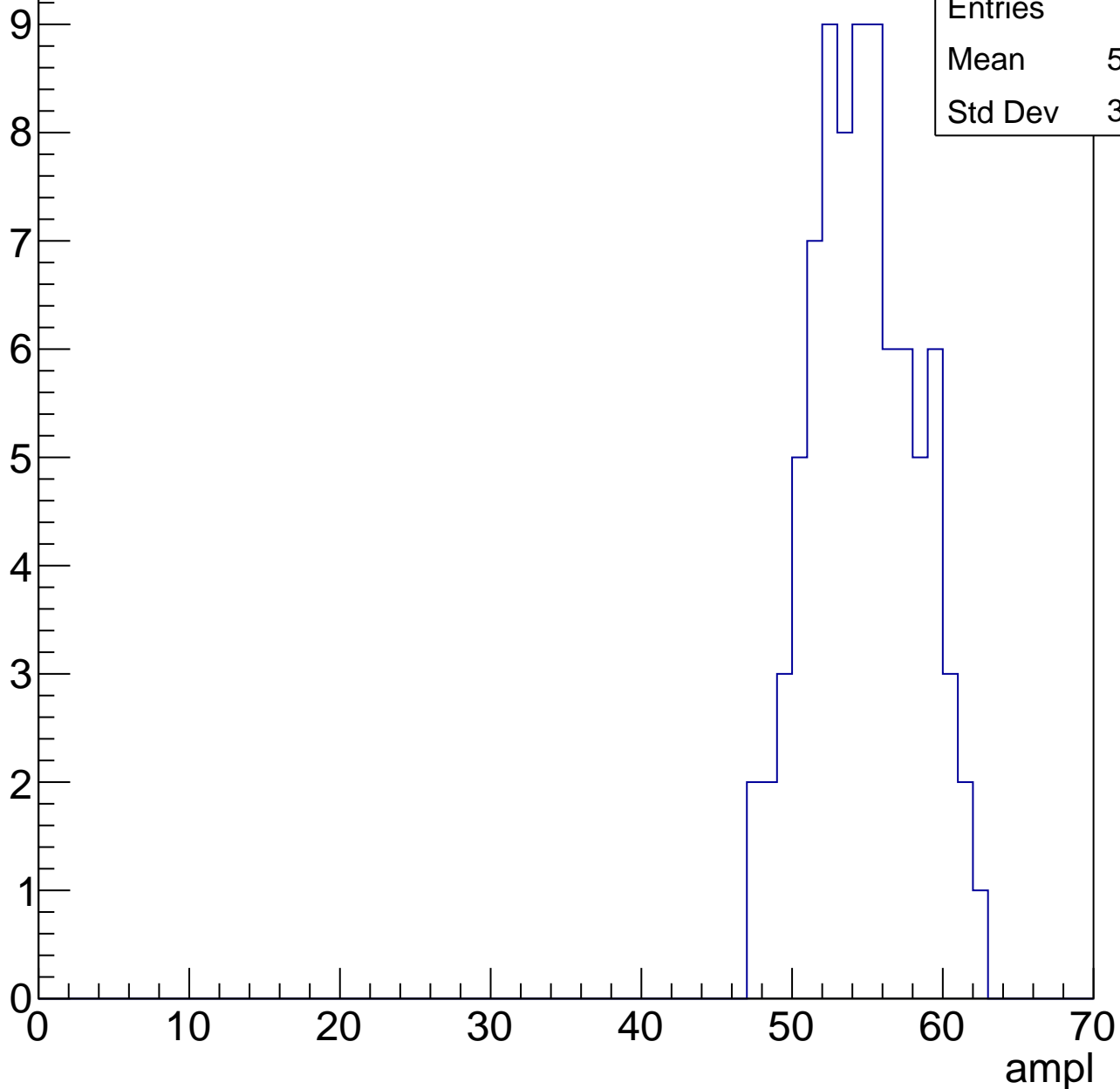
Entries	40
Mean	48.48
Std Dev	3.178



# B0L000S, U25-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



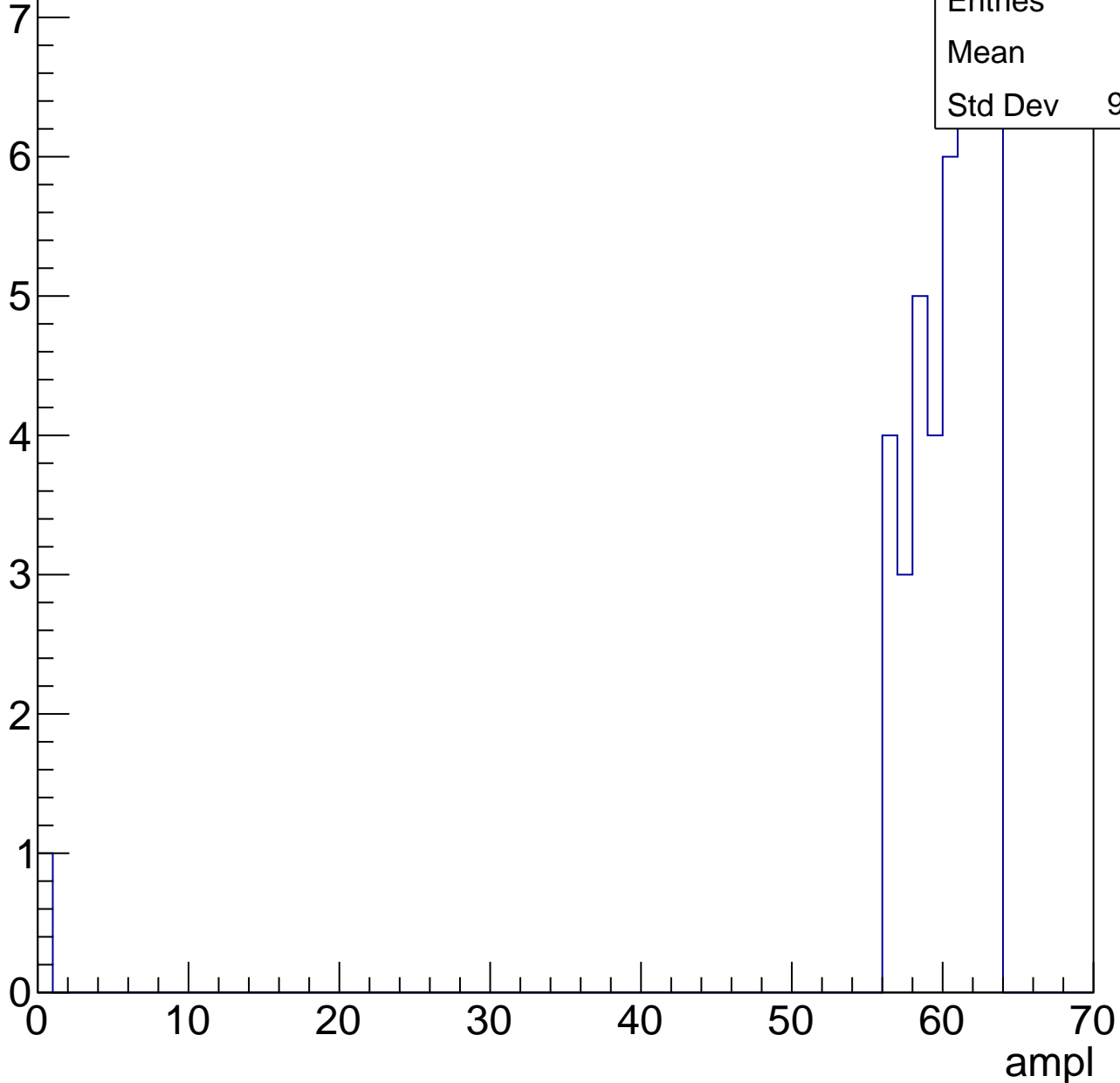
Entries	83
Mean	54.25
Std Dev	3.495

# B0L000S, U25-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	44
Mean	58.7
Std Dev	9.219



# B0L000S, U25-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	7
Mean	62
Std Dev	1.069



# B0L000S, U25-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch111, adc0

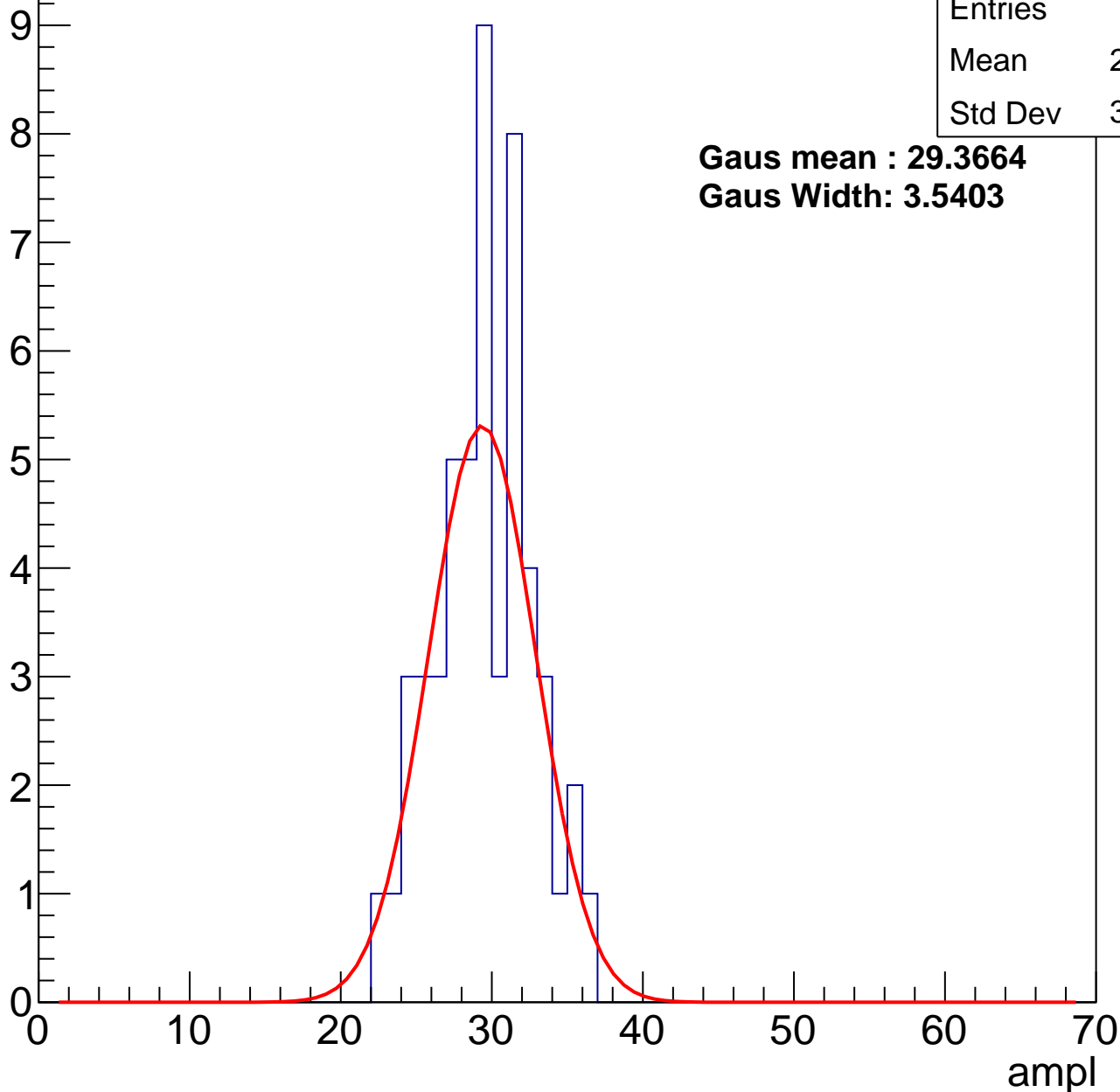
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	29.06
Std Dev	3.177

**Gaus mean : 29.3664**

**Gaus Width: 3.5403**



# B0L000S, U25-ch111, adc1

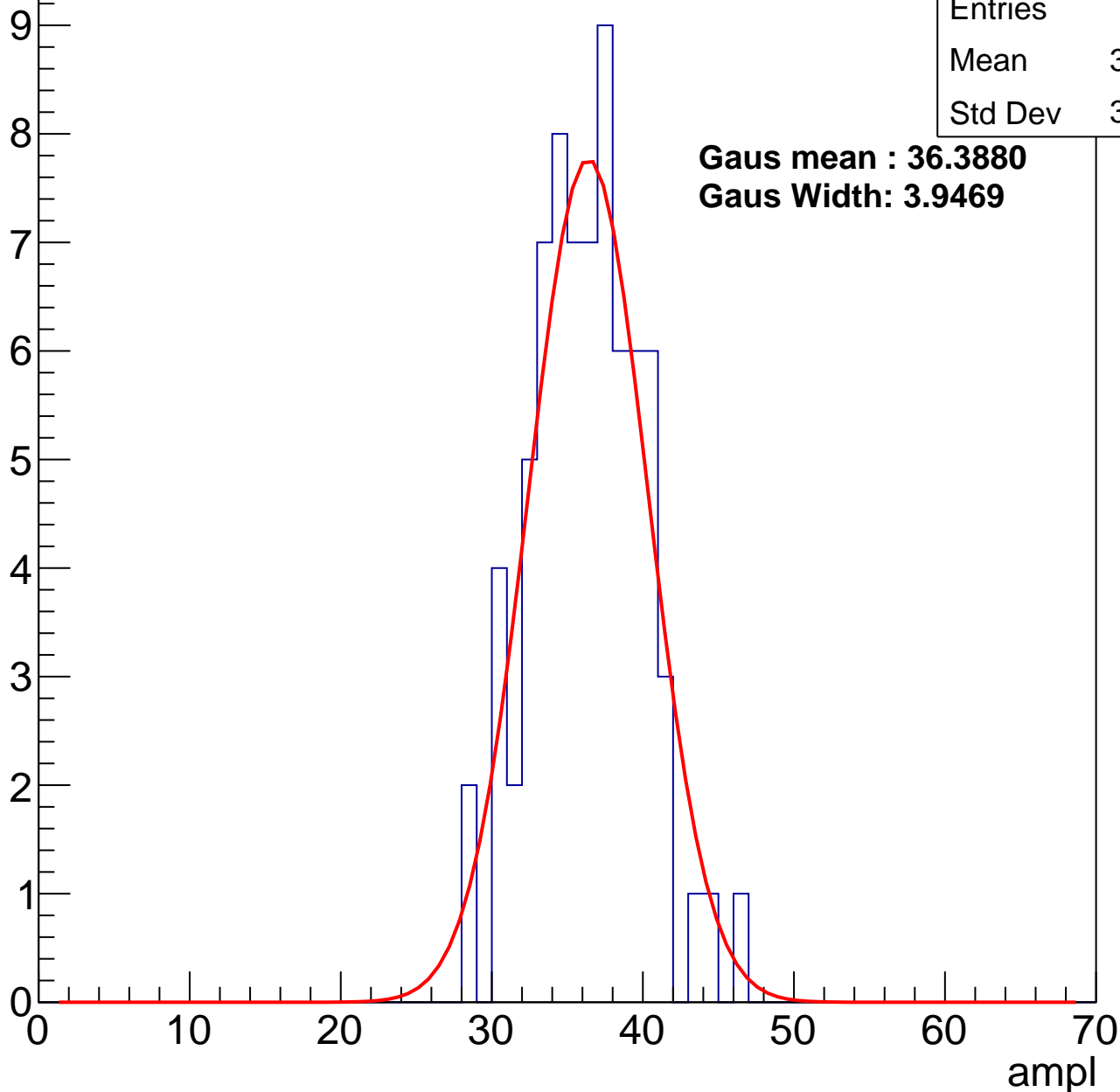
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	35.85
Std Dev	3.603

**Gaus mean : 36.3880**

**Gaus Width: 3.9469**



# B0L000S, U25-ch111, adc2

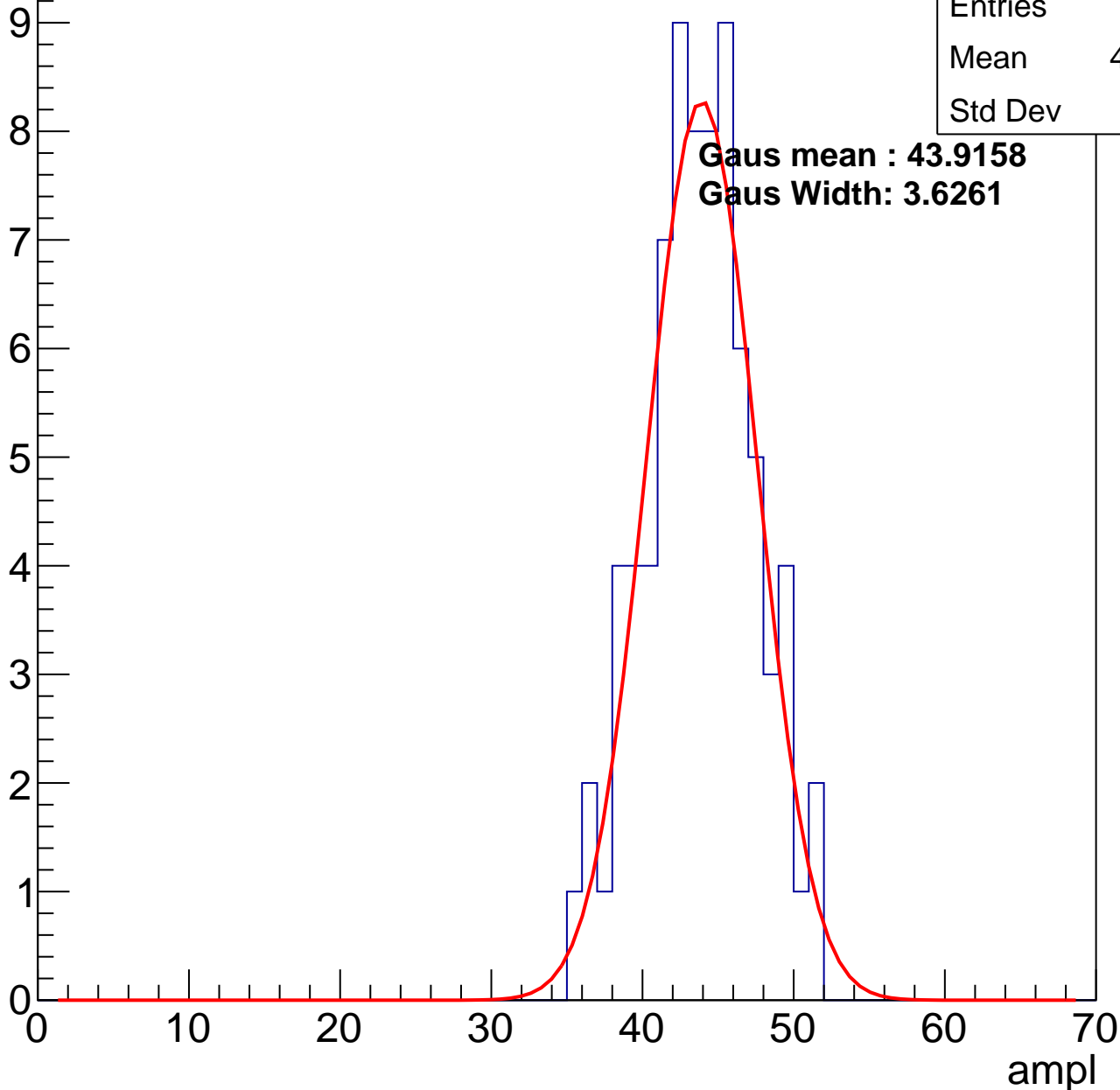
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	43.35
Std Dev	3.58

**Gaus mean : 43.9158**

**Gaus Width: 3.6261**

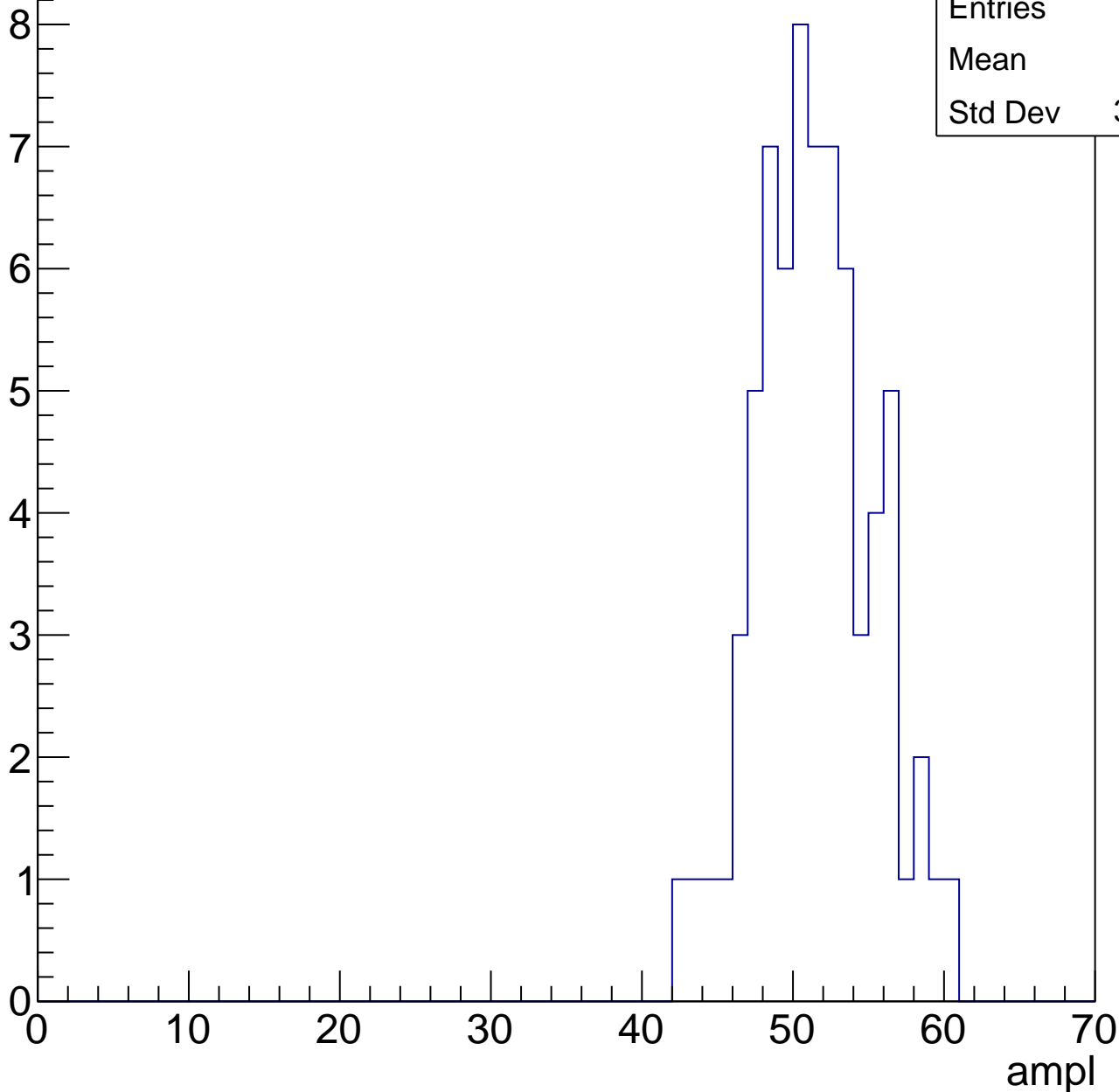


# B0L000S, U25-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	51
Std Dev	3.791

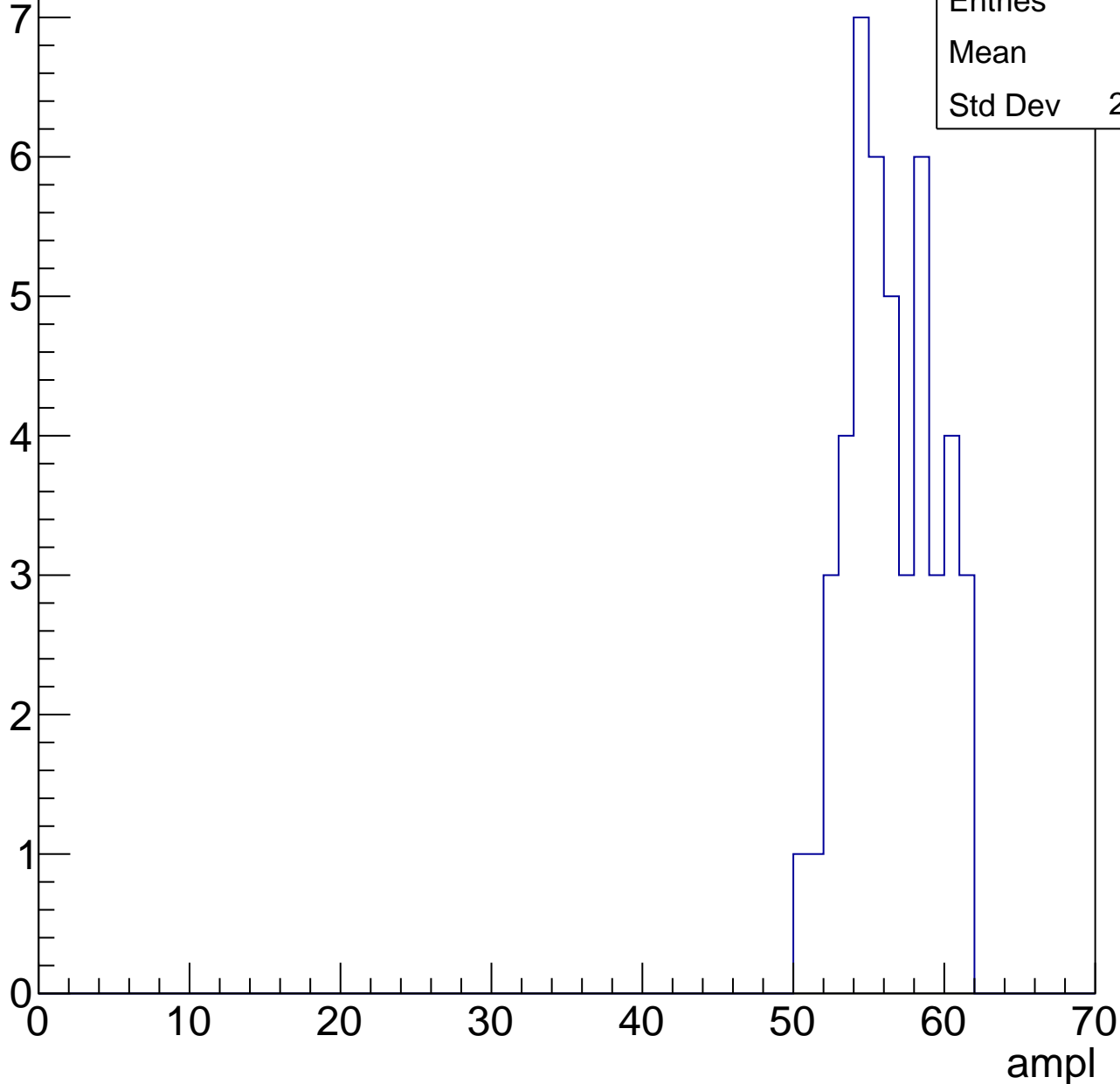


# B0L000S, U25-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	46
Mean	56
Std Dev	2.844

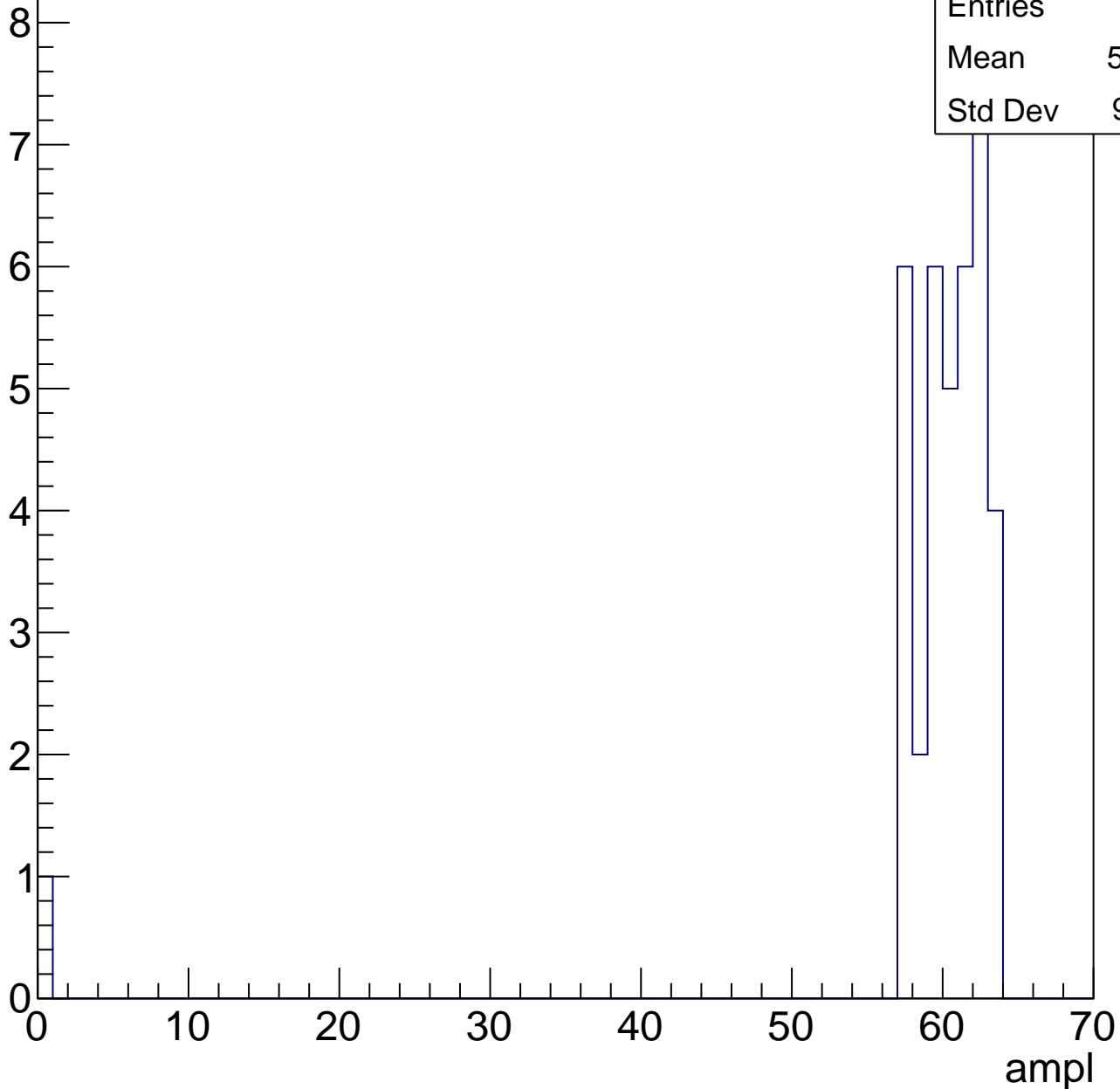


# B0L000S, U25-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.58
Std Dev	9.821

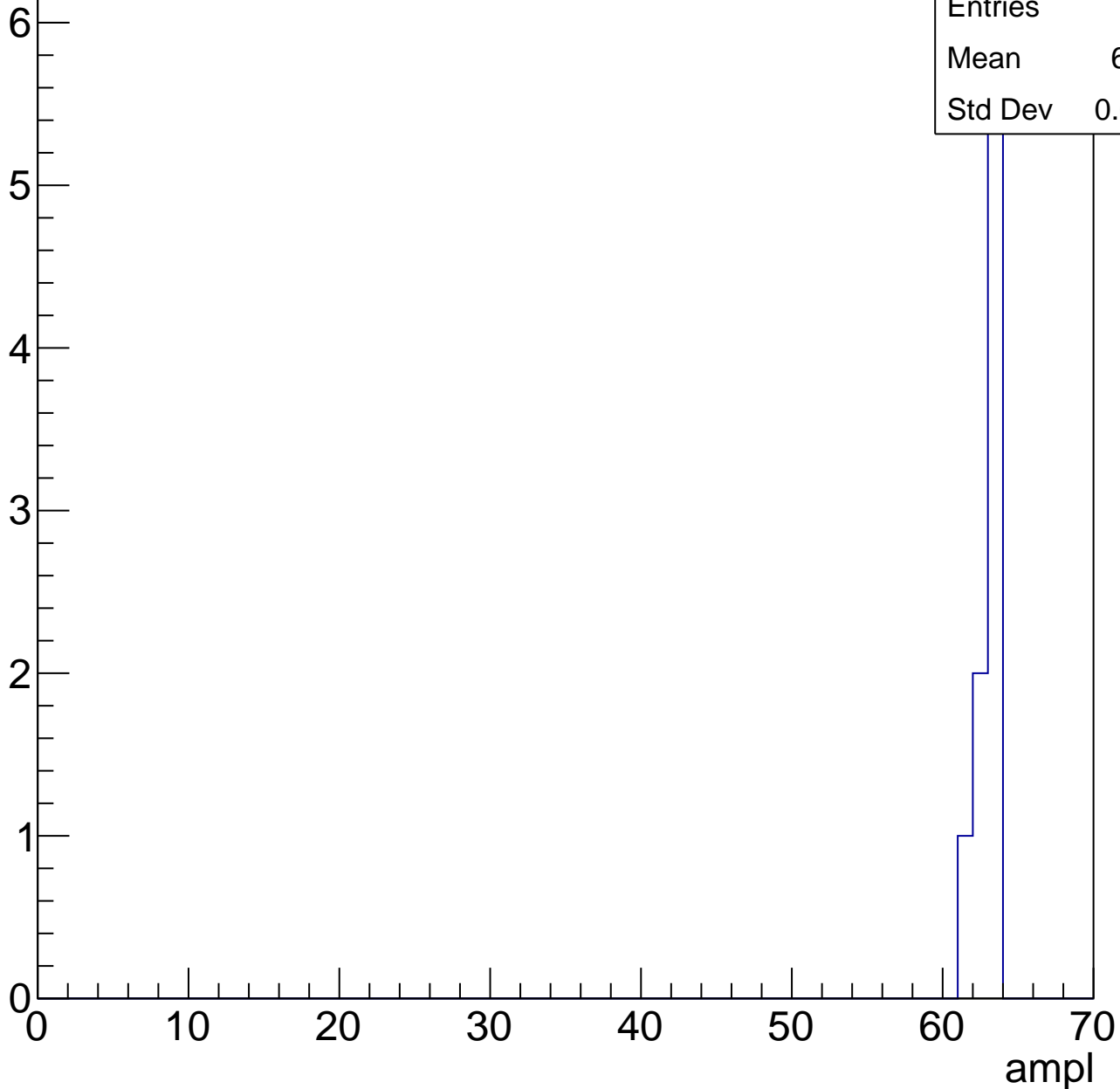


# B0L000S, U25-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	9
Mean	62.56
Std Dev	0.6849





# B0L000S, U25-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch112, adc0

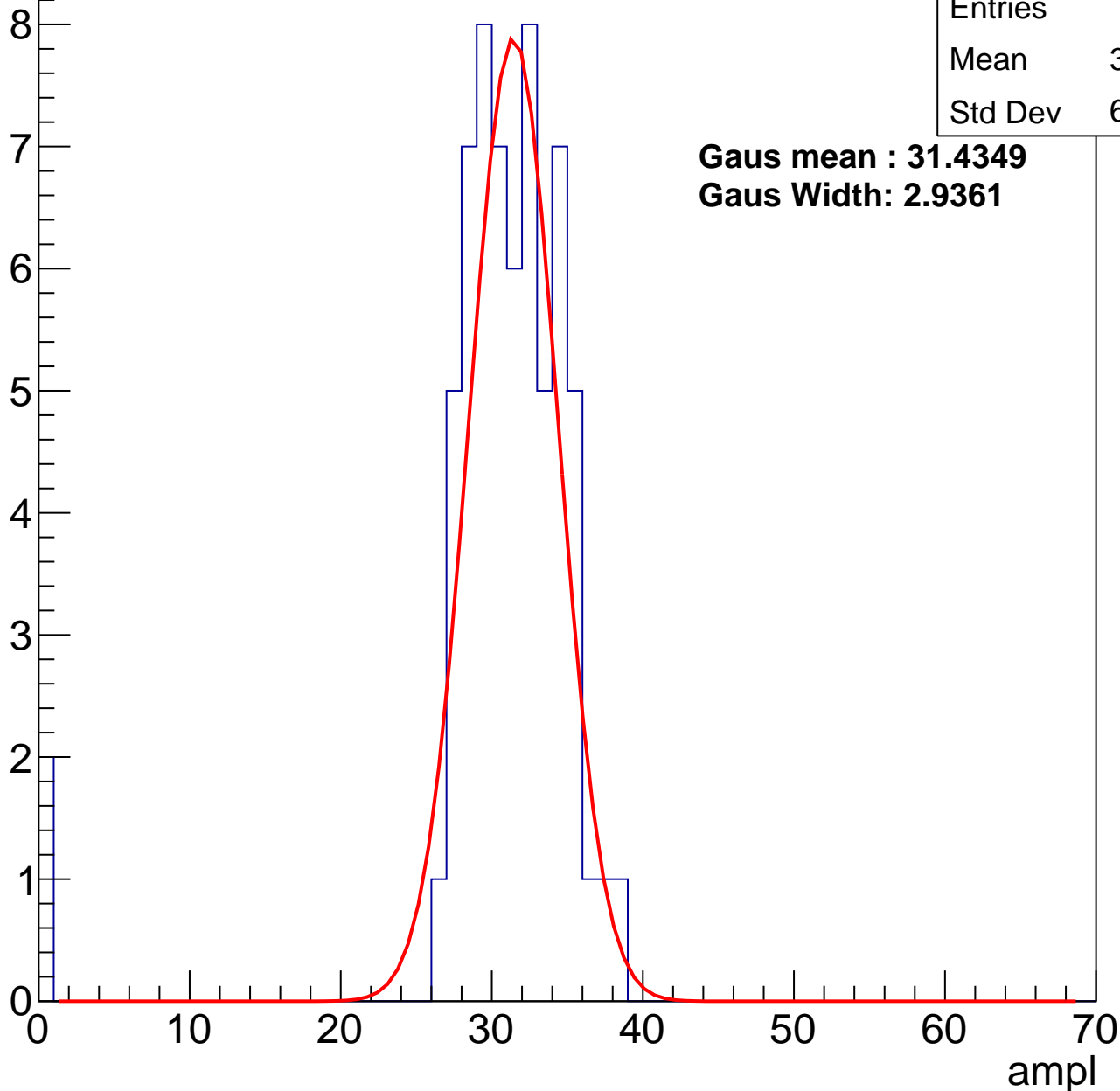
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.16
Std Dev	6.078

**Gaus mean : 31.4349**

**Gaus Width: 2.9361**



# B0L000S, U25-ch112, adc1

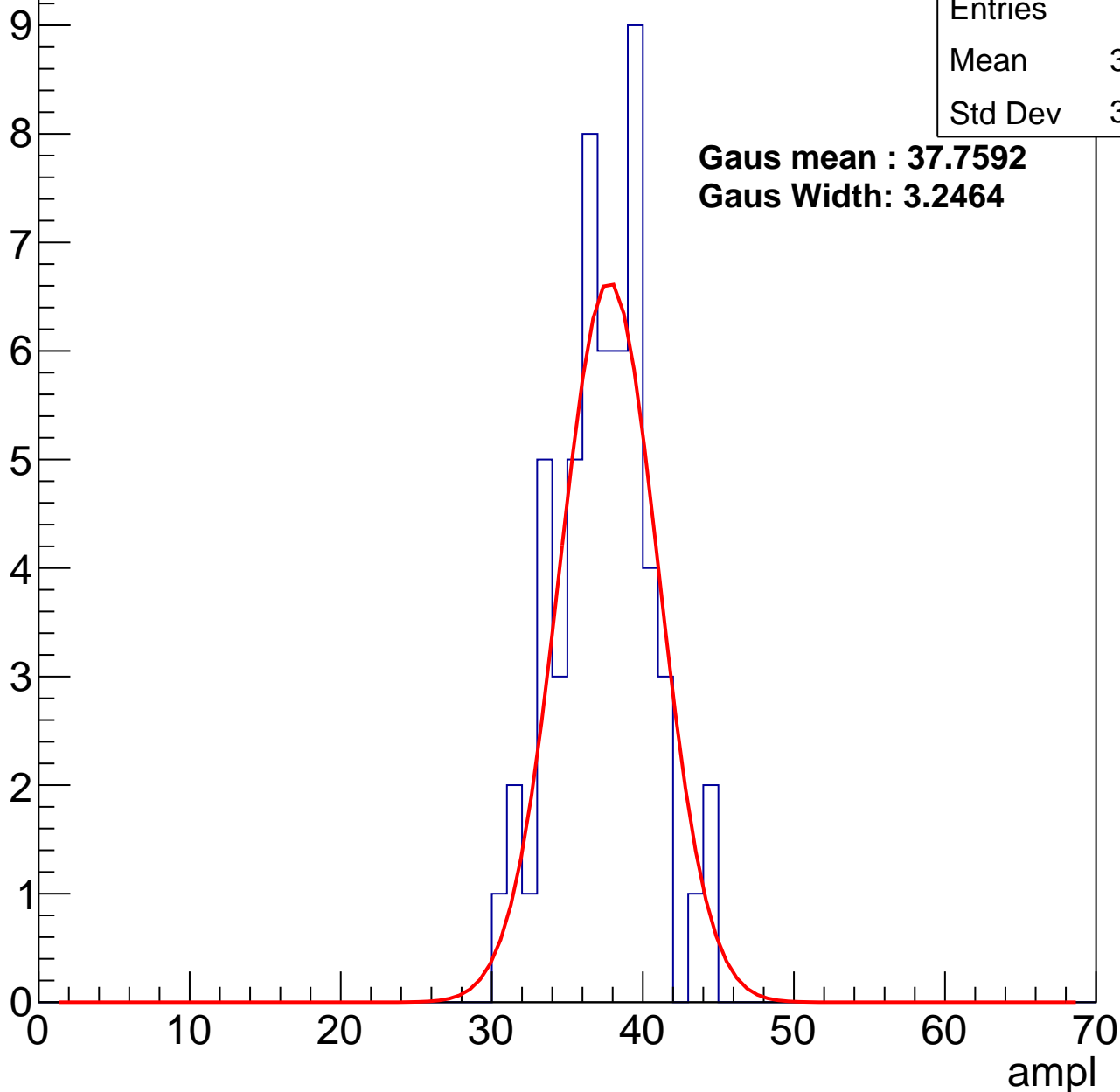
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	36.95
Std Dev	3.108

**Gaus mean : 37.7592**

**Gaus Width: 3.2464**



# B0L000S, U25-ch112, adc2

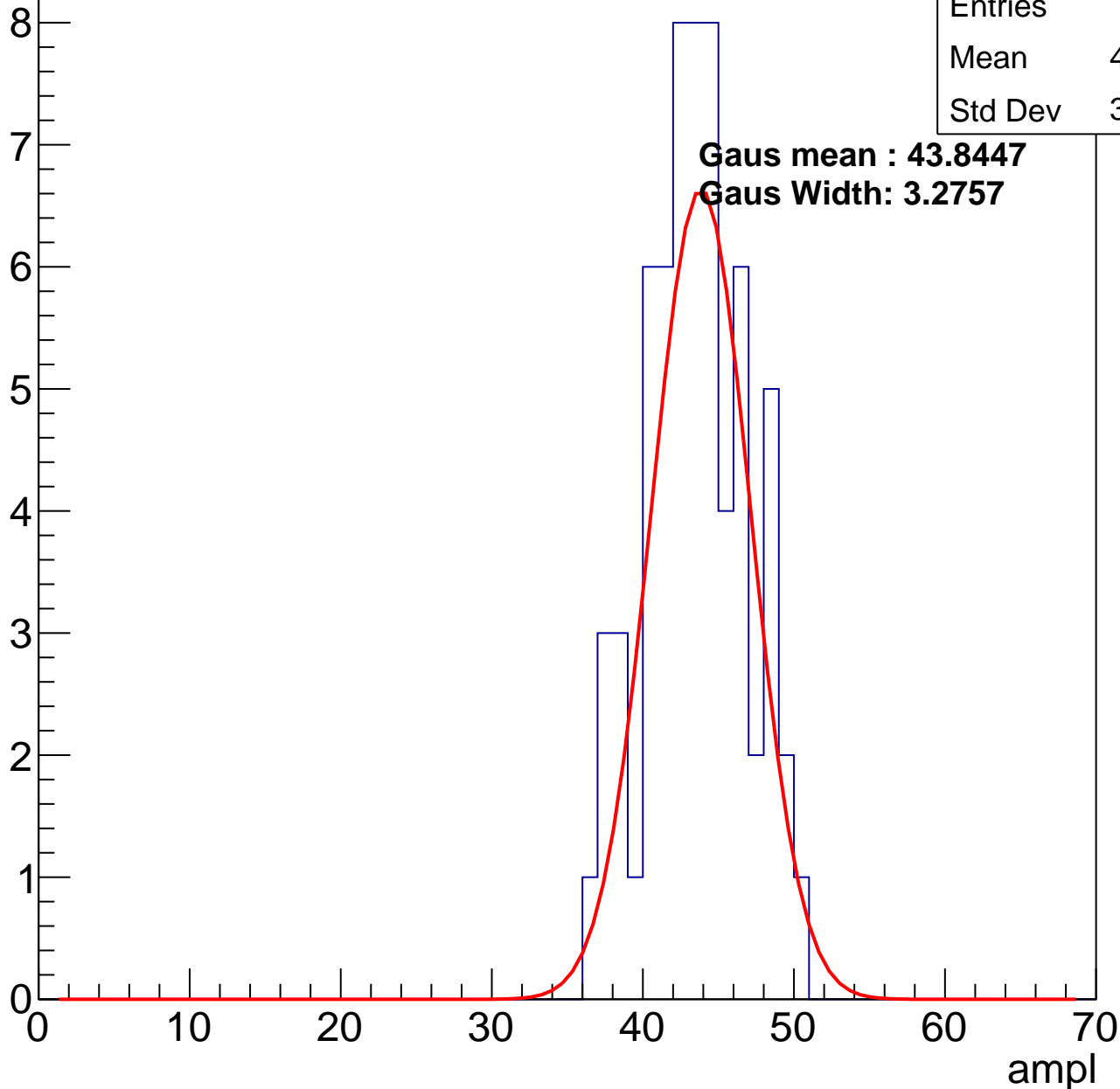
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.06
Std Dev	3.283

**Gaus mean : 43.8447**

**Gaus Width: 3.2757**

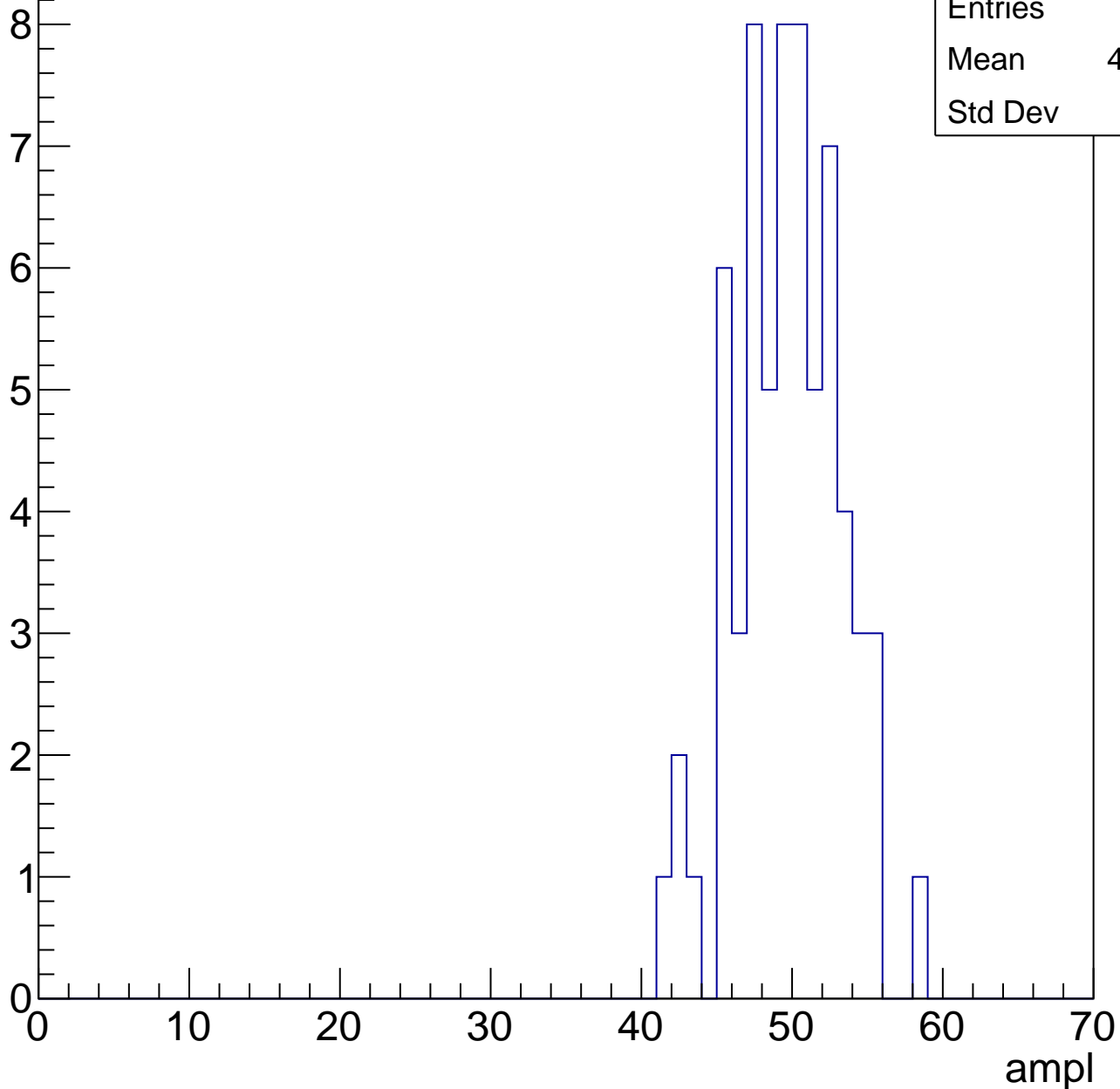


# B0L000S, U25-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	49.23
Std Dev	3.45

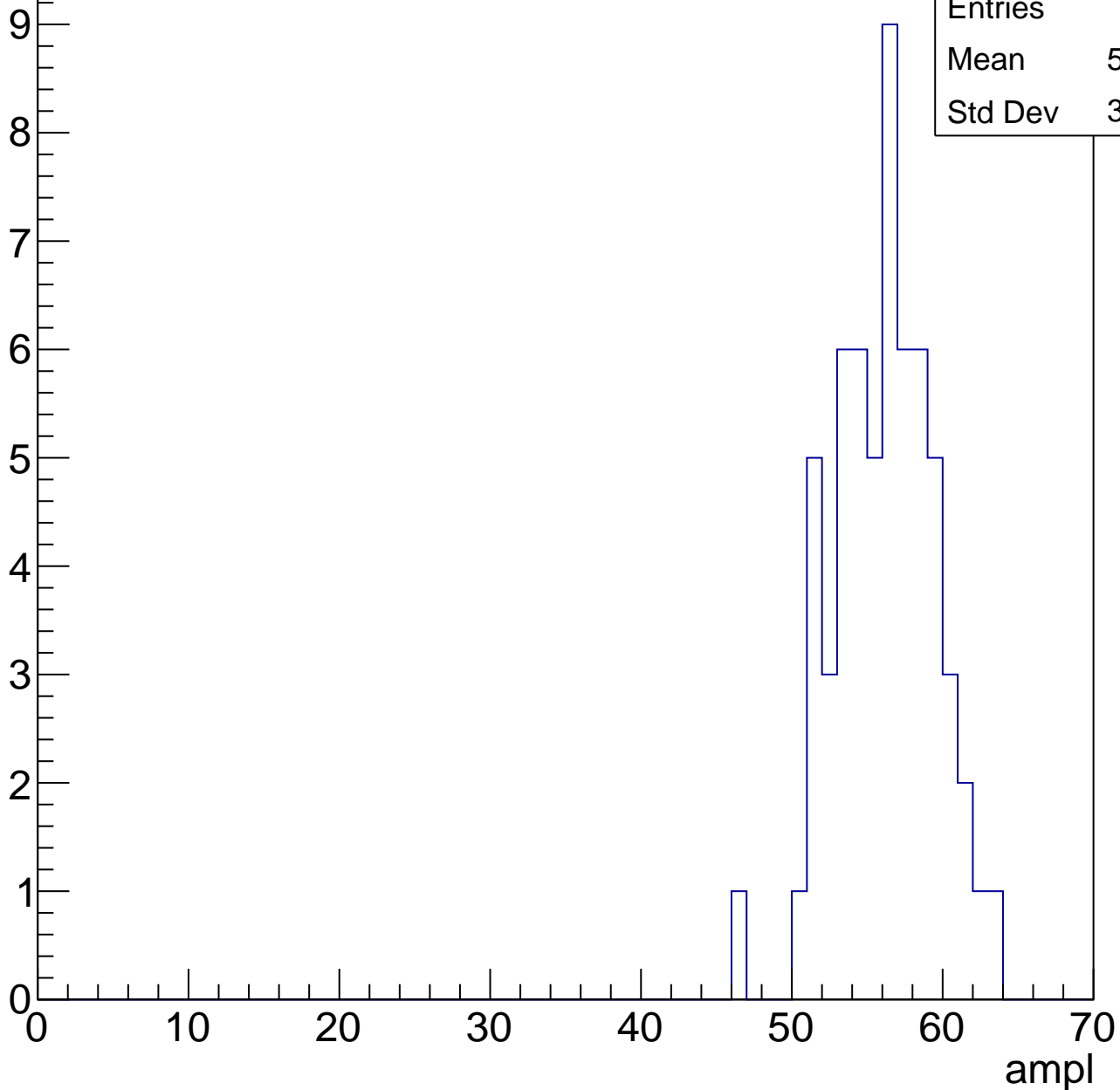


# B0L000S, U25-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	55.67
Std Dev	3.274

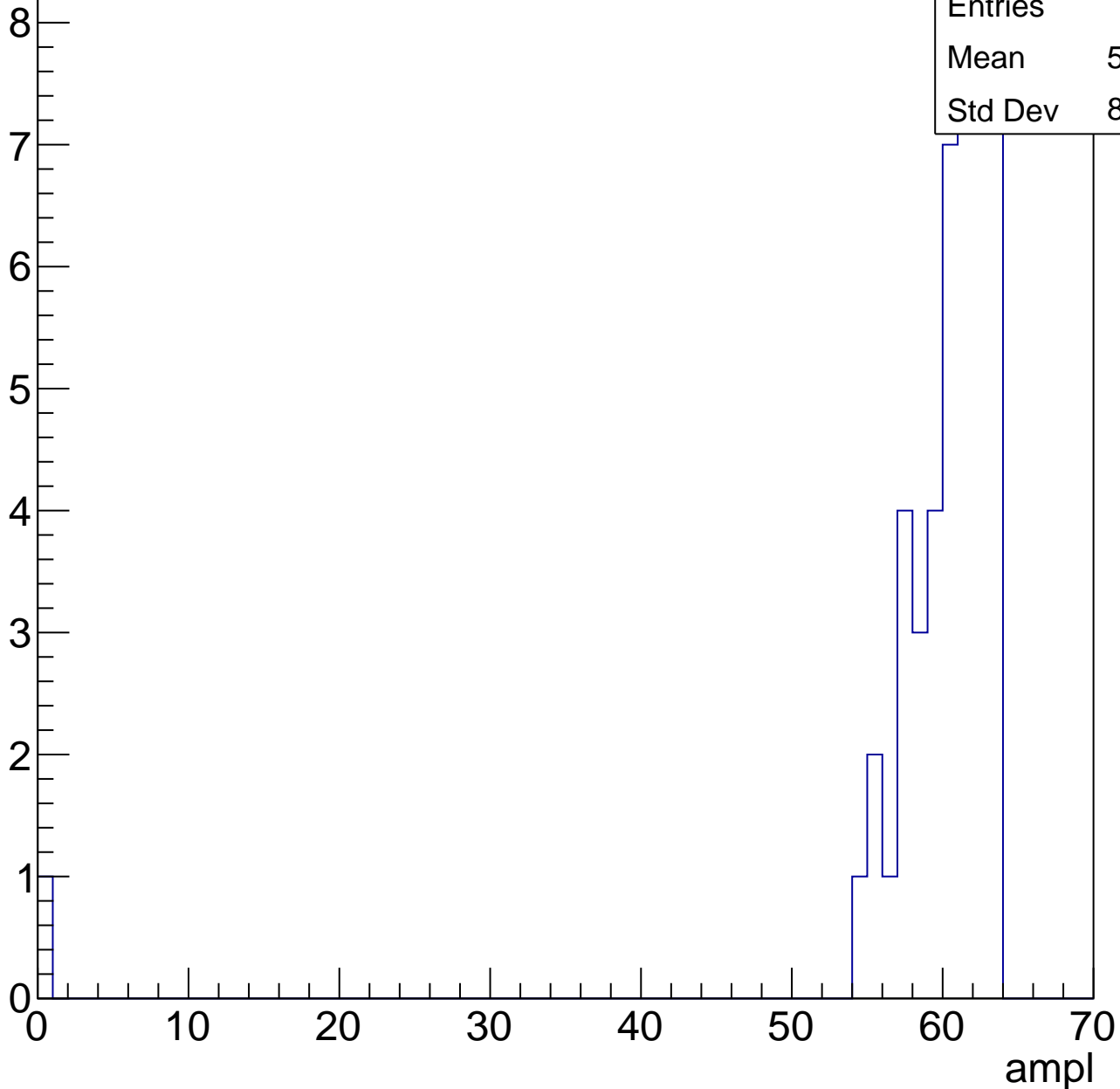


# B0L000S, U25-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

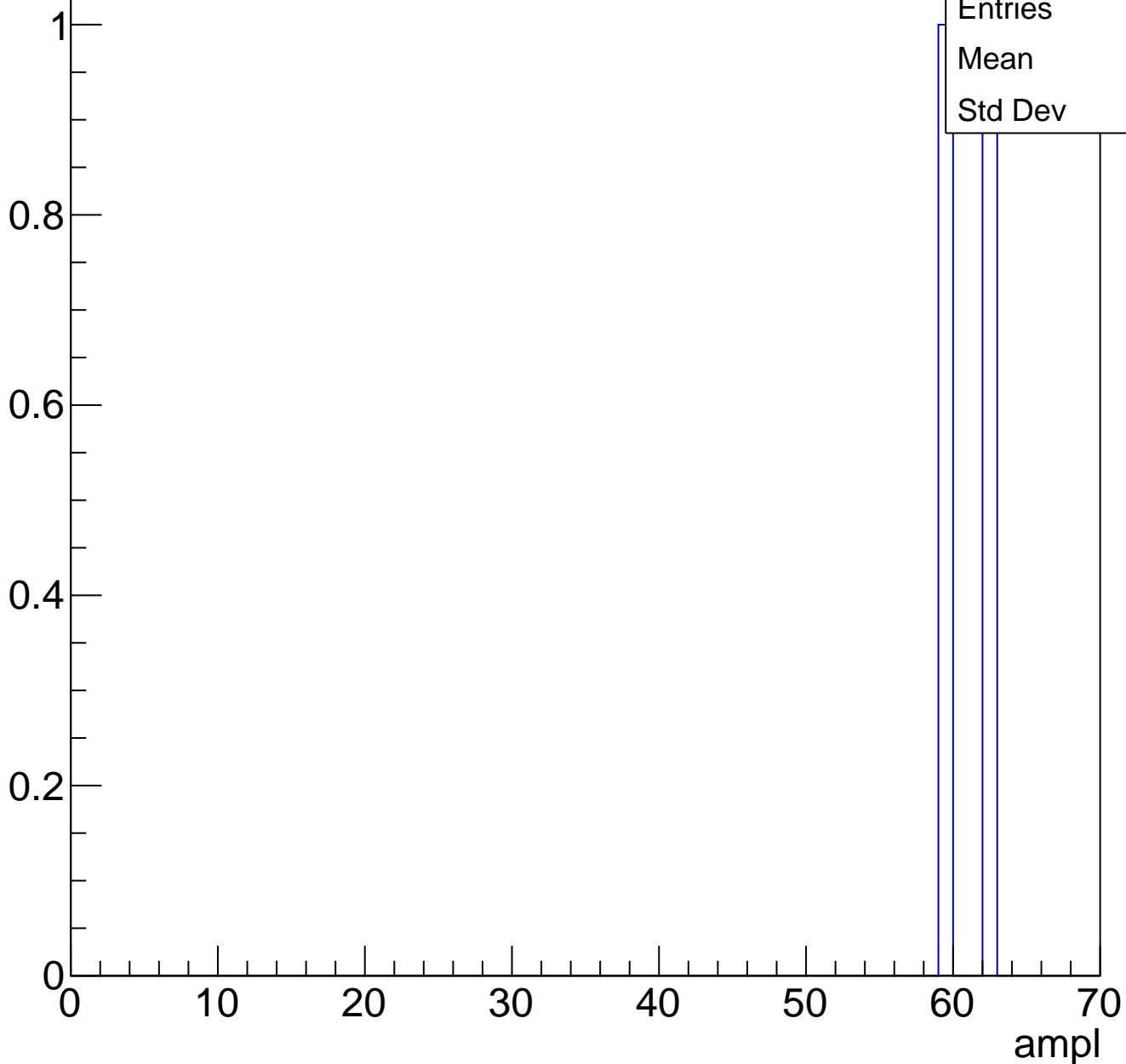
Entries	47
Mean	58.85
Std Dev	8.996



# B0L000S, U25-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch113, adc0

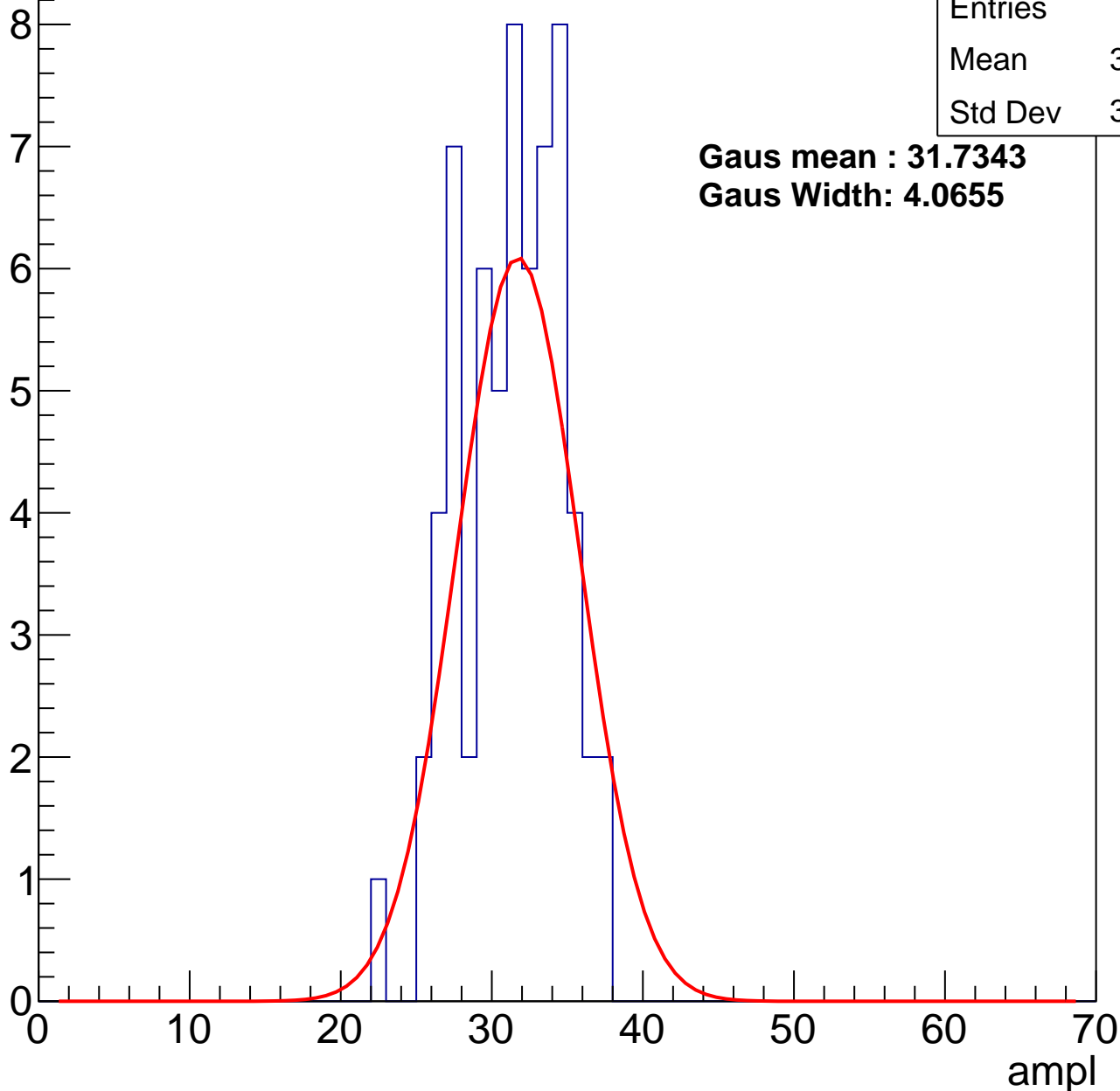
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	30.84
Std Dev	3.313

**Gaus mean : 31.7343**

**Gaus Width: 4.0655**



# B0L000S, U25-ch113, adc1

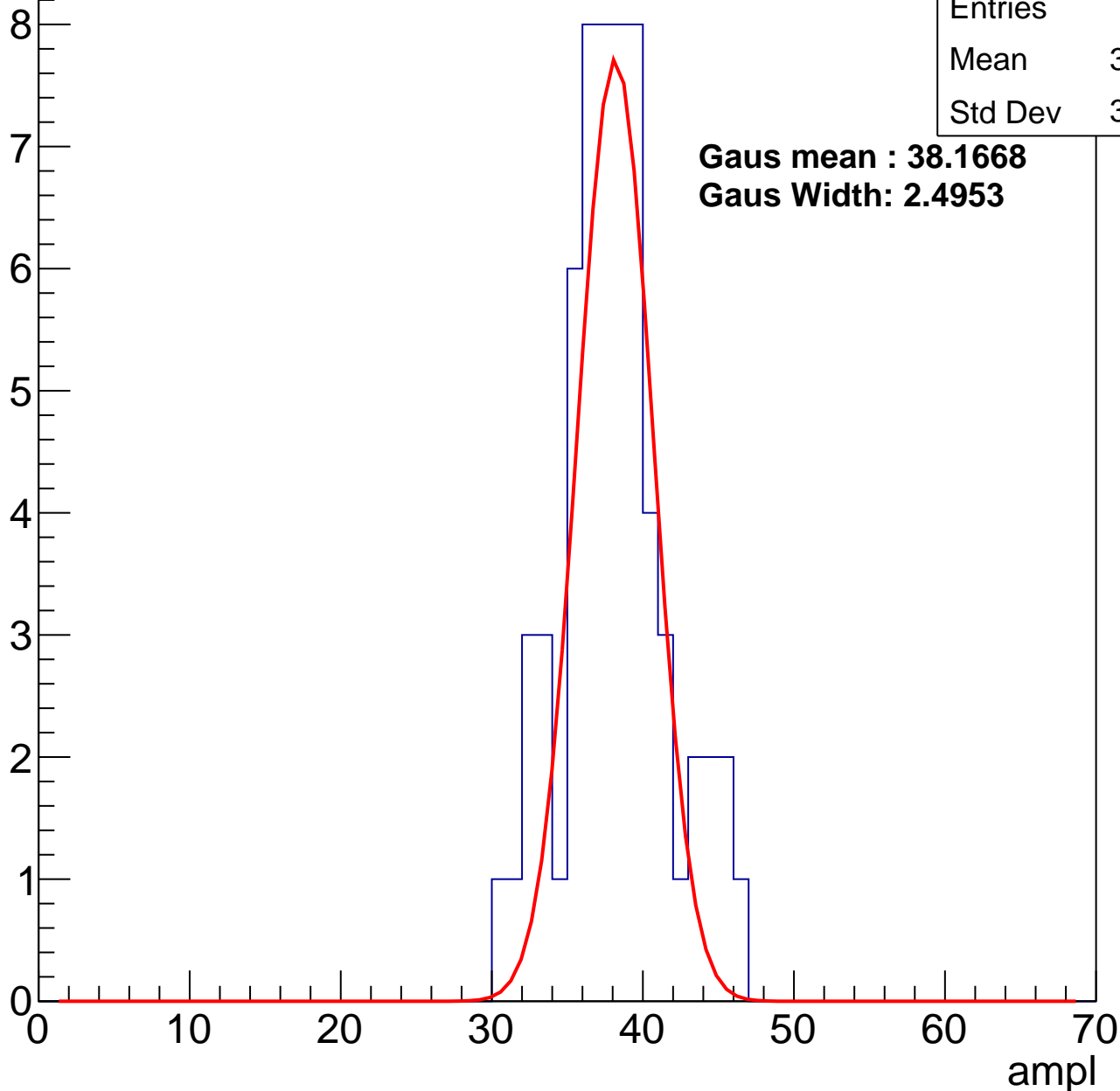
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	37.66
Std Dev	3.478

**Gaus mean : 38.1668**

**Gaus Width: 2.4953**



# B0L000S, U25-ch113, adc2

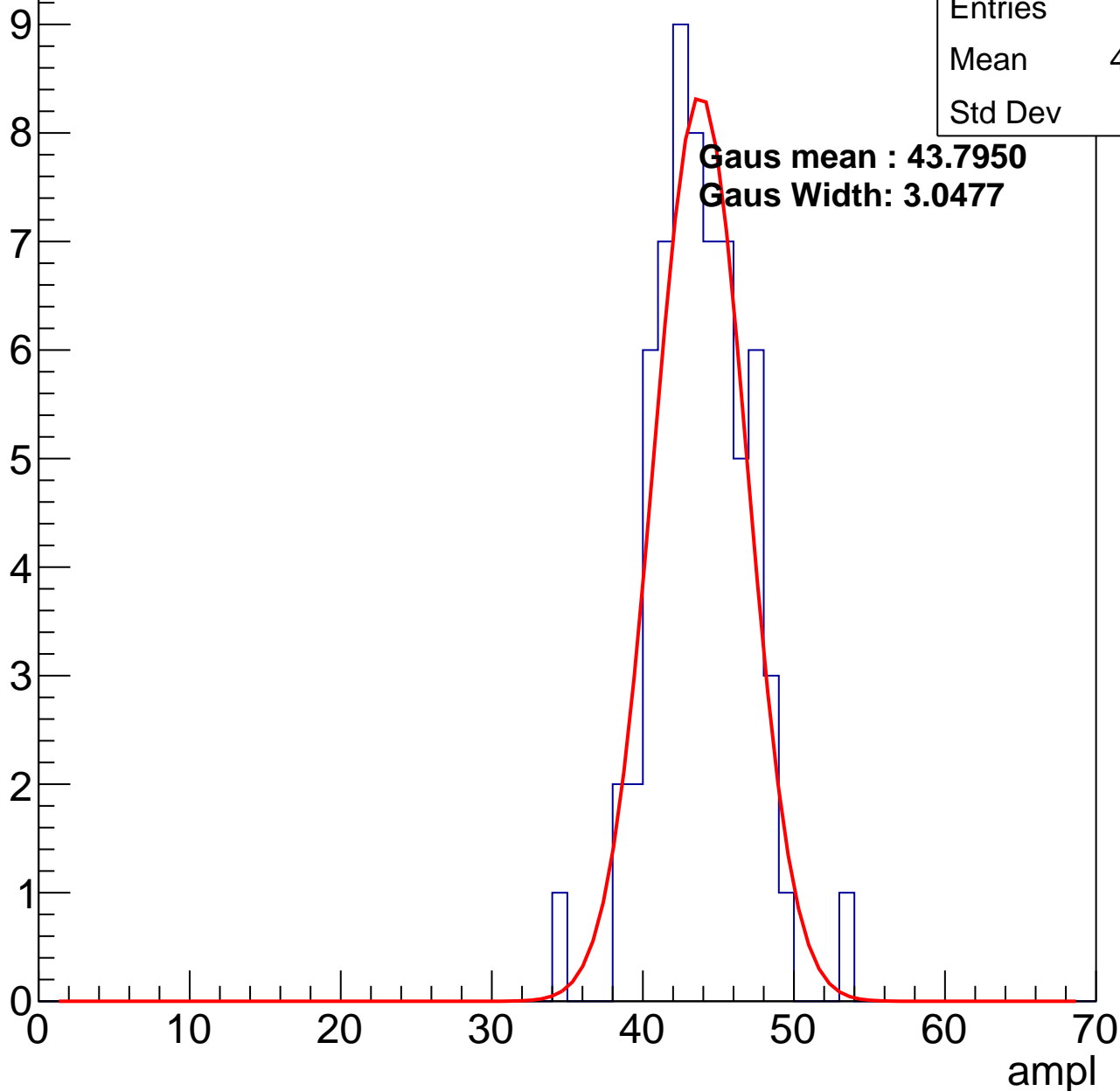
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.35
Std Dev	3.12

**Gaus mean : 43.7950**

**Gaus Width: 3.0477**



# B0L000S, U25-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

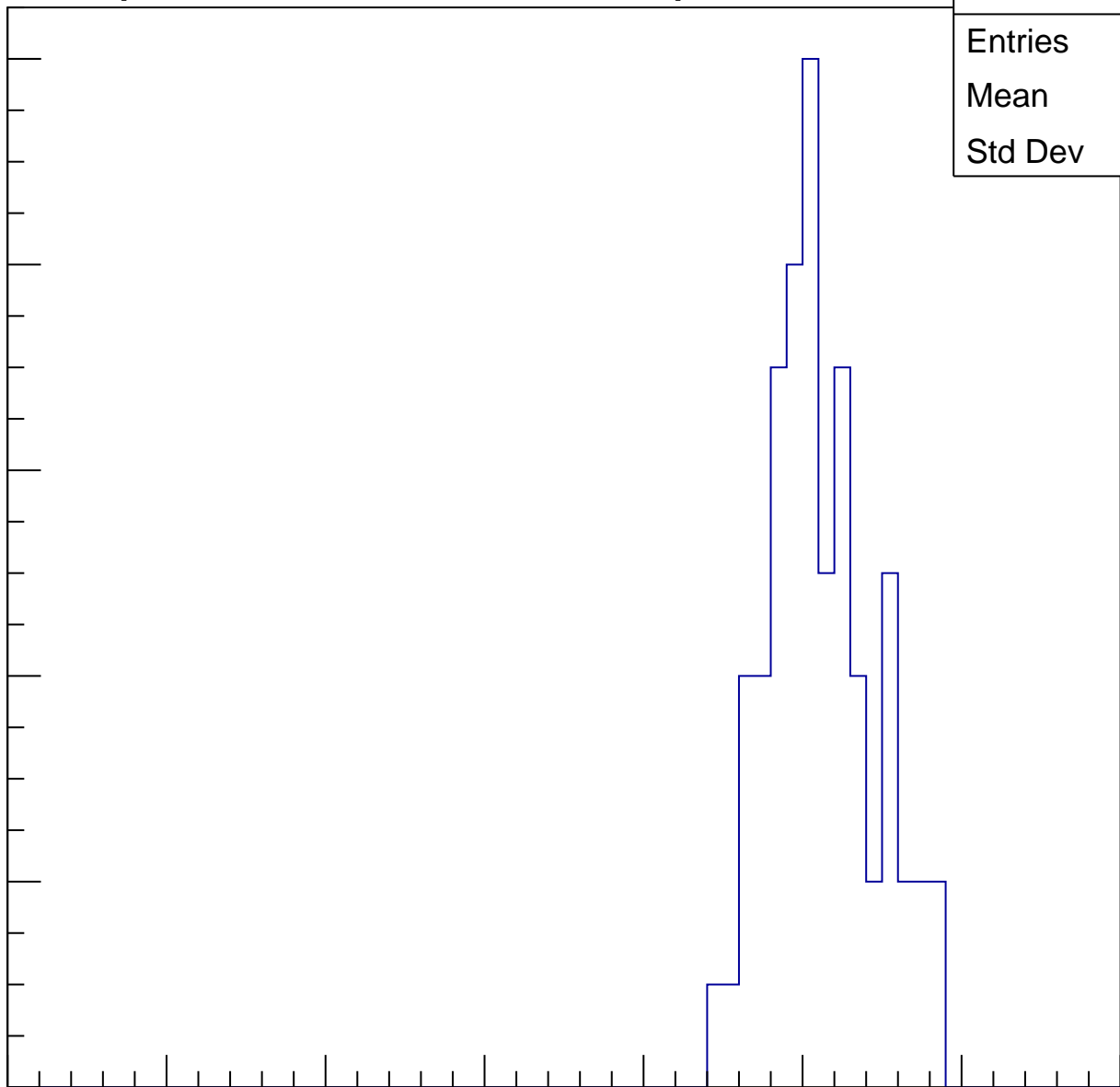
Entries	64
Mean	50.7
Std Dev	3.282

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

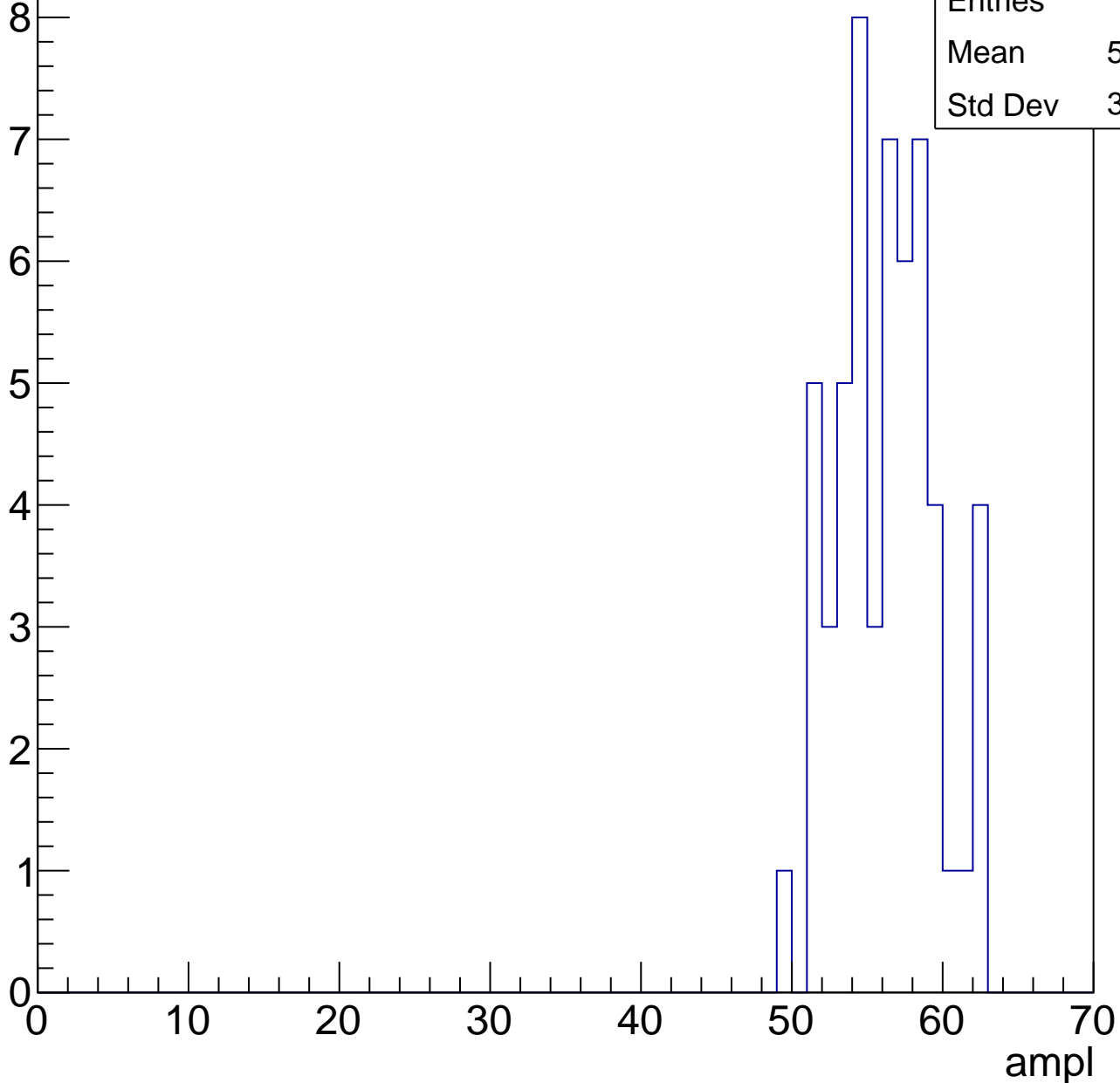


# B0L000S, U25-ch113, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	55
Mean	55.76
Std Dev	3.174

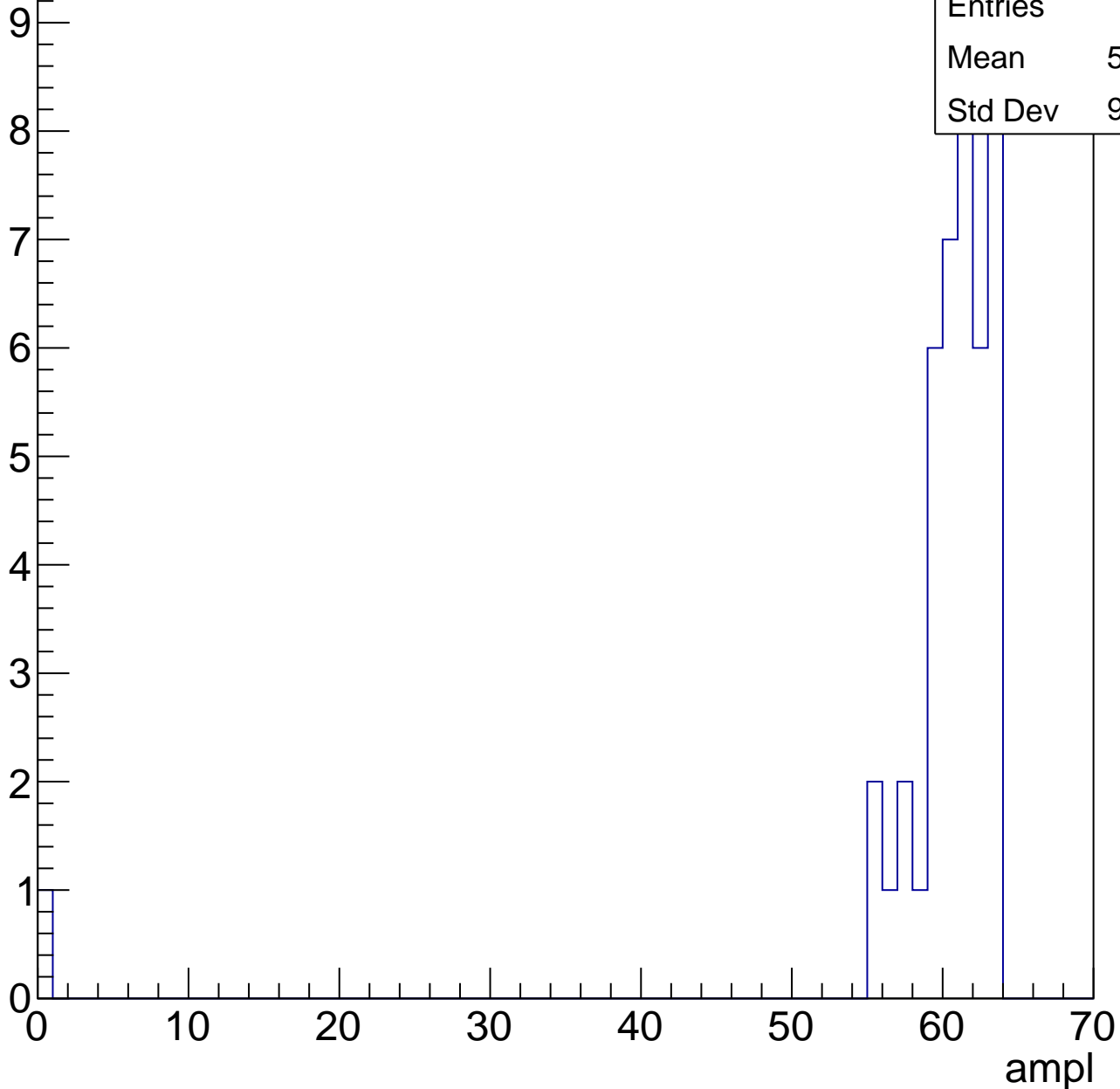


# B0L000S, U25-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

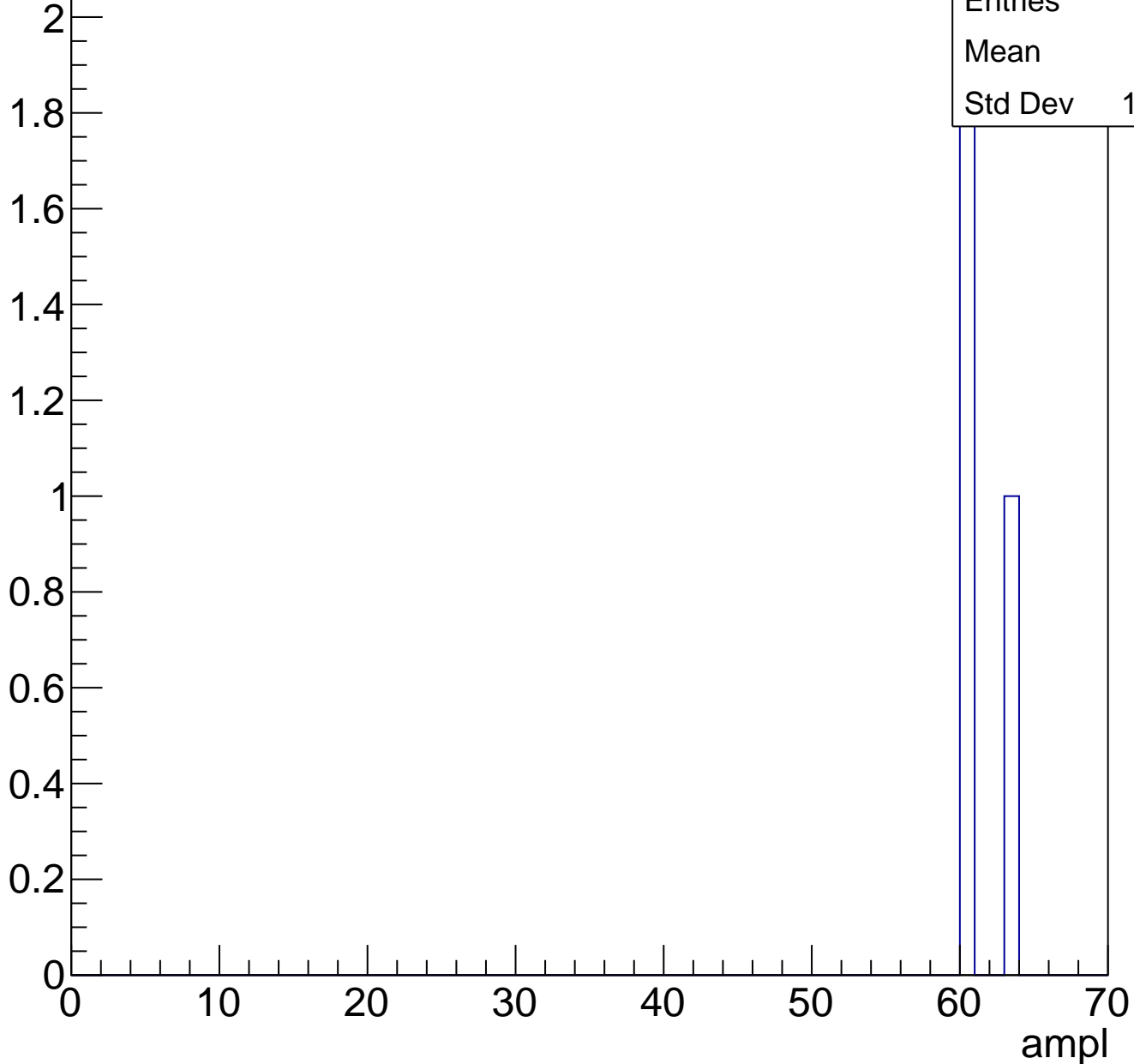
Entries	44
Mean	59.09
Std Dev	9.258



# B0L000S, U25-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	0
Std Dev	0

# B0L000S, U25-ch114, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	69
Mean	29.39
Std Dev	4.882

**Gaus mean : 30.0086**

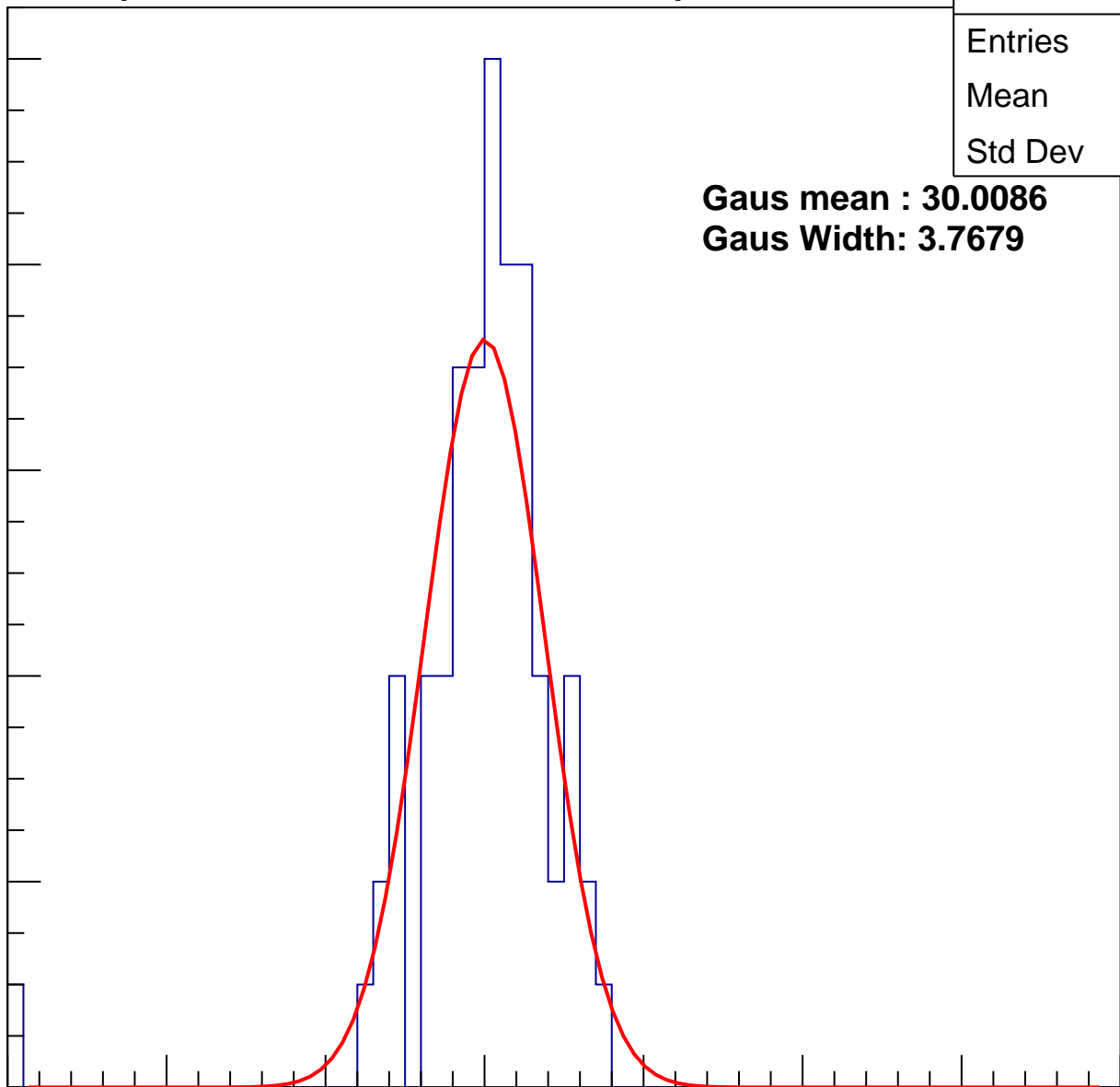
**Gaus Width: 3.7679**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch114, adc1

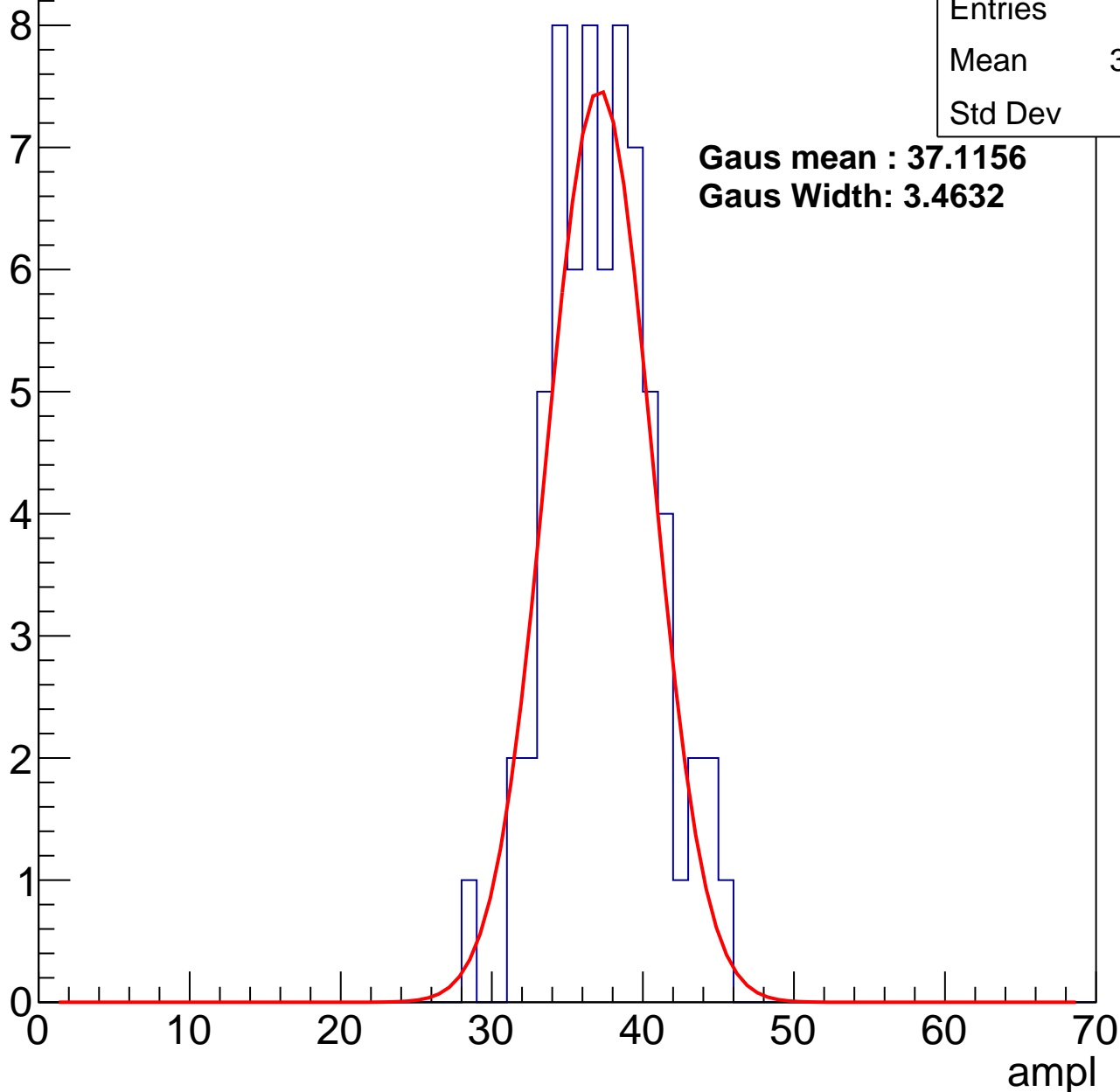
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	36.96
Std Dev	3.41

**Gaus mean : 37.1156**

**Gaus Width: 3.4632**



# B0L000S, U25-ch114, adc2

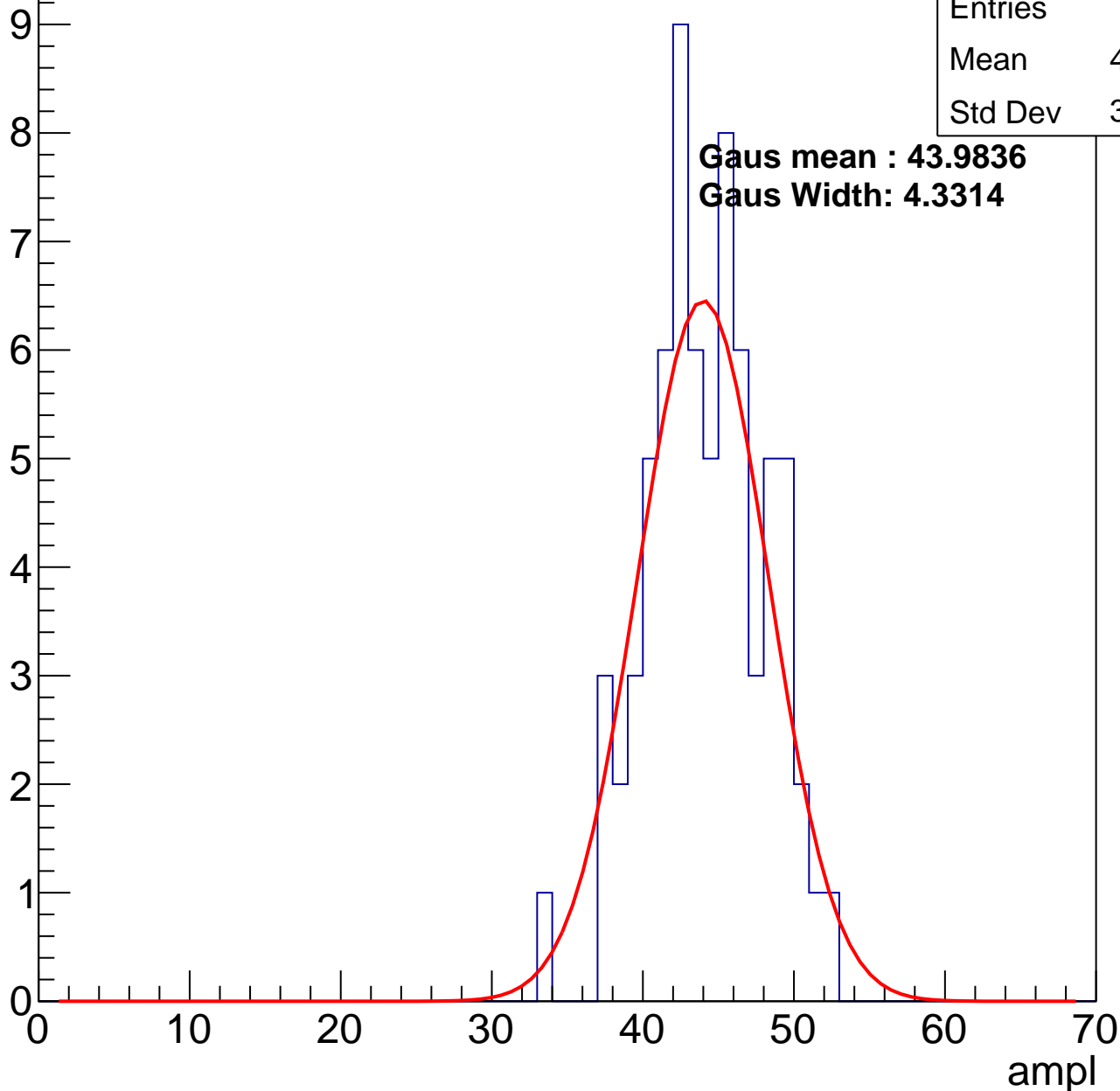
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	43.72
Std Dev	3.817

**Gaus mean : 43.9836**

**Gaus Width: 4.3314**

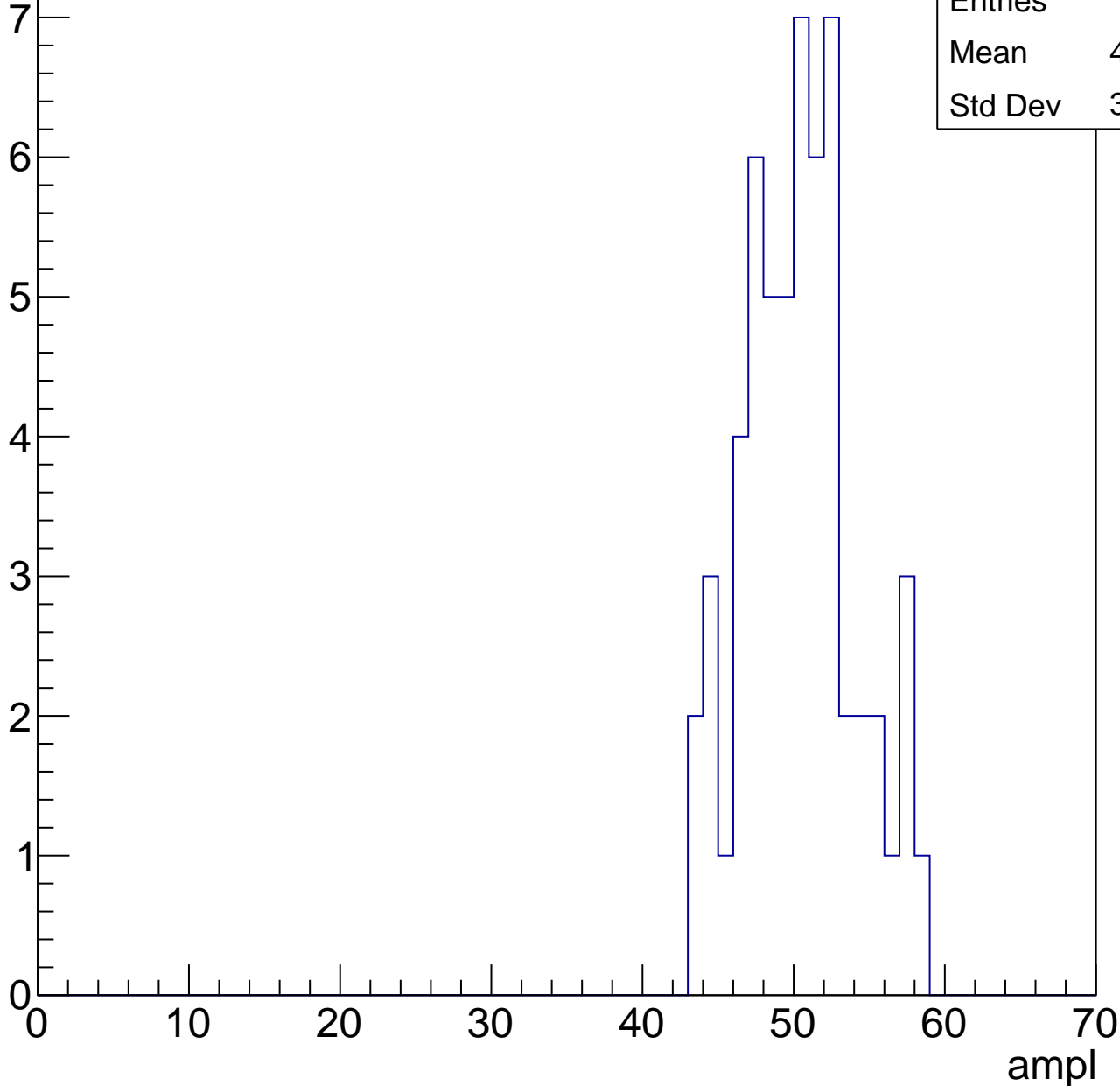


# B0L000S, U25-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	49.88
Std Dev	3.637

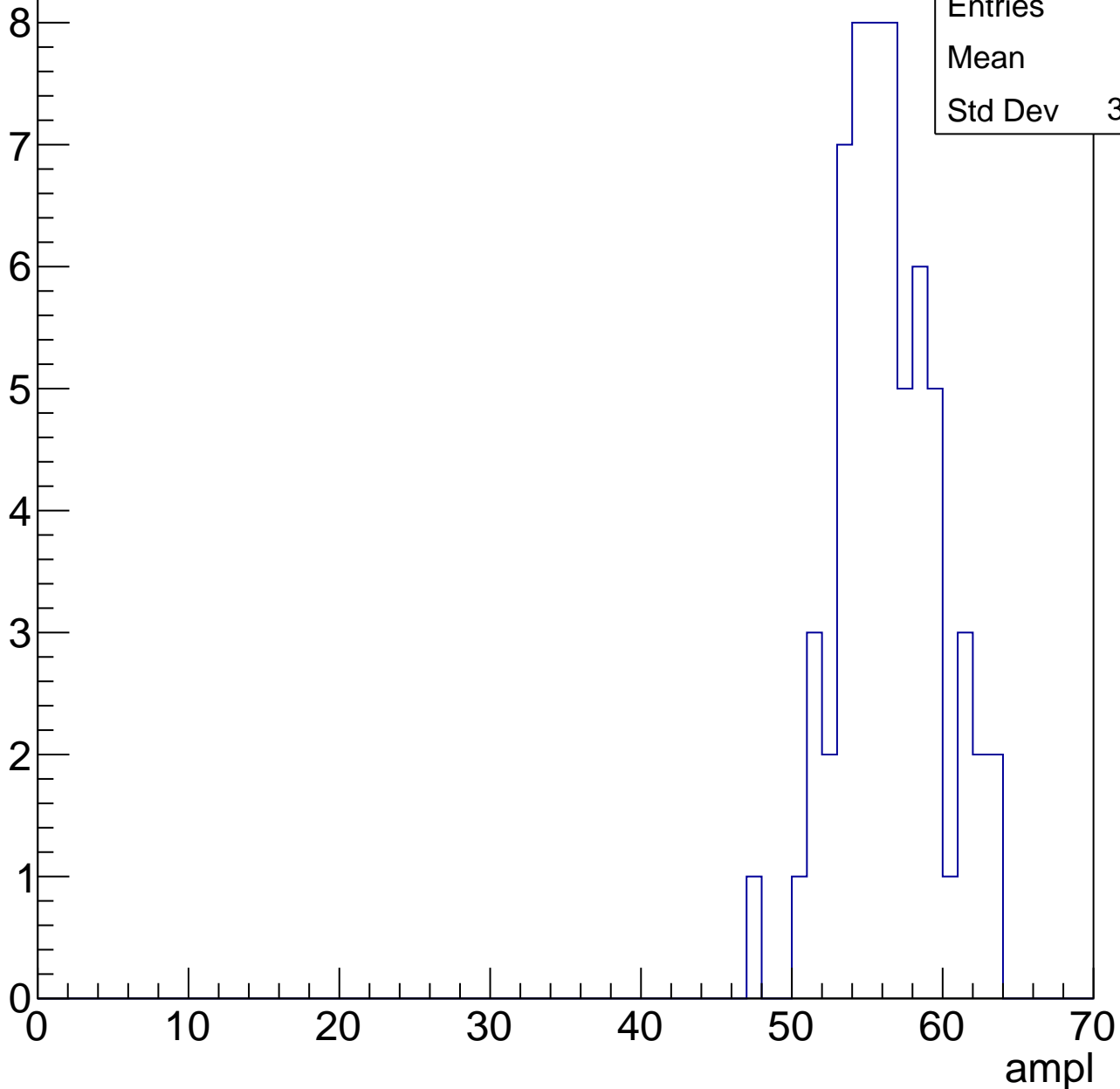


# B0L000S, U25-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	55.9
Std Dev	3.266

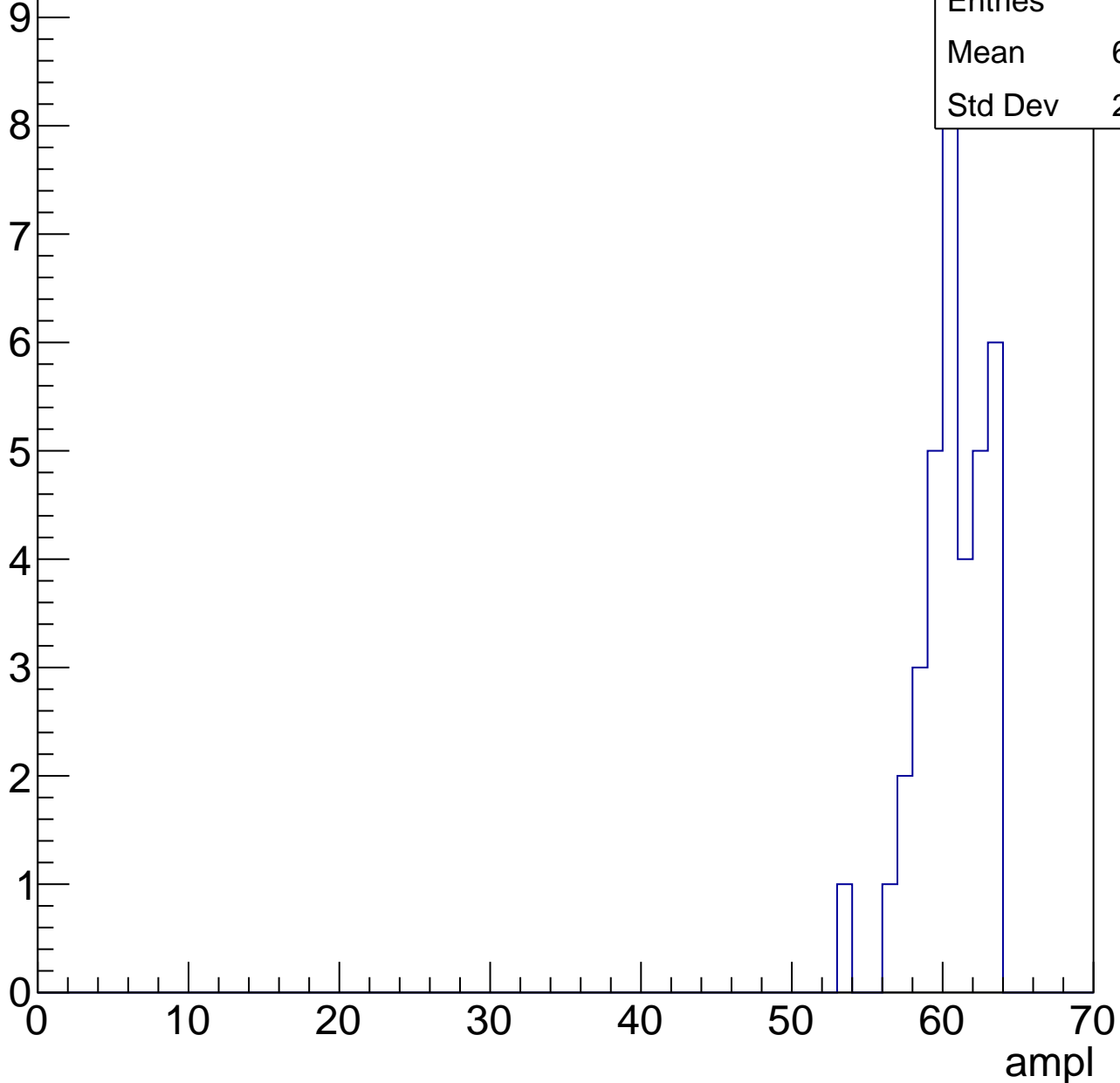


# B0L000S, U25-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	36
Mean	60.11
Std Dev	2.221



# B0L000S, U25-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

3

2.5

2

1.5

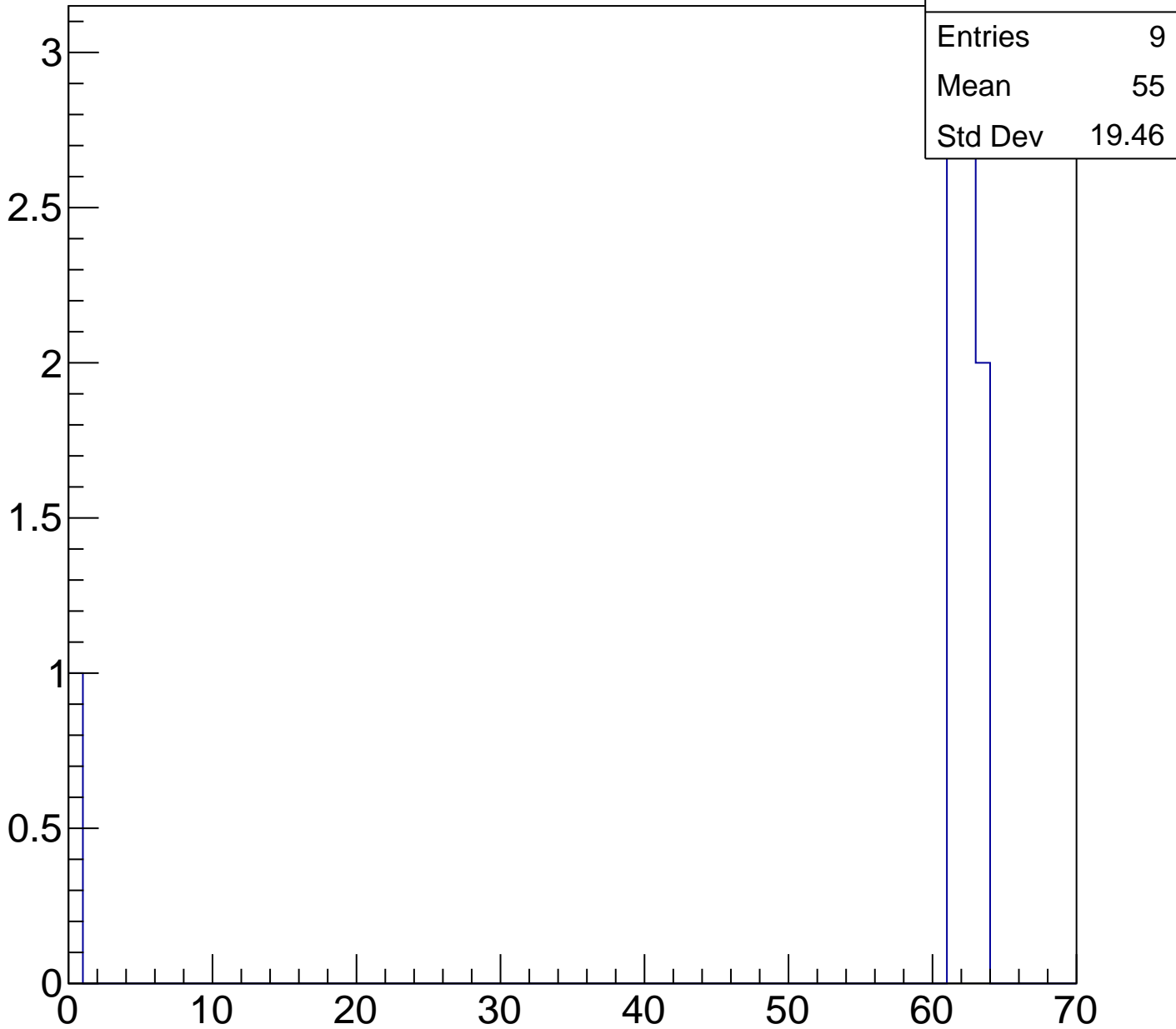
1

0.5

0

ampl

Entries	9
Mean	55
Std Dev	19.46





# B0L000S, U25-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch115, adc0

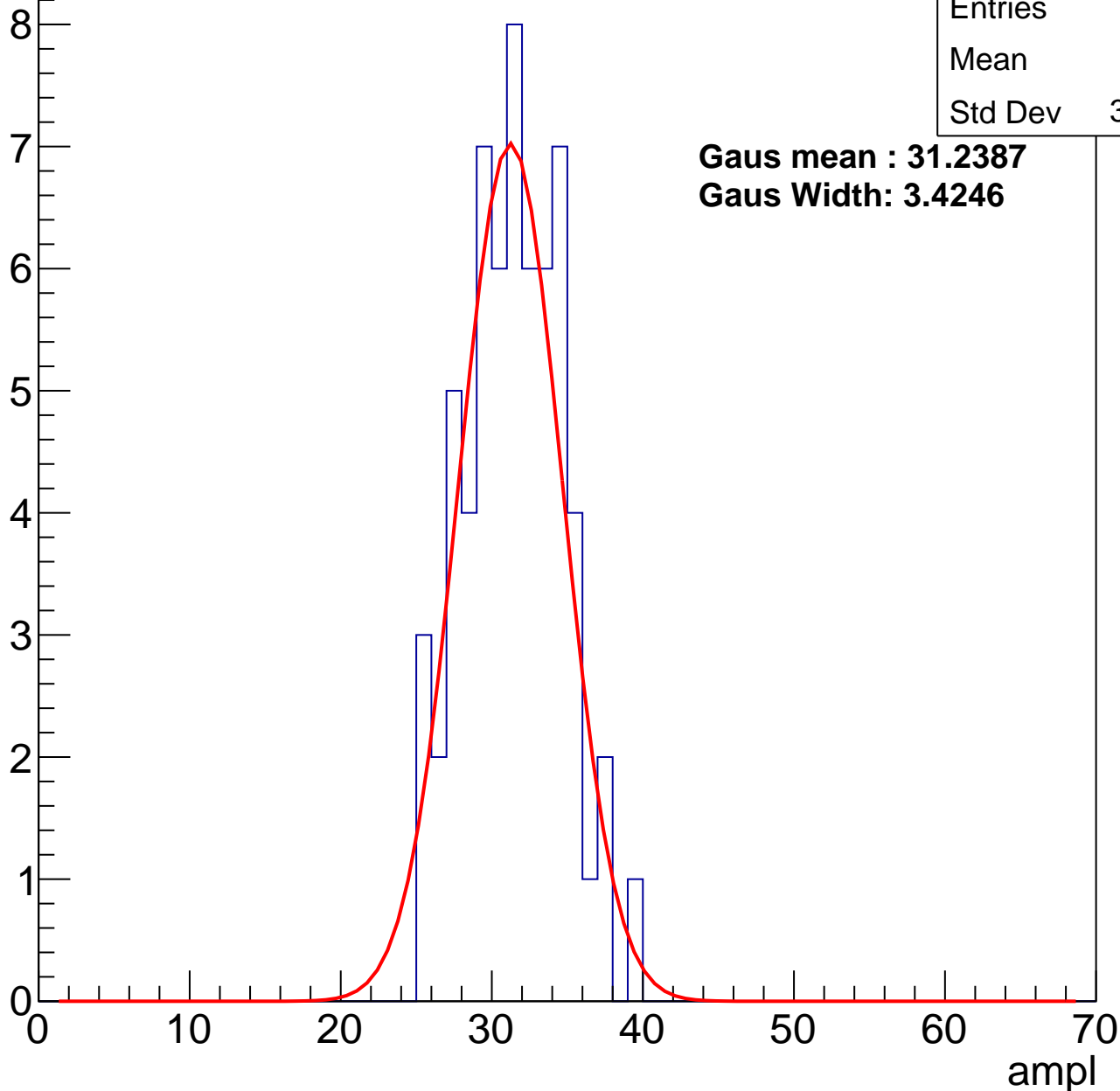
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	31
Std Dev	3.178

**Gaus mean : 31.2387**

**Gaus Width: 3.4246**



# B0L000S, U25-ch115, adc1

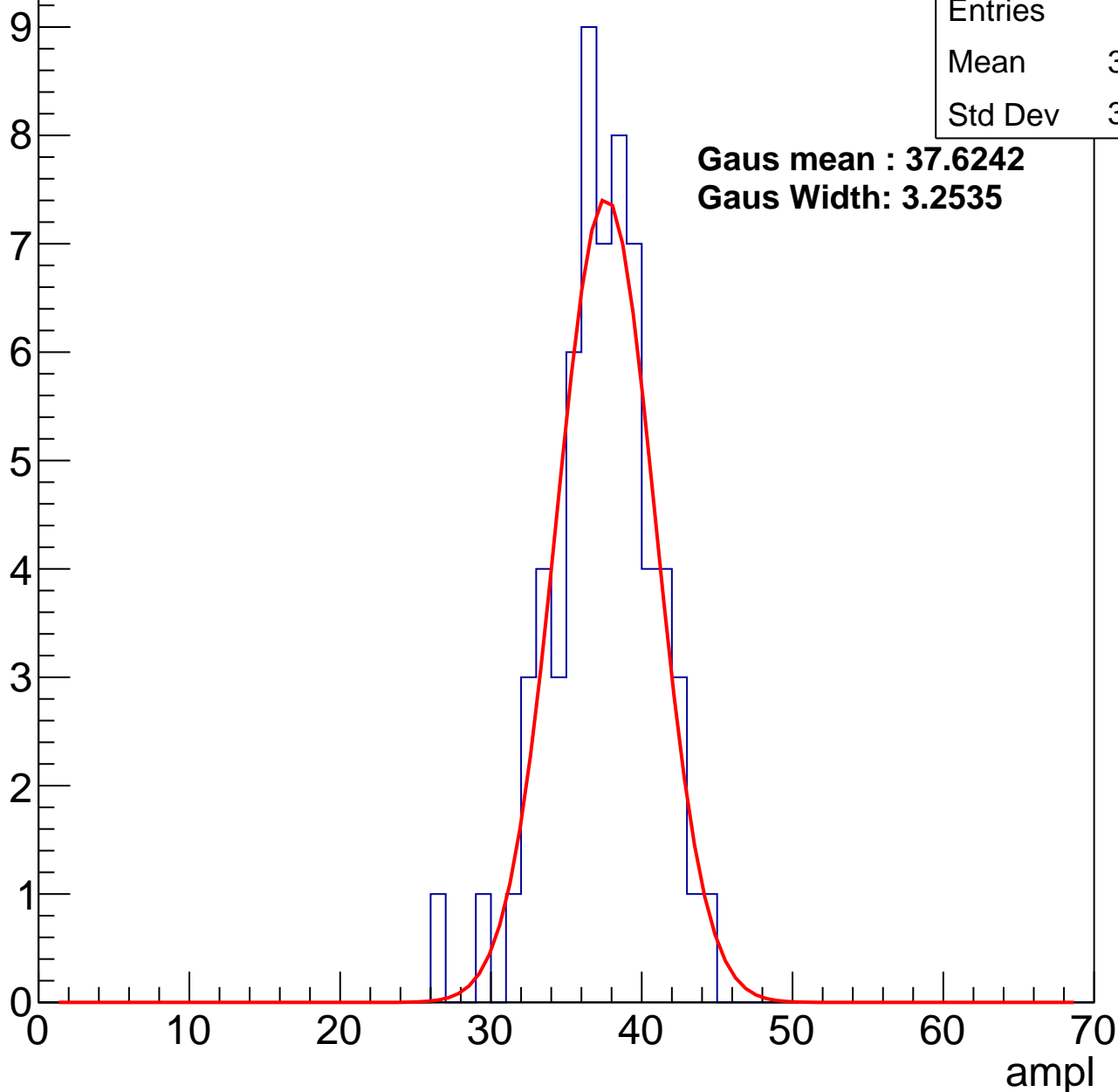
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	36.87
Std Dev	3.369

**Gaus mean : 37.6242**

**Gaus Width: 3.2535**



# B0L000S, U25-ch115, adc2

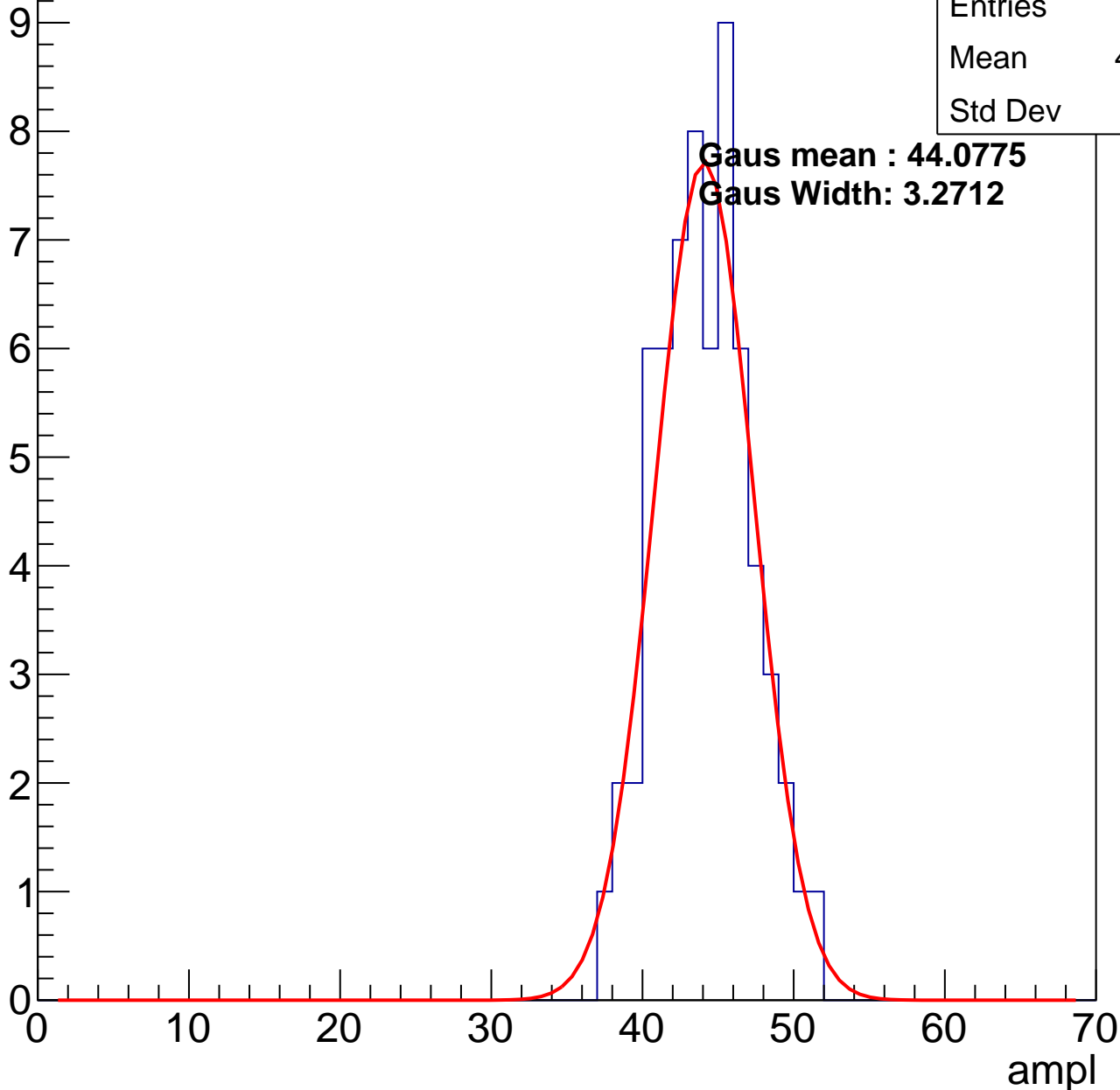
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	64
Mean	43.61
Std Dev	3.06

**Gaus mean : 44.0775**

**Gaus Width: 3.2712**

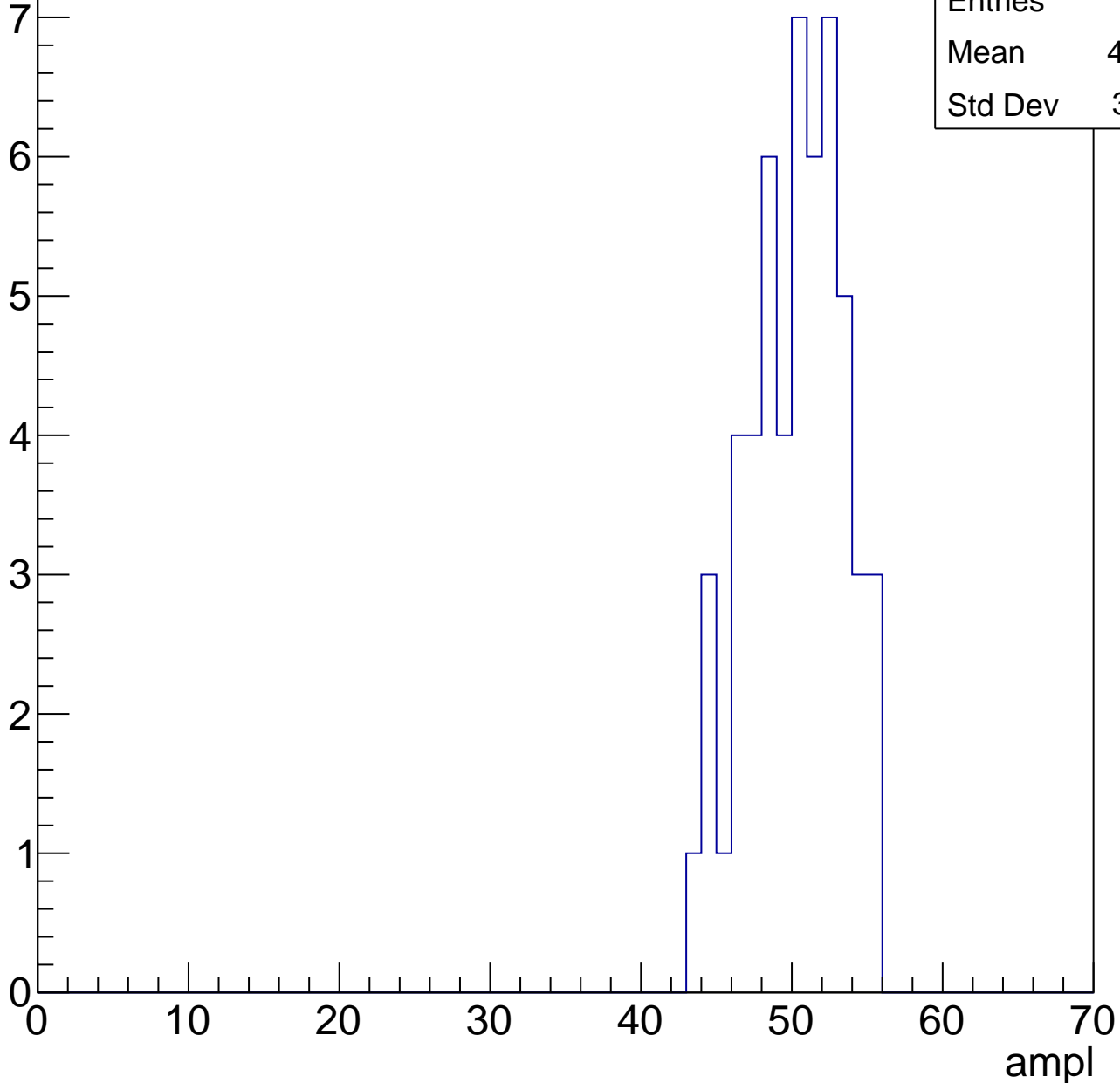


# B0L000S, U25-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	54
Mean	49.78
Std Dev	3.071



# B0L000S, U25-ch115, adc4

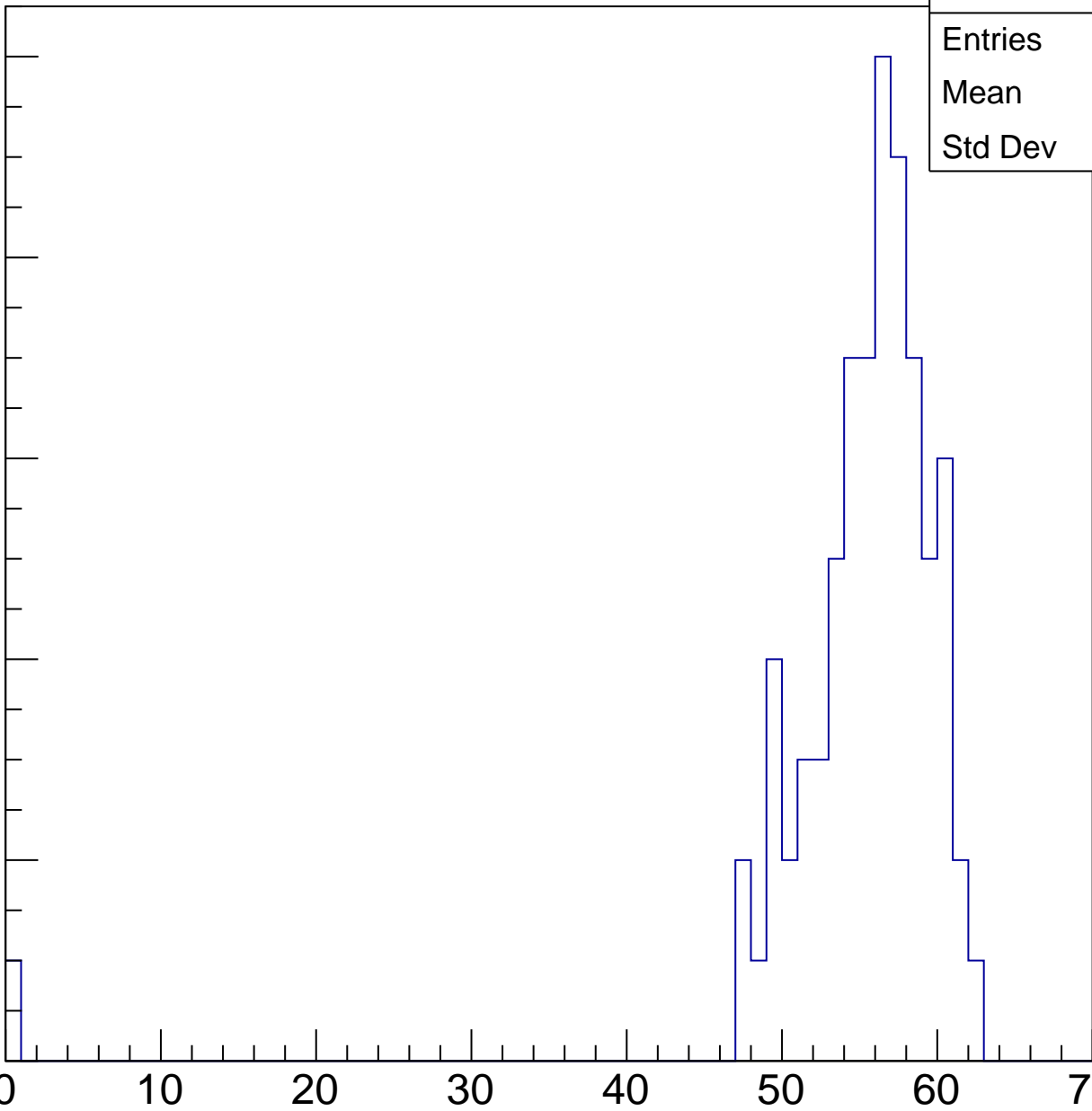
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	54.57
Std Dev	7.252

Entry

10  
8  
6  
4  
2  
0

ampl



# B0L000S, U25-ch115, adc5

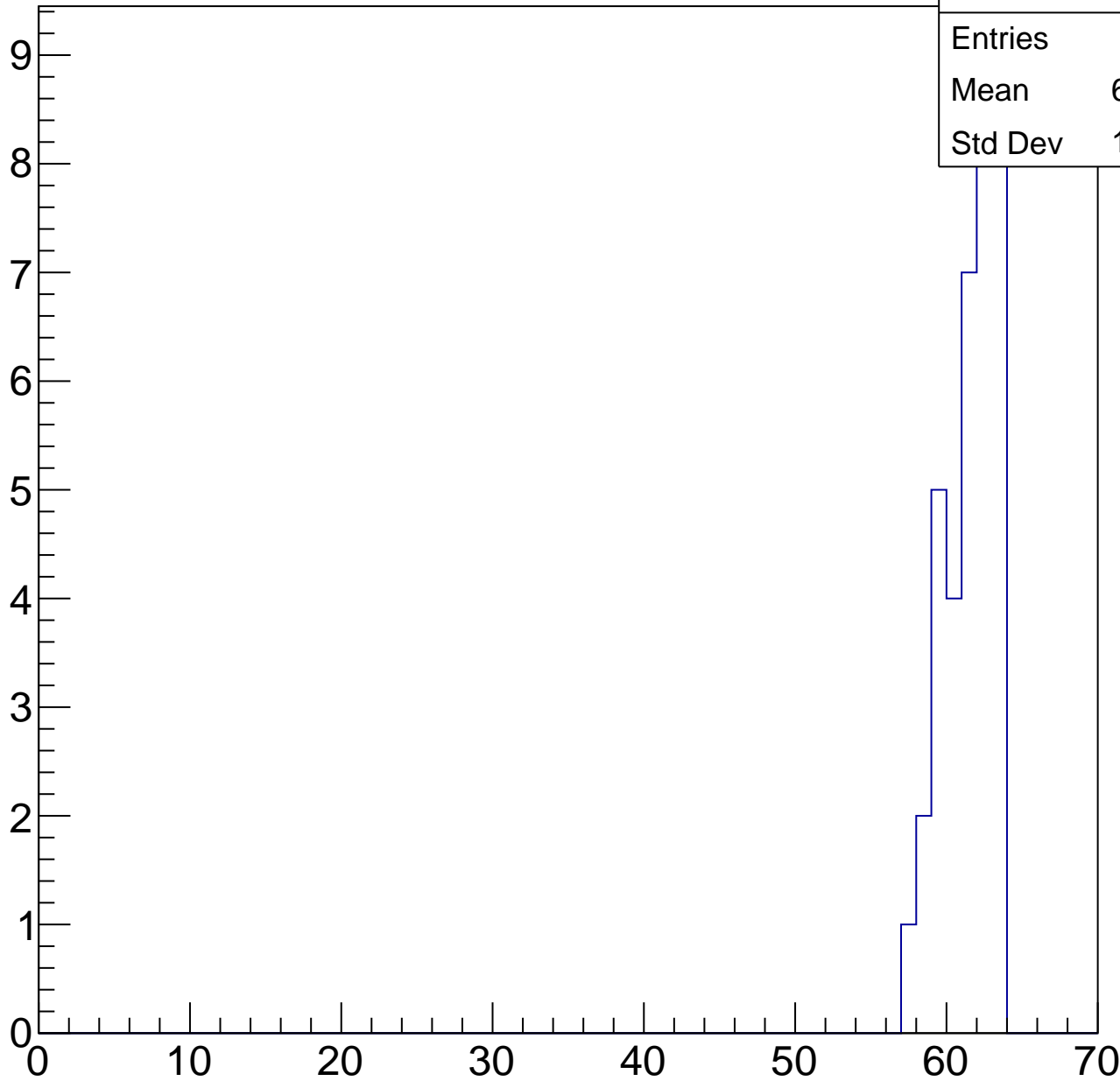
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	36
Mean	61.03
Std Dev	1.658

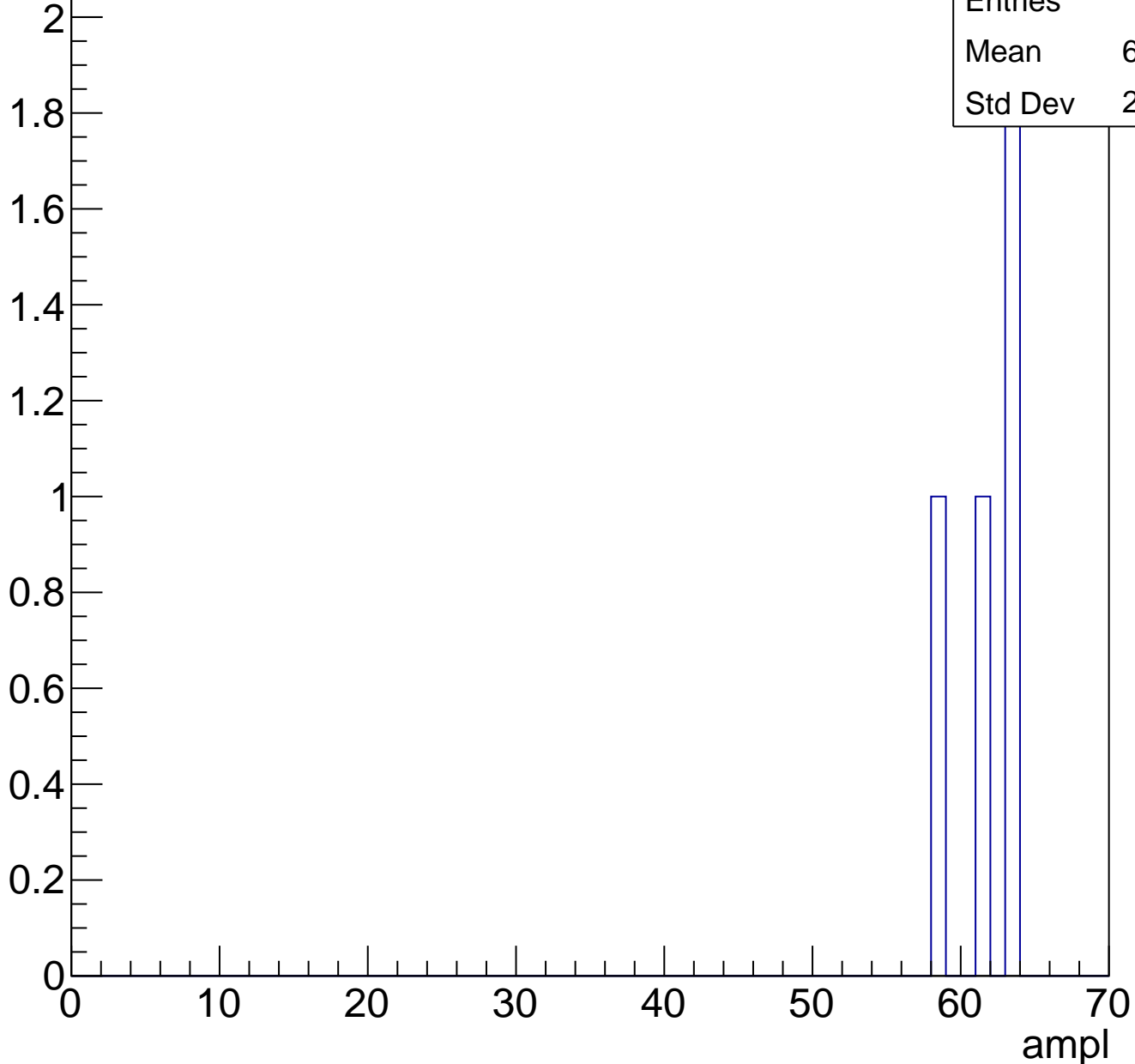
ampl



# B0L000S, U25-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch116, adc0

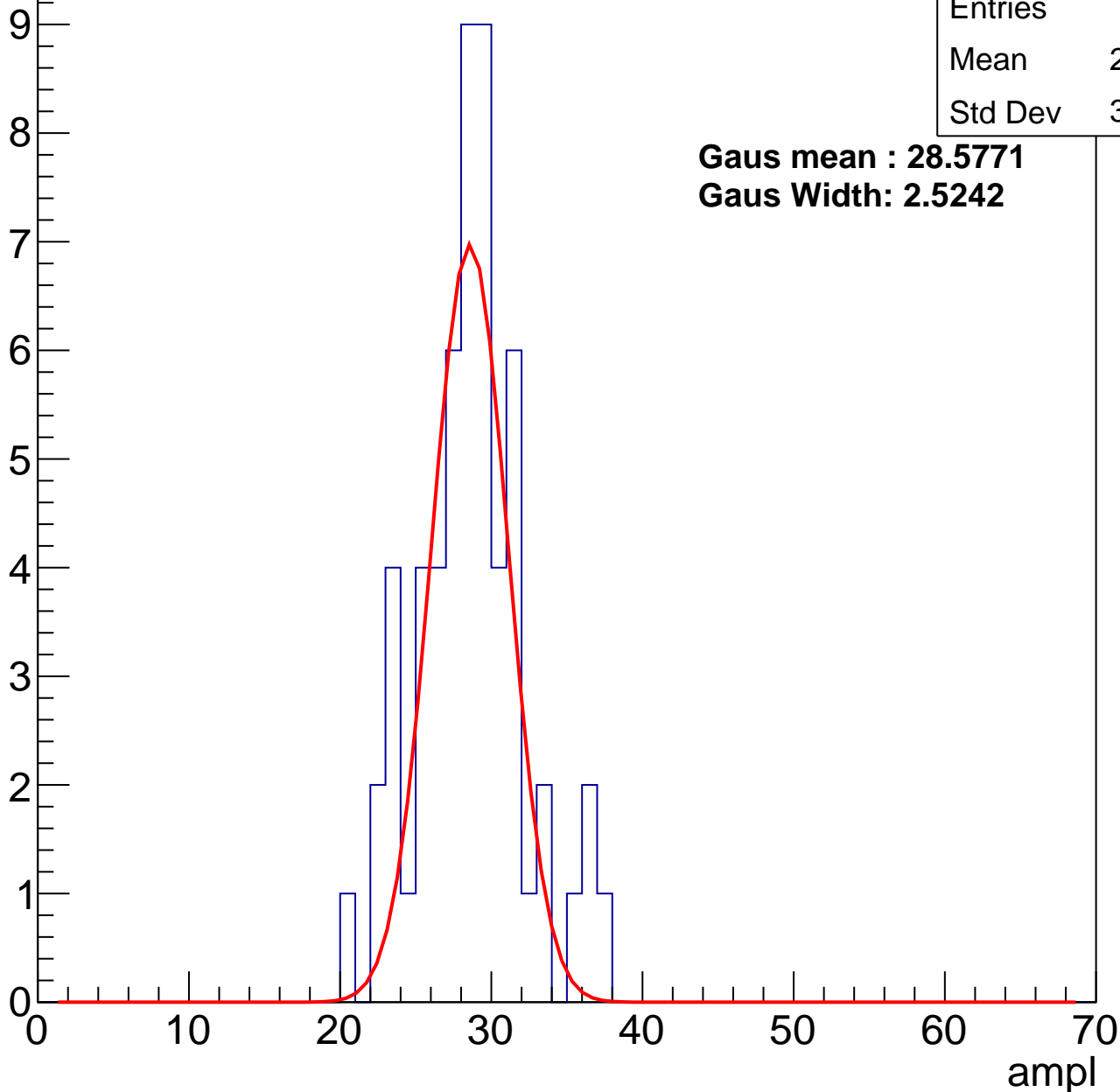
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	28.19
Std Dev	3.532

**Gaus mean : 28.5771**

**Gaus Width: 2.5242**



# B0L000S, U25-ch116, adc1

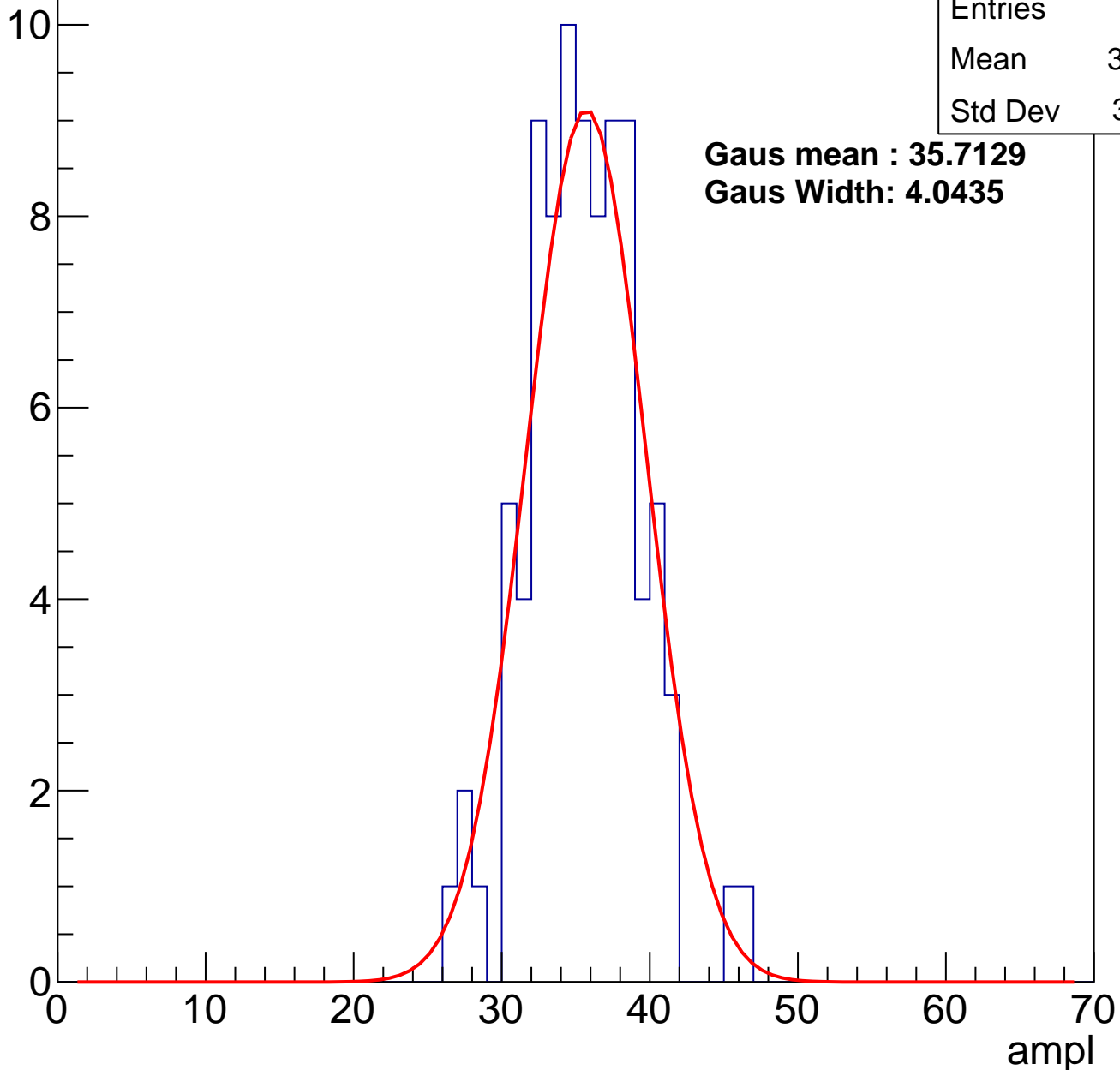
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	89
Mean	35.08
Std Dev	3.691

**Gaus mean : 35.7129**

**Gaus Width: 4.0435**

Entry



# B0L000S, U25-ch116, adc2

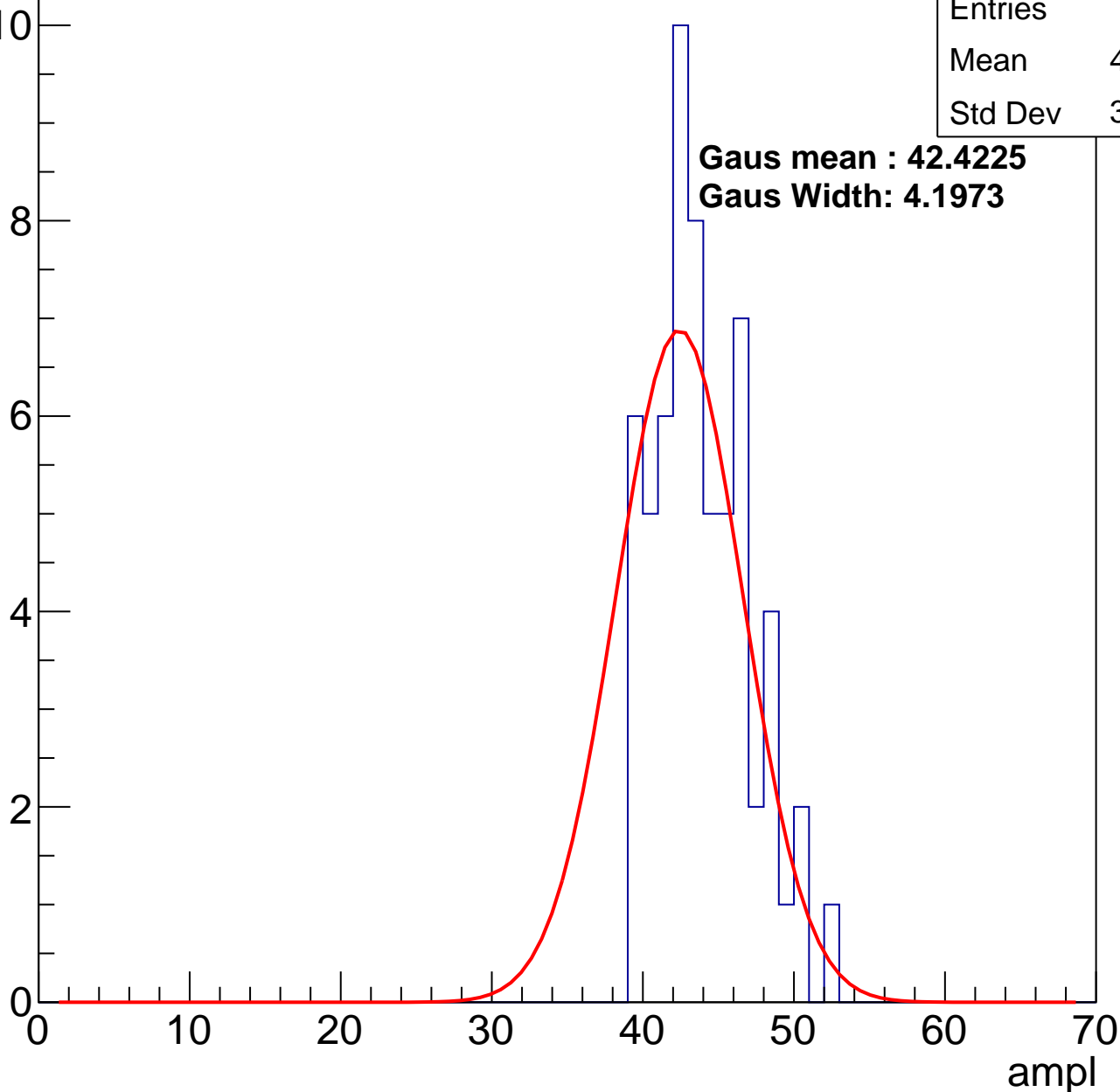
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	43.52
Std Dev	3.094

**Gaus mean : 42.4225**

**Gaus Width: 4.1973**

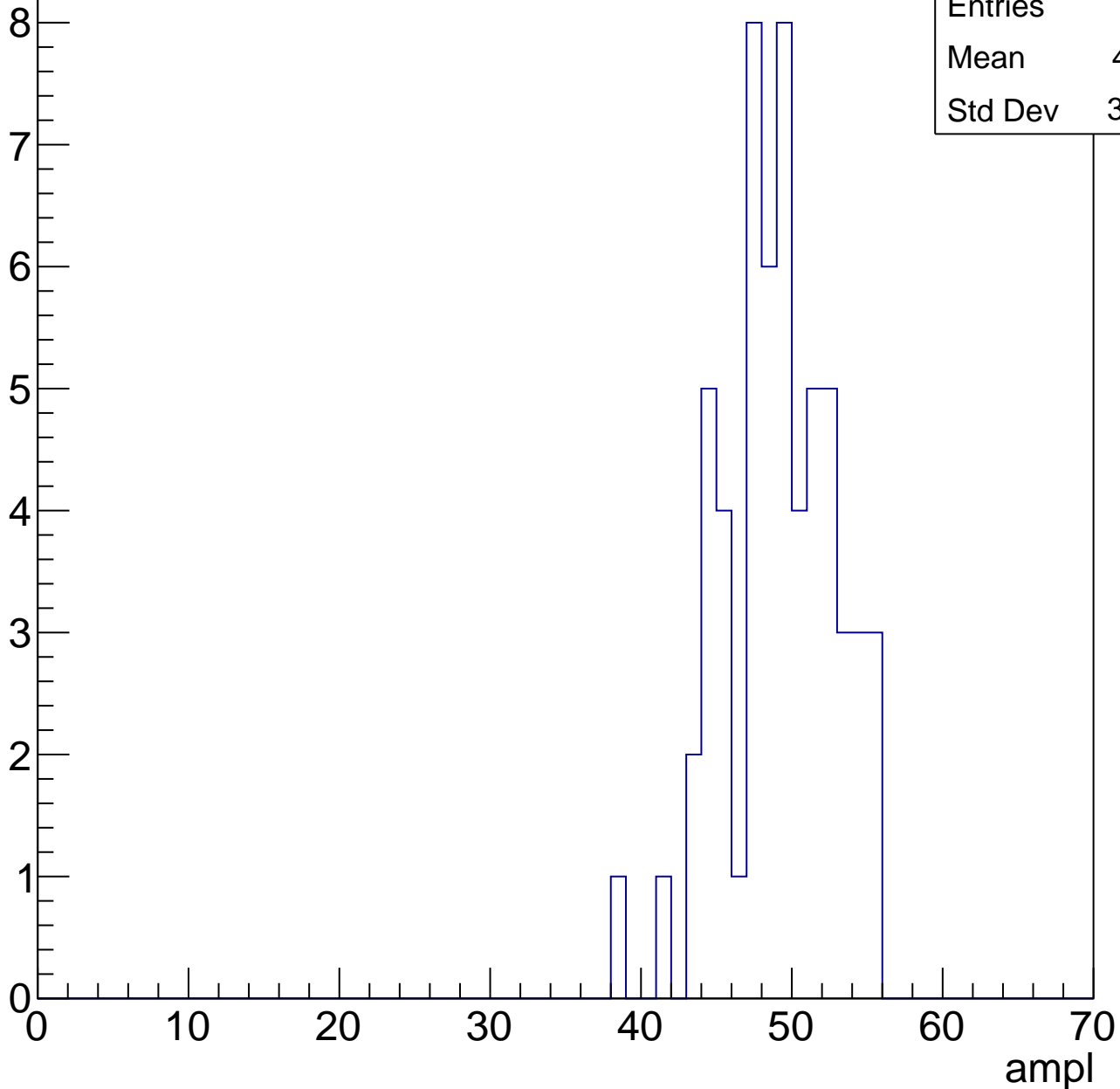


# B0L000S, U25-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	59
Mean	48.61
Std Dev	3.645

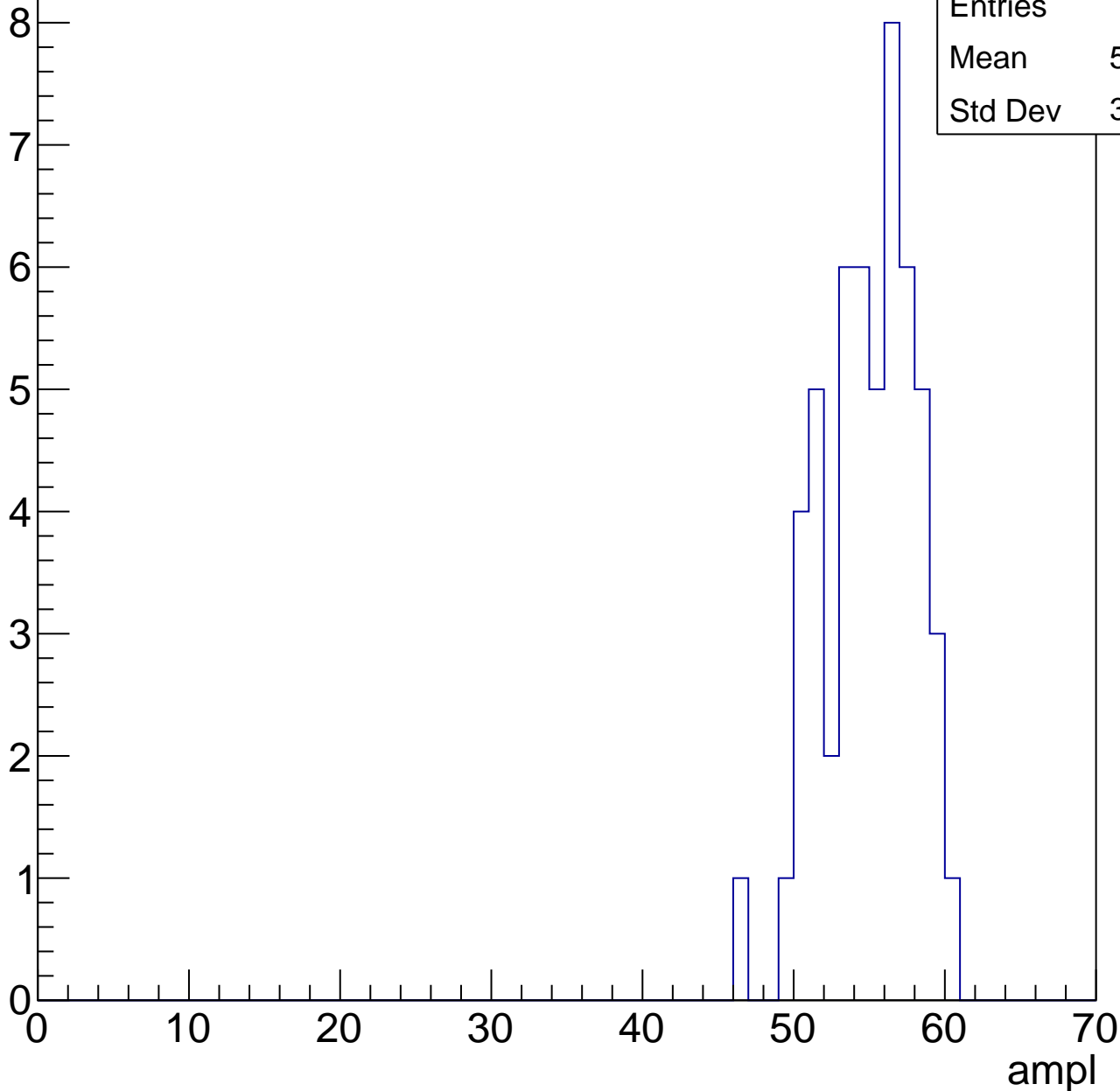


# B0L000S, U25-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

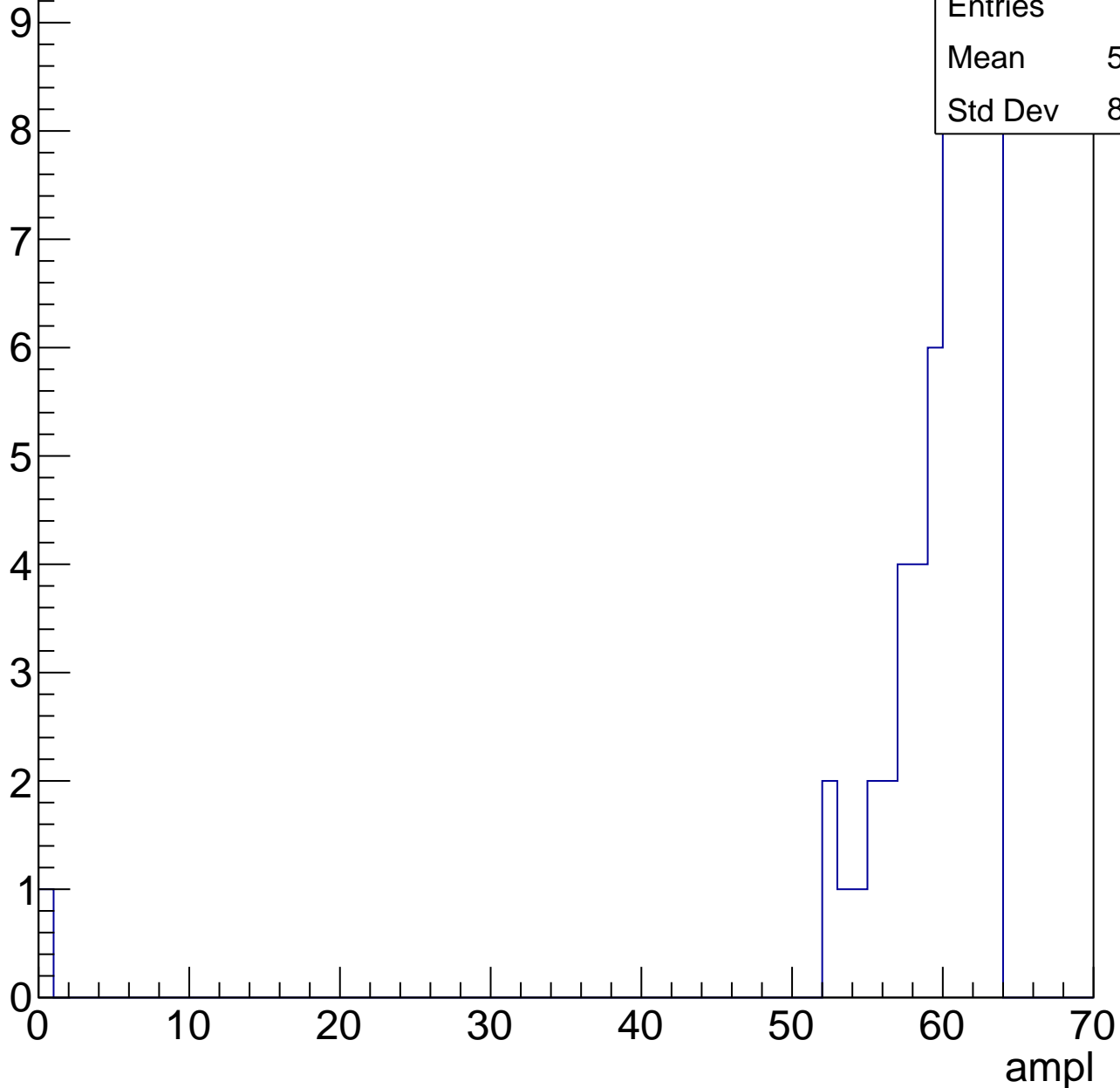
Entries	53
Mean	54.49
Std Dev	3.013



# B0L000S, U25-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

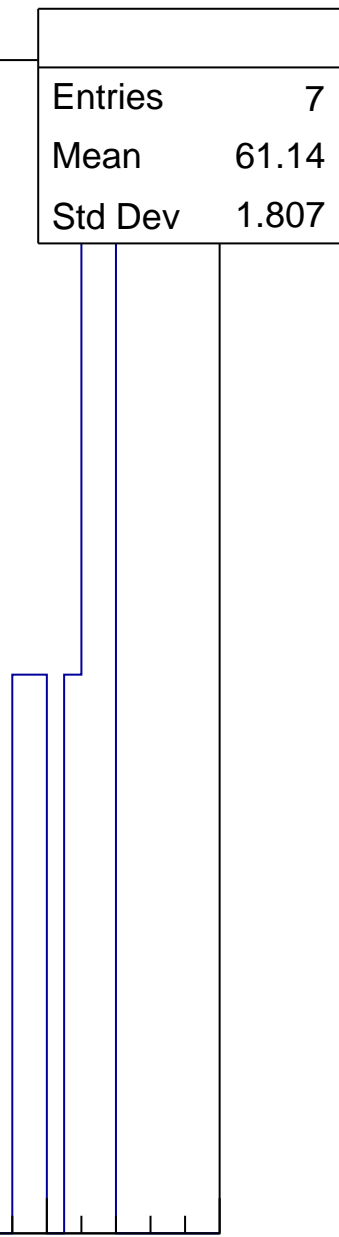
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	61.14
Std Dev	1.807

ampl

0 10 20 30 40 50 60 70





# B0L000S, U25-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	1
Mean	0
Std Dev	0

# B0L000S, U25-ch117, adc0

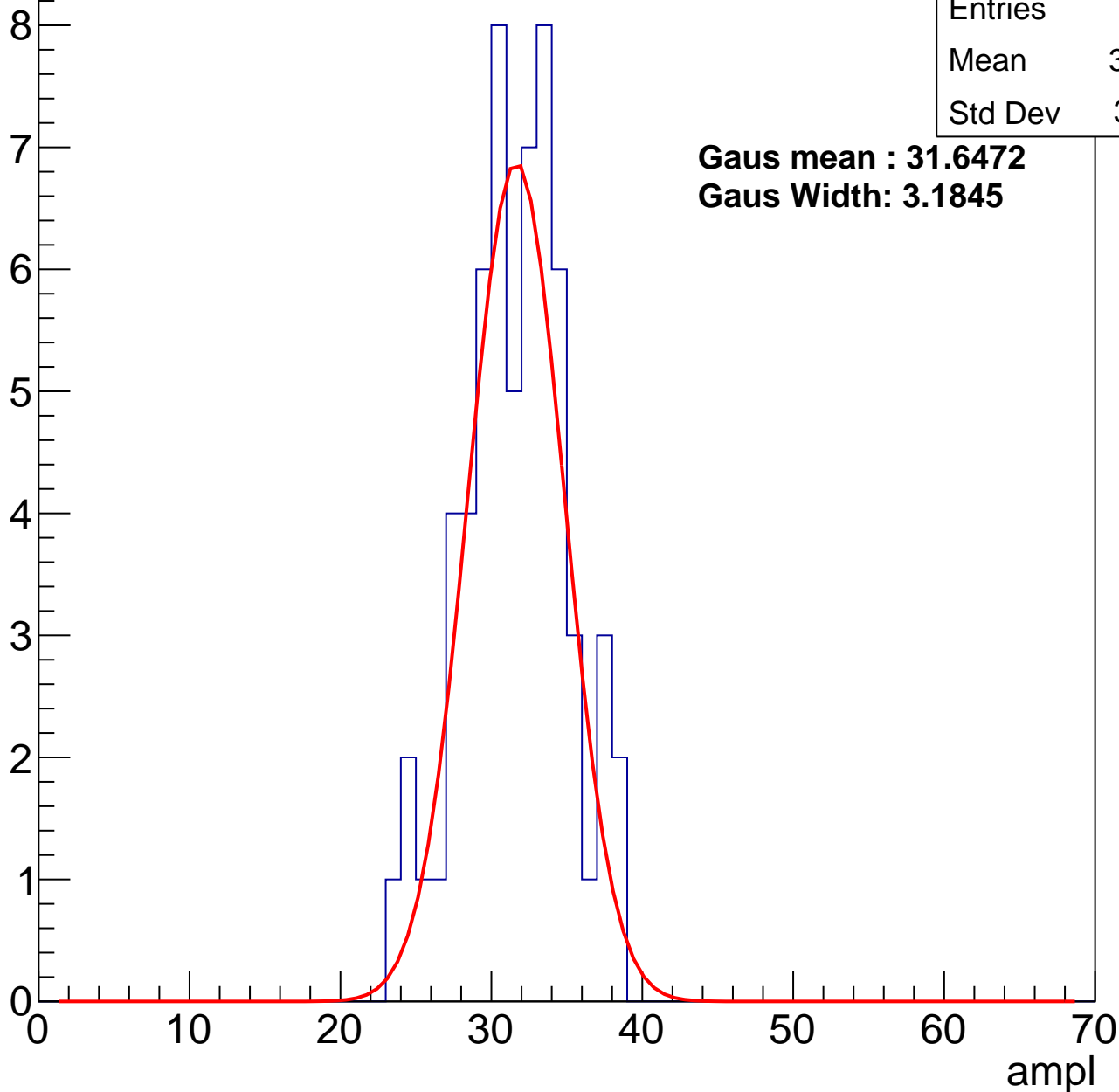
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	31.15
Std Dev	3.421

**Gaus mean : 31.6472**

**Gaus Width: 3.1845**



# B0L000S, U25-ch117, adc1

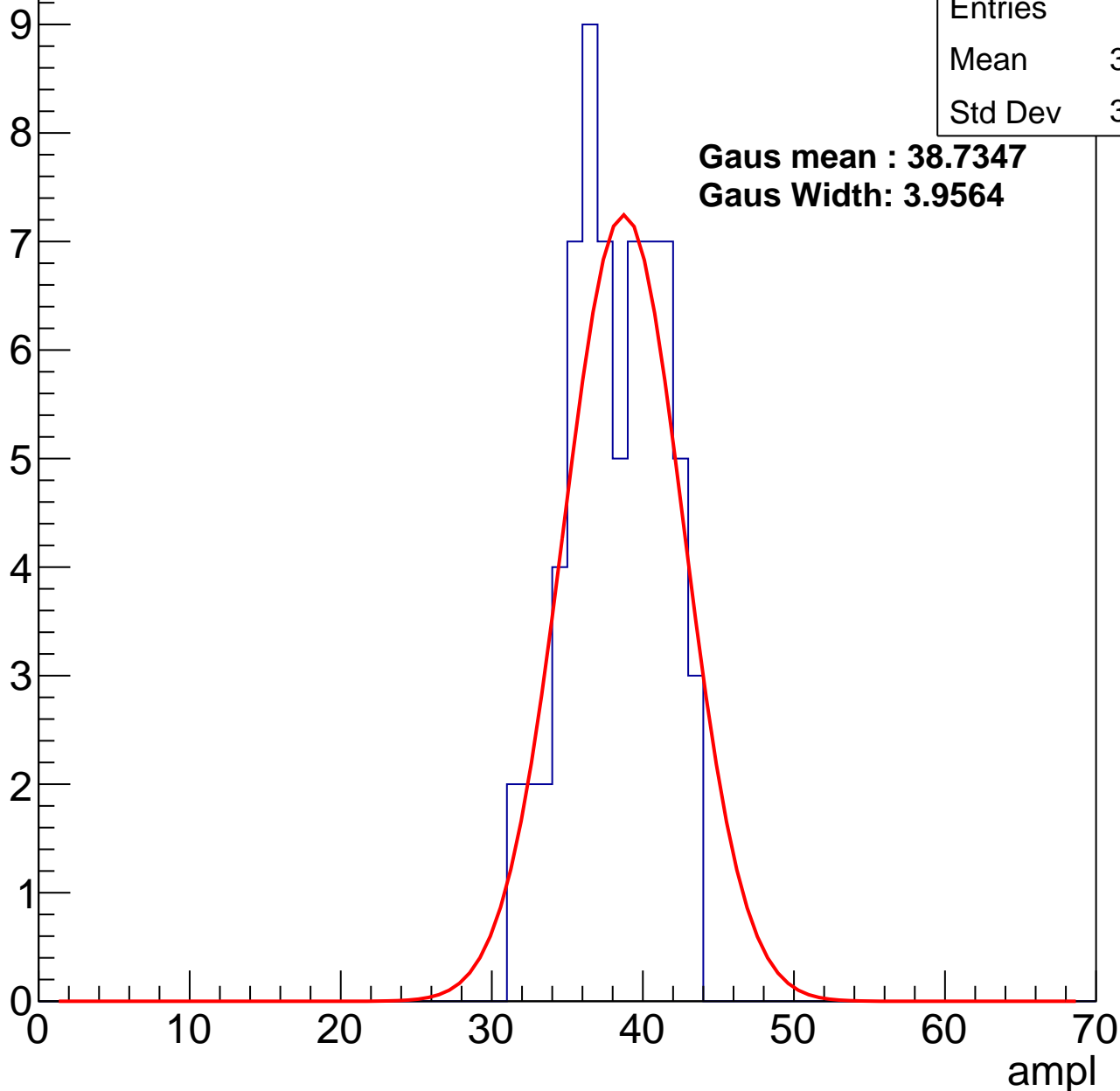
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	67
Mean	37.69
Std Dev	3.082

**Gaus mean : 38.7347**

**Gaus Width: 3.9564**



# B0L000S, U25-ch117, adc2

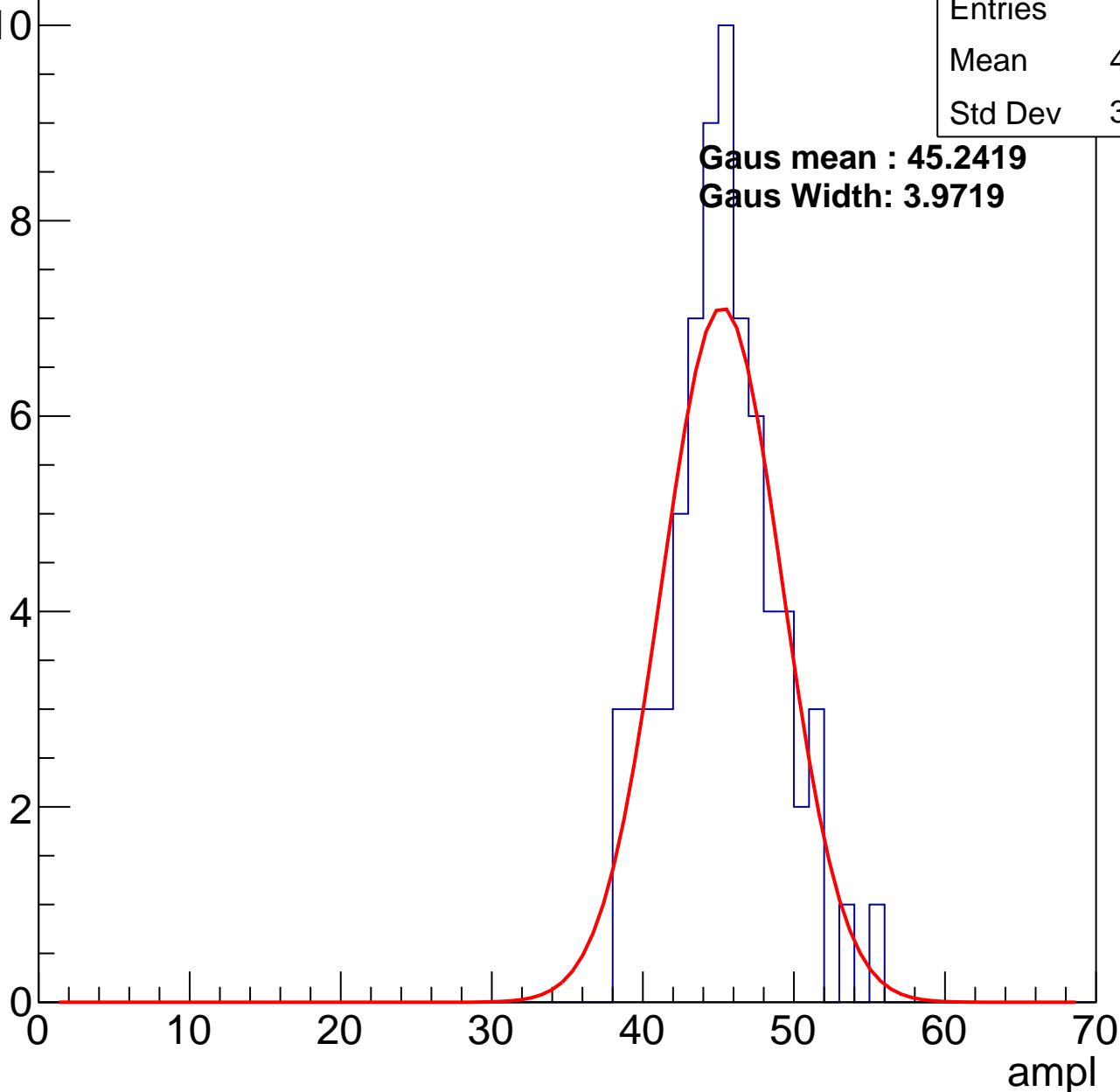
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	44.85
Std Dev	3.583

**Gaus mean : 45.2419**

**Gaus Width: 3.9719**

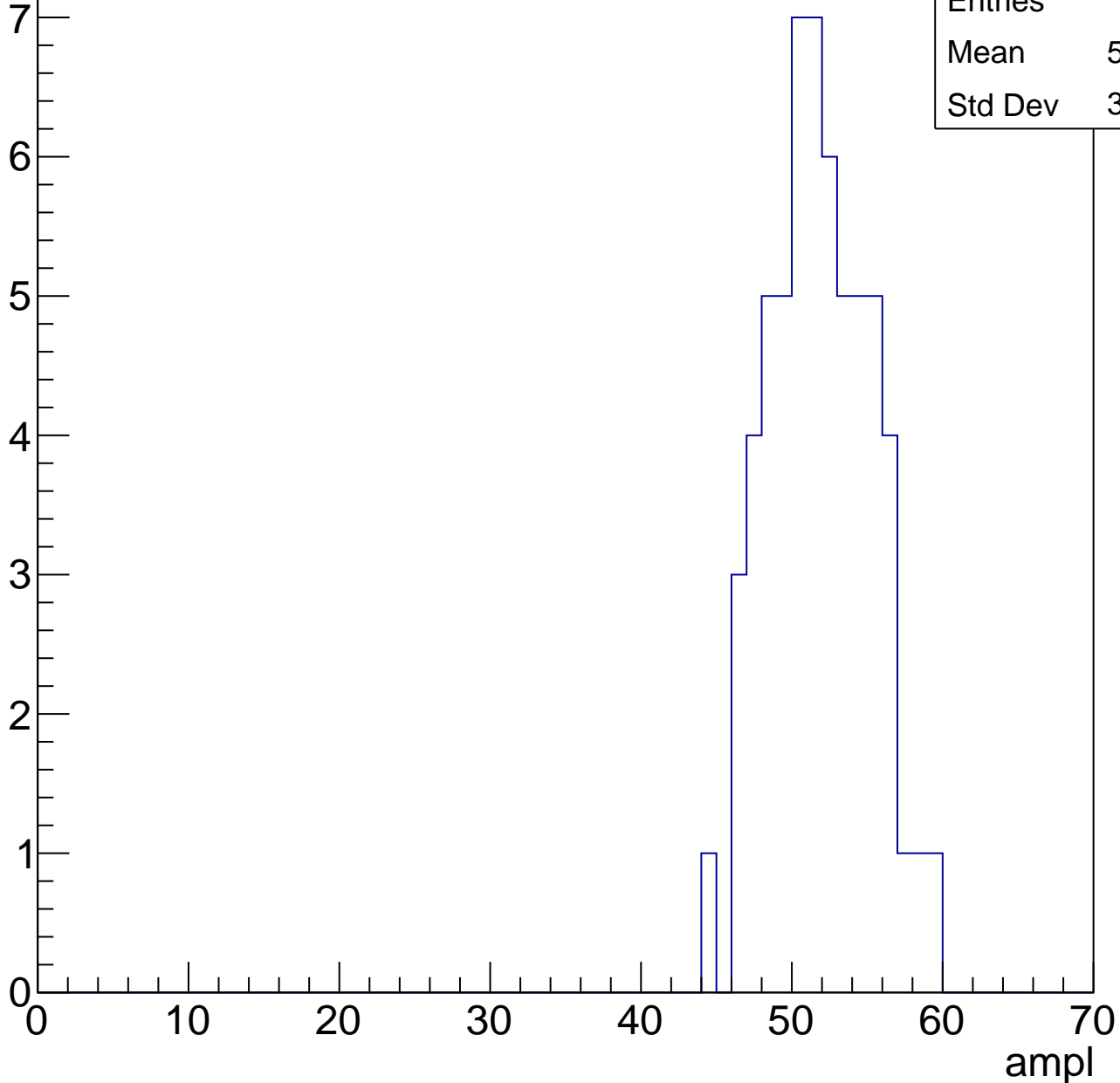


# B0L000S, U25-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	51.37
Std Dev	3.296

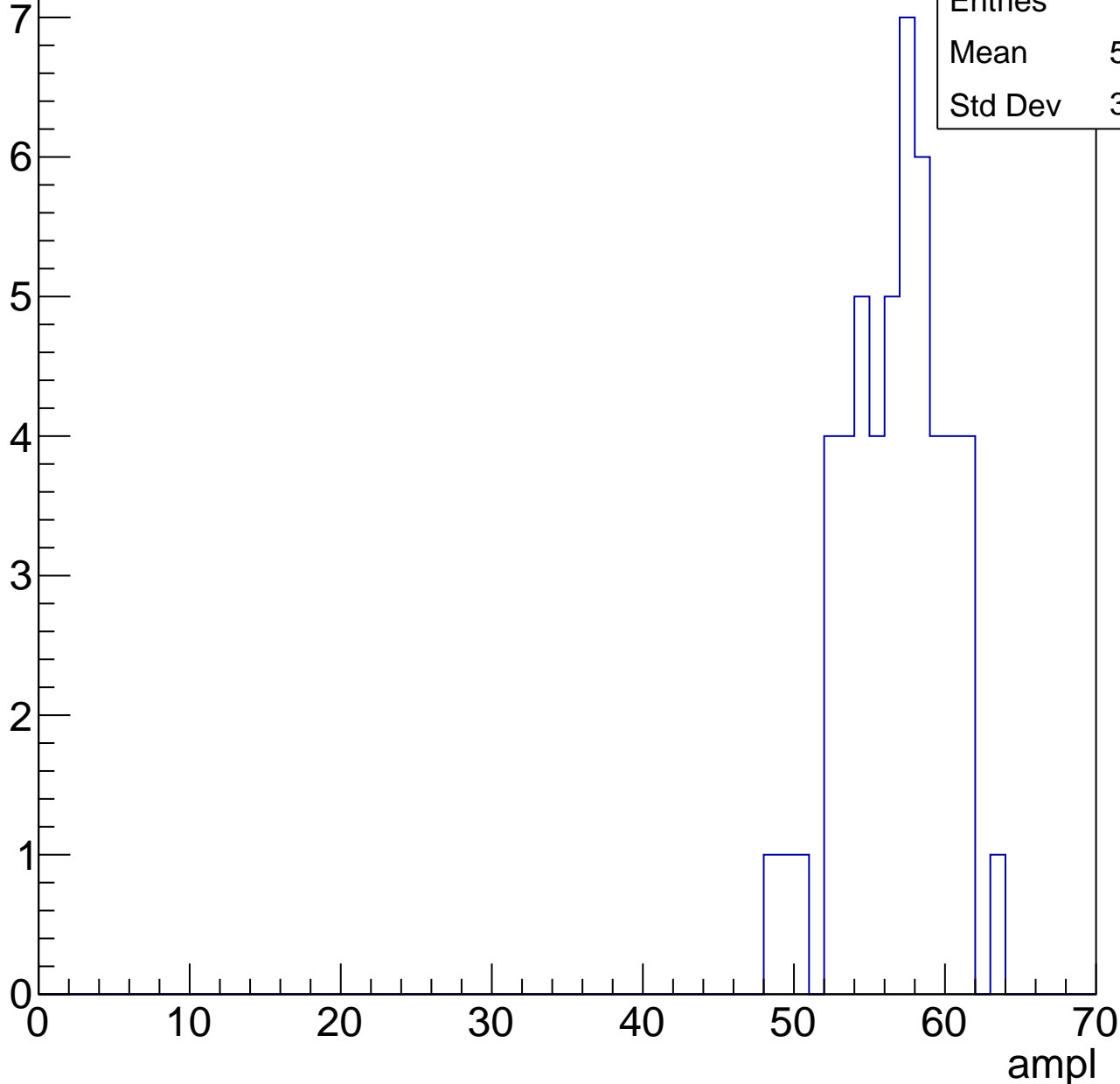


# B0L000S, U25-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	56.22
Std Dev	3.286

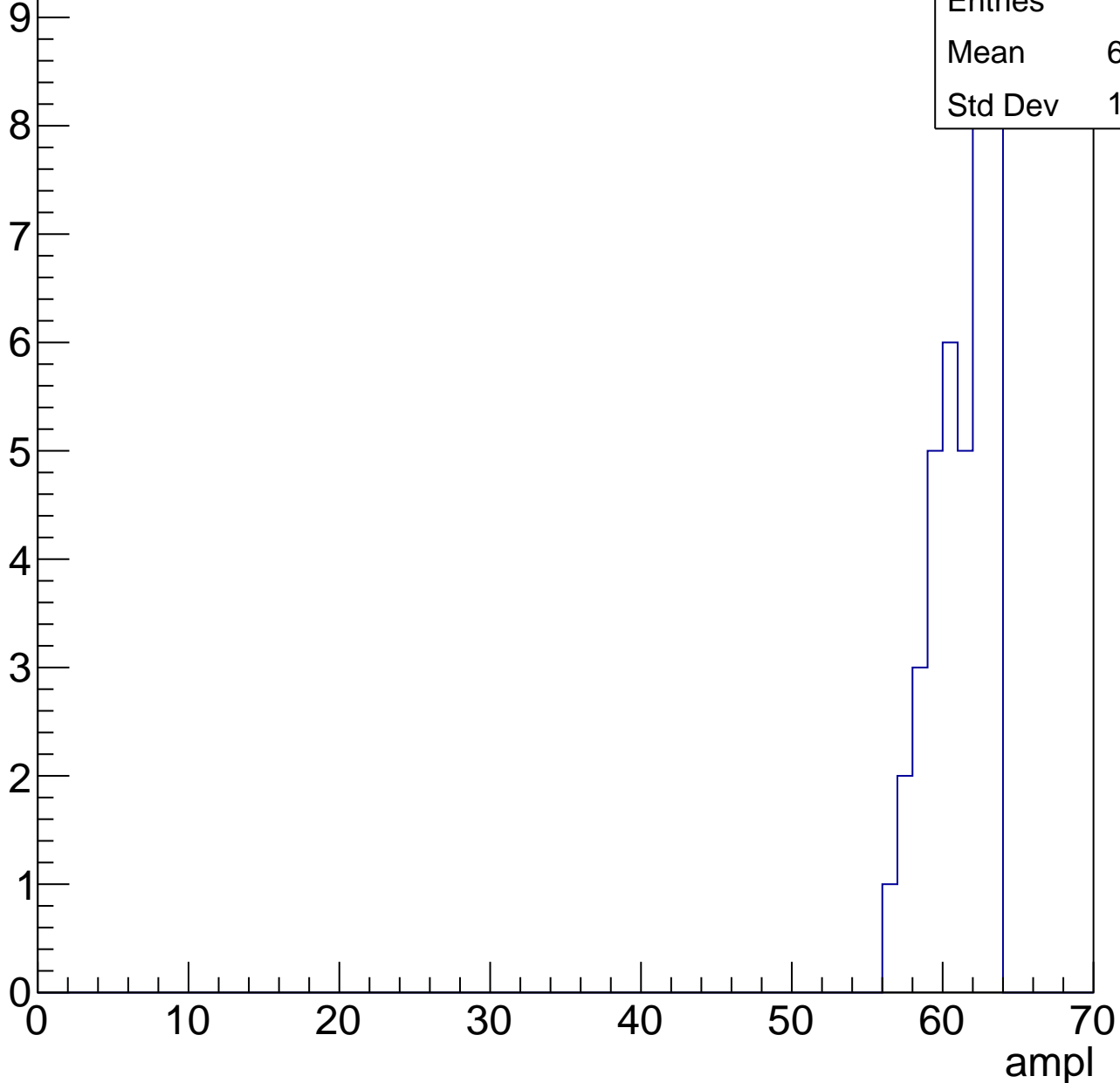


# B0L000S, U25-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

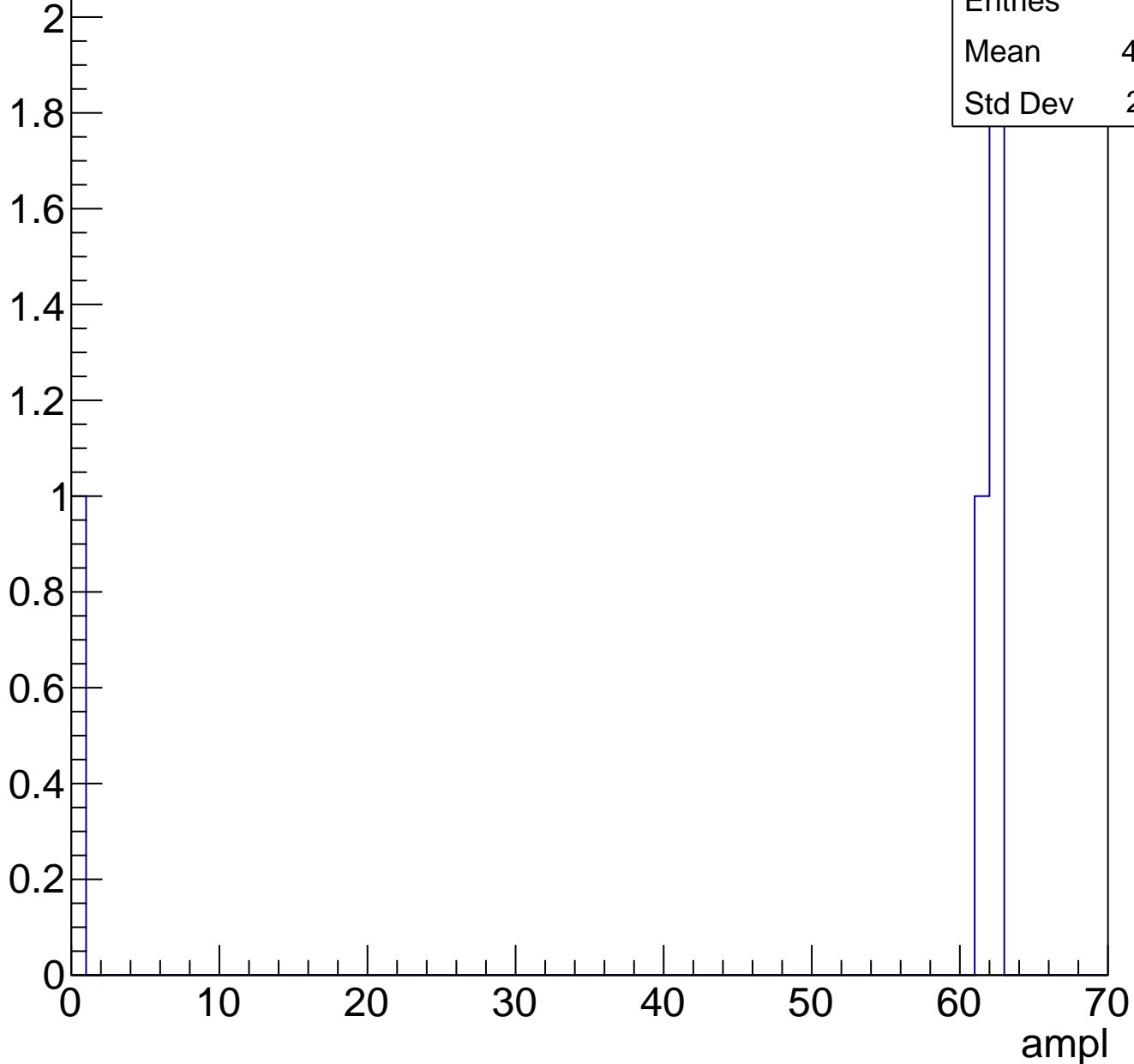
Entries	39
Mean	60.69
Std Dev	1.963



# B0L000S, U25-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

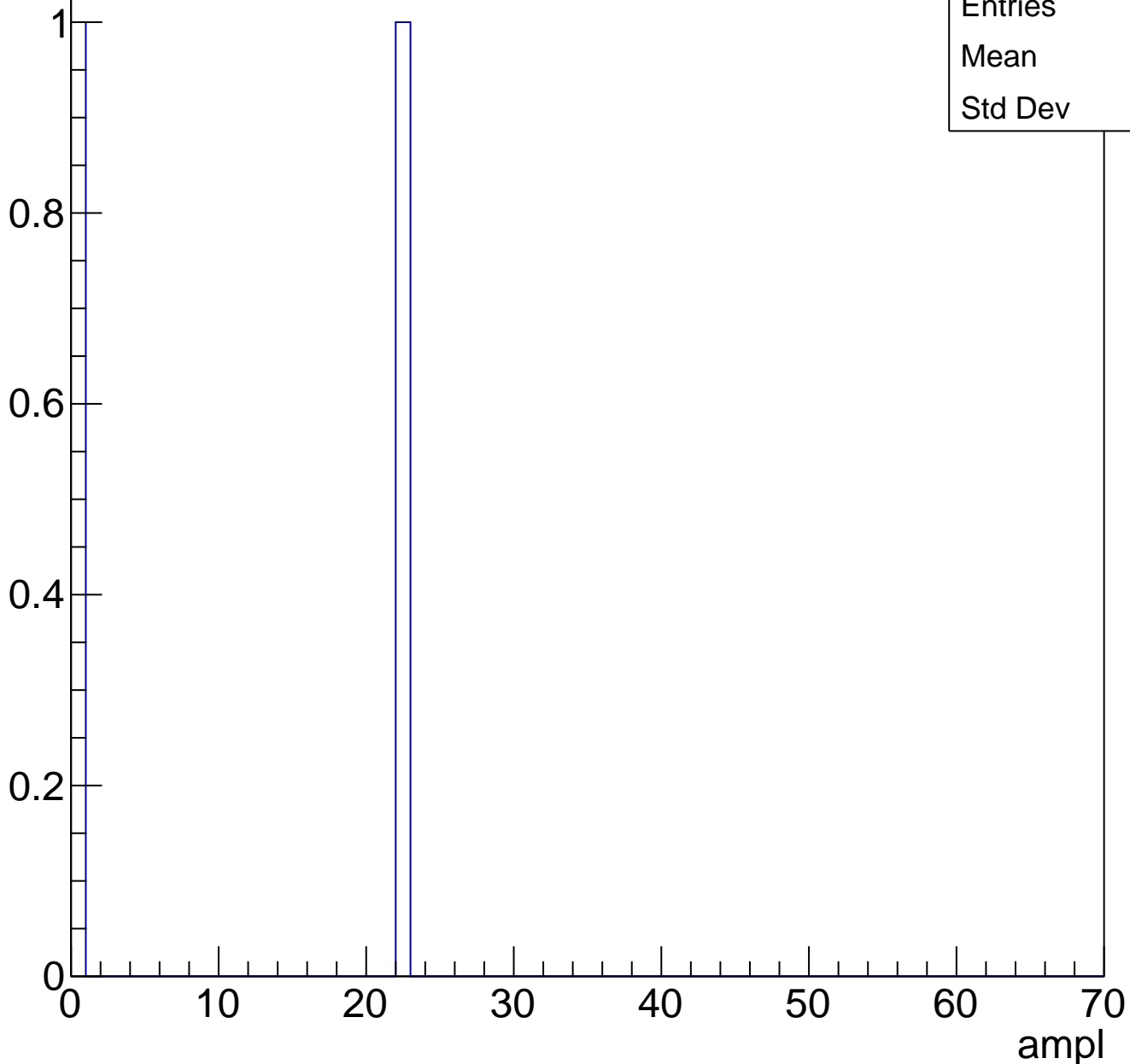




# B0L000S, U25-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	2
Mean	11
Std Dev	11

# B0L000S, U25-ch118, adc0

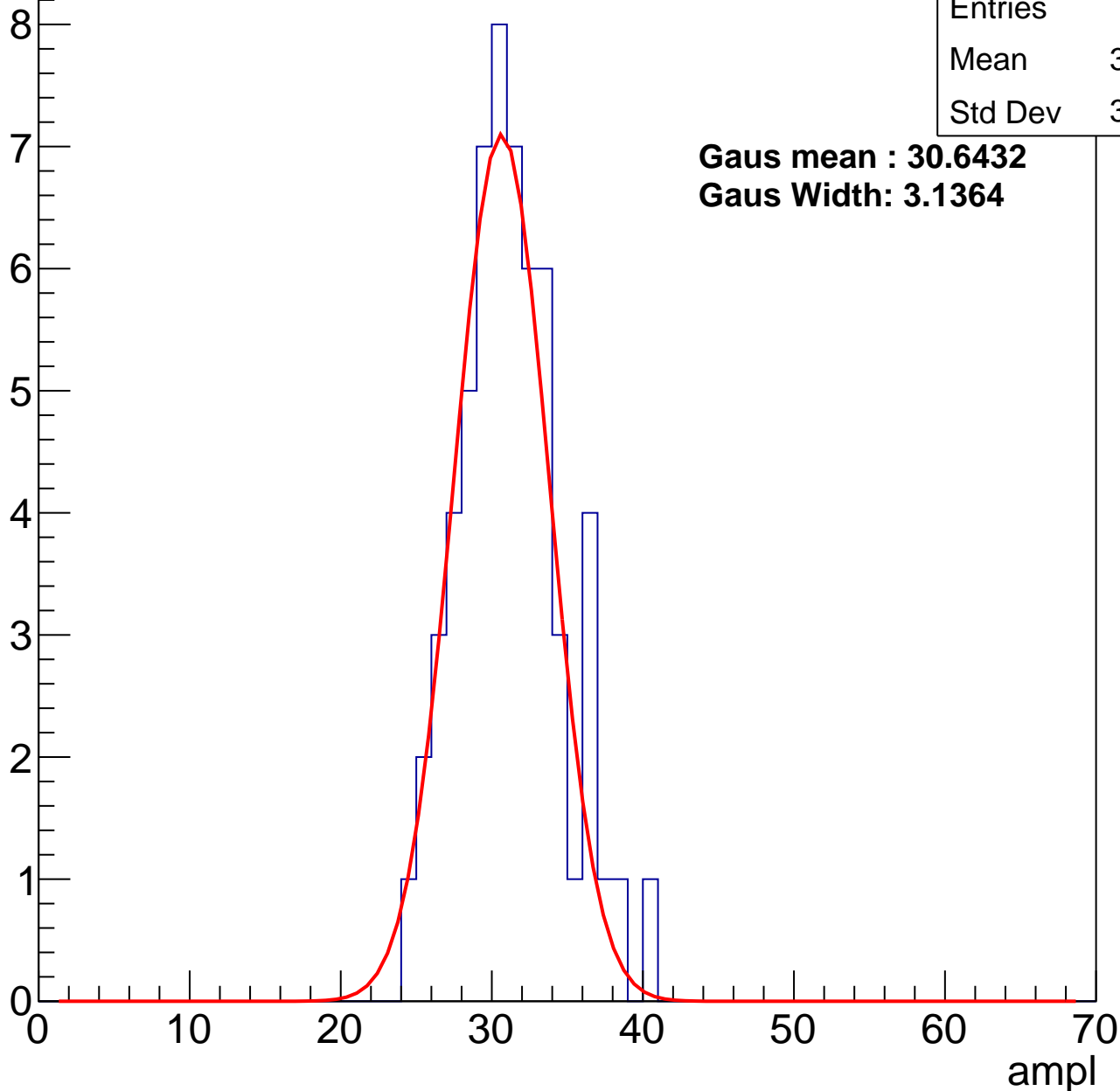
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	30.77
Std Dev	3.358

**Gaus mean : 30.6432**

**Gaus Width: 3.1364**



# B0L000S, U25-ch118, adc1

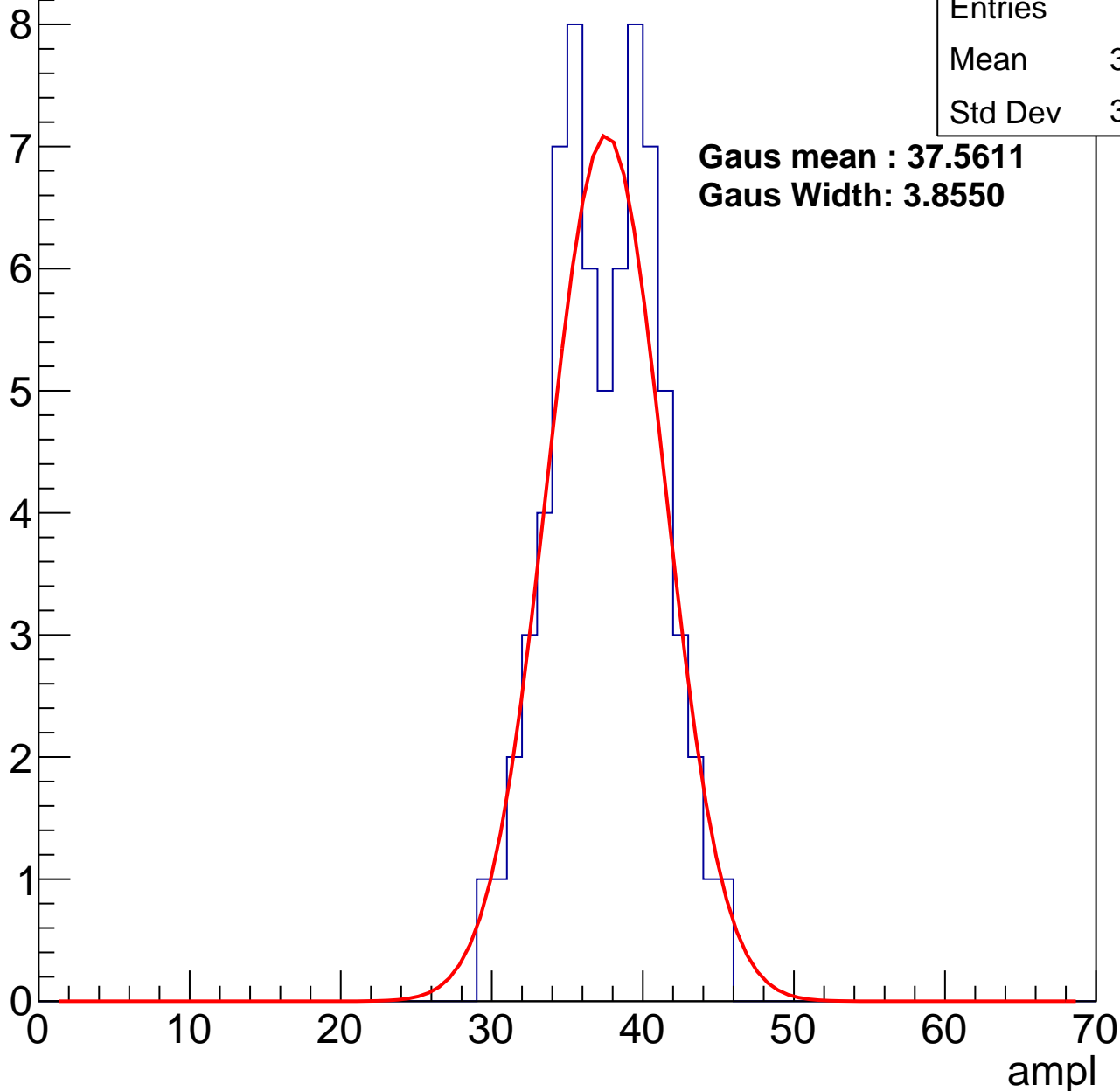
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	37.06
Std Dev	3.517

**Gaus mean : 37.5611**

**Gaus Width: 3.8550**



# B0L000S, U25-ch118, adc2

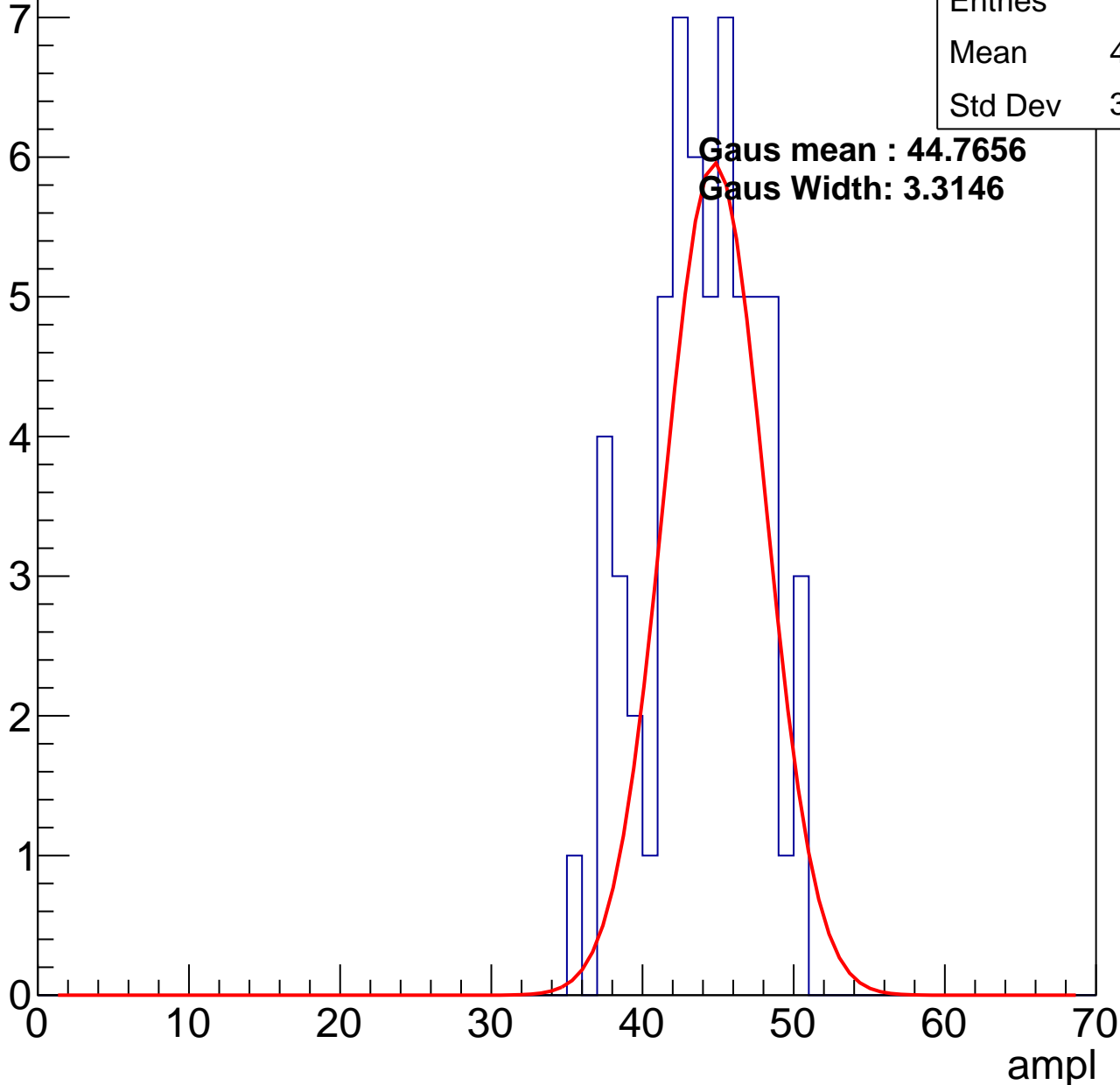
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	43.52
Std Dev	3.654

Gaus mean : 44.7656

Gaus Width: 3.3146

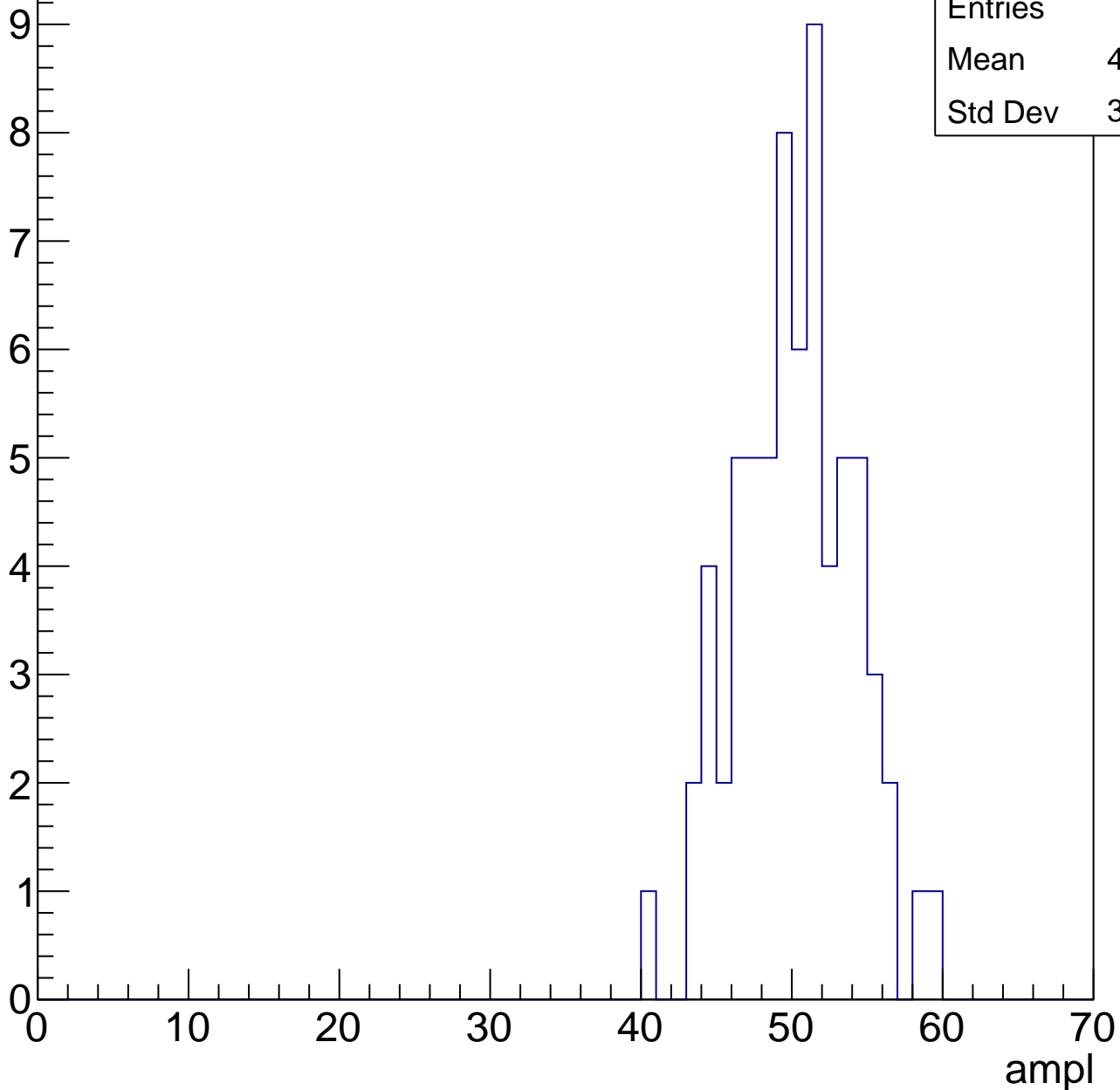


# B0L000S, U25-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	49.78
Std Dev	3.803

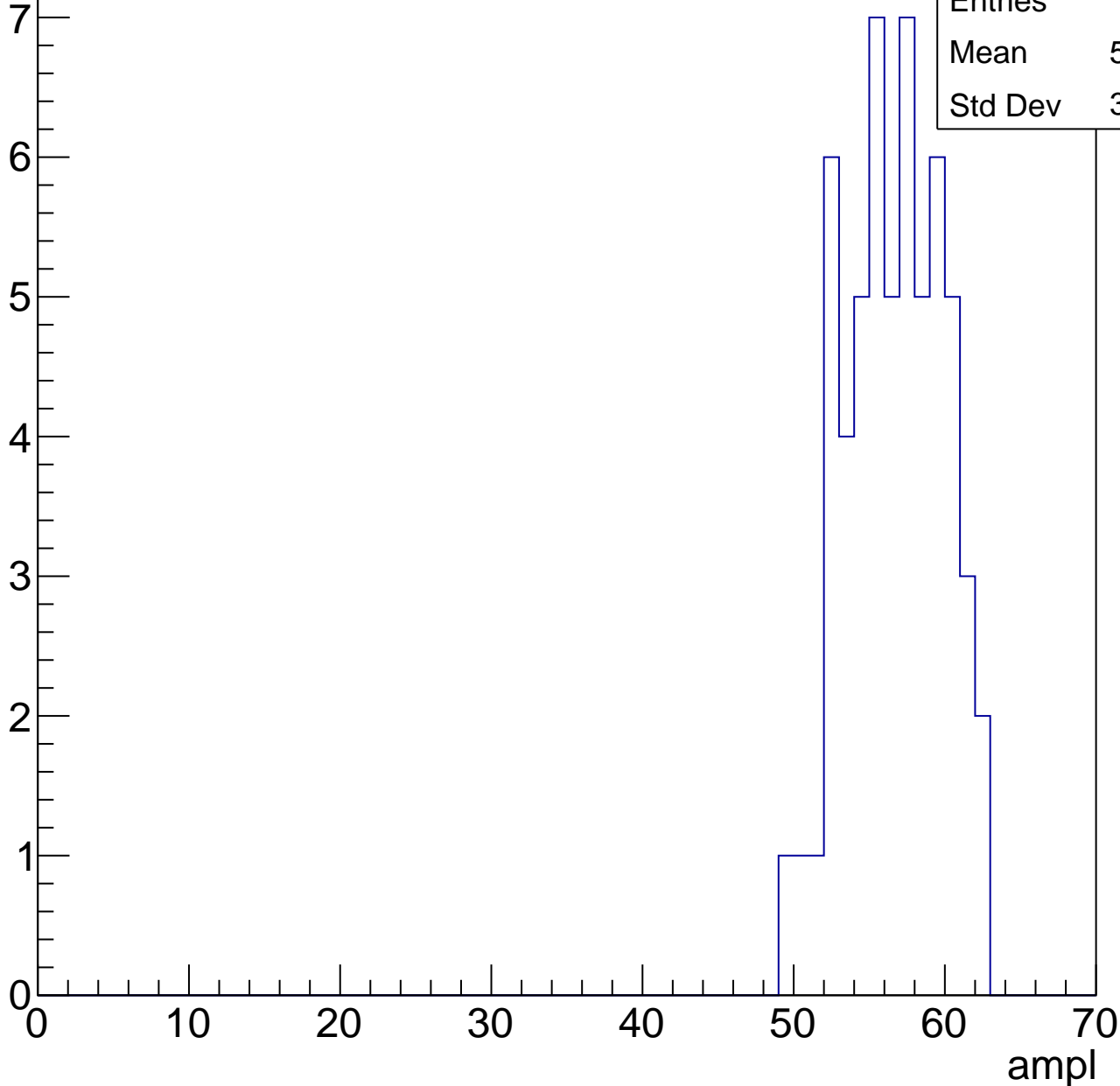


# B0L000S, U25-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	56.19
Std Dev	3.148

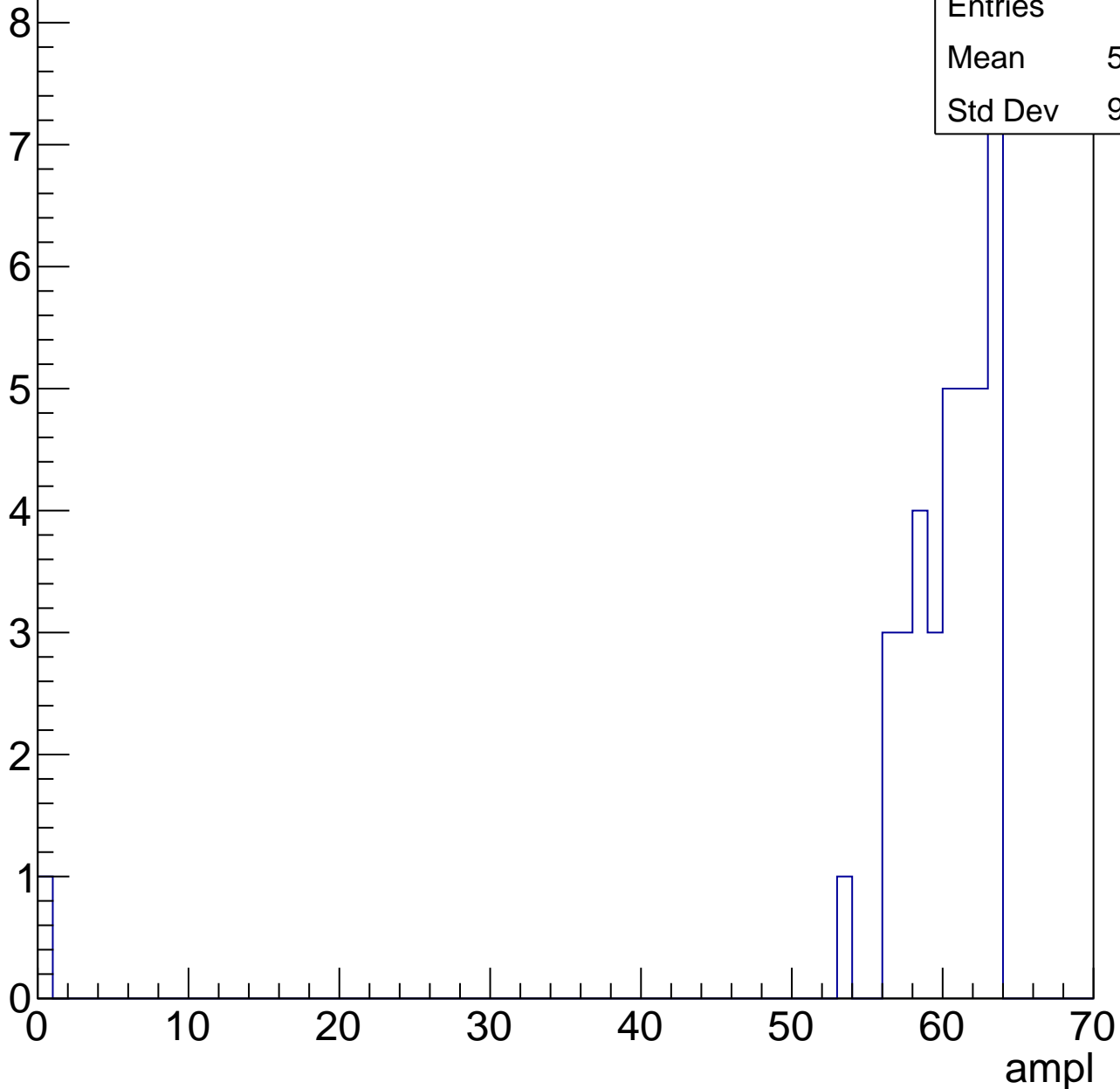


# B0L000S, U25-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

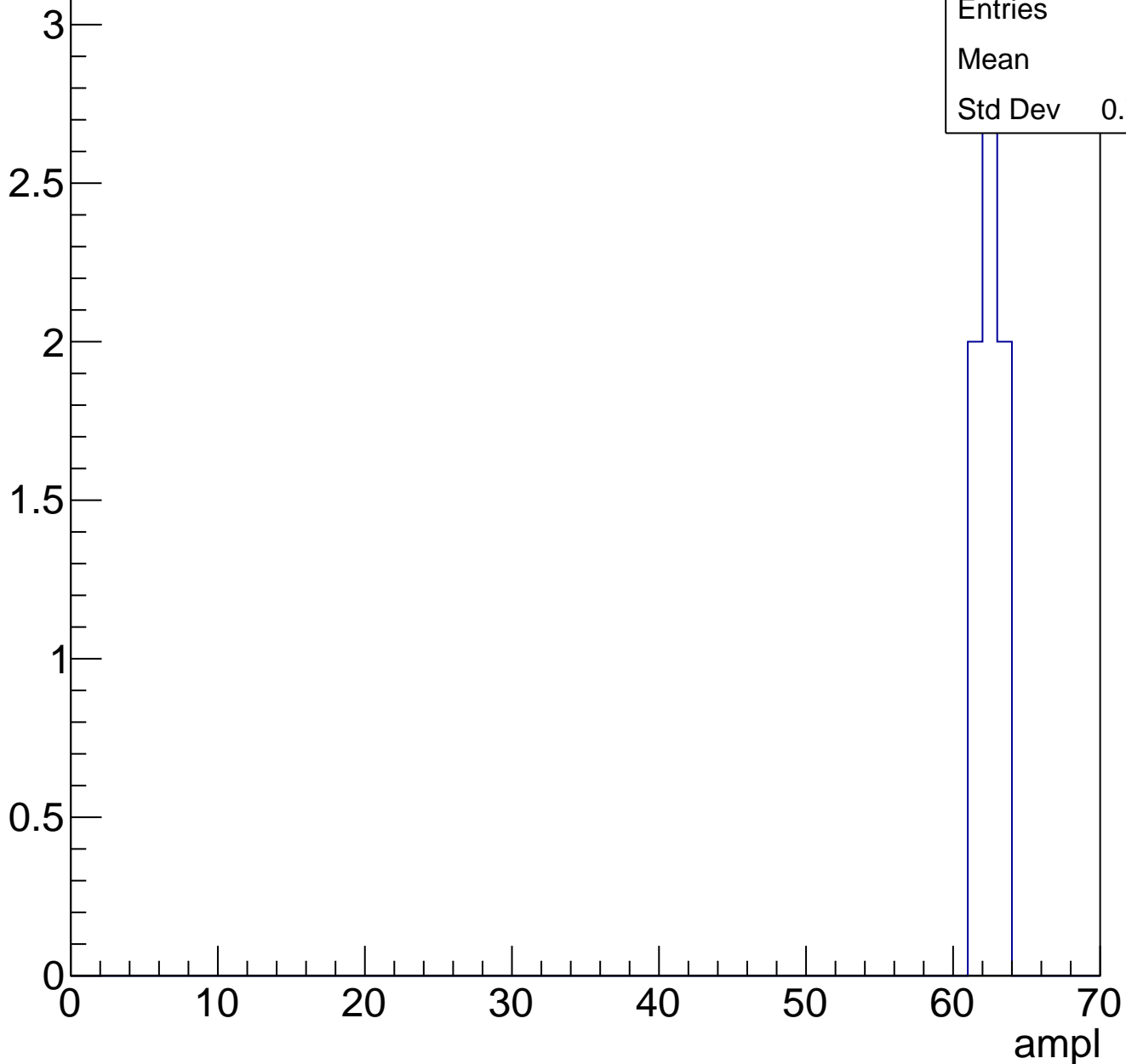
Entries	38
Mean	58.42
Std Dev	9.928



# B0L000S, U25-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch119, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	83
Mean	31.33
Std Dev	3.723

**Gaus mean : 32.1218**

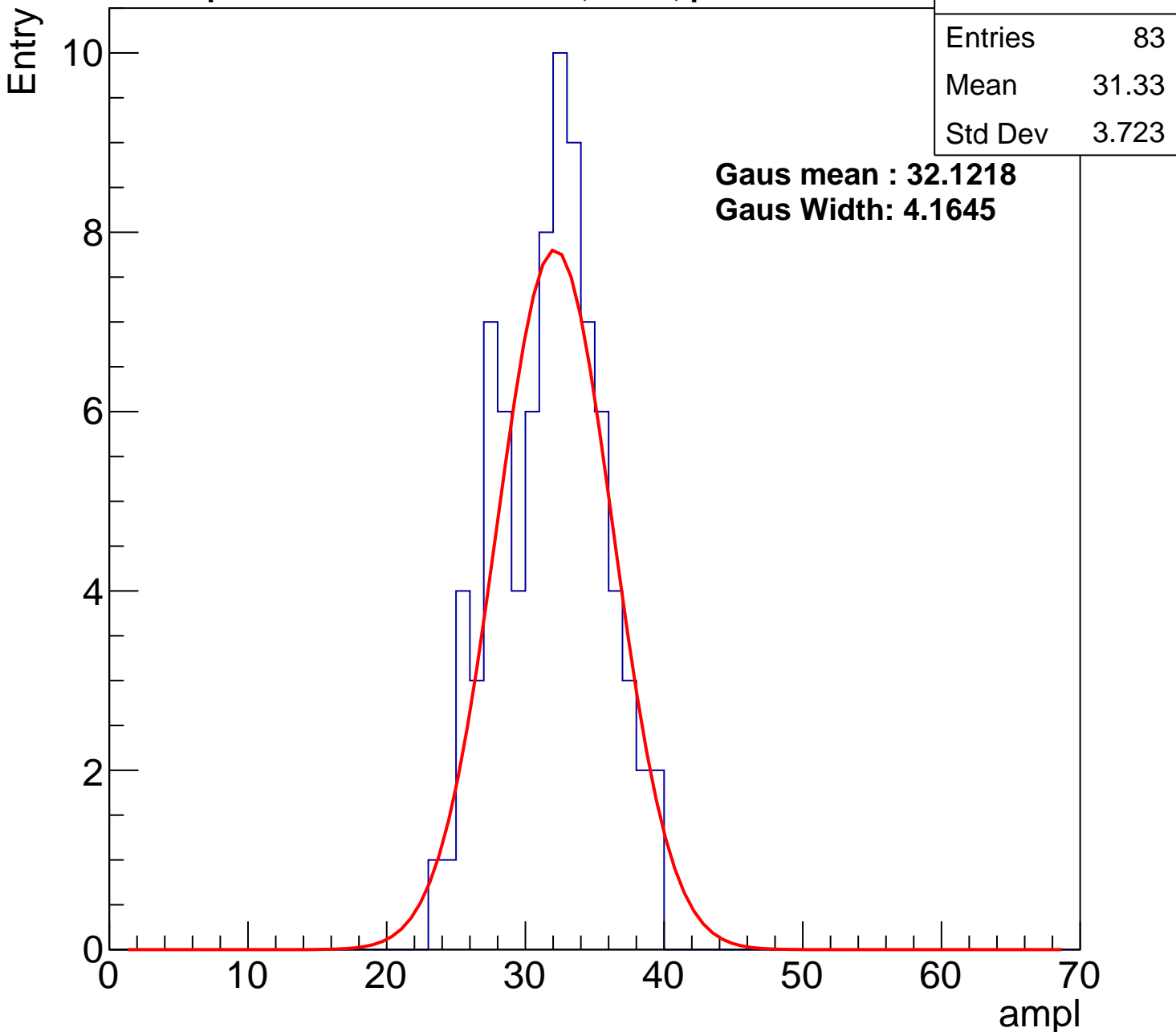
**Gaus Width: 4.1645**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



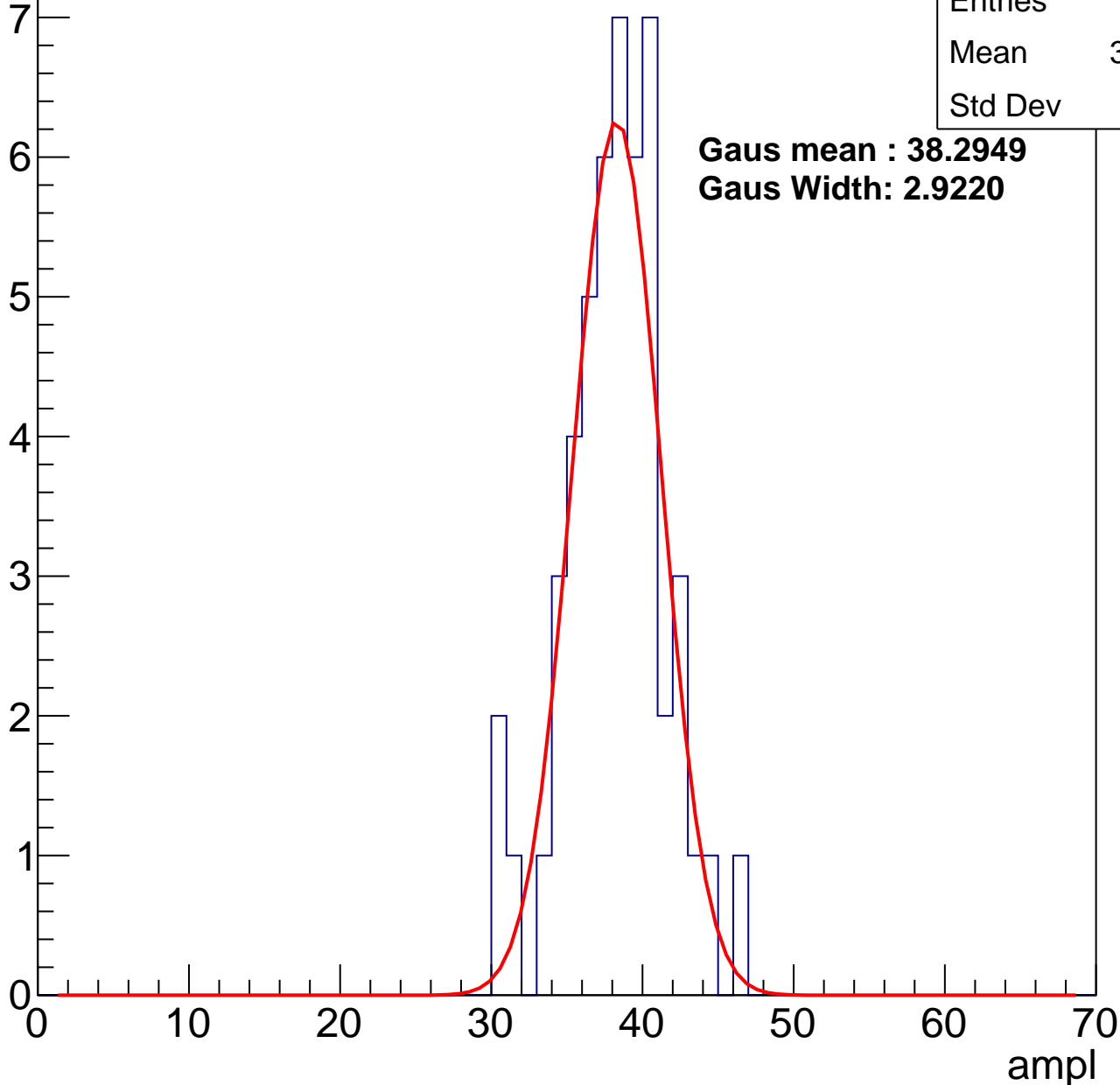
# B0L000S, U25-ch119, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	37.78
Std Dev	3.27

**Gaus mean : 38.2949**  
**Gaus Width: 2.9220**



# B0L000S, U25-ch119, adc2

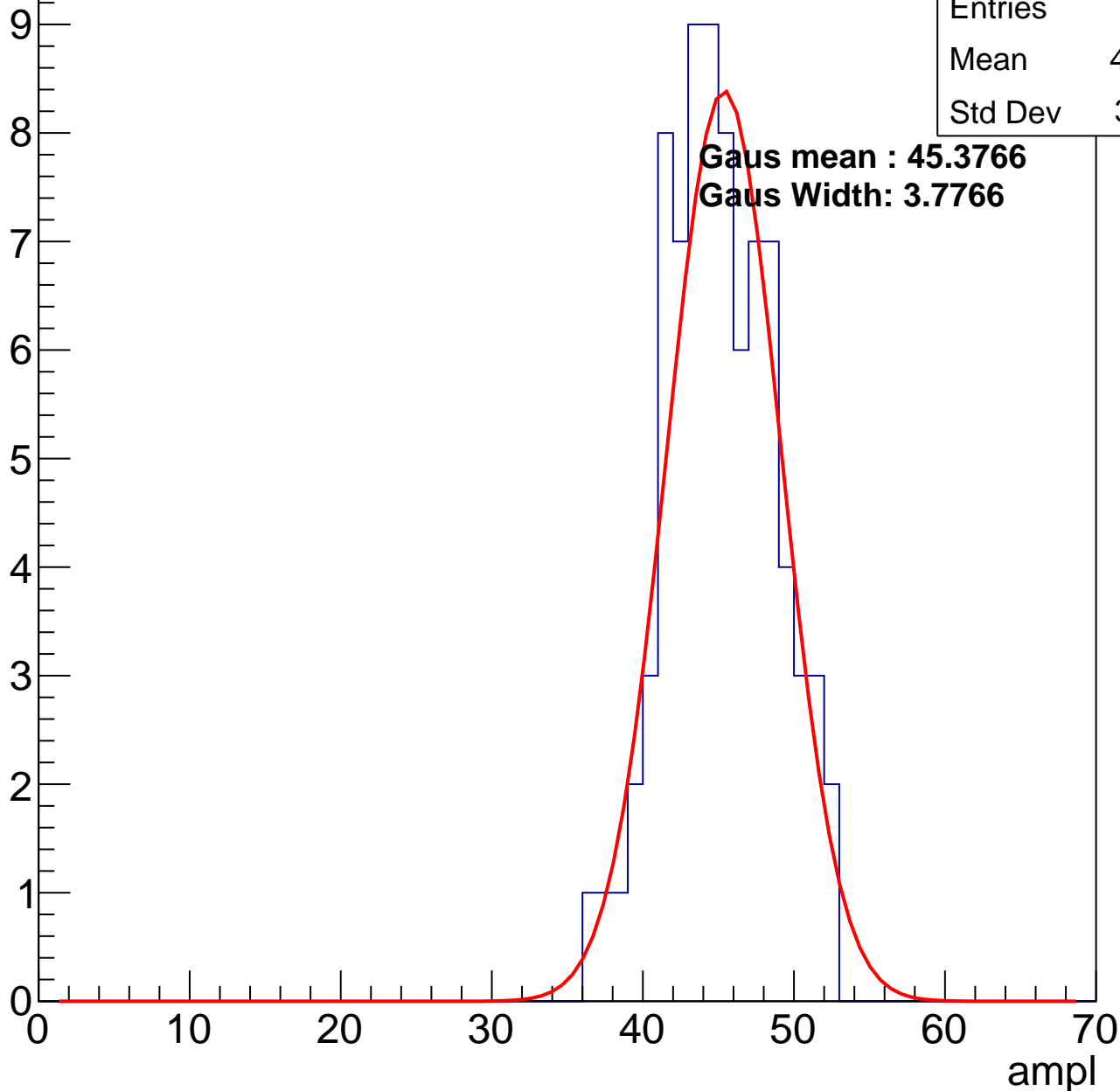
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	44.67
Std Dev	3.531

**Gaus mean : 45.3766**

**Gaus Width: 3.7766**

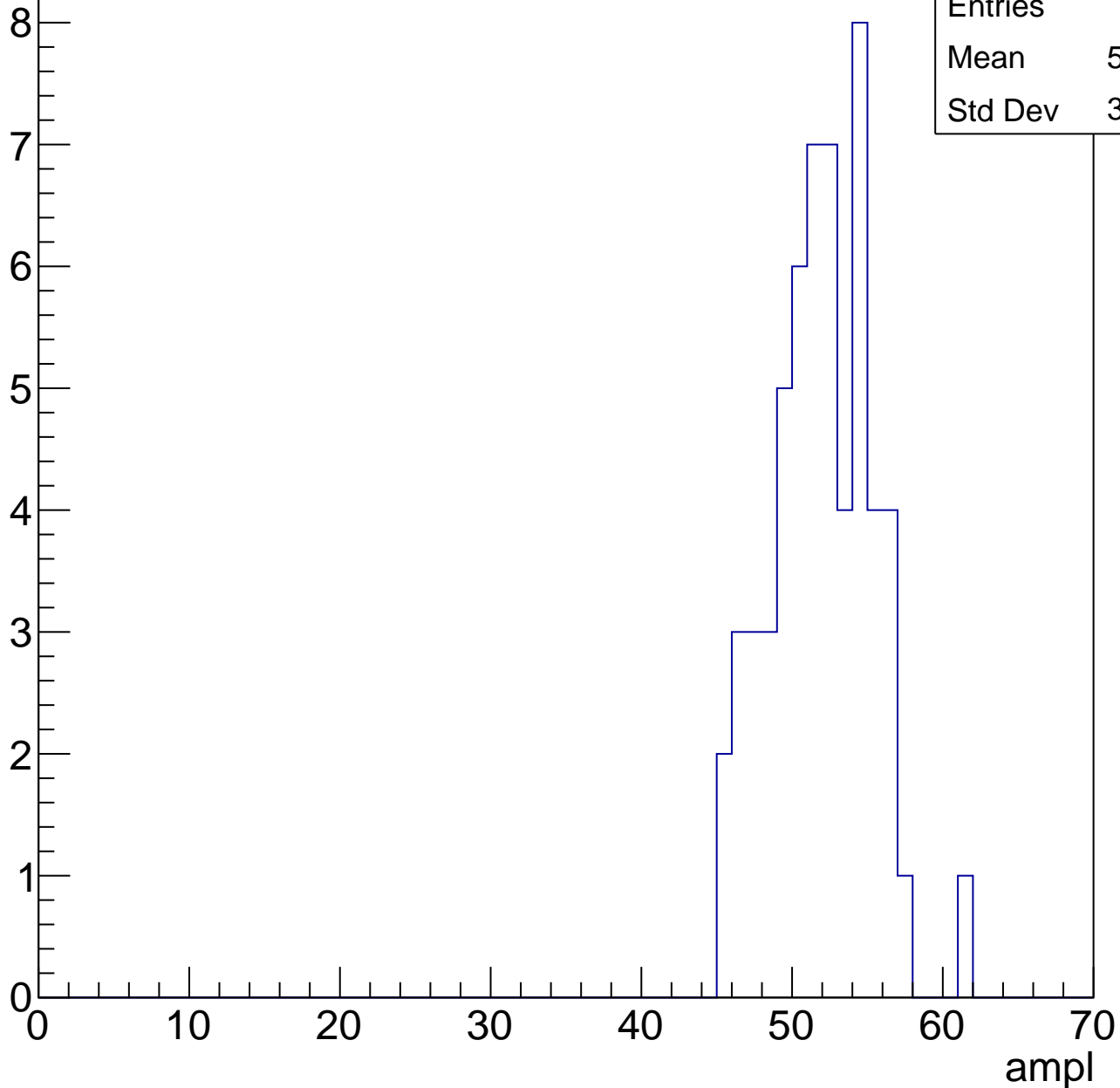


# B0L000S, U25-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	58
Mean	51.47
Std Dev	3.297

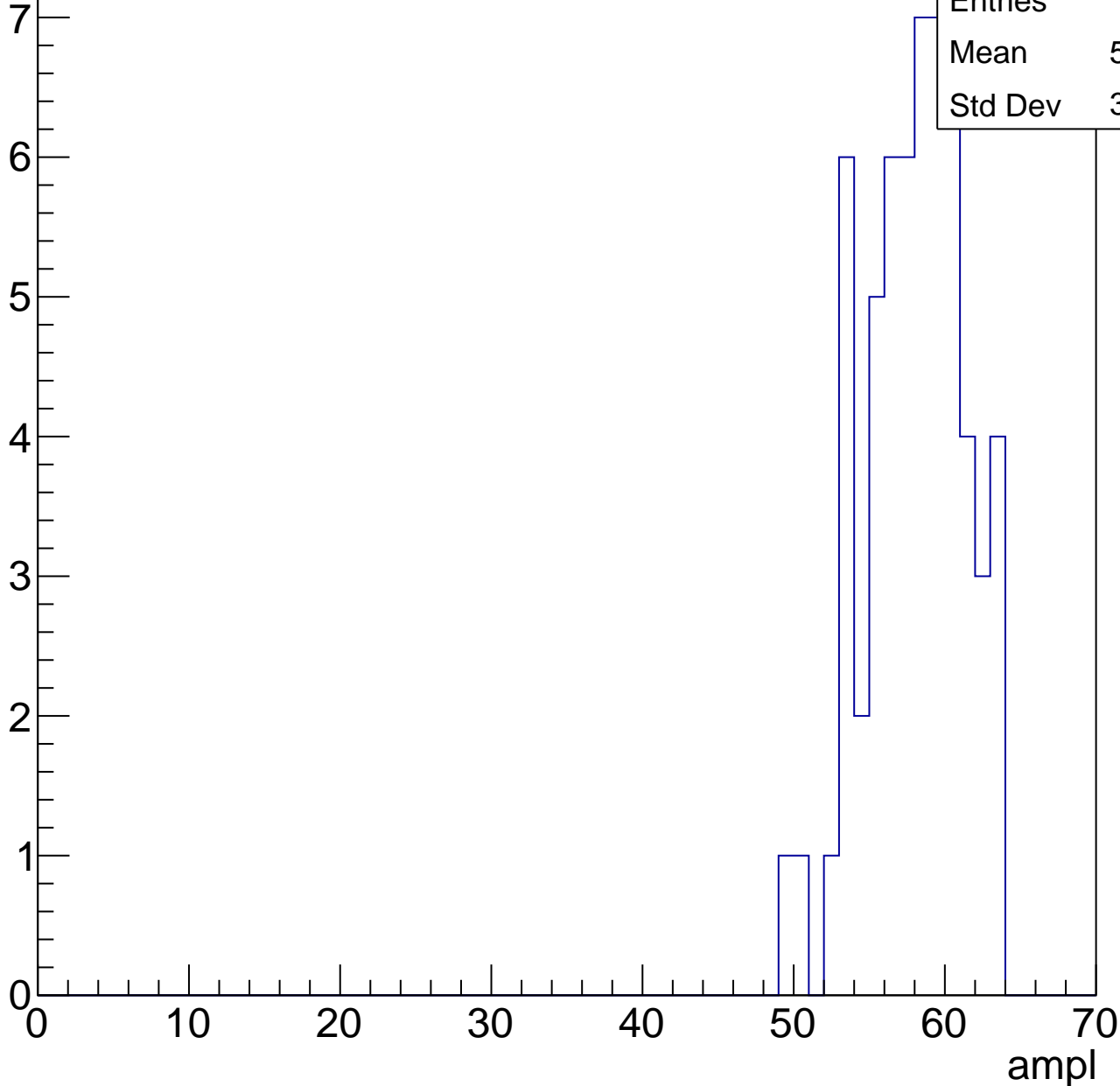


# B0L000S, U25-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	57.52
Std Dev	3.274

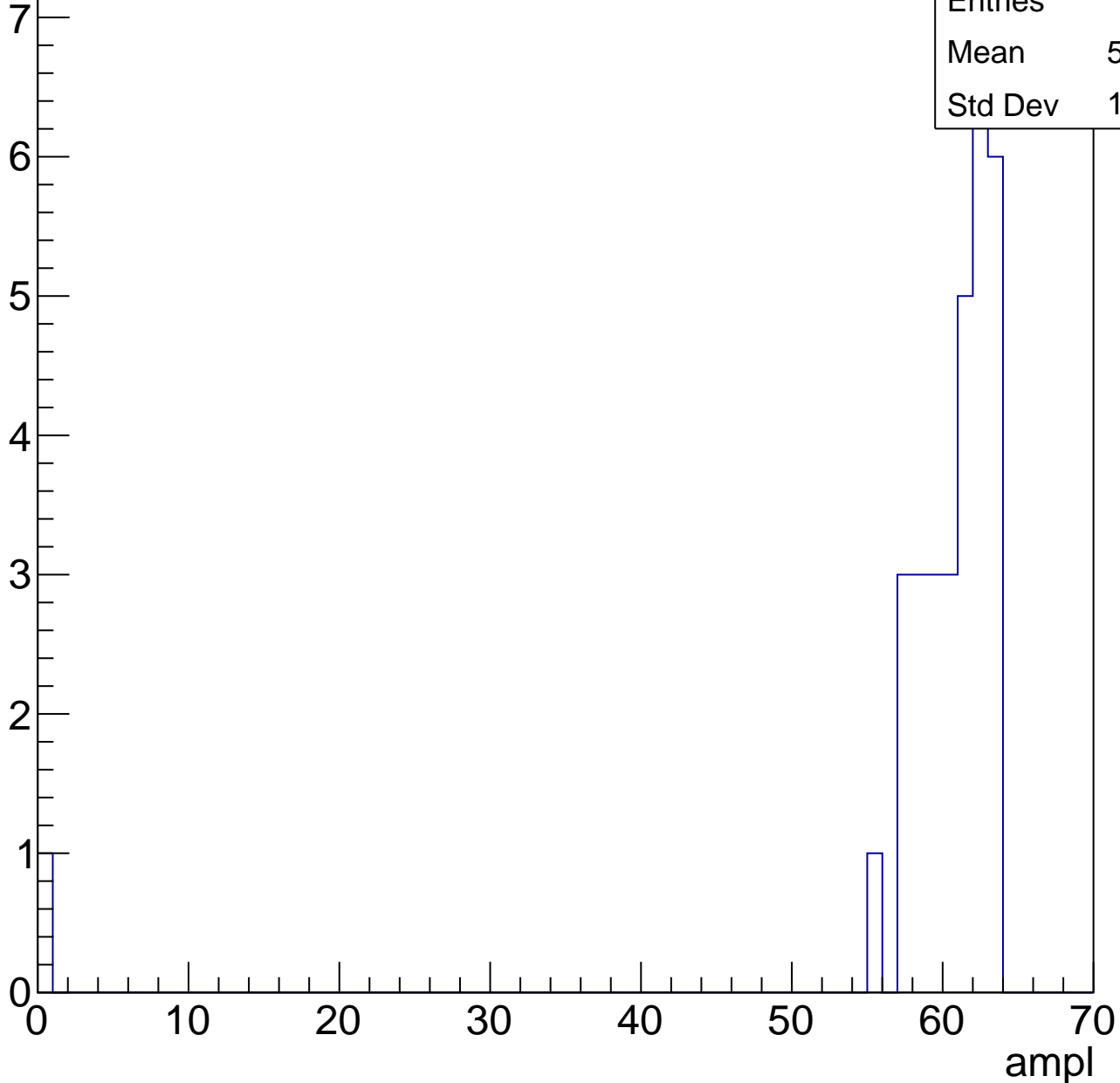


# B0L000S, U25-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.56
Std Dev	10.74



# B0L000S, U25-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0



# B0L000S, U25-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch120, adc0

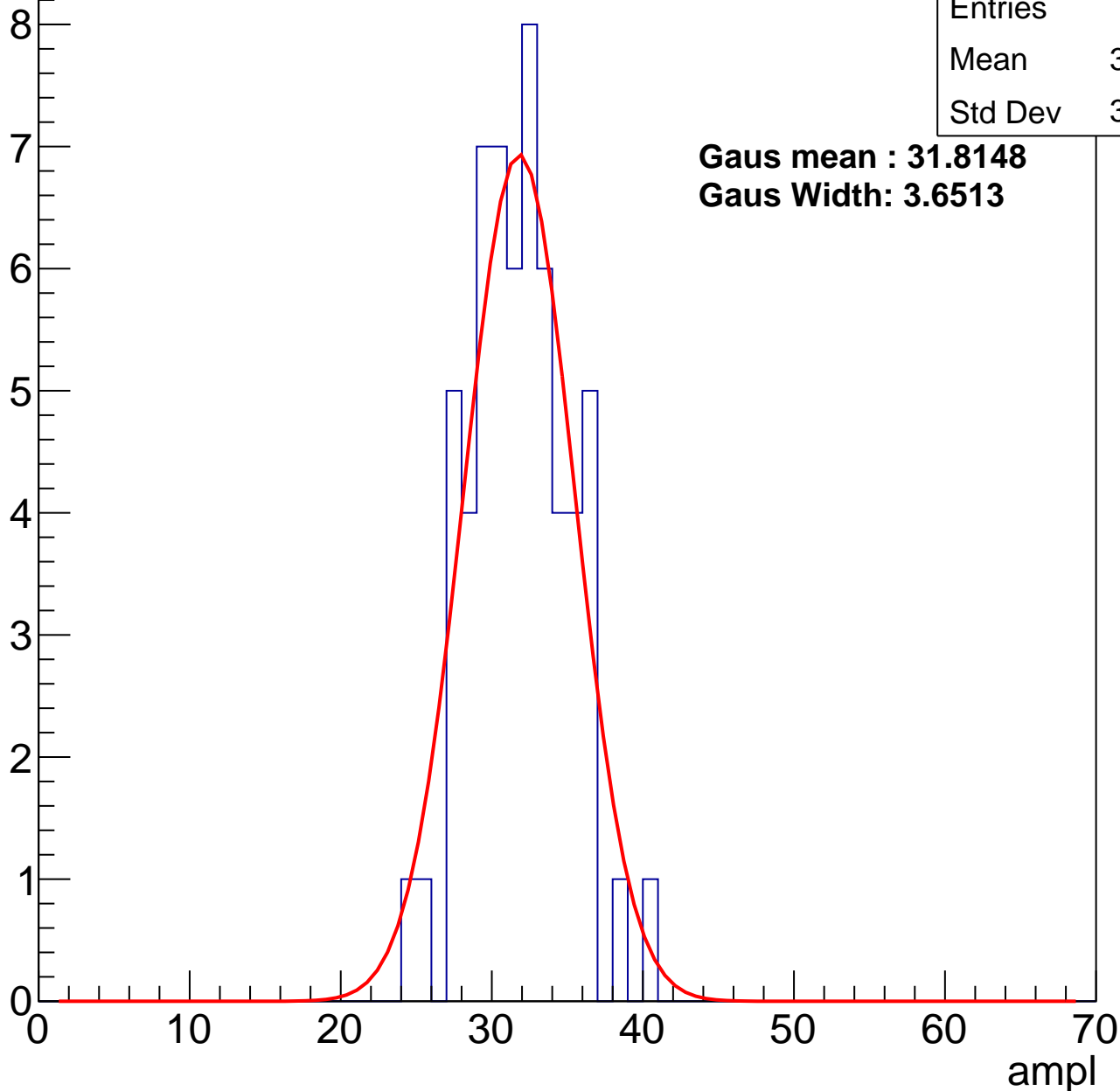
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	31.38
Std Dev	3.199

**Gaus mean : 31.8148**

**Gaus Width: 3.6513**



# B0L000S, U25-ch120, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	87
Mean	38.57
Std Dev	3.923

**Gaus mean : 38.8454**

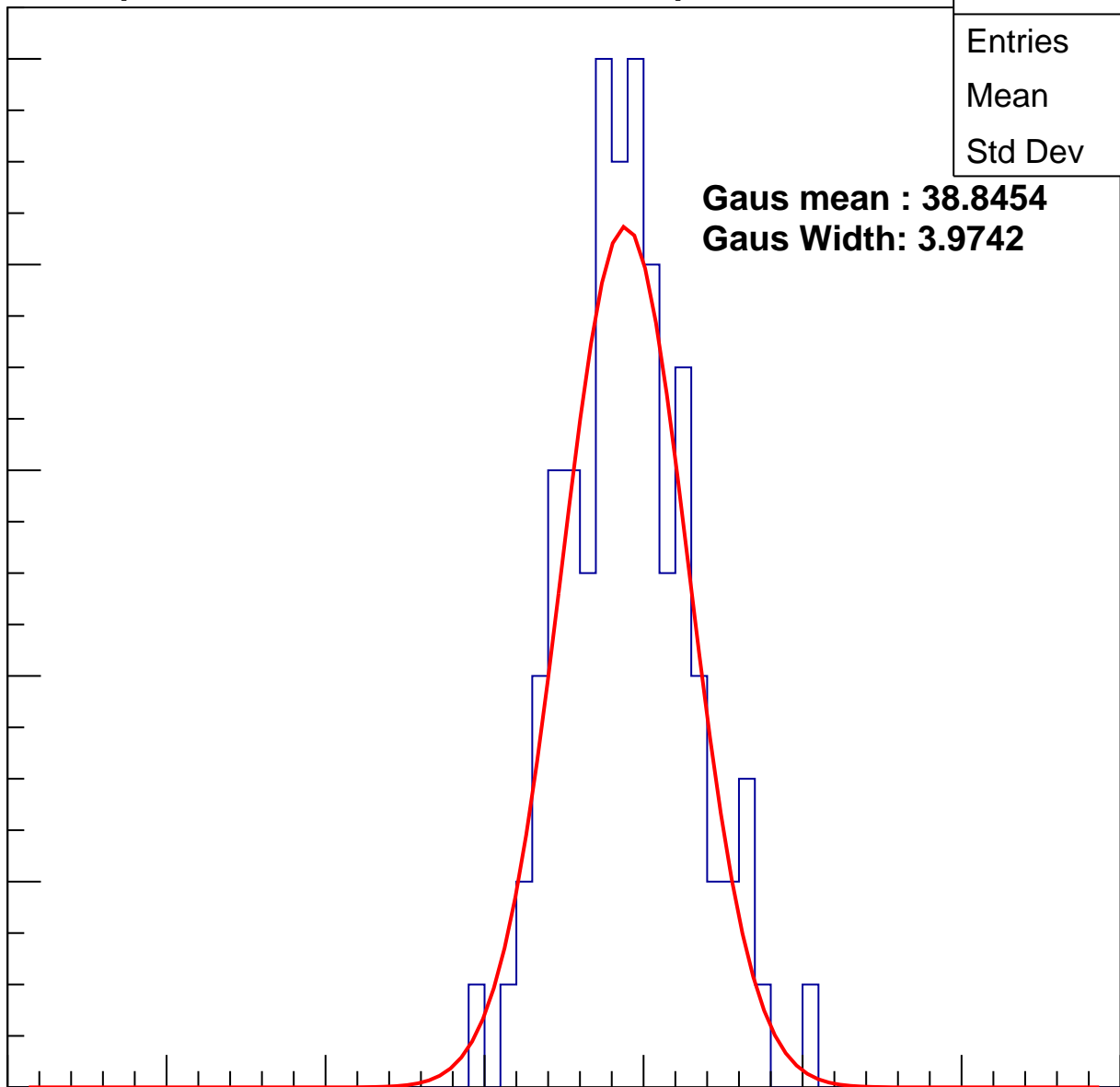
**Gaus Width: 3.9742**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch120, adc2

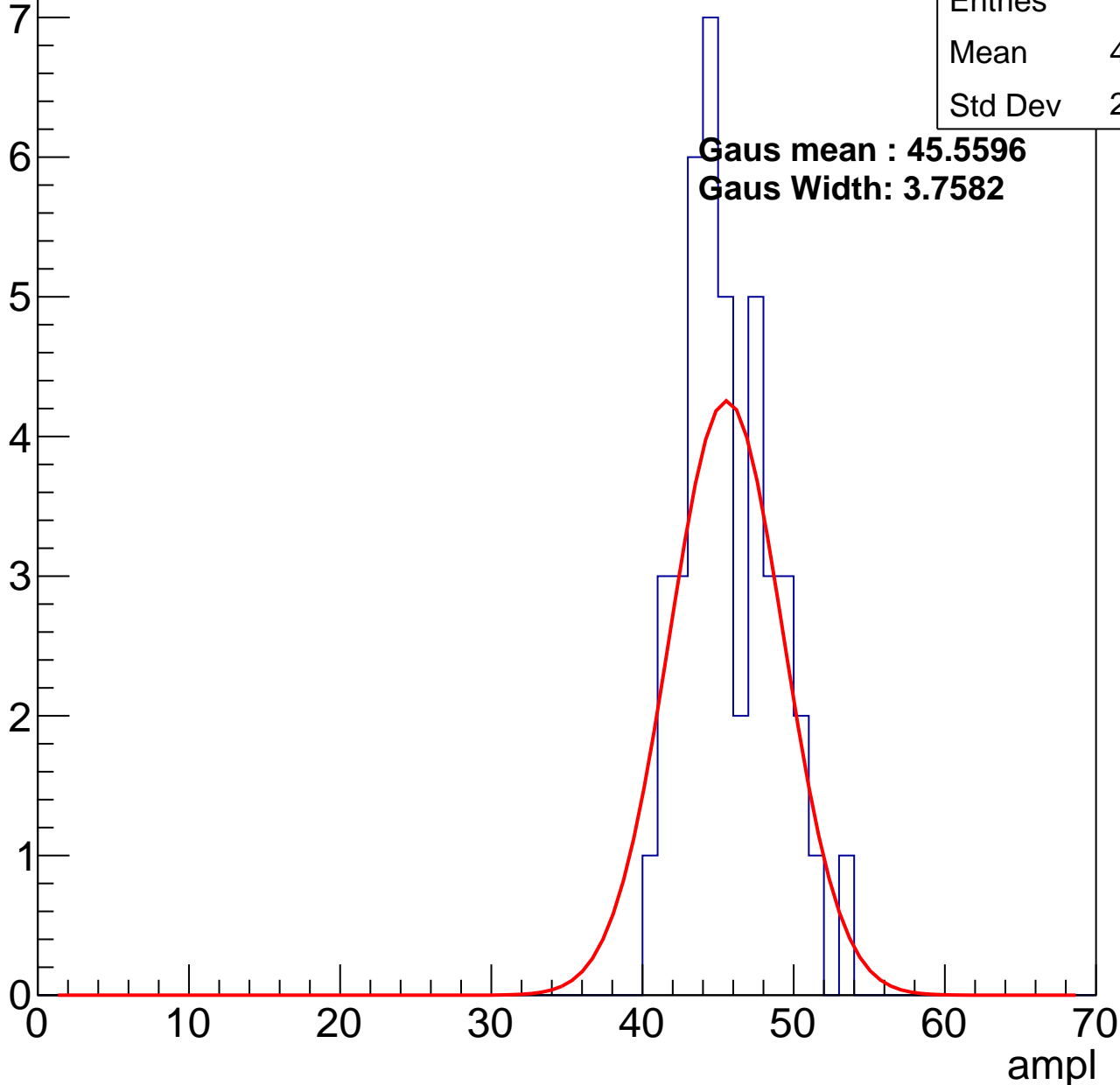
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	42
Mean	45.29
Std Dev	2.986

**Gaus mean : 45.5596**

**Gaus Width: 3.7582**

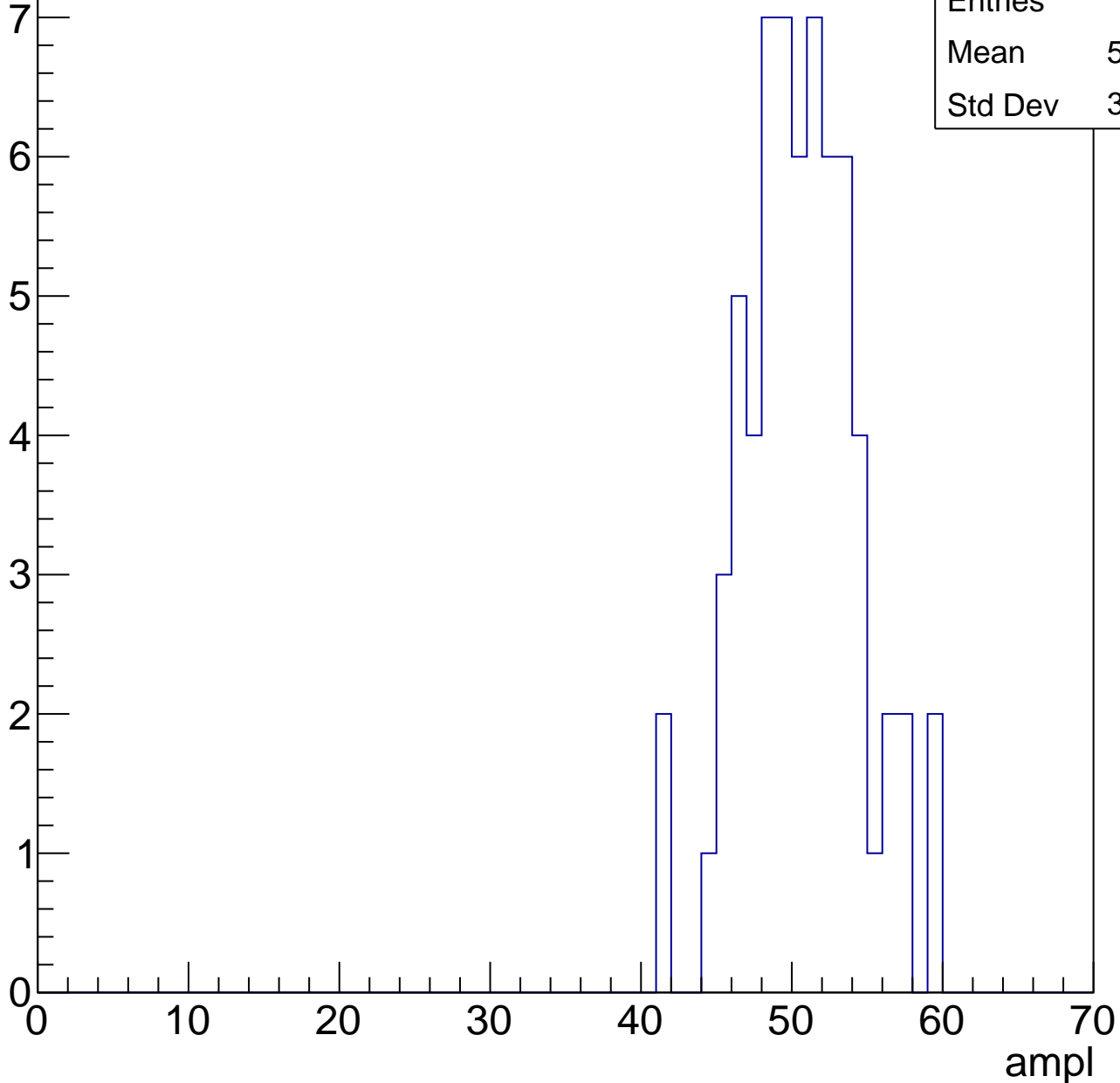


# B0L000S, U25-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

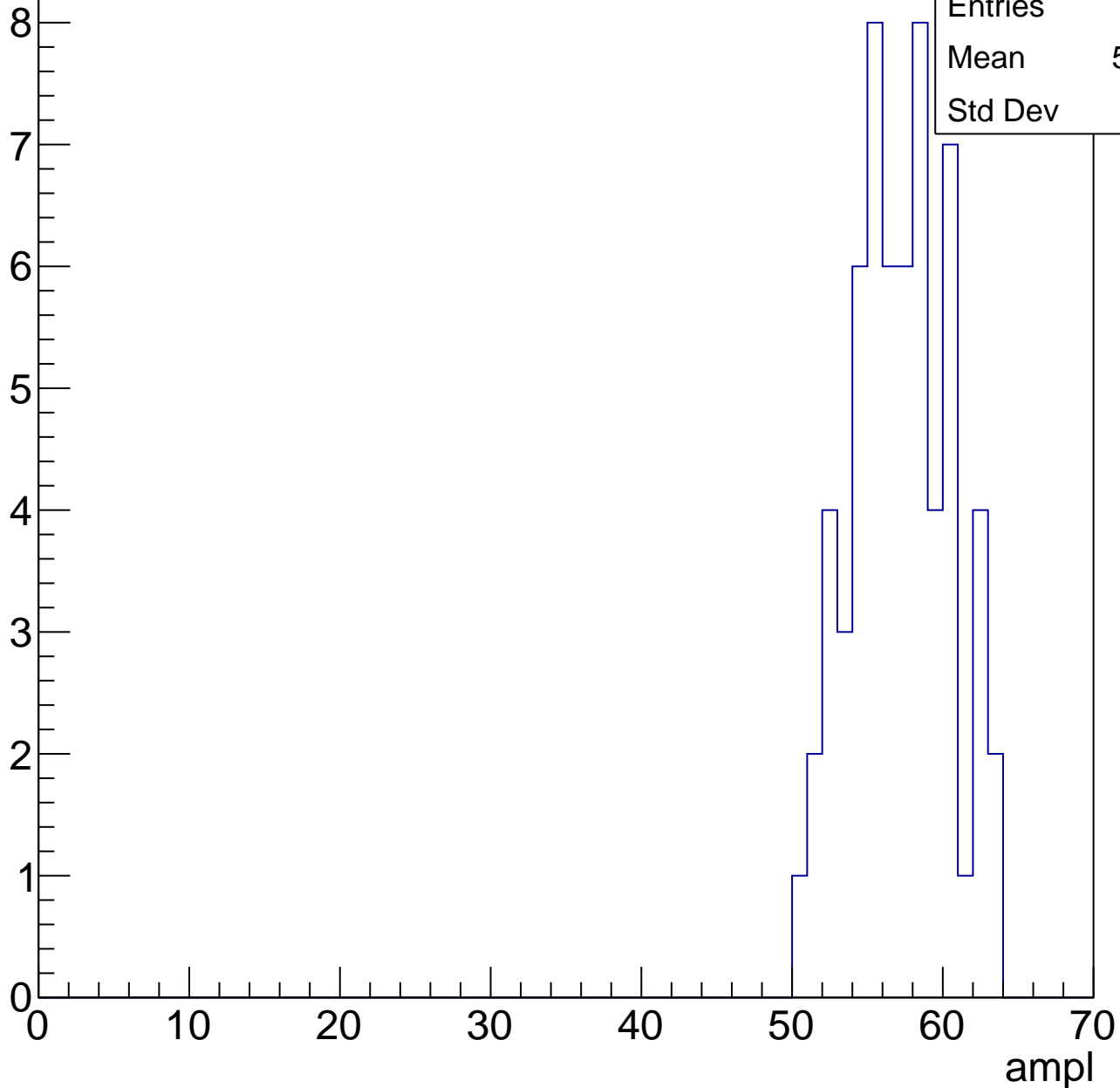
Entries	65
Mean	50.15
Std Dev	3.779



# B0L000S, U25-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

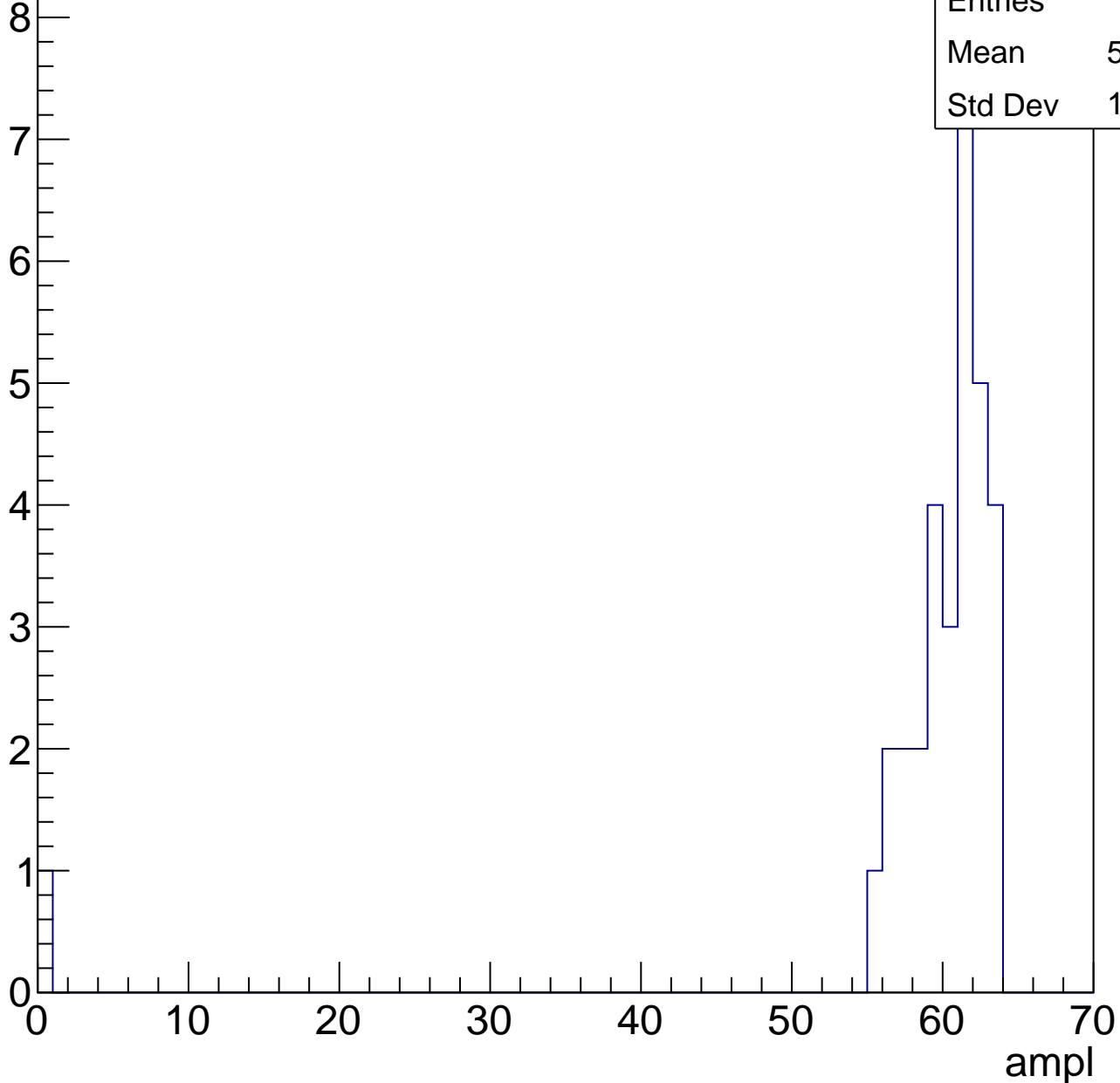


# B0L000S, U25-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	32
Mean	58.22
Std Dev	10.68



# B0L000S, U25-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

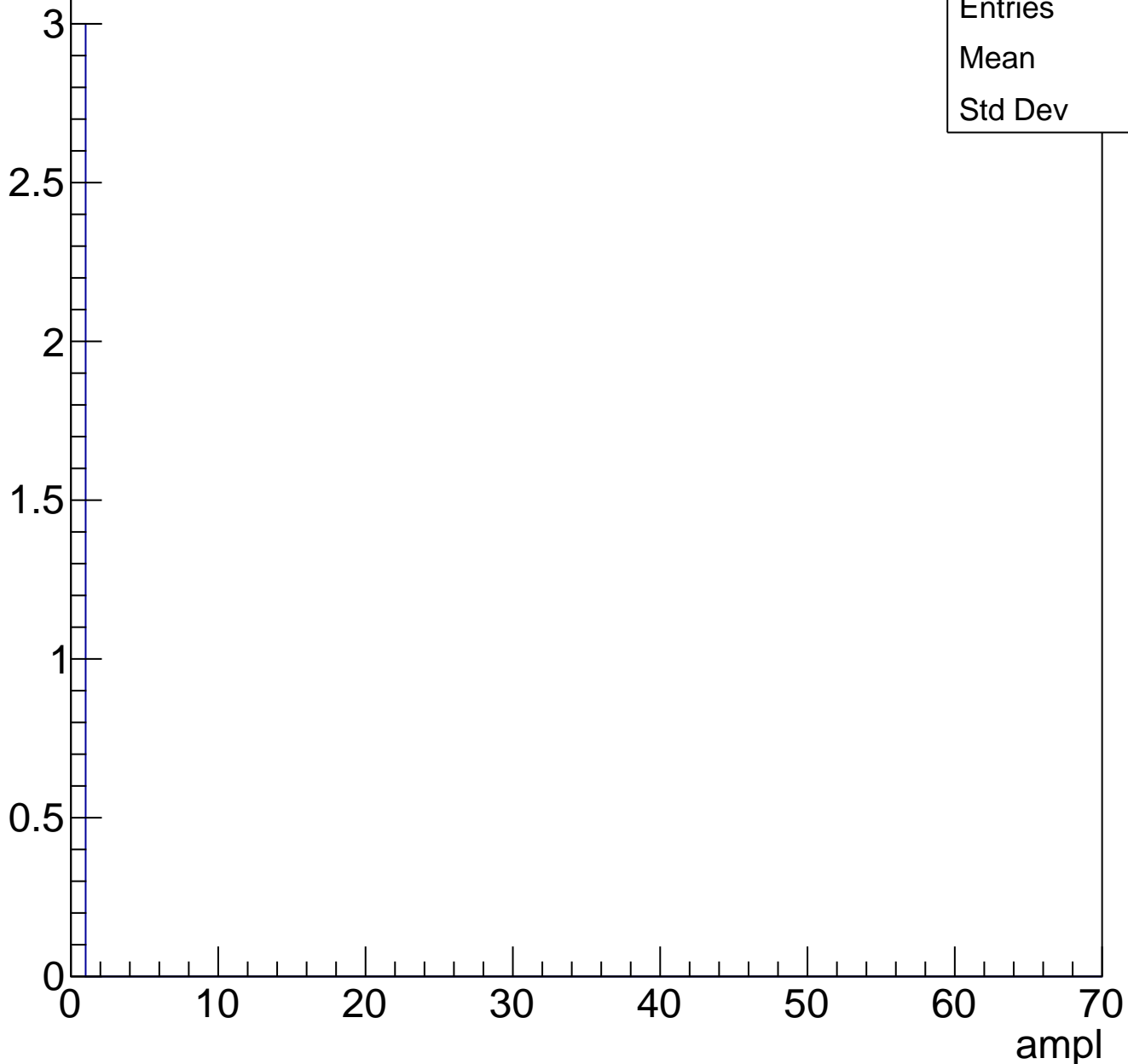




# B0L000S, U25-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	0
Std Dev	0

# B0L000S, U25-ch121, adc0

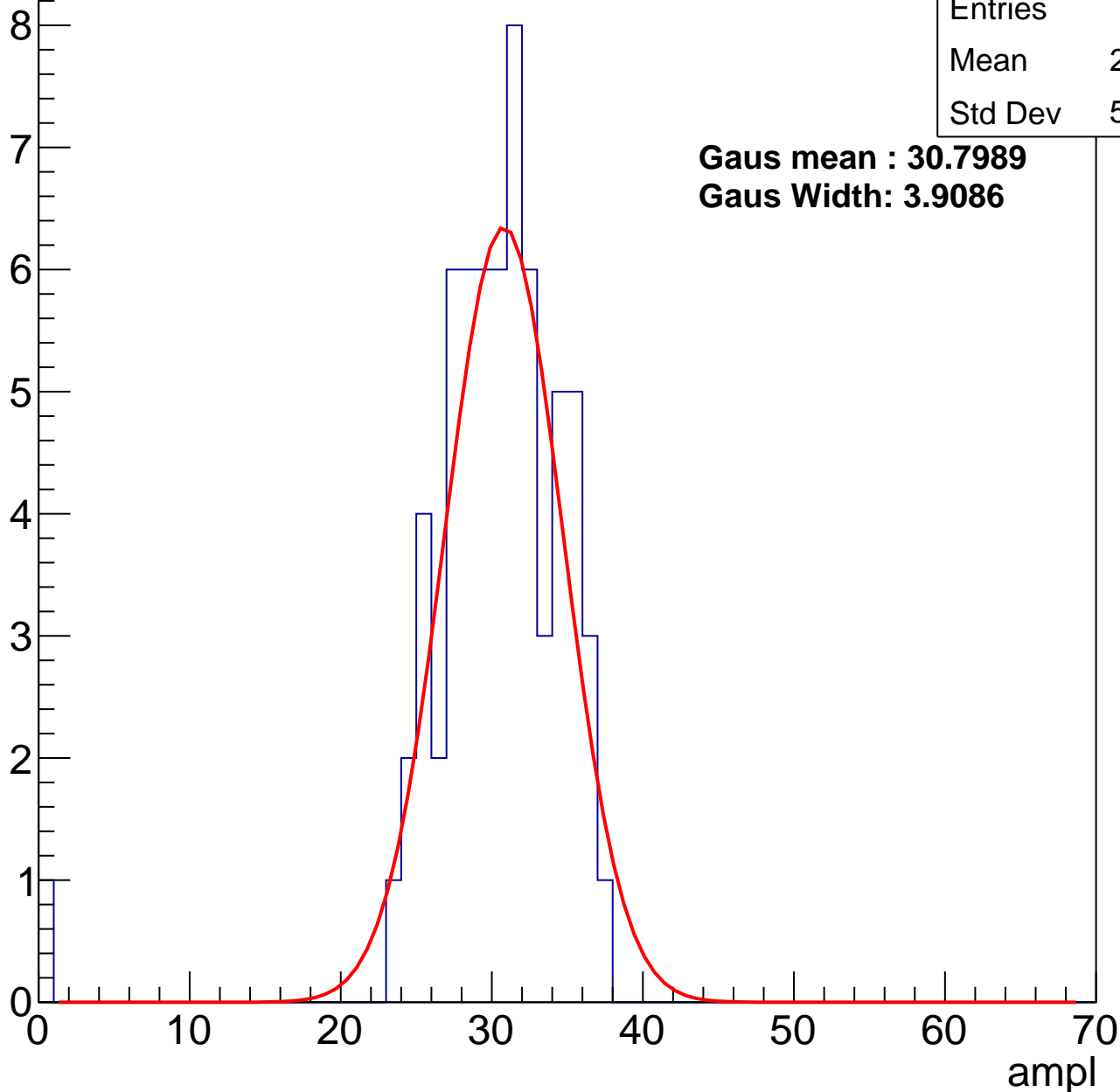
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	29.78
Std Dev	5.046

**Gaus mean : 30.7989**

**Gaus Width: 3.9086**



# B0L000S, U25-ch121, adc1

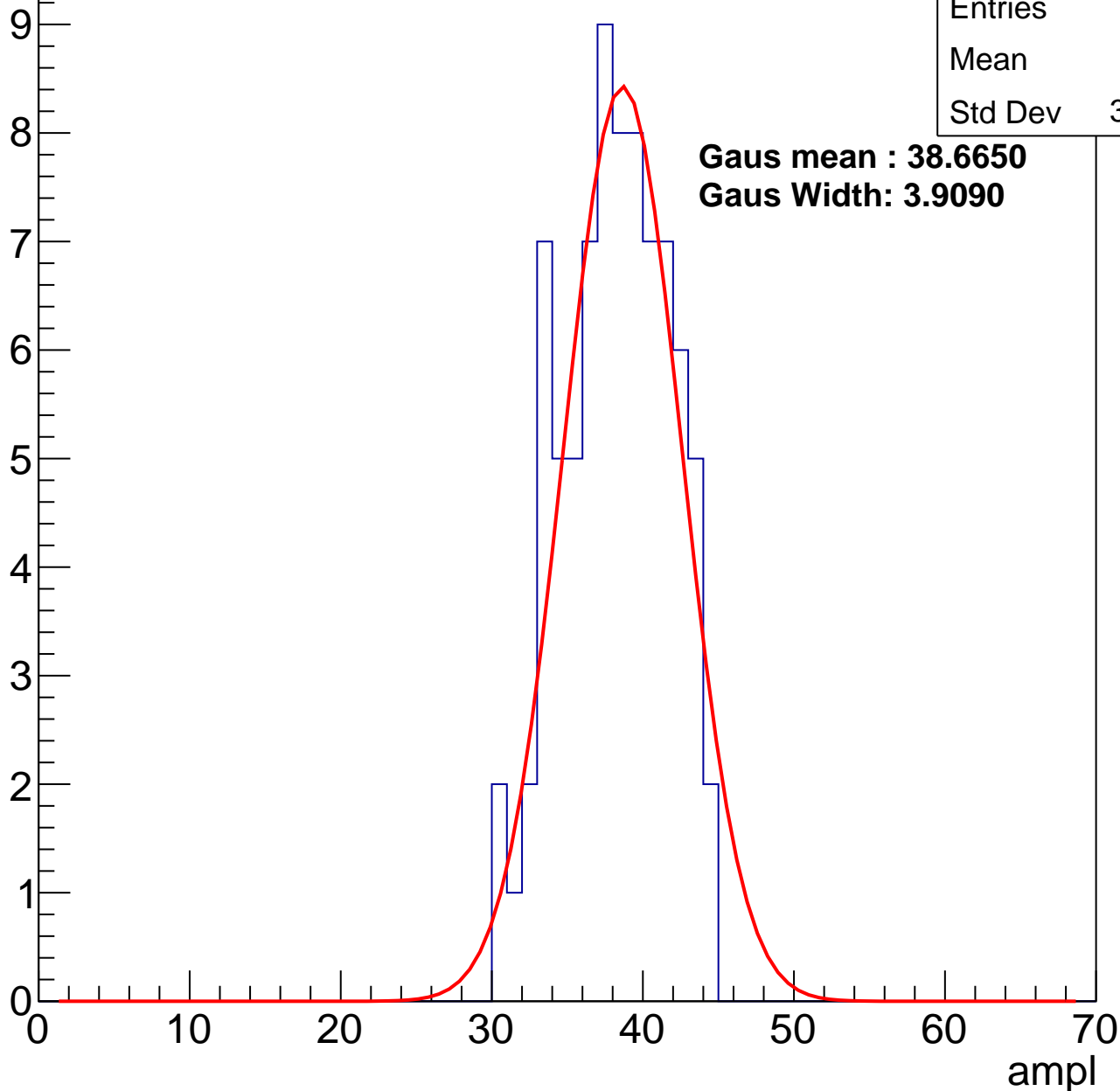
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	81
Mean	37.7
Std Dev	3.462

**Gaus mean : 38.6650**

**Gaus Width: 3.9090**



# B0L000S, U25-ch121, adc2

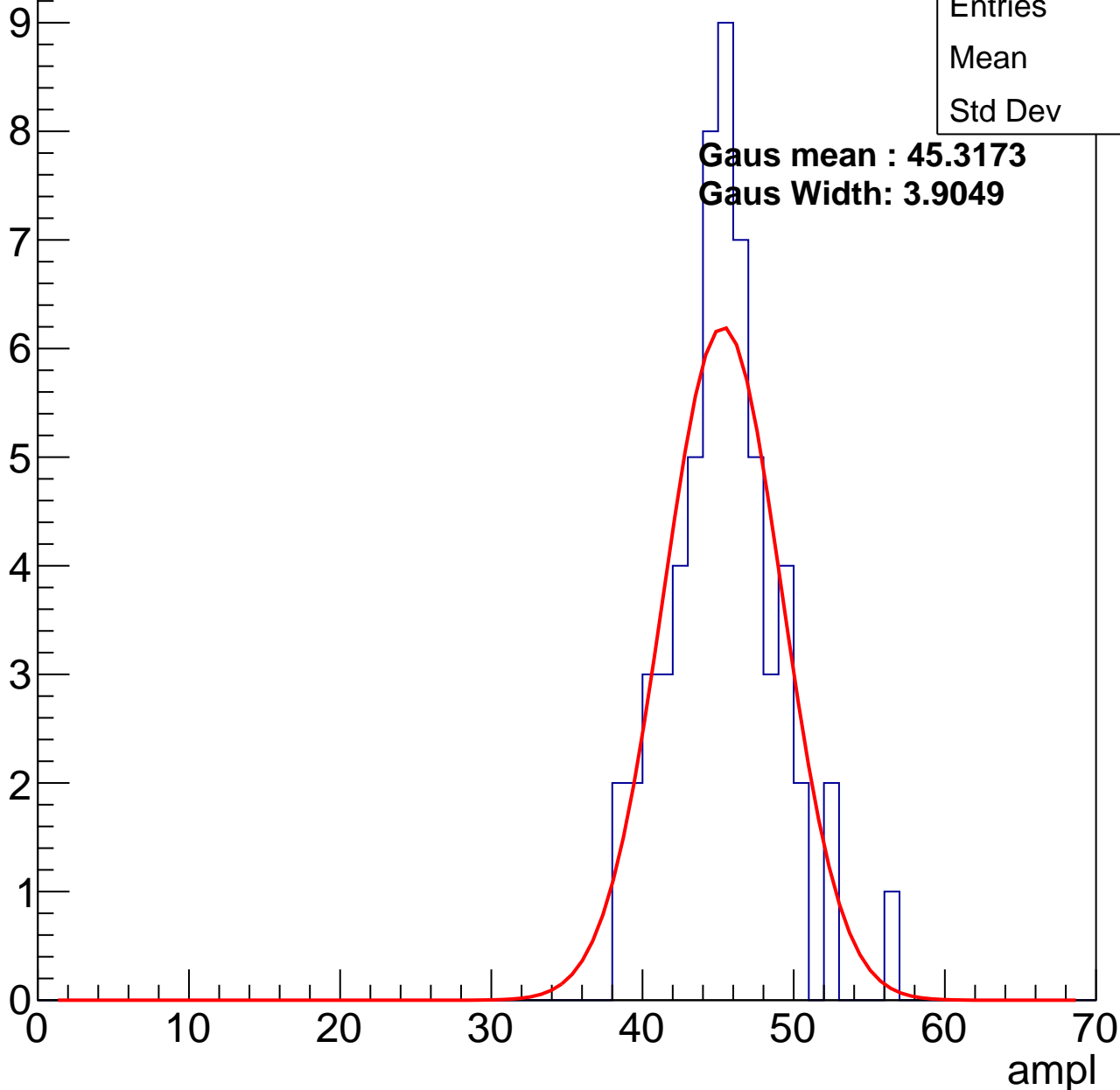
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	44.9
Std Dev	3.52

**Gaus mean : 45.3173**

**Gaus Width: 3.9049**



# B0L000S, U25-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

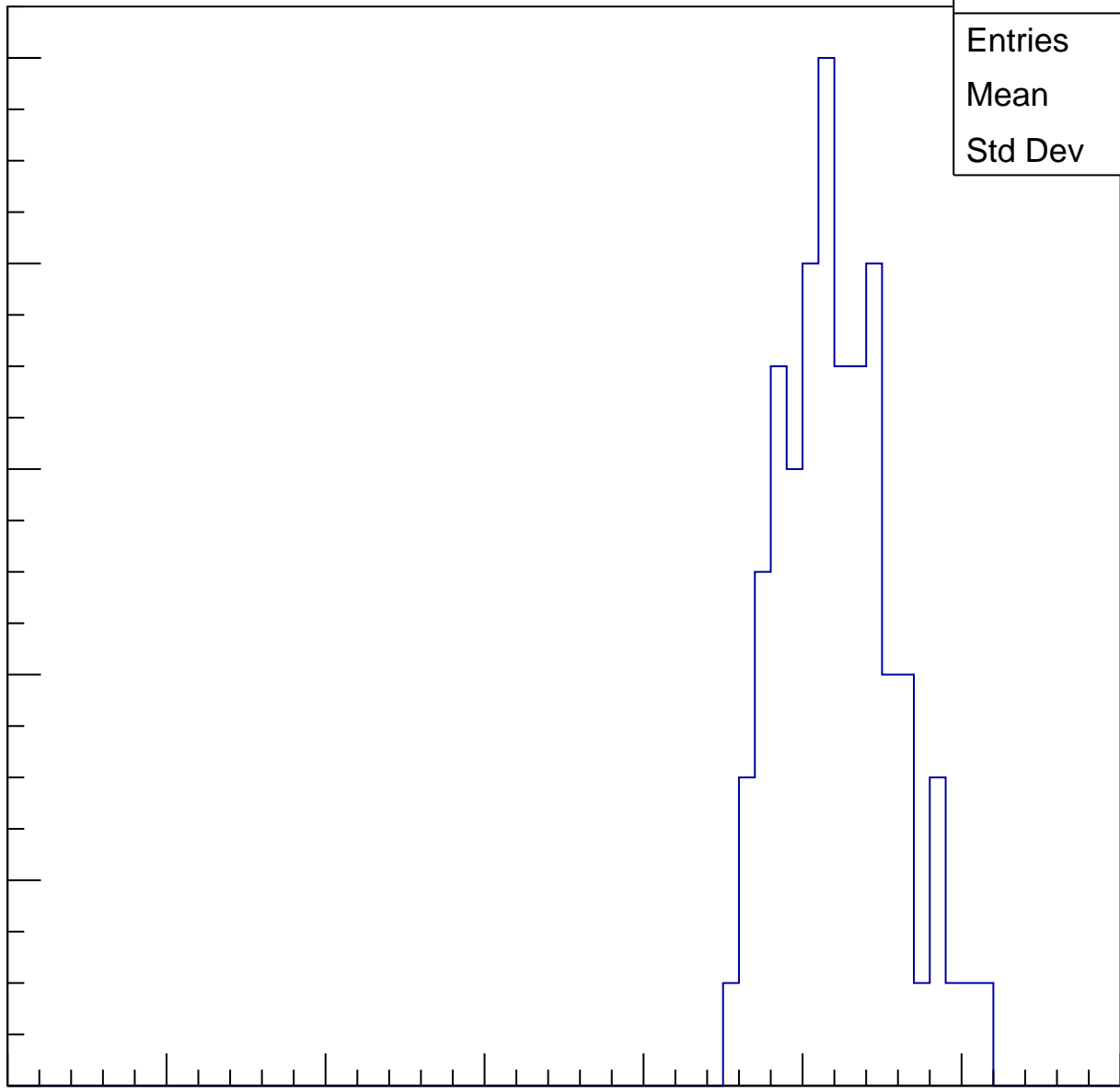
Entries	77
Mean	51.69
Std Dev	3.51

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B0L000S, U25-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

6

5

4

3

2

1

0

Entries	44
Mean	57.73
Std Dev	2.823

ampl

0

10

20

30

40

50

60

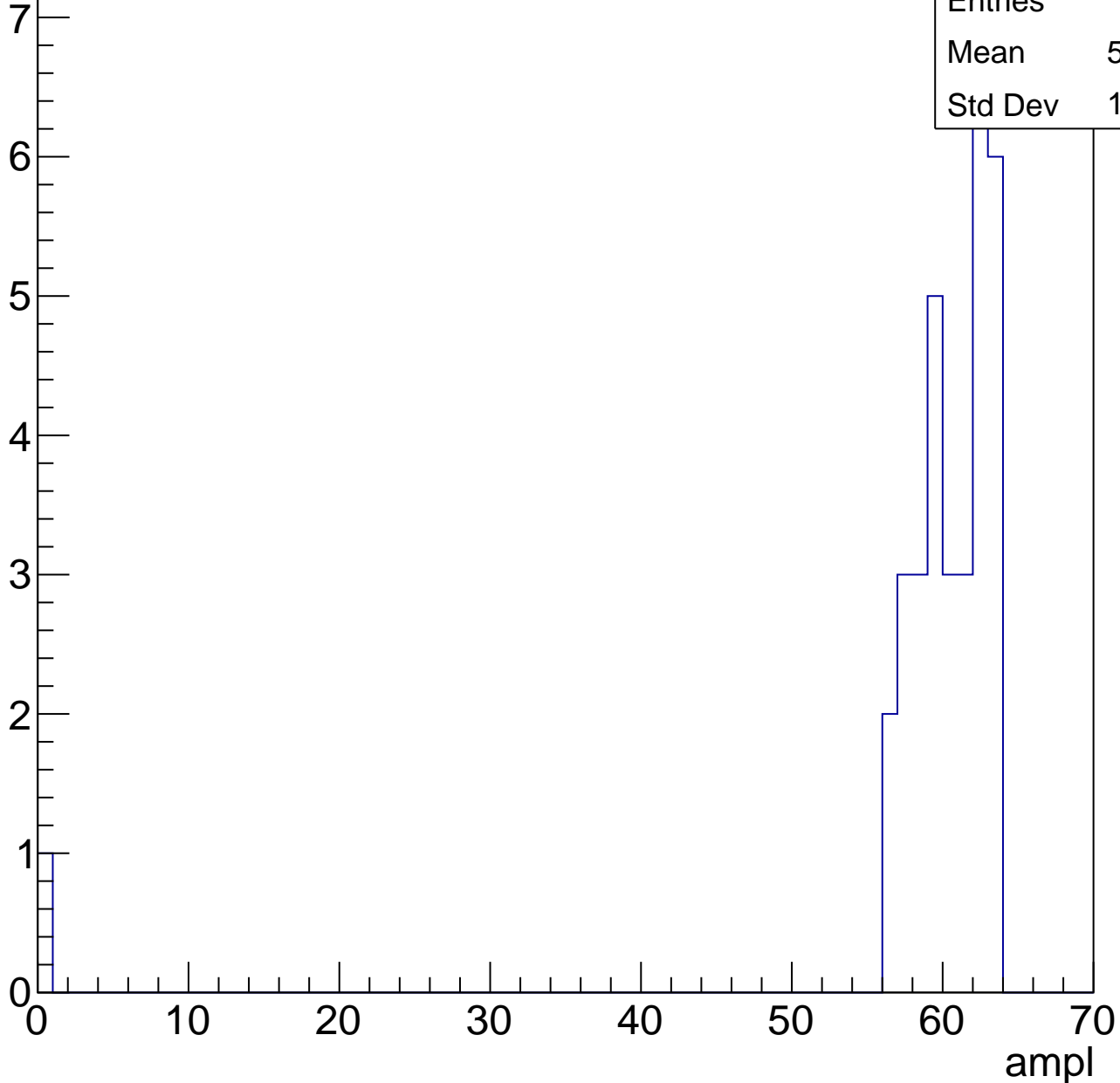
70

# B0L000S, U25-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	33
Mean	58.39
Std Dev	10.55



# B0L000S, U25-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch122, adc0

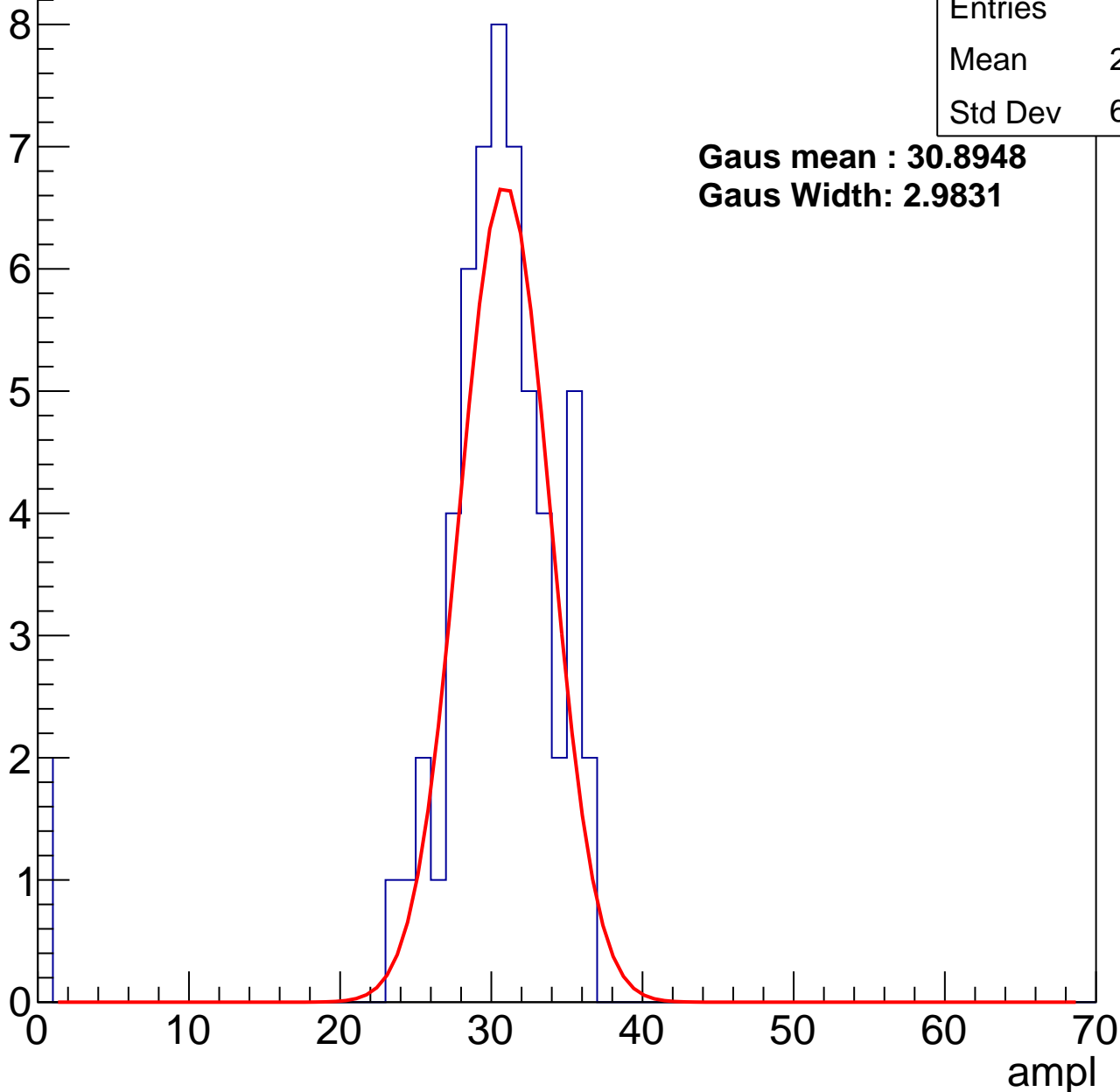
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	57
Mean	29.23
Std Dev	6.319

**Gaus mean : 30.8948**

**Gaus Width: 2.9831**



# B0L000S, U25-ch122, adc1

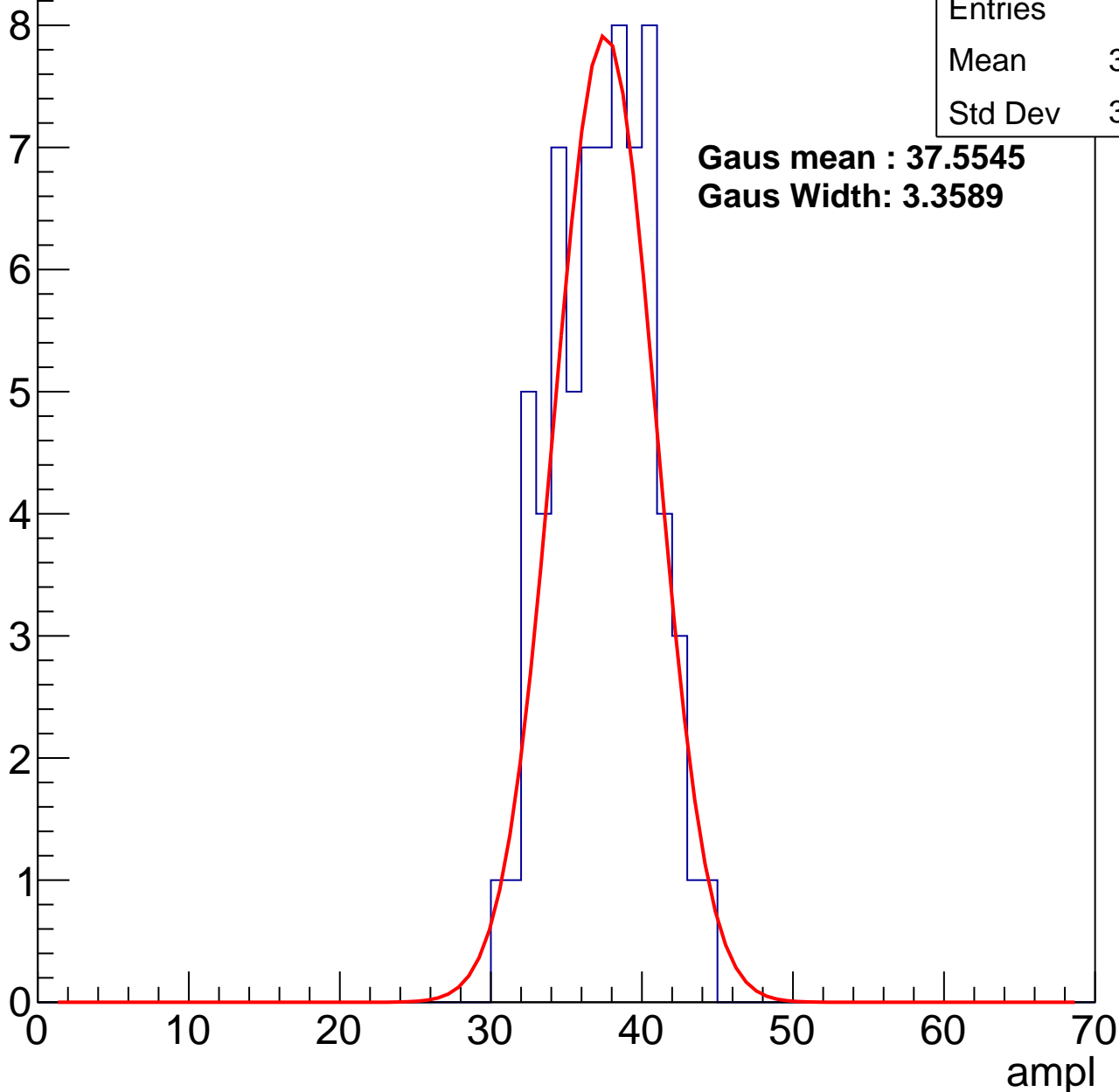
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	69
Mean	36.97
Std Dev	3.176

**Gaus mean : 37.5545**

**Gaus Width: 3.3589**



# B0L000S, U25-ch122, adc2

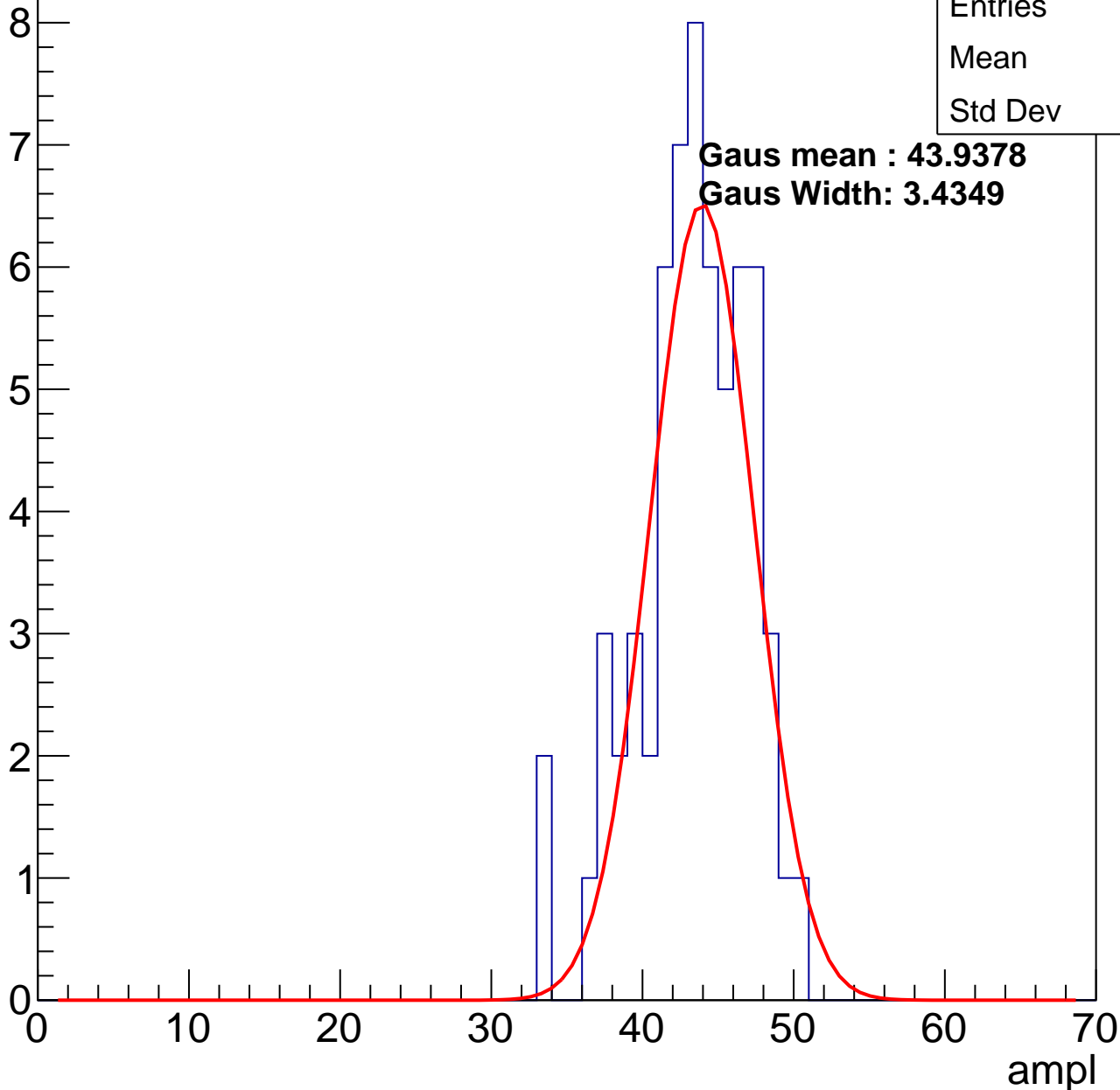
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	62
Mean	42.9
Std Dev	3.68

**Gaus mean : 43.9378**

**Gaus Width: 3.4349**

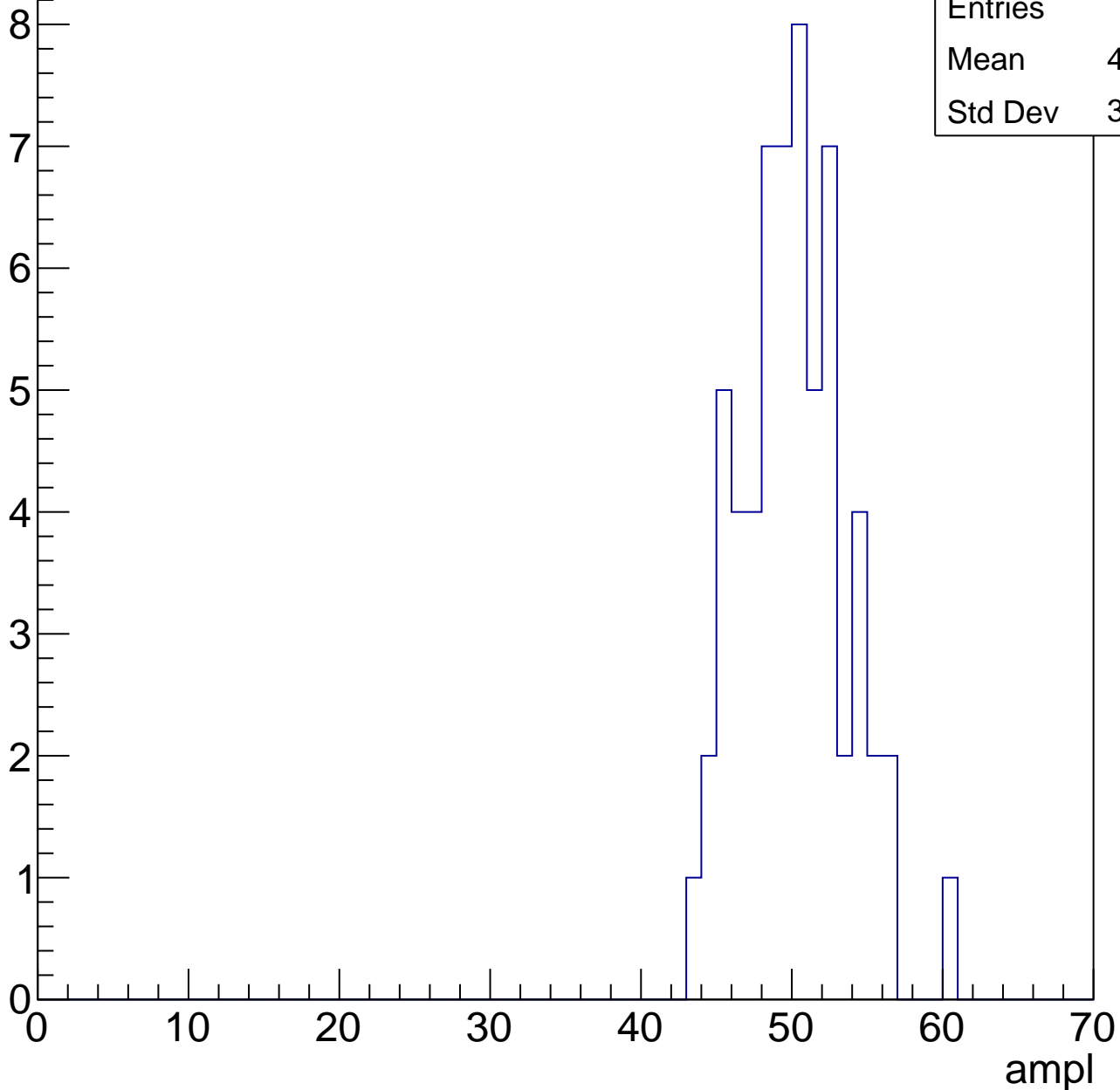


# B0L000S, U25-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	61
Mean	49.67
Std Dev	3.415

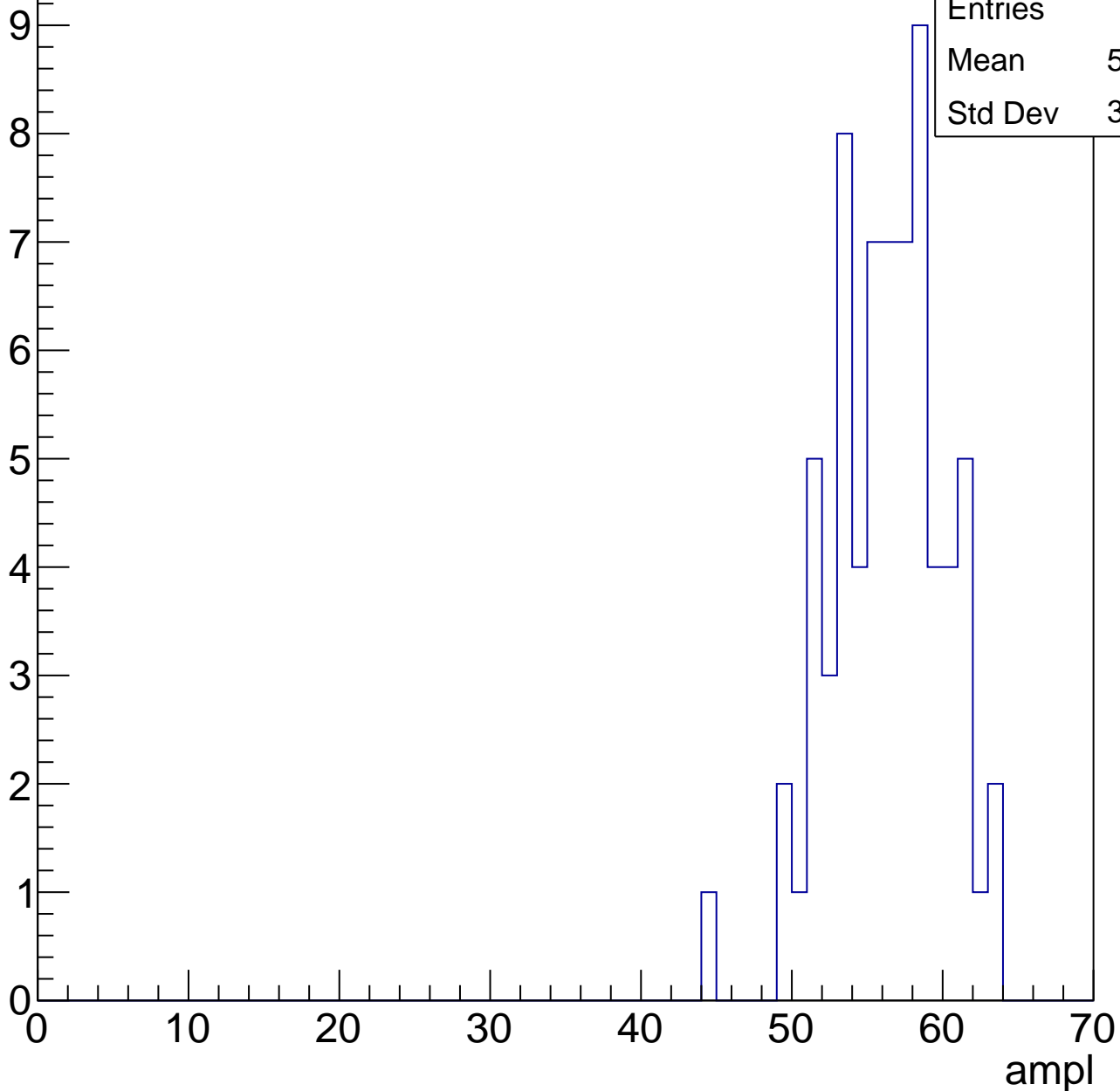


# B0L000S, U25-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	55.86
Std Dev	3.677

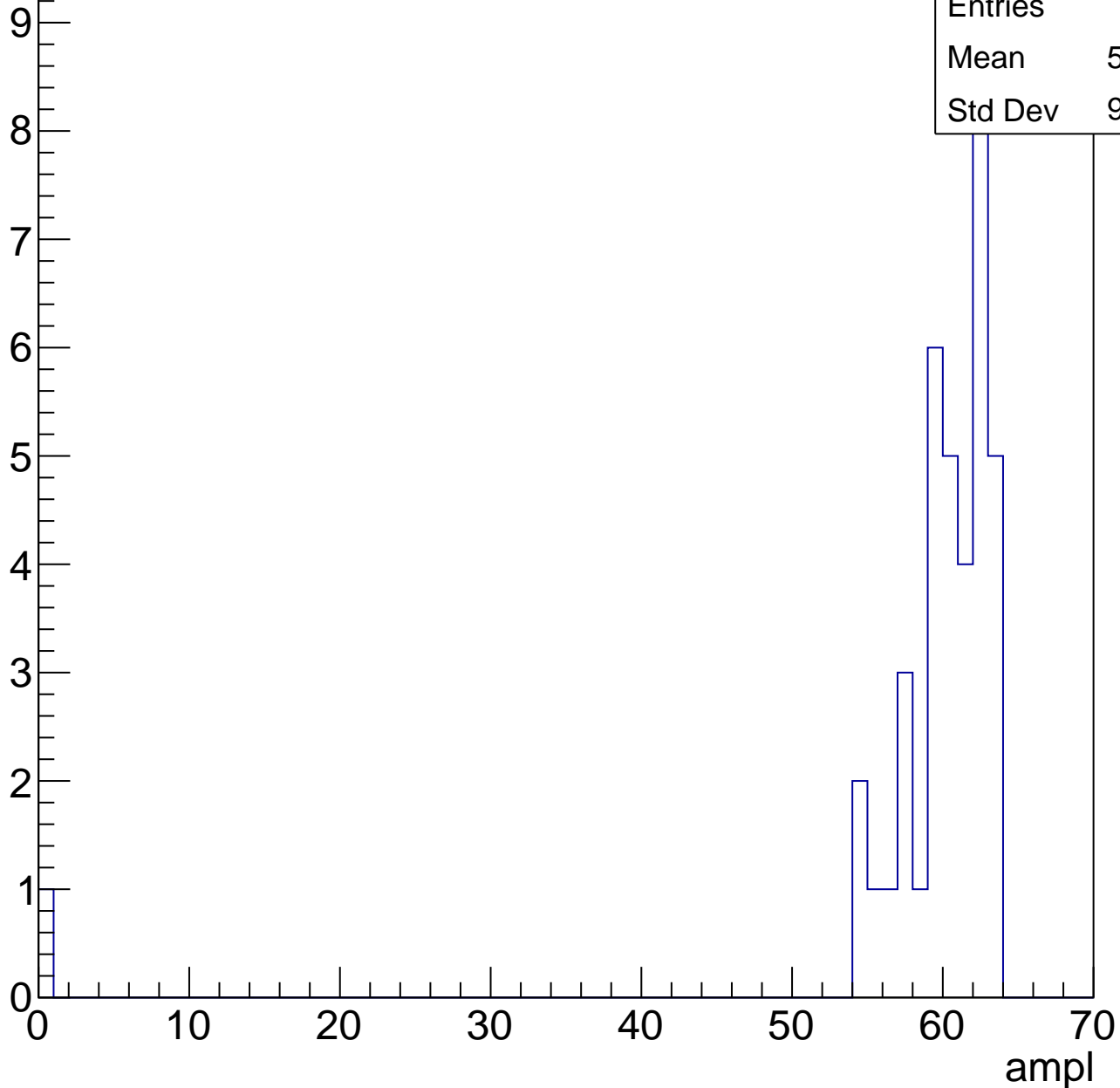


# B0L000S, U25-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.39
Std Dev	9.917



# B0L000S, U25-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	0
Mean	0
Std Dev	0

# B0L000S, U25-ch123, adc0

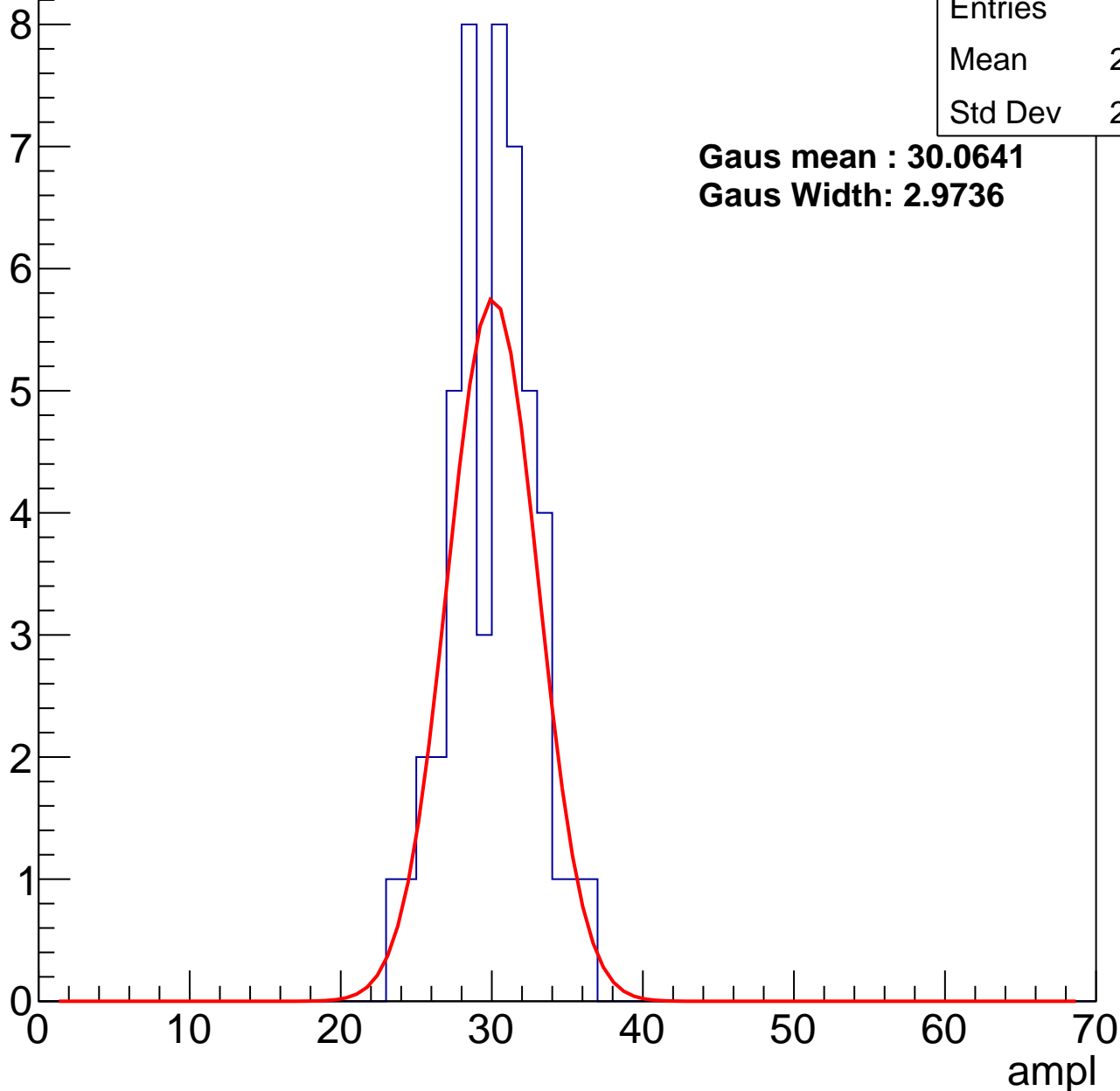
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	49
Mean	29.57
Std Dev	2.777

**Gaus mean : 30.0641**

**Gaus Width: 2.9736**



# B0L000S, U25-ch123, adc1

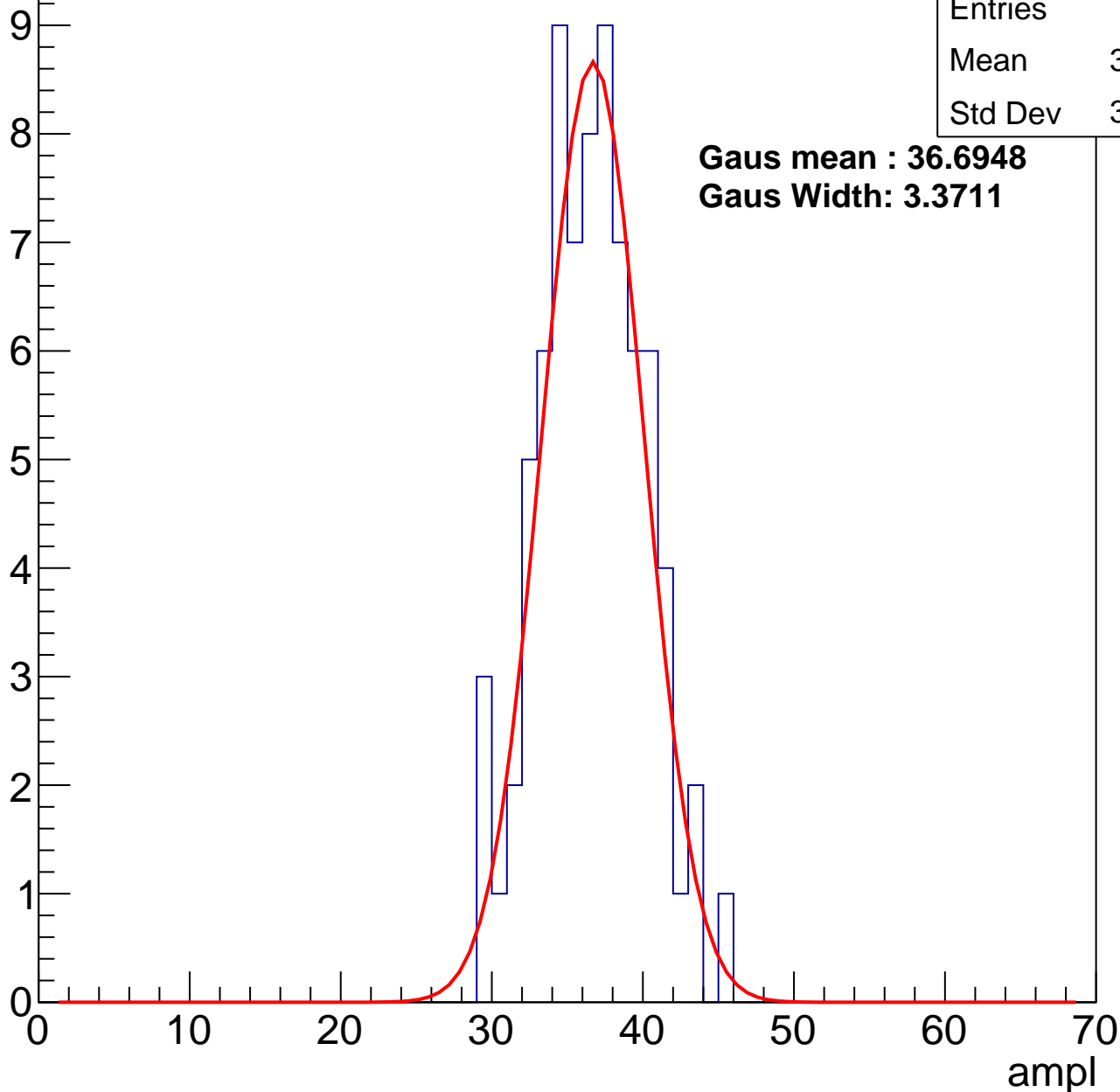
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	36.18
Std Dev	3.437

**Gaus mean : 36.6948**

**Gaus Width: 3.3711**



# B0L000S, U25-ch123, adc2

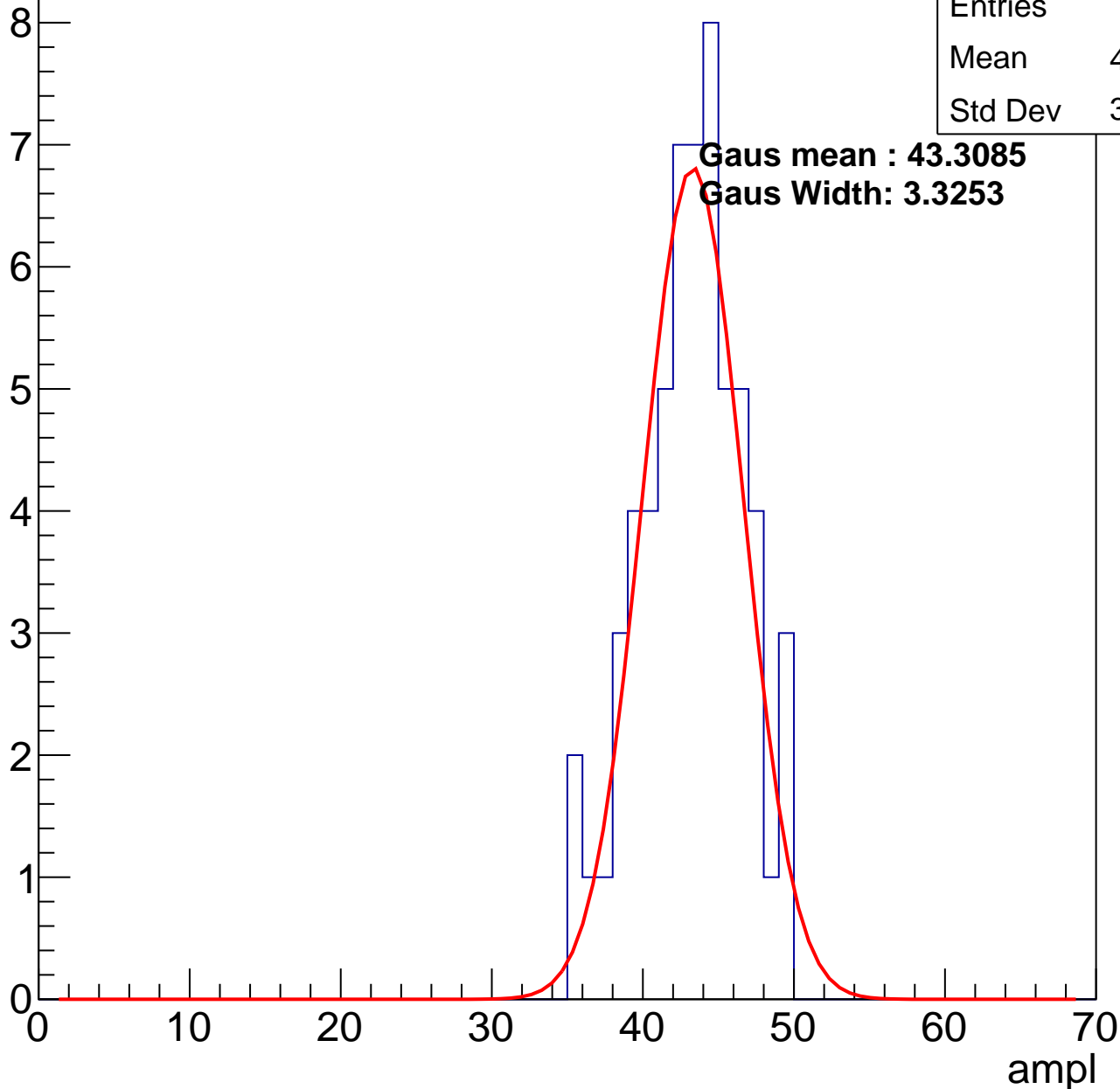
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	60
Mean	42.72
Std Dev	3.367

**Gaus mean : 43.3085**

**Gaus Width: 3.3253**

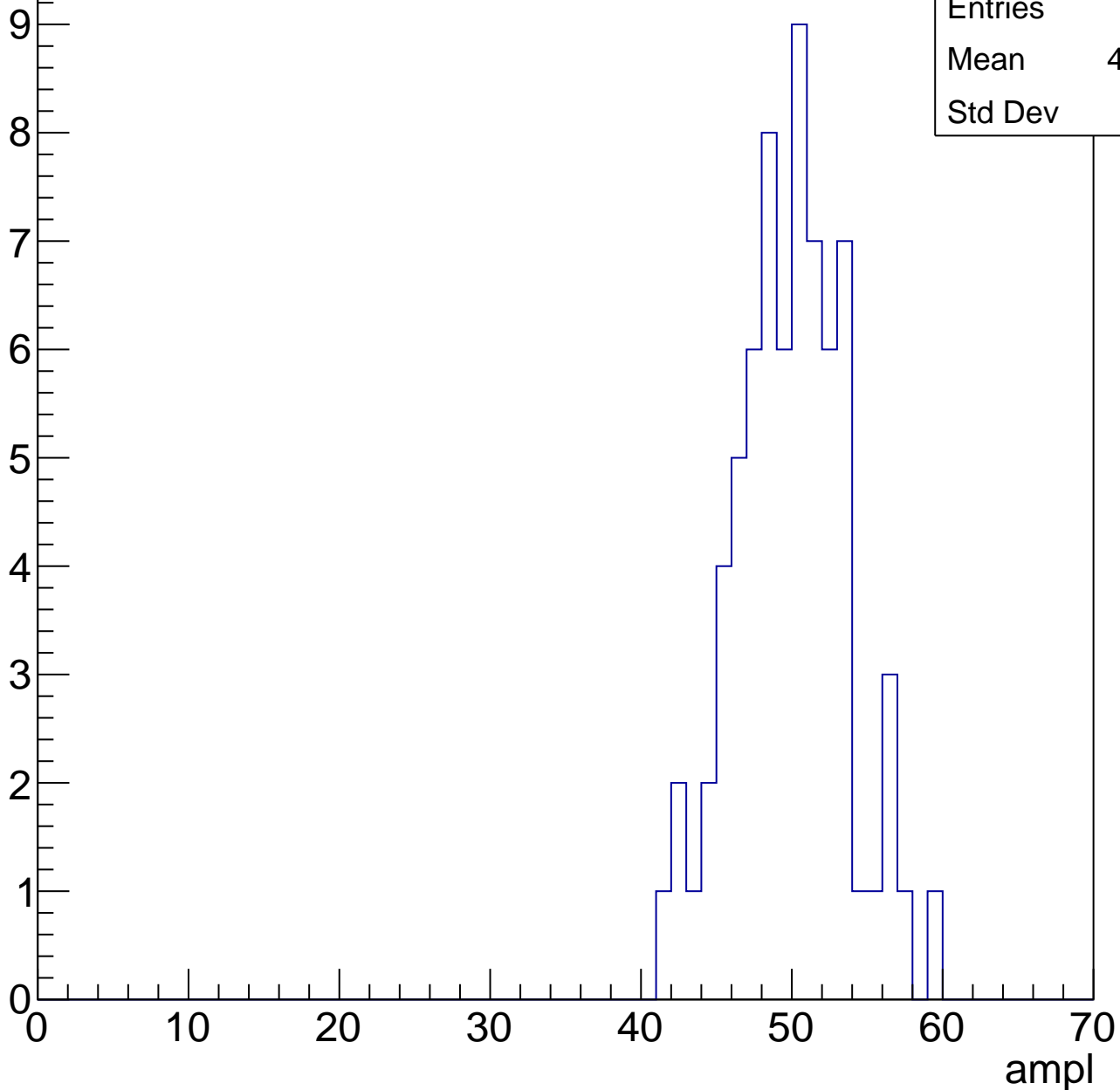


# B0L000S, U25-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	71
Mean	49.42
Std Dev	3.66

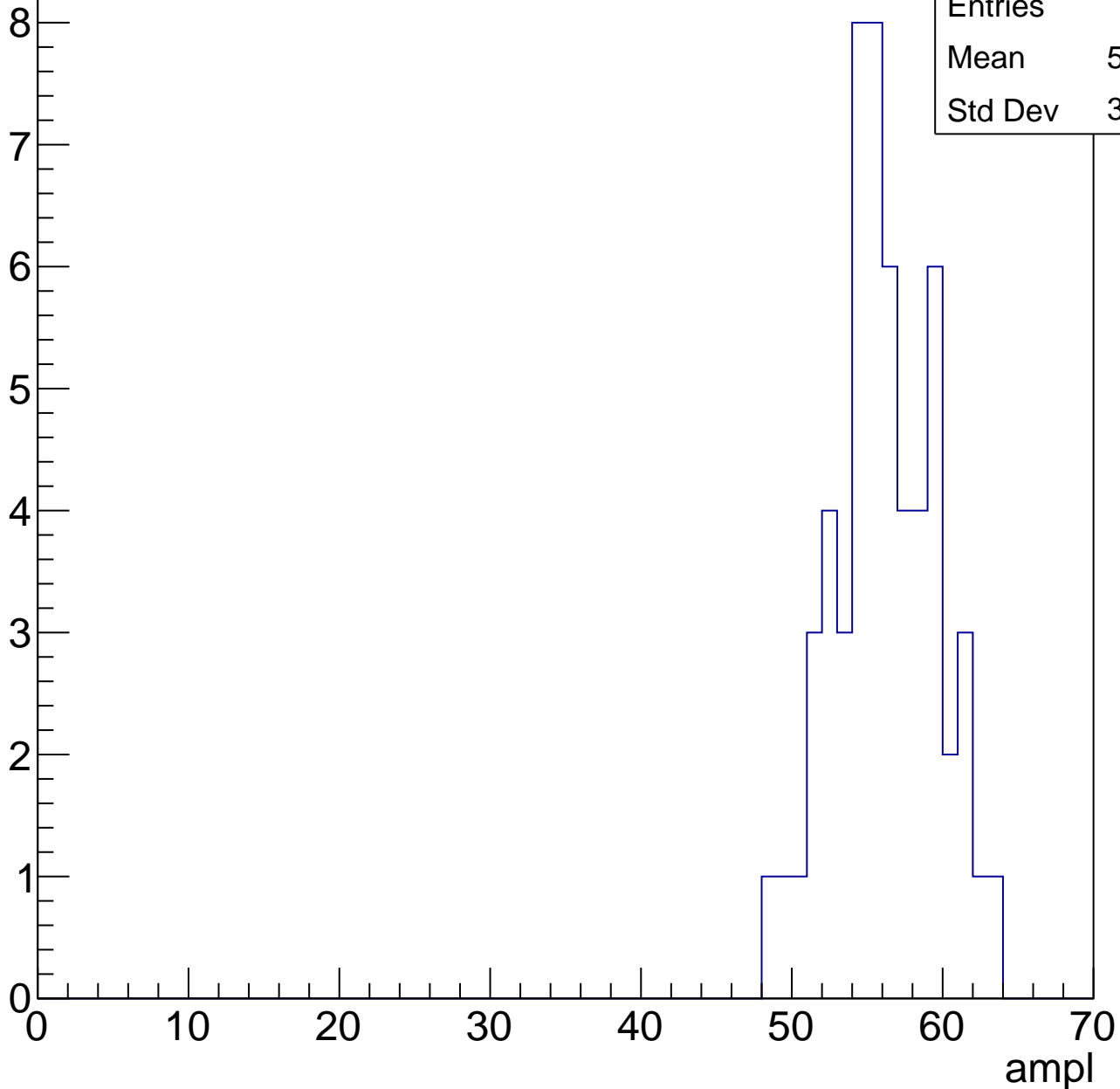


# B0L000S, U25-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	56
Mean	55.66
Std Dev	3.313

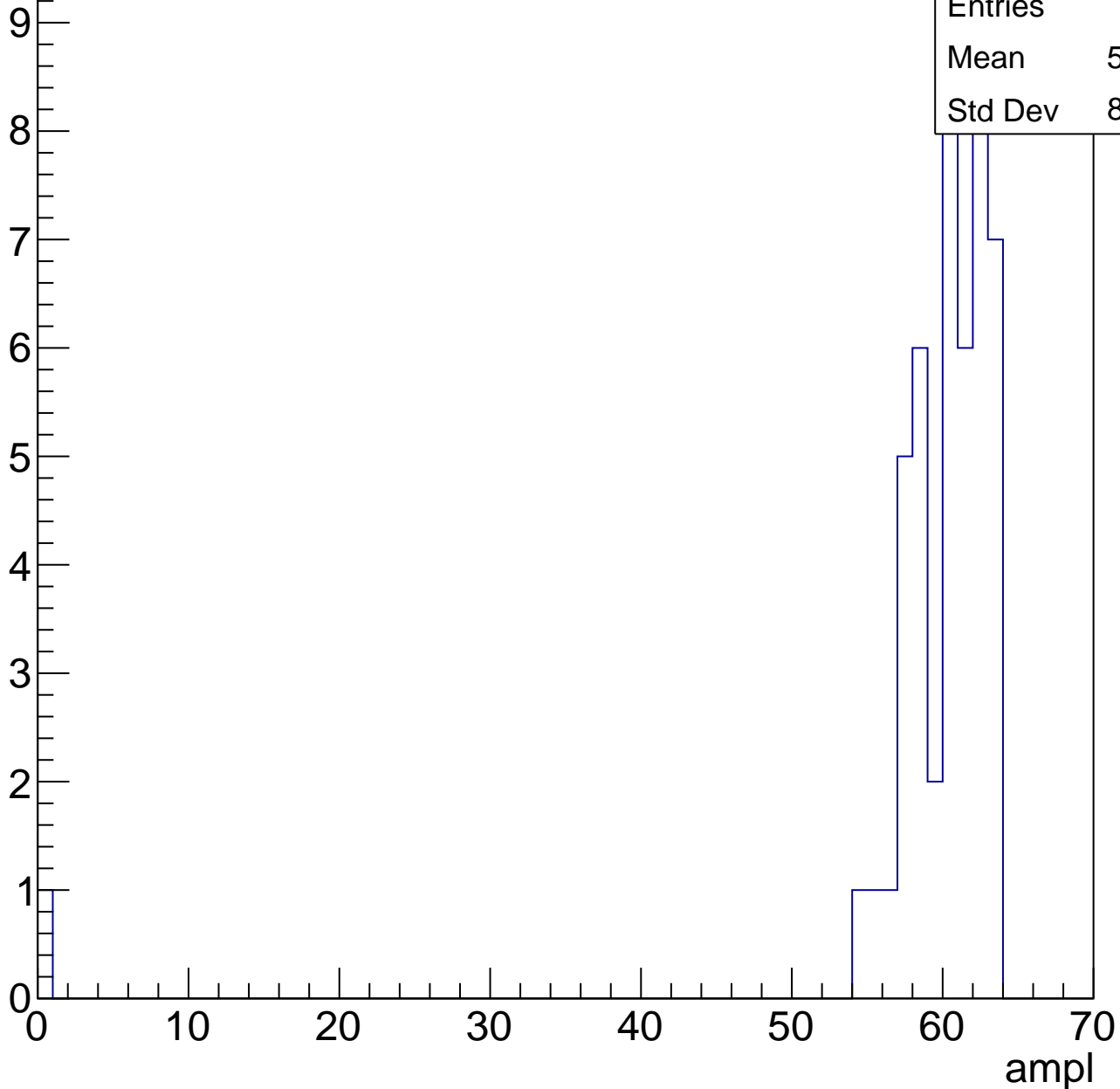


# B0L000S, U25-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

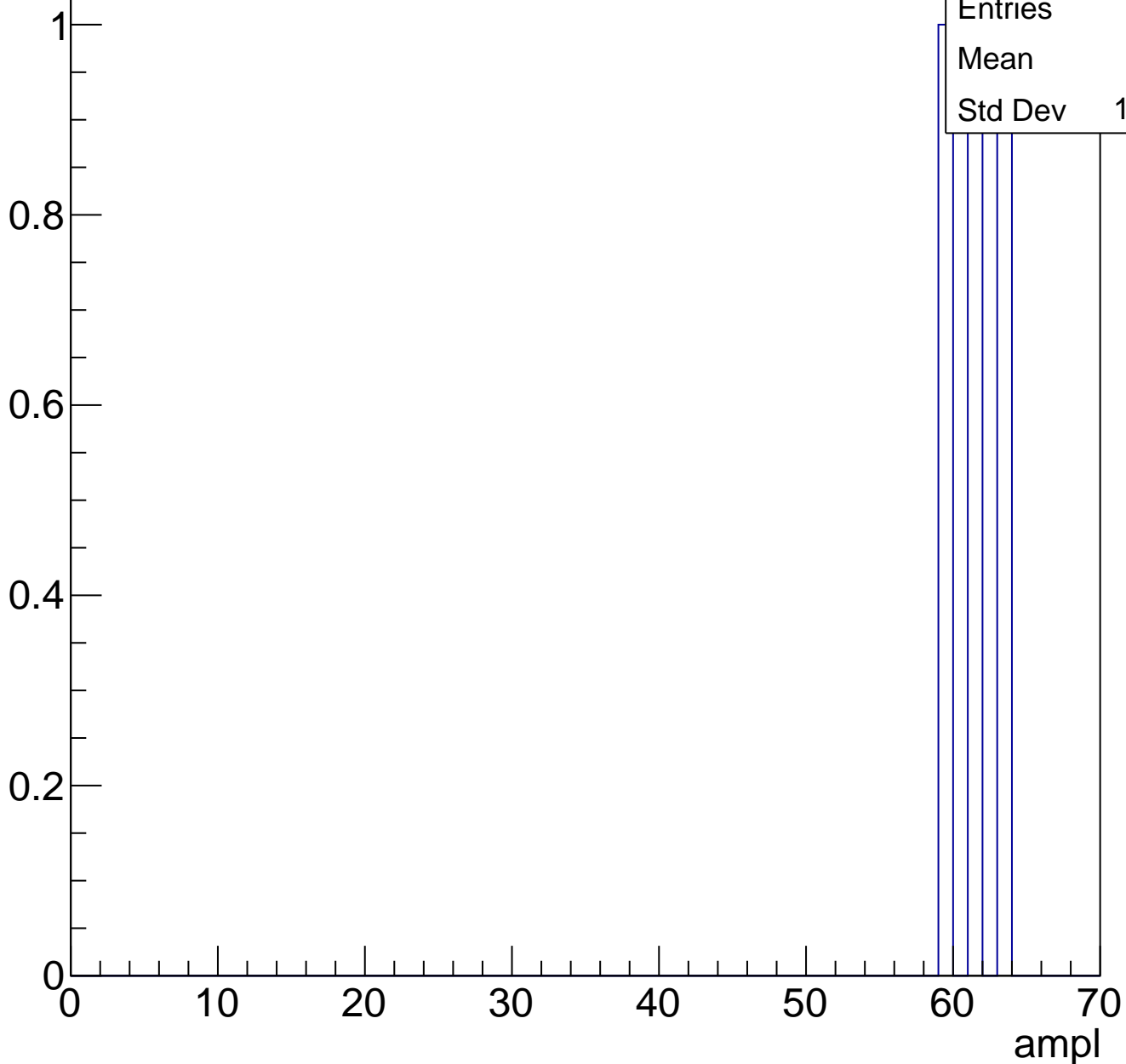
Entries	47
Mean	58.74
Std Dev	8.967



# B0L000S, U25-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch124, adc0

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	74
Mean	31.31
Std Dev	3.361

**Gaus mean : 31.9254**

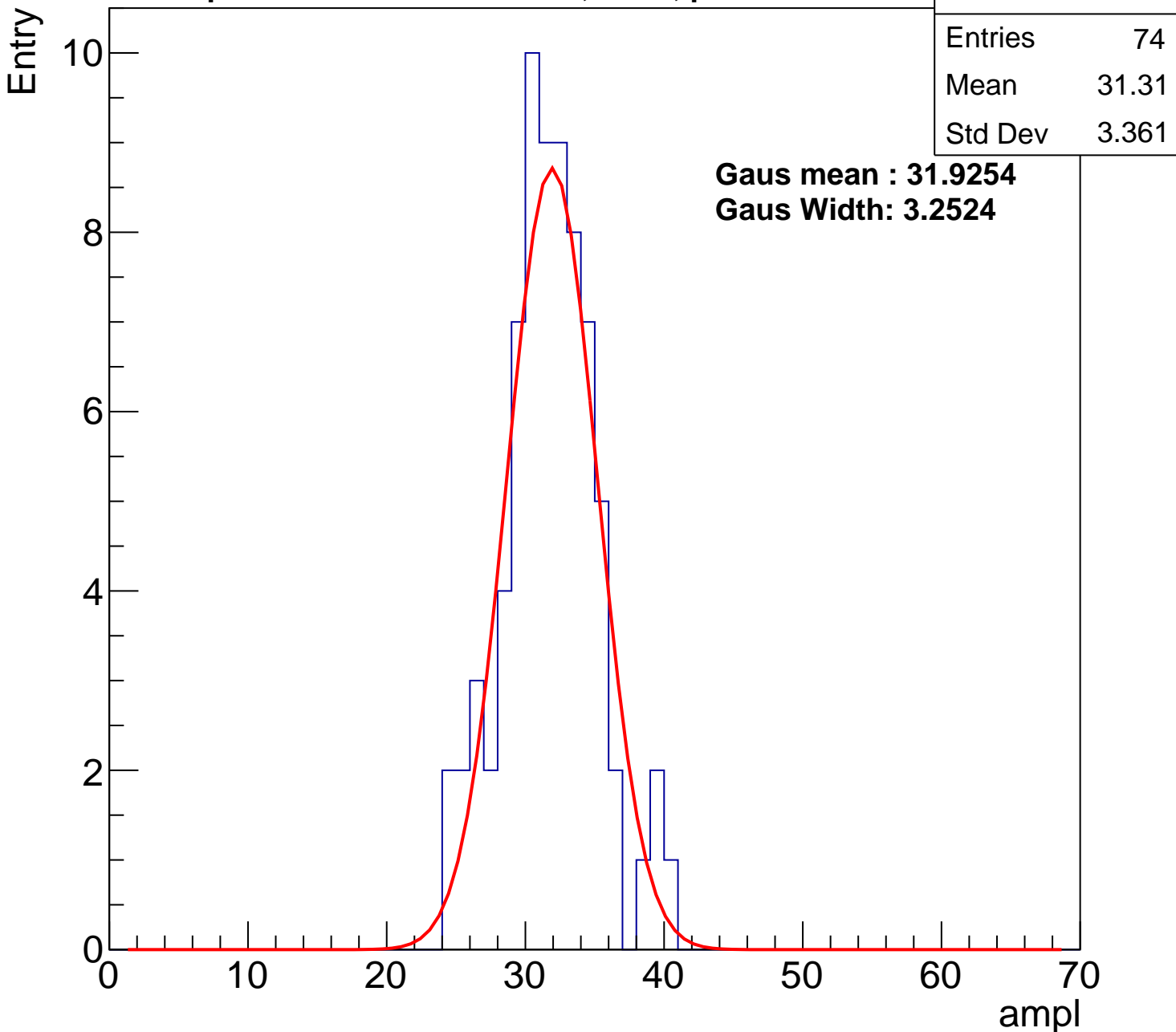
**Gaus Width: 3.2524**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch124, adc1

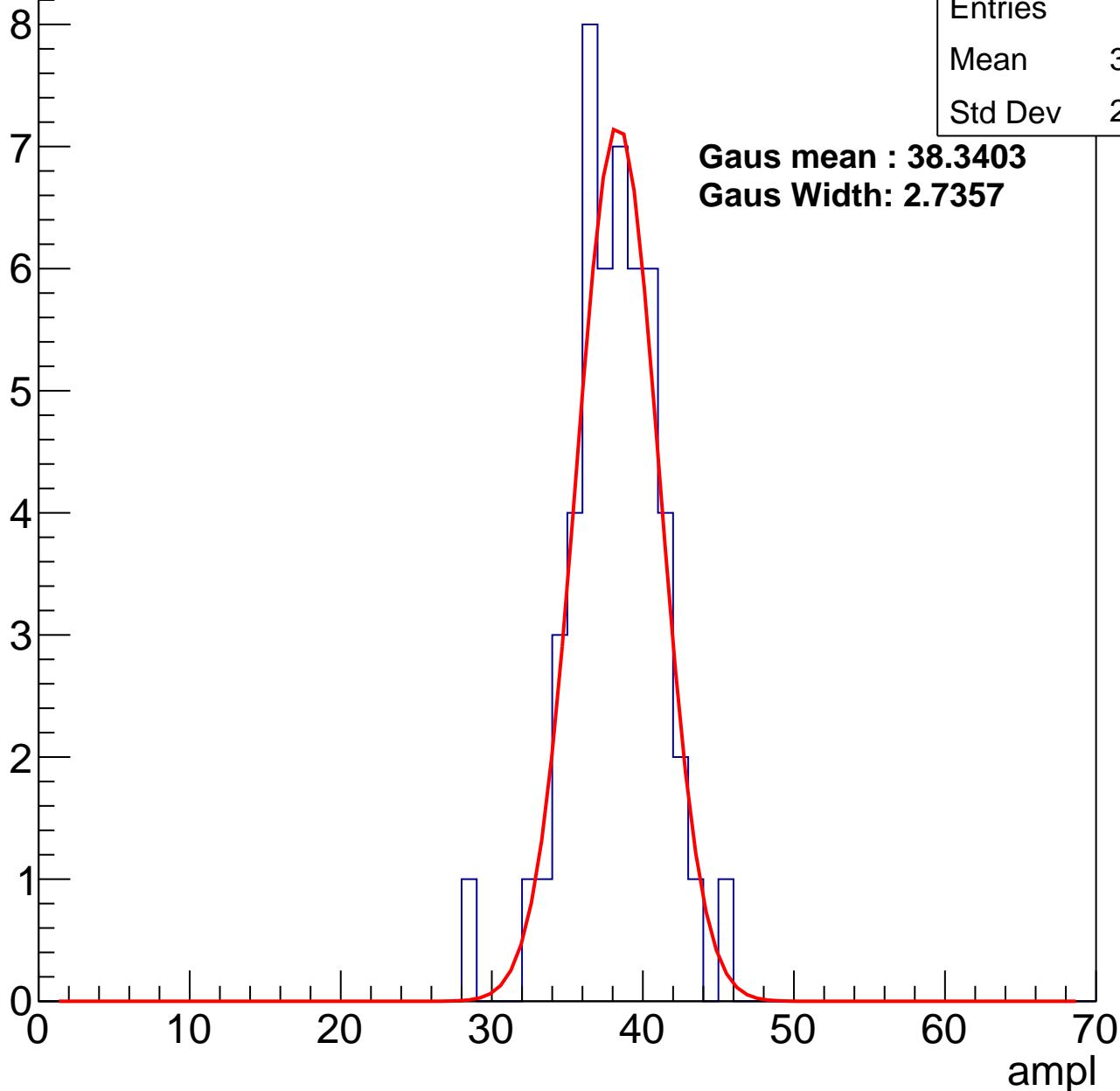
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	51
Mean	37.67
Std Dev	2.968

**Gaus mean : 38.3403**

**Gaus Width: 2.7357**



# B0L000S, U25-ch124, adc2

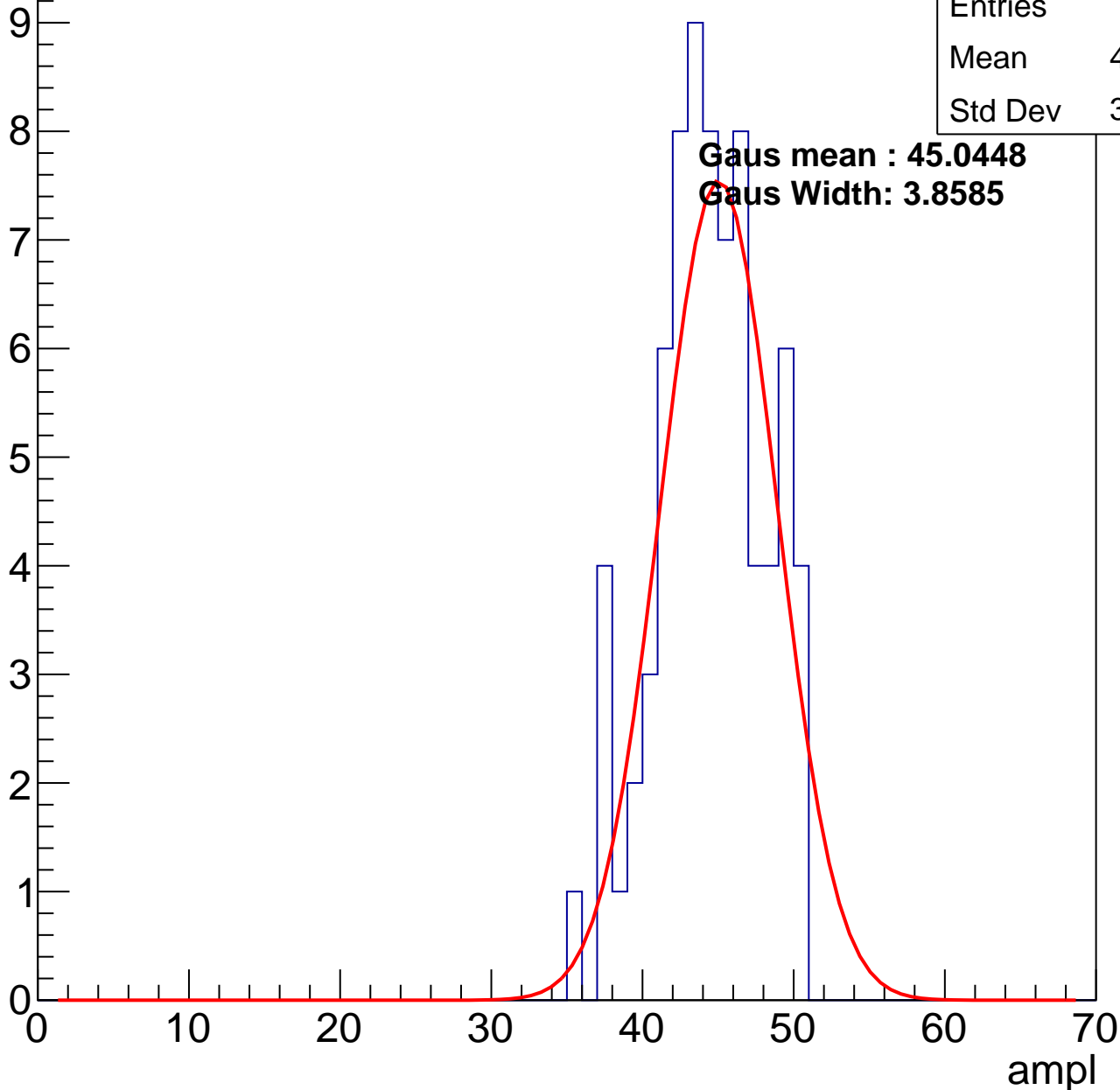
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	75
Mean	43.96
Std Dev	3.538

**Gaus mean : 45.0448**

**Gaus Width: 3.8585**



# B0L000S, U25-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

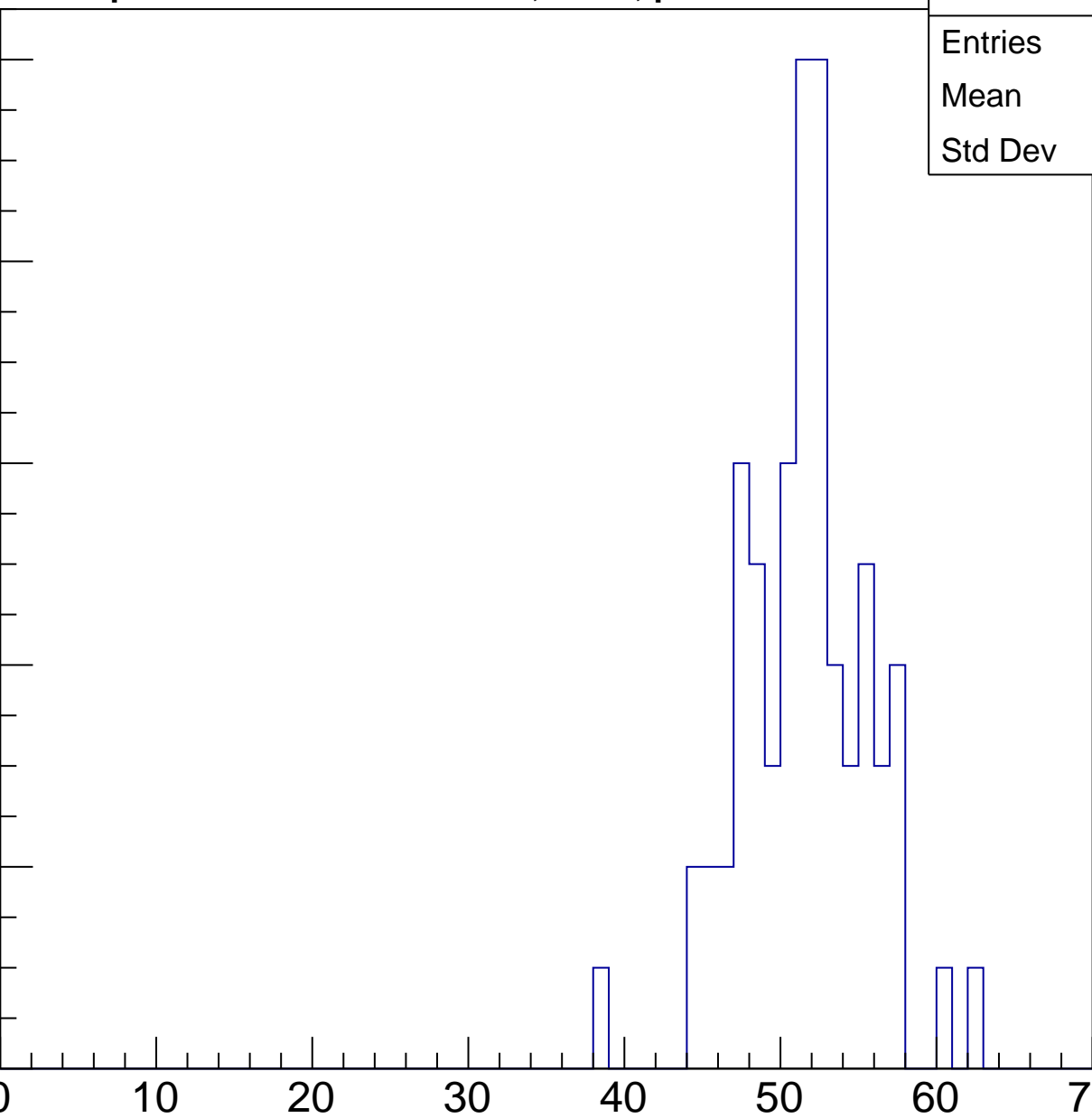
Entries	68
Mean	51.09
Std Dev	4.025

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

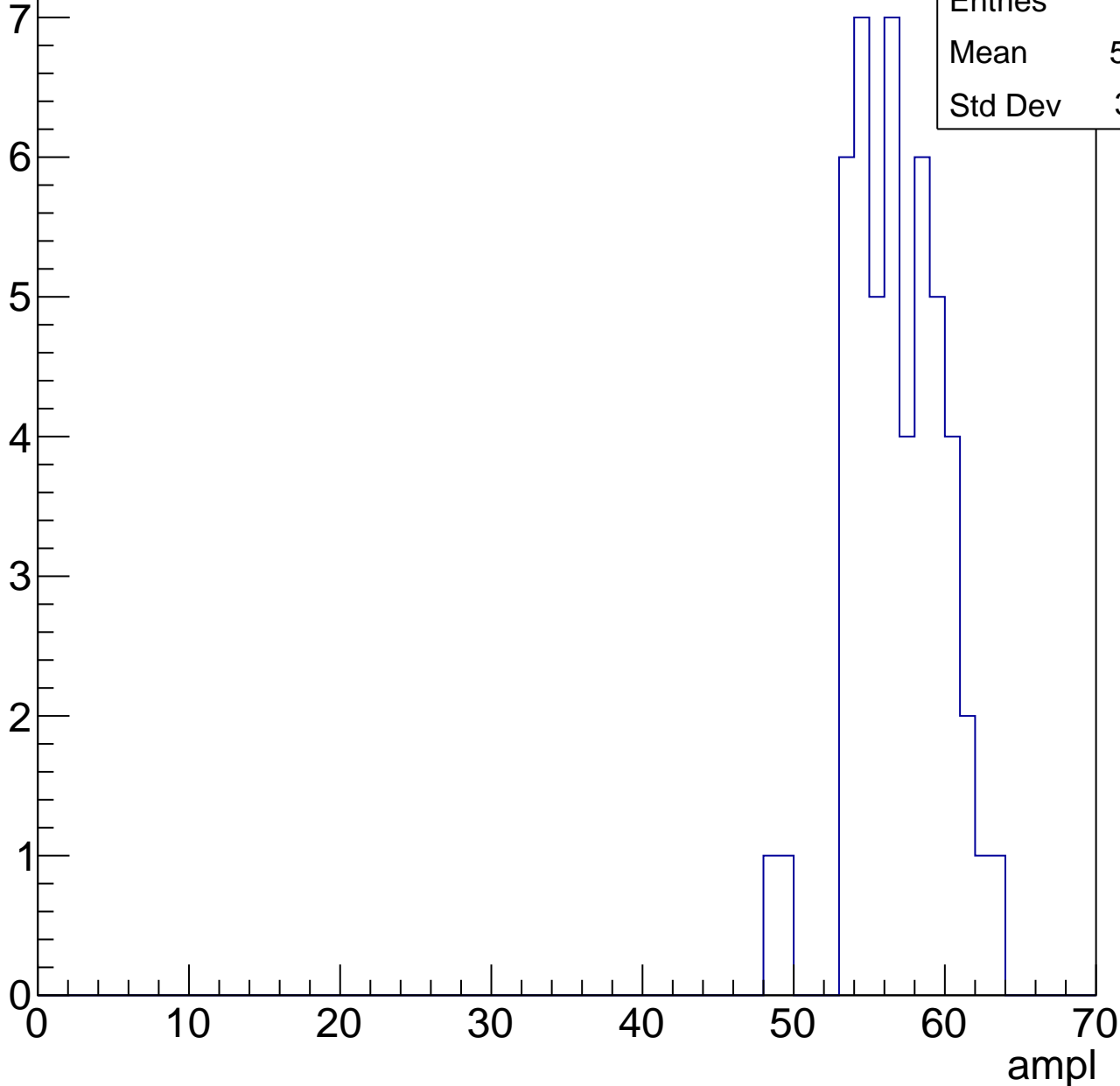


# B0L000S, U25-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	50
Mean	56.36
Std Dev	3.051

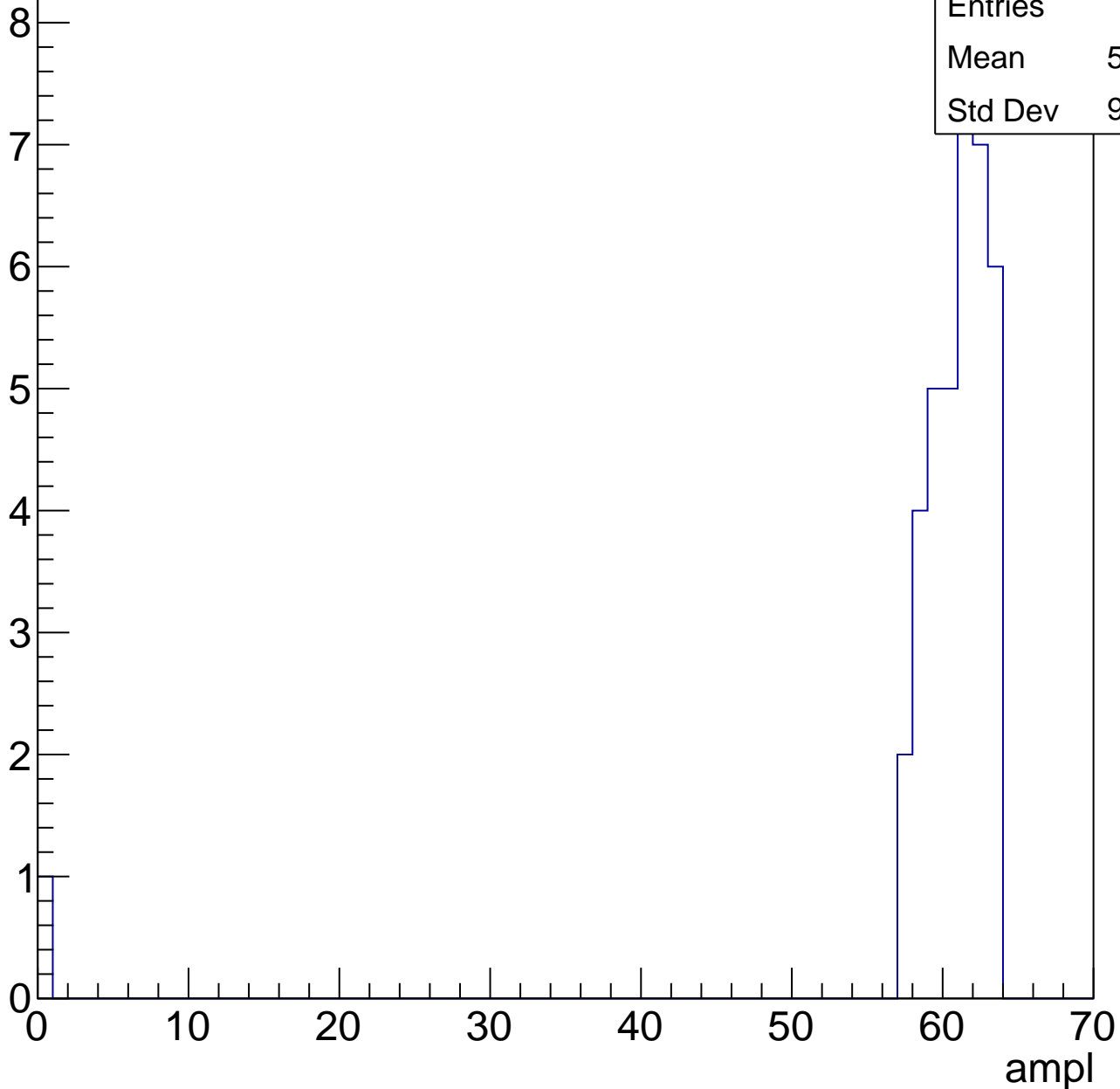


# B0L000S, U25-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	38
Mean	58.97
Std Dev	9.853



# B0L000S, U25-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry





# B0L000S, U25-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	8
Std Dev	11.31

# B0L000S, U25-ch125, adc0

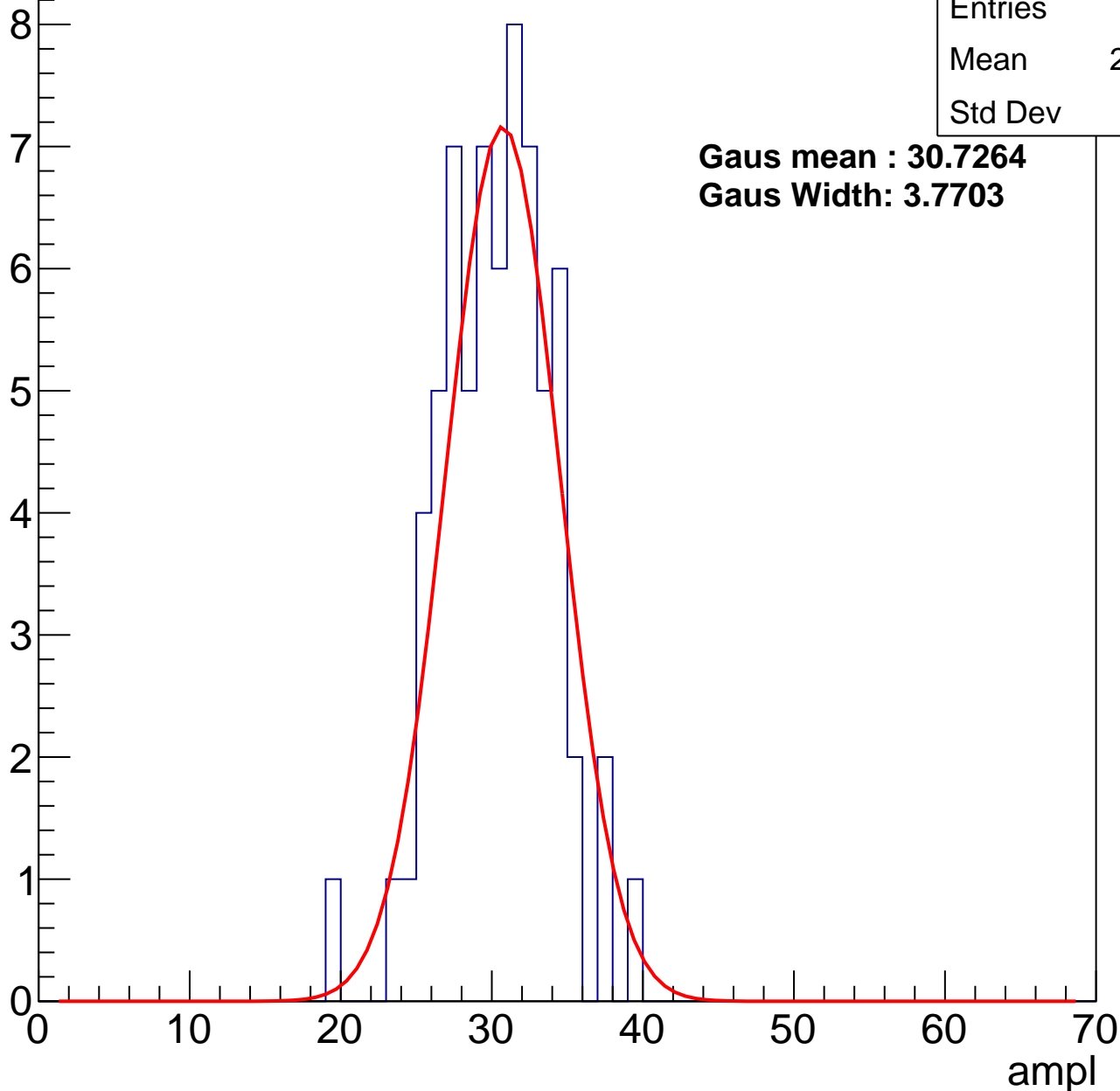
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	68
Mean	29.88
Std Dev	3.6

**Gaus mean : 30.7264**

**Gaus Width: 3.7703**



# B0L000S, U25-ch125, adc1

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entries	75
Mean	36.88
Std Dev	3.604

**Gaus mean : 37.3500**

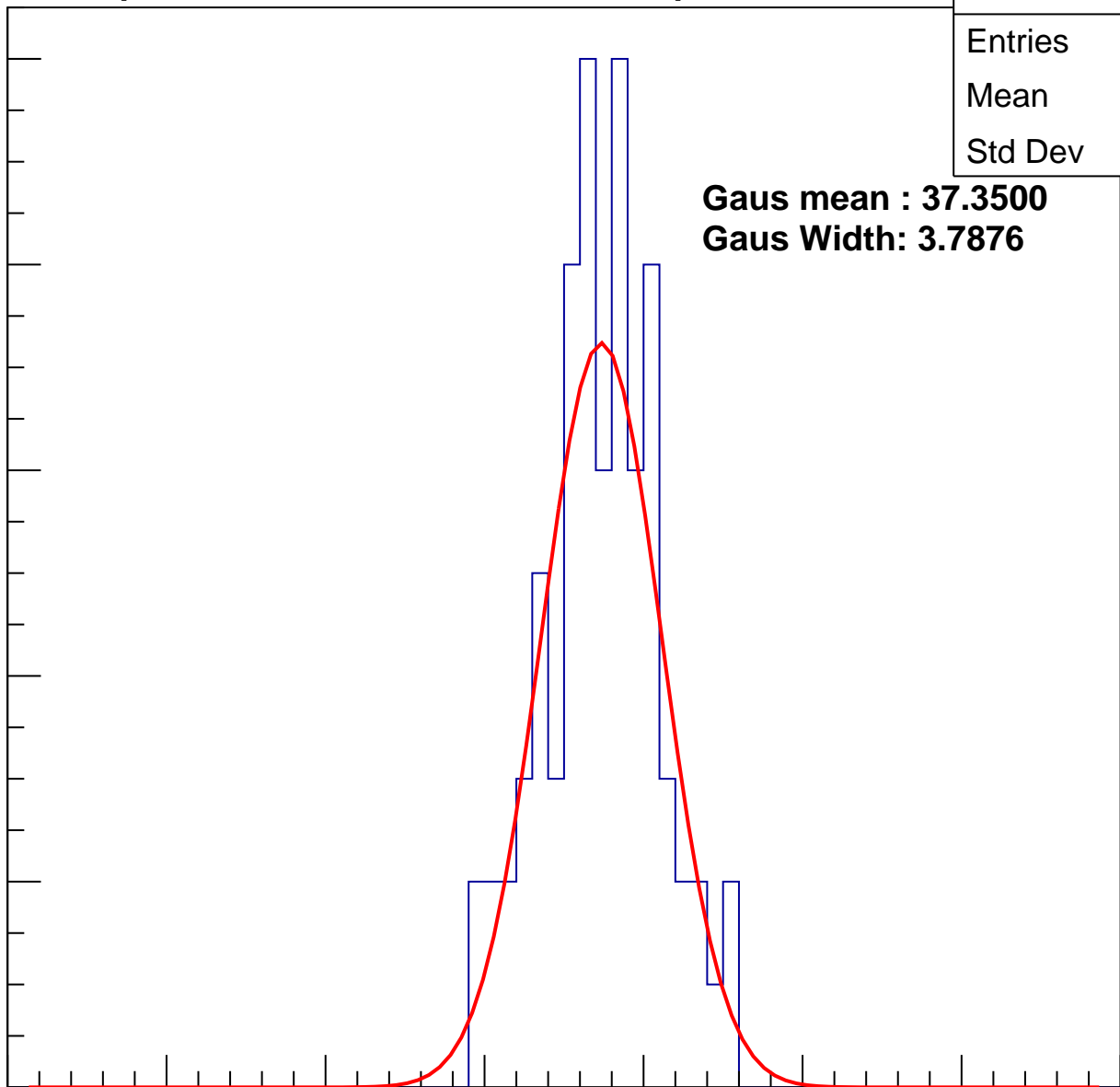
**Gaus Width: 3.7876**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L000S, U25-ch125, adc2

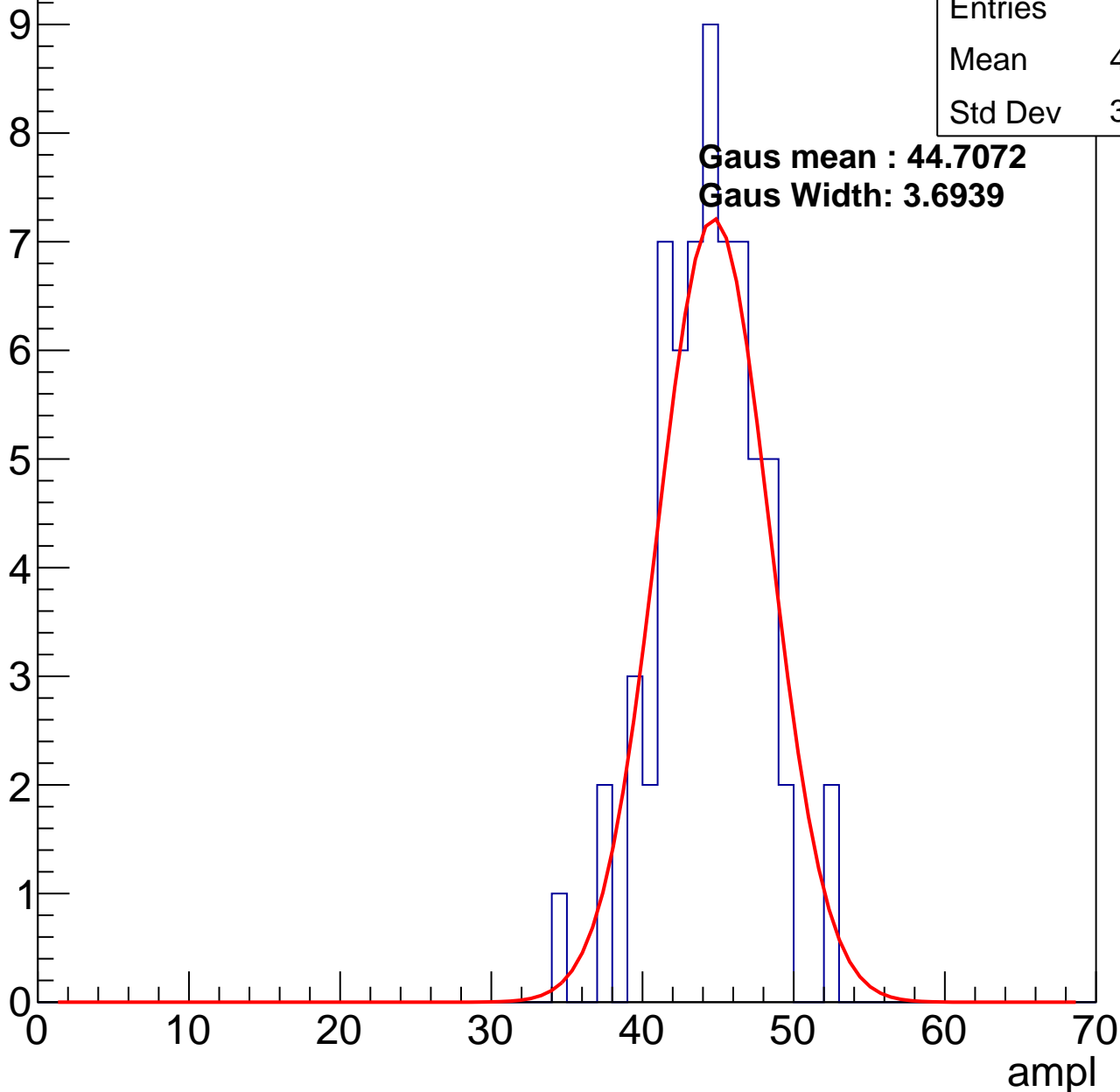
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	43.92
Std Dev	3.366

**Gaus mean : 44.7072**

**Gaus Width: 3.6939**

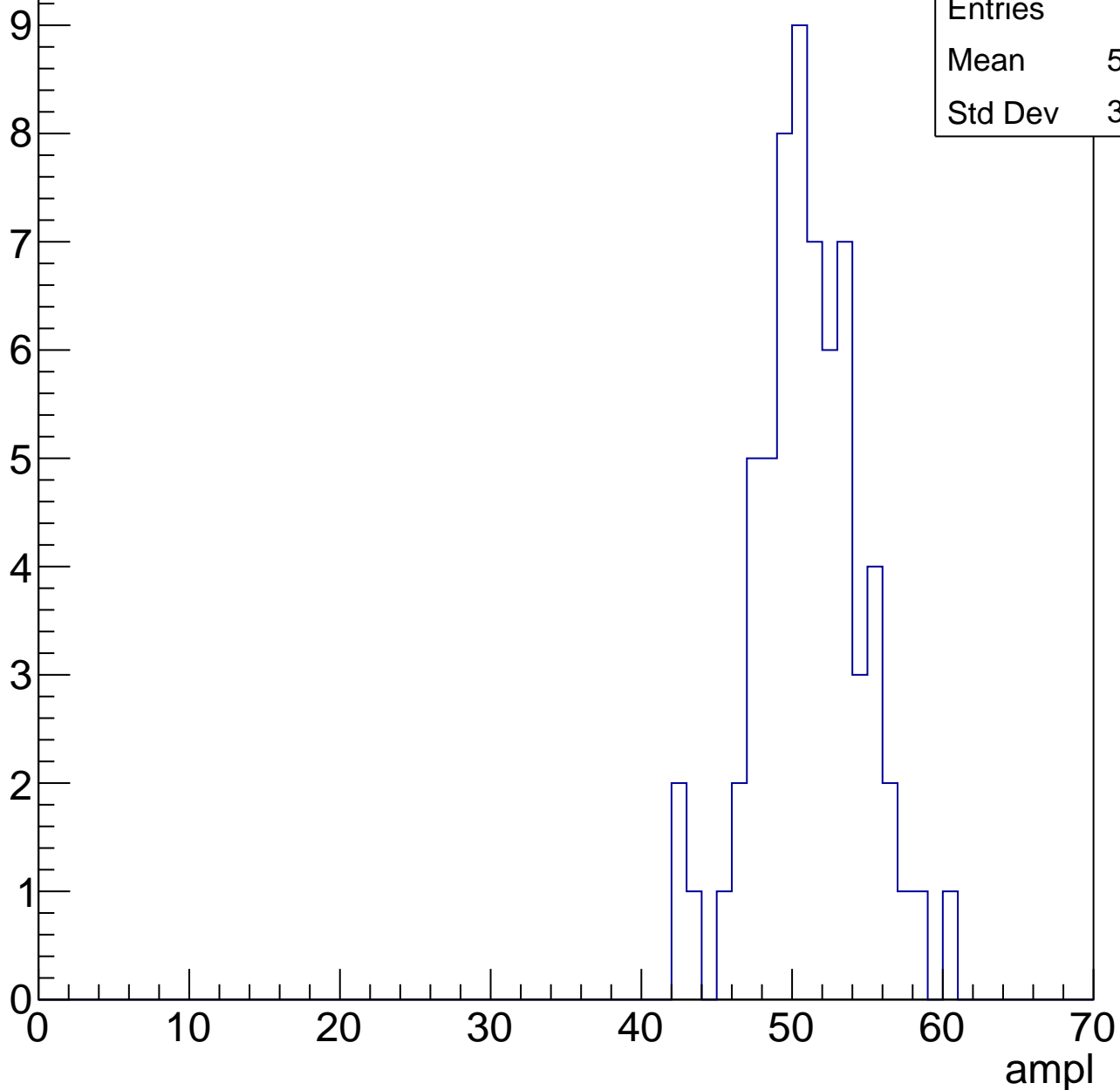


# B0L000S, U25-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	65
Mean	50.62
Std Dev	3.529

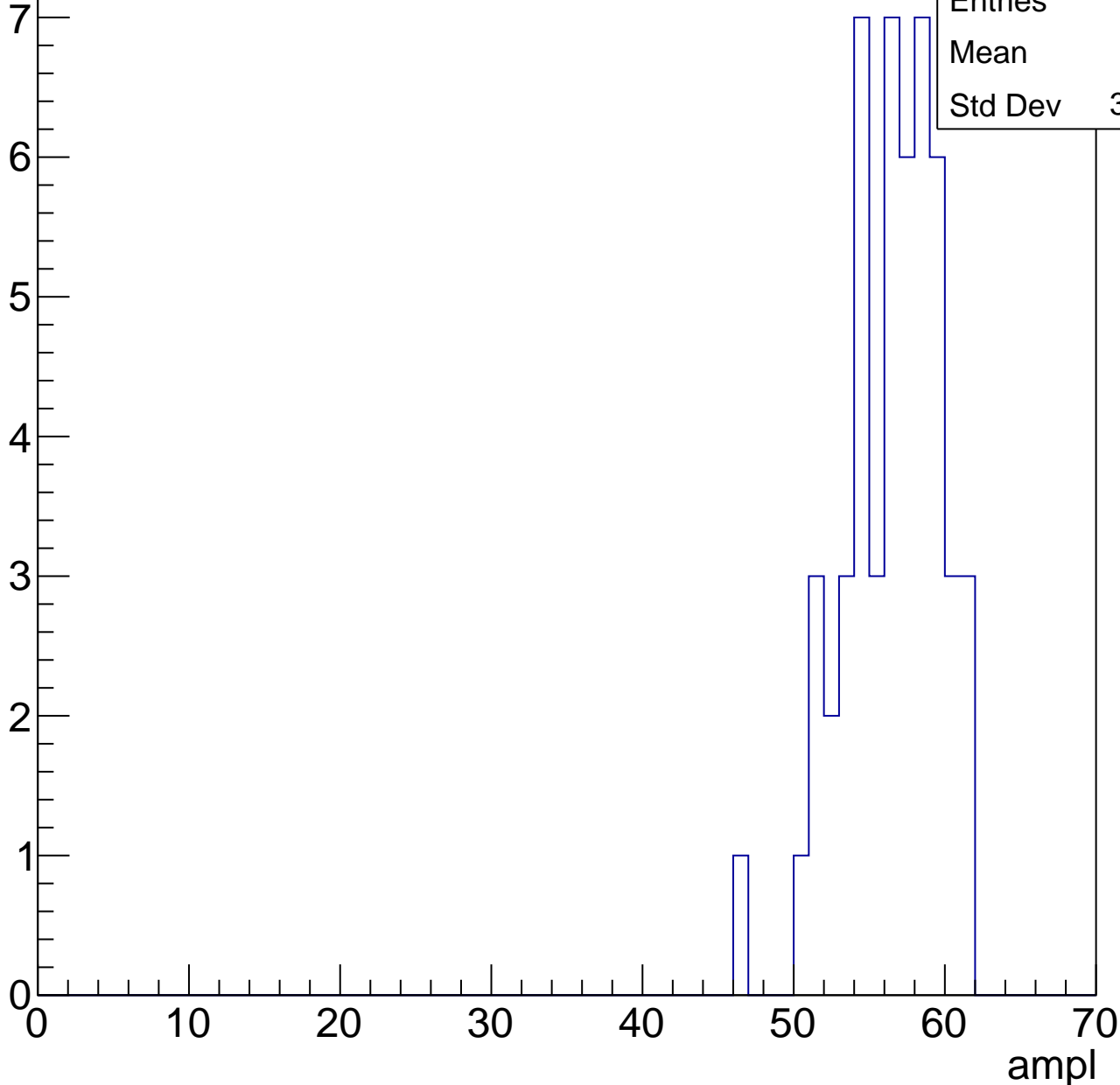


# B0L000S, U25-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	56
Std Dev	3.138



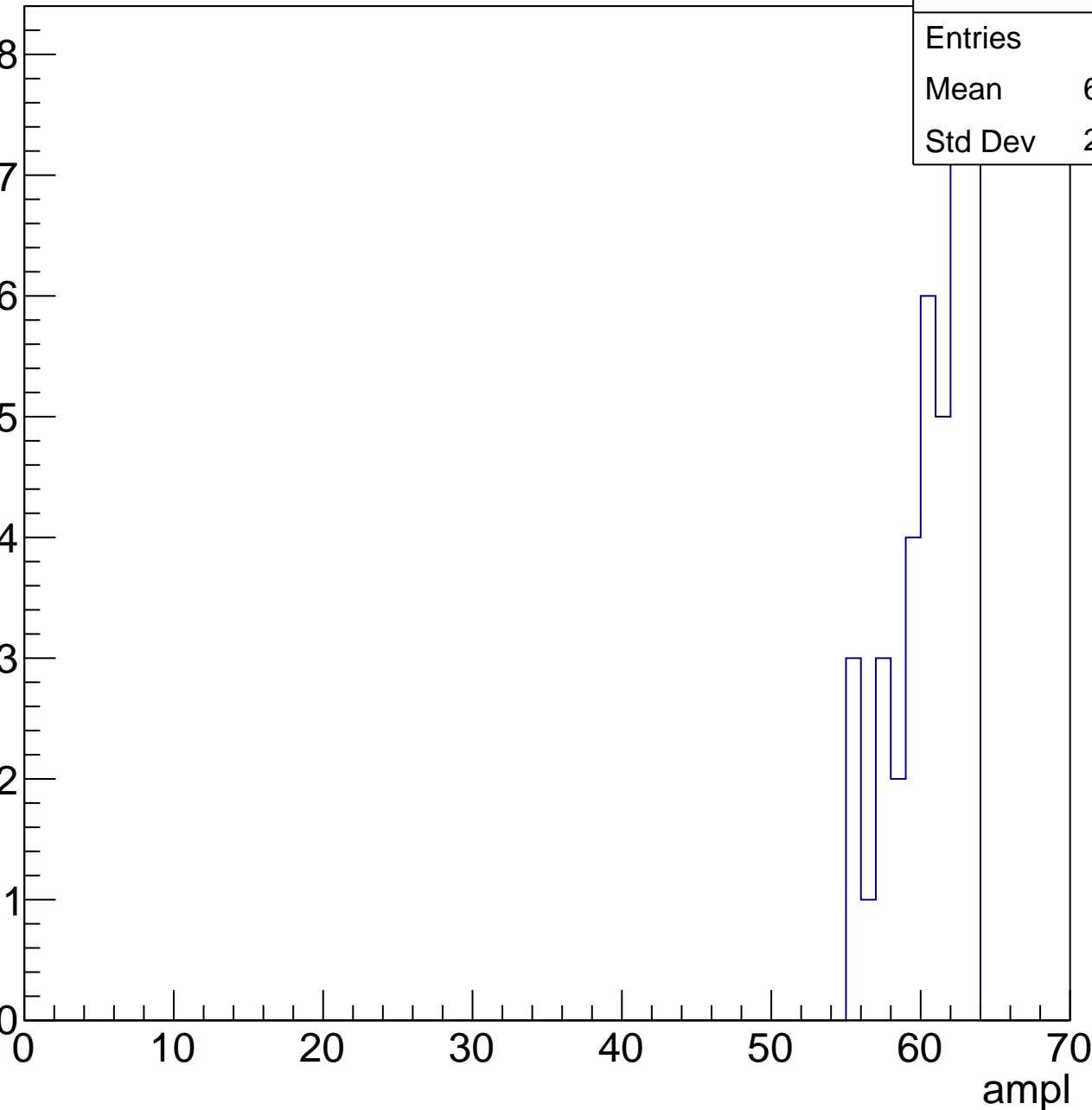
# B0L000S, U25-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	40
Mean	60.23
Std Dev	2.434

8  
7  
6  
5  
4  
3  
2  
1  
0



# B0L000S, U25-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	7
Mean	53.14
Std Dev	21.71

ampl



# B0L000S, U25-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch126, adc0

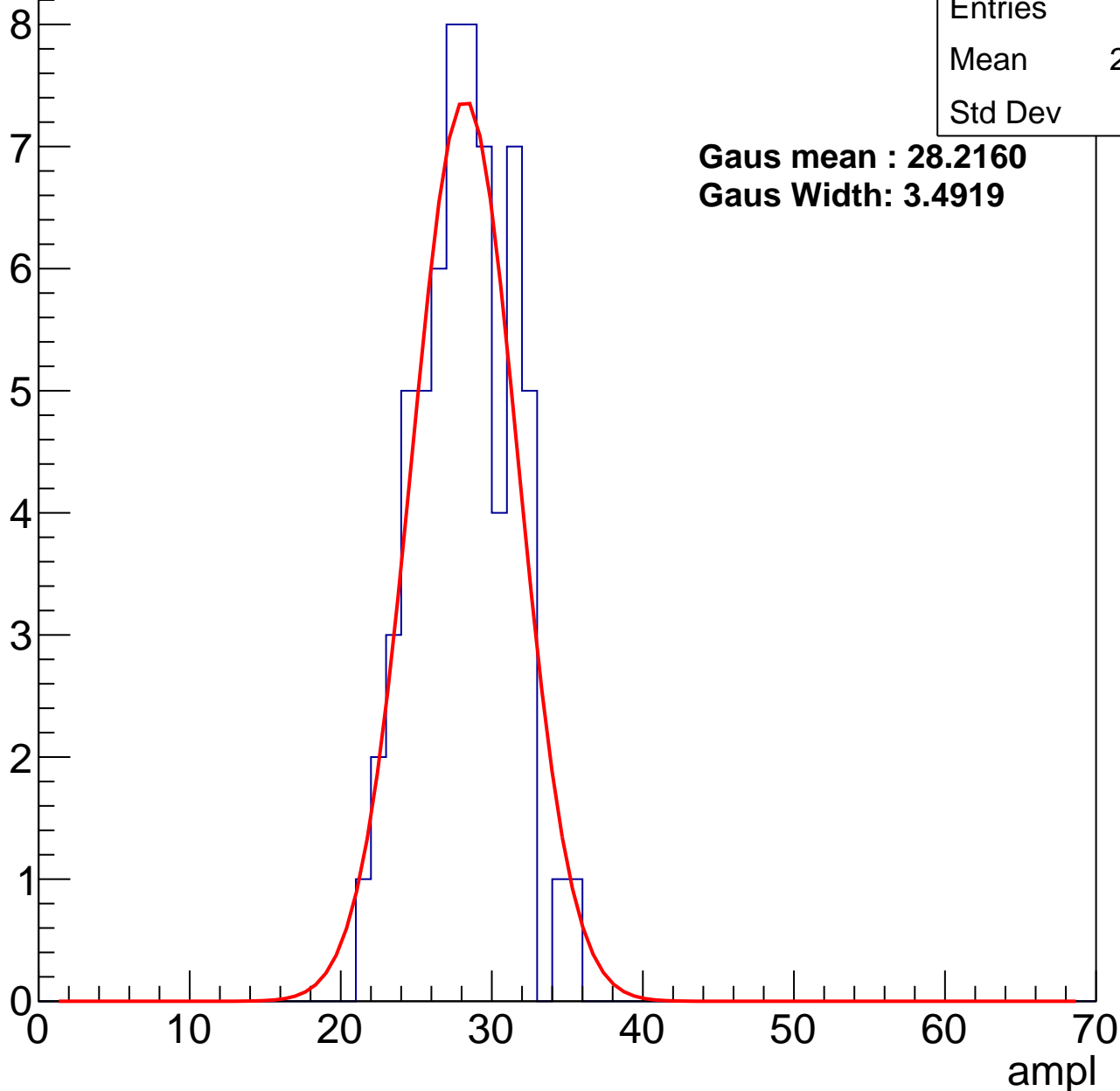
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	63
Mean	27.68
Std Dev	3.08

**Gaus mean : 28.2160**

**Gaus Width: 3.4919**



# B0L000S, U25-ch126, adc1

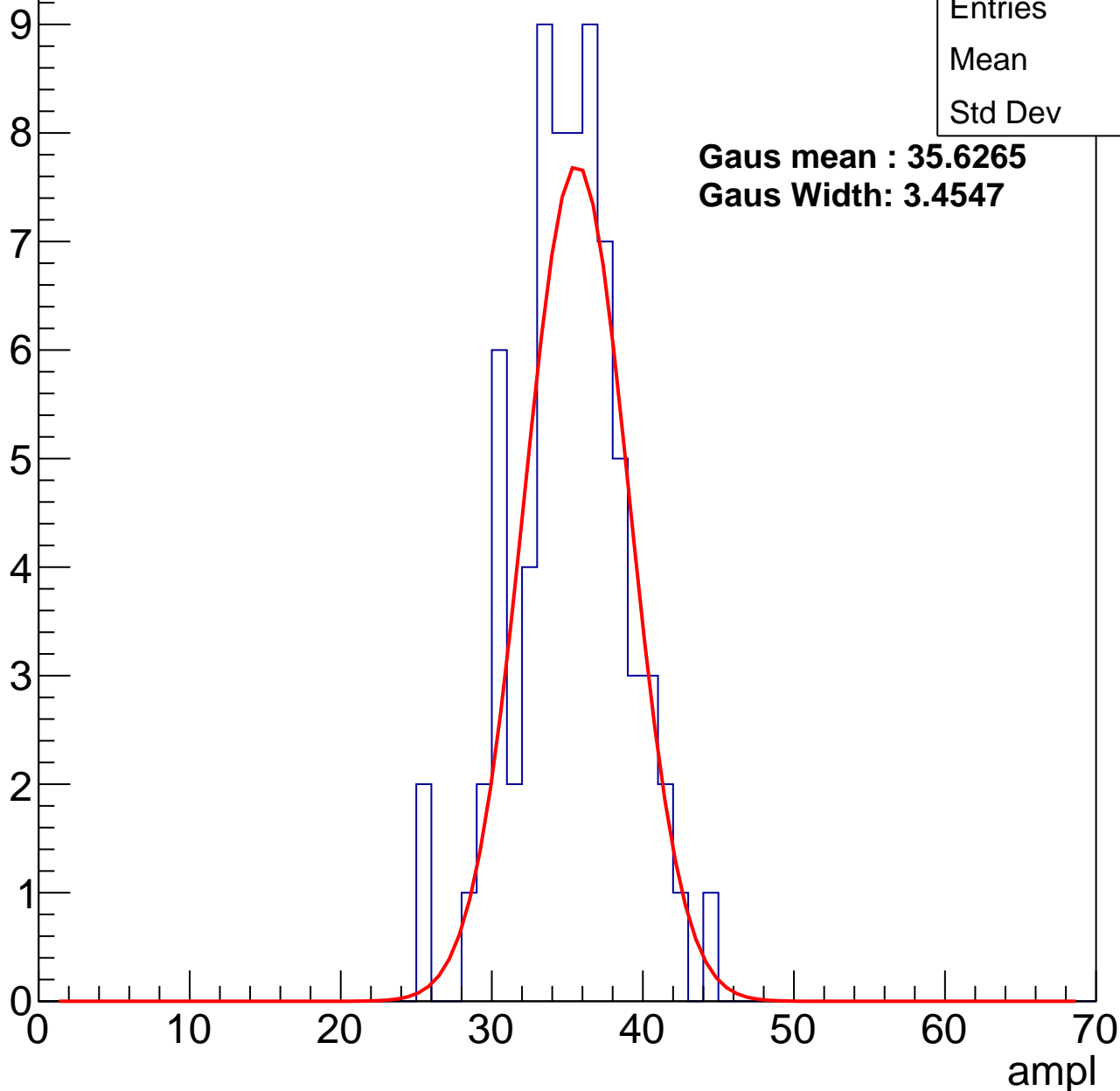
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	73
Mean	34.7
Std Dev	3.67

**Gaus mean : 35.6265**

**Gaus Width: 3.4547**



# B0L000S, U25-ch126, adc2

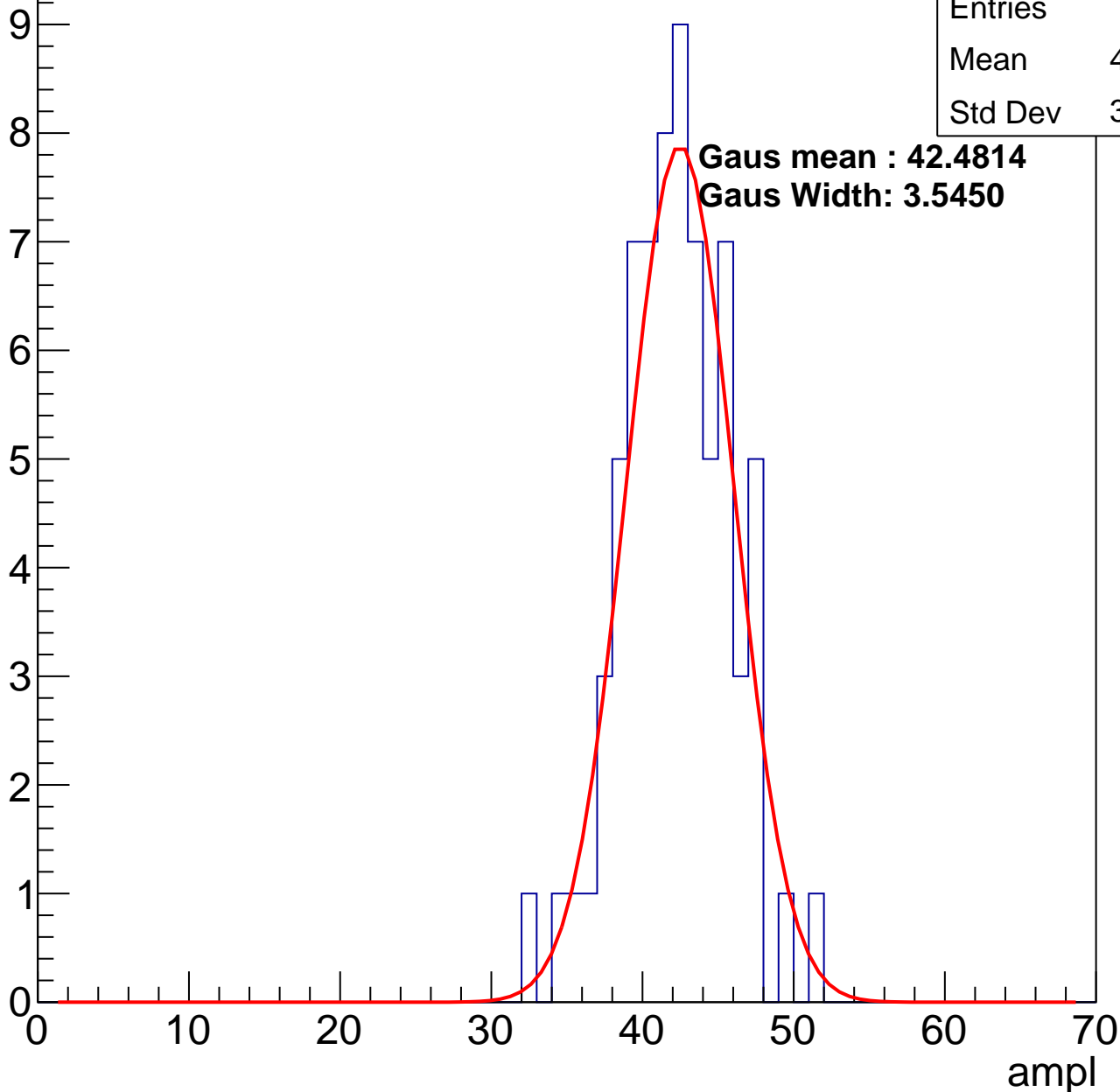
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	72
Mean	41.75
Std Dev	3.519

**Gaus mean : 42.4814**

**Gaus Width: 3.5450**

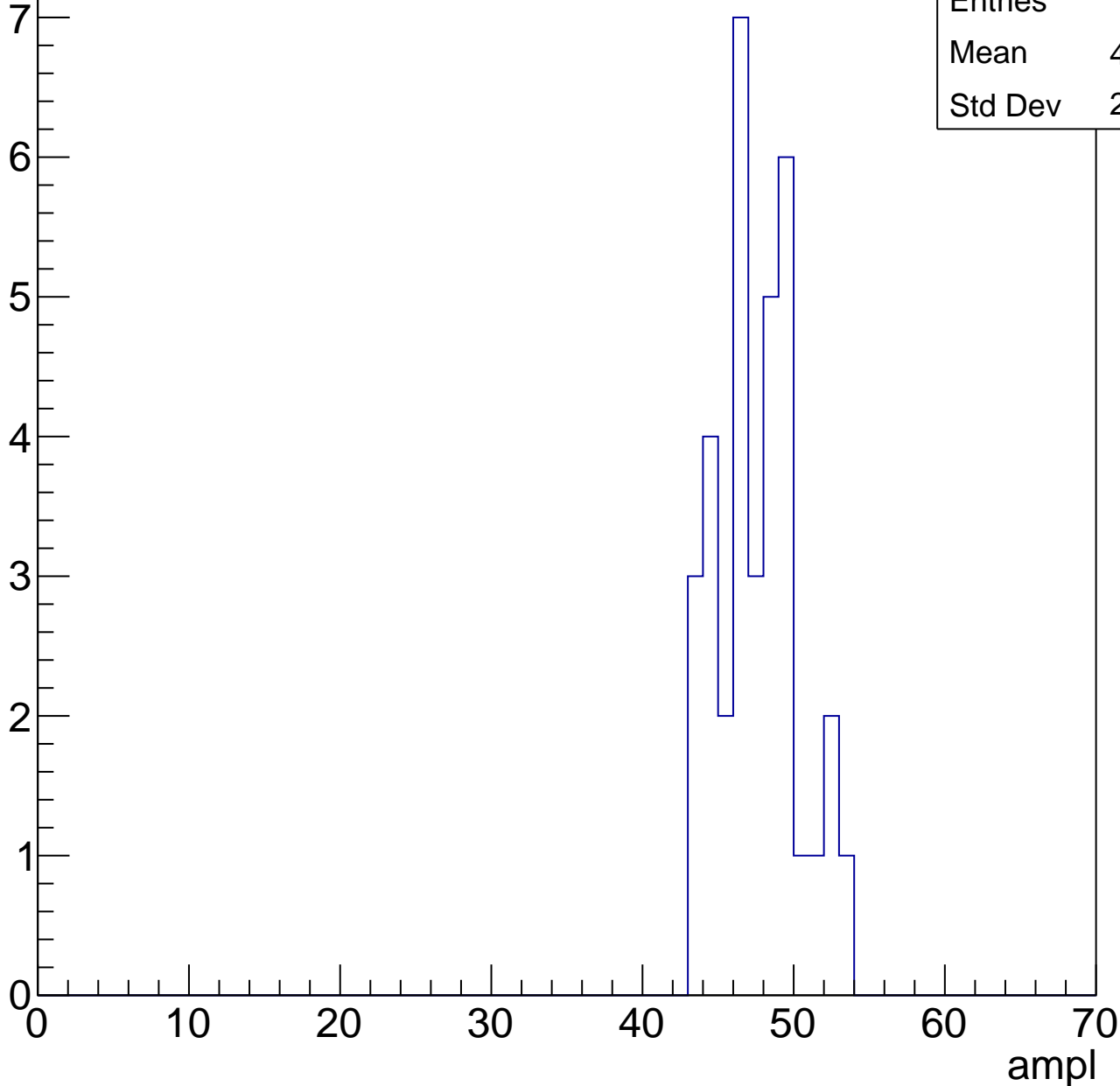


# B0L000S, U25-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	35
Mean	47.14
Std Dev	2.609

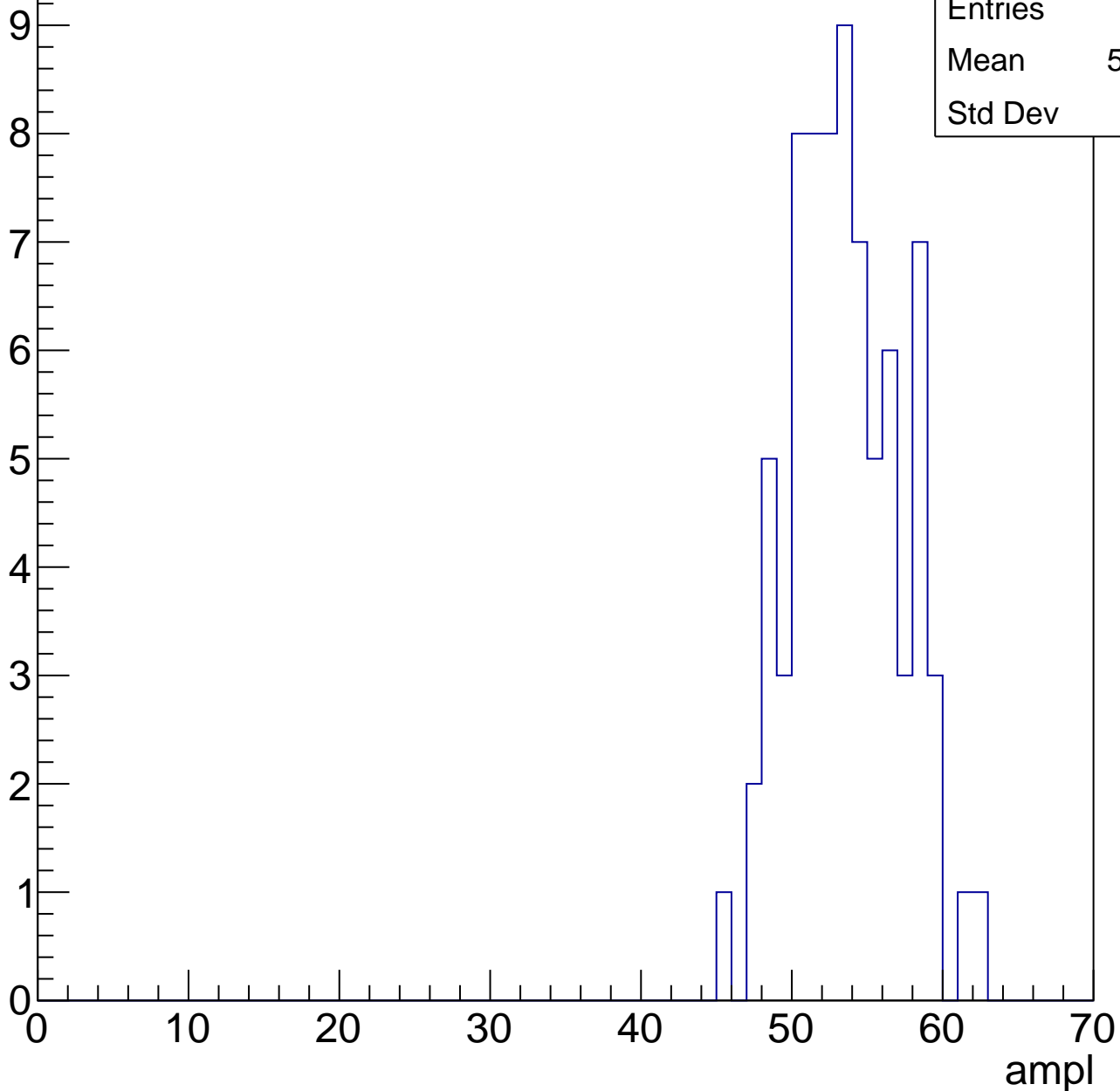


# B0L000S, U25-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	77
Mean	53.16
Std Dev	3.56

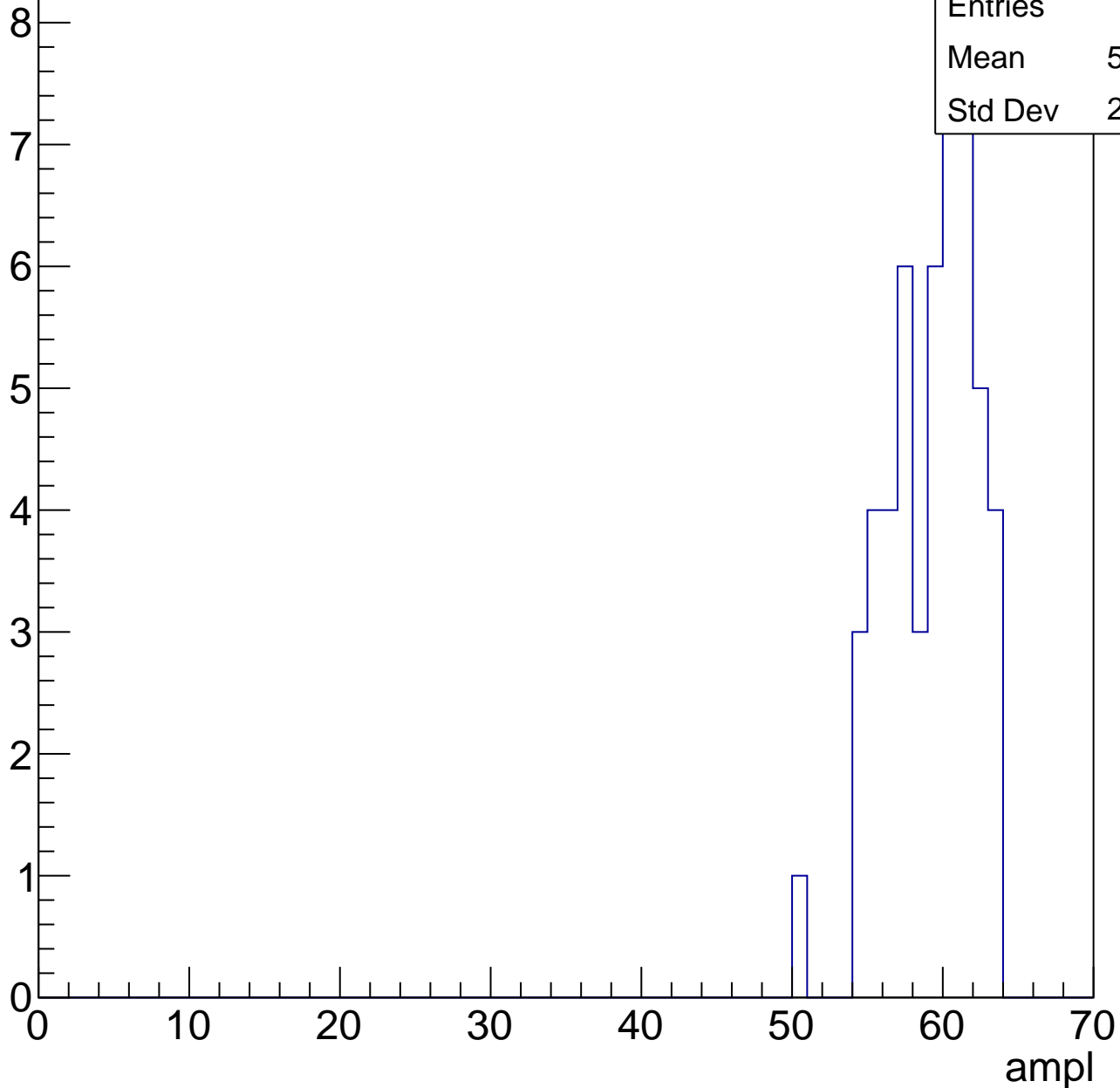


# B0L000S, U25-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	58.77
Std Dev	2.873

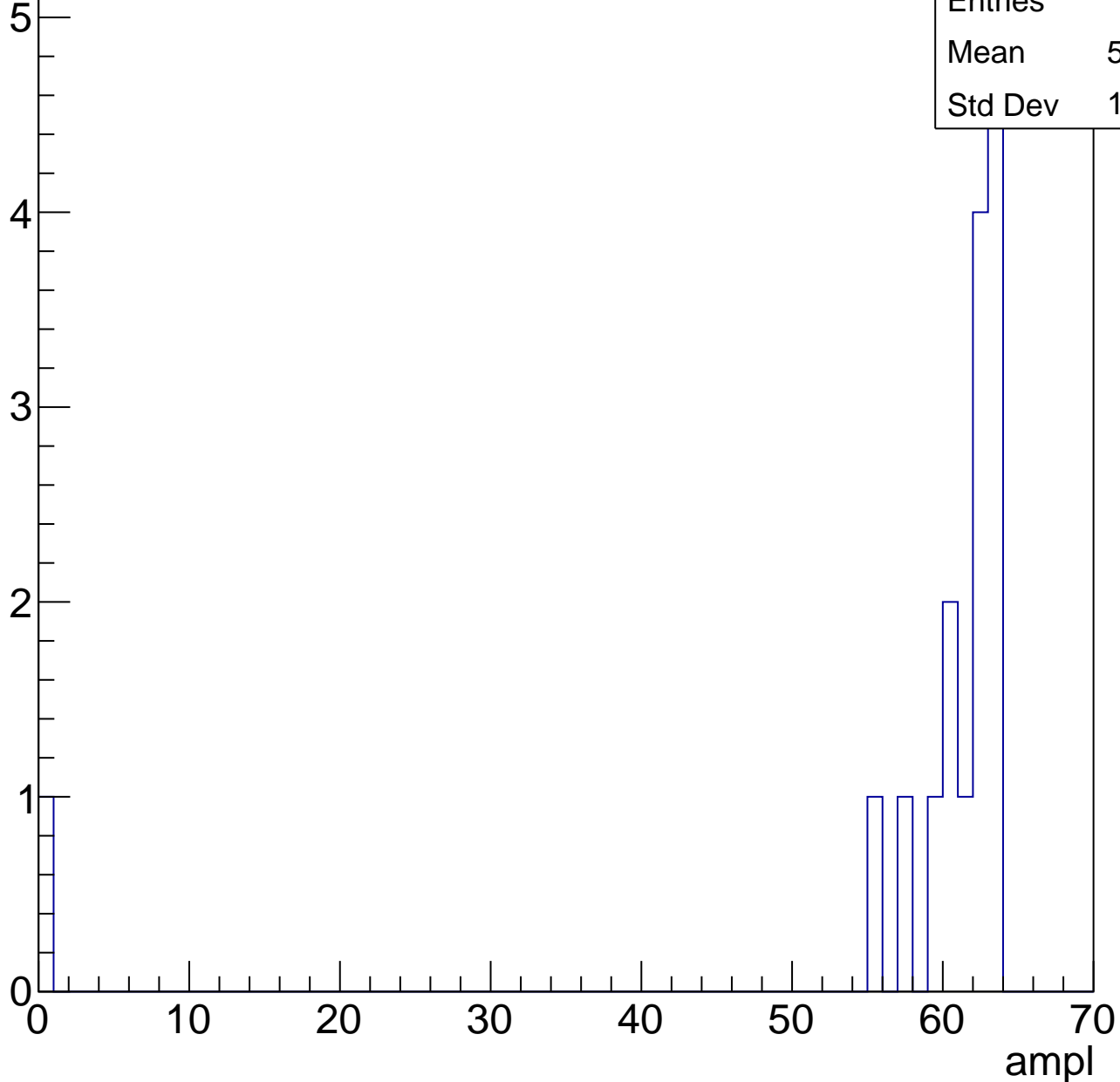


# B0L000S, U25-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	16
Mean	57.19
Std Dev	14.94

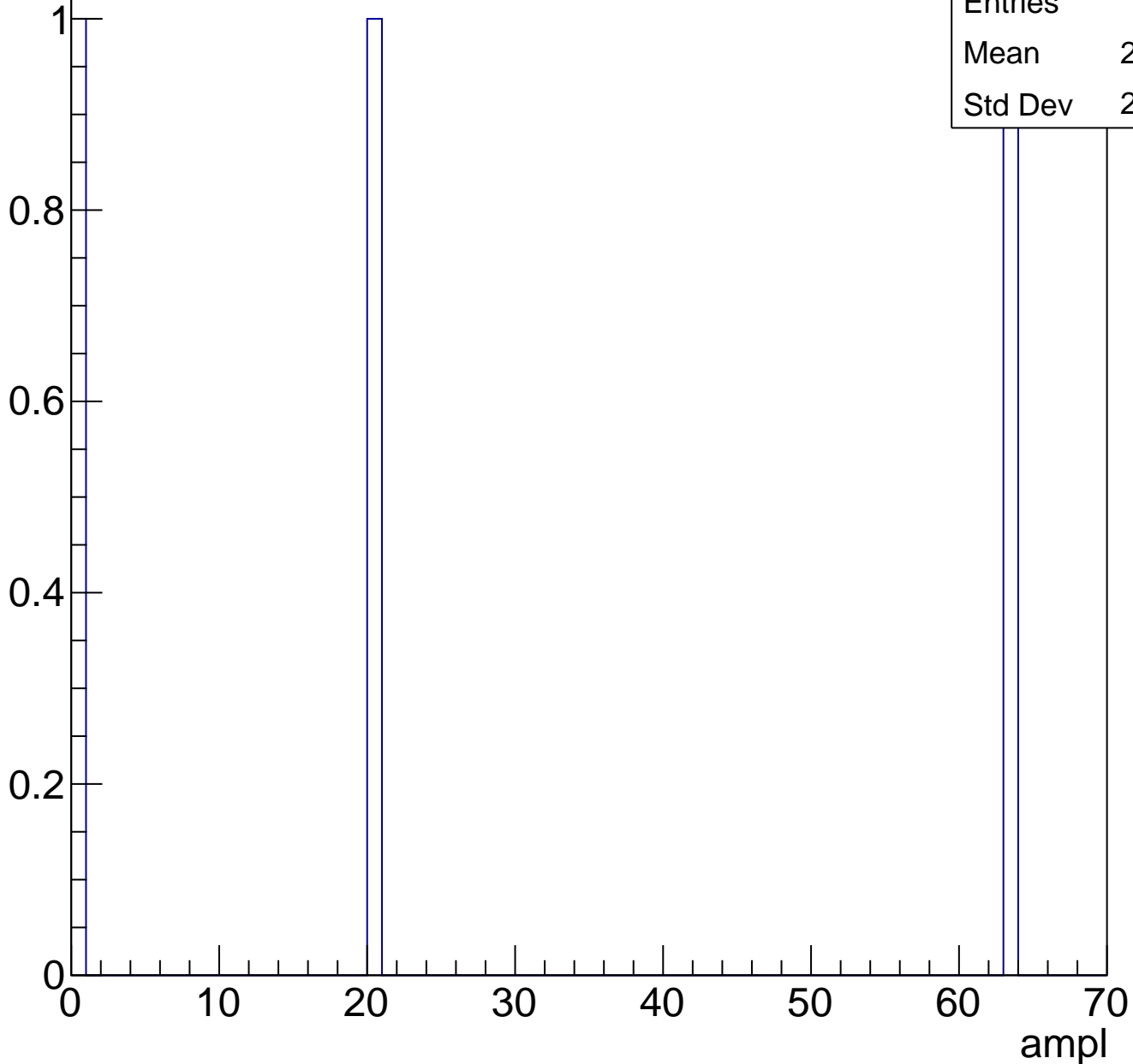




# B0L000S, U25-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch127, adc0

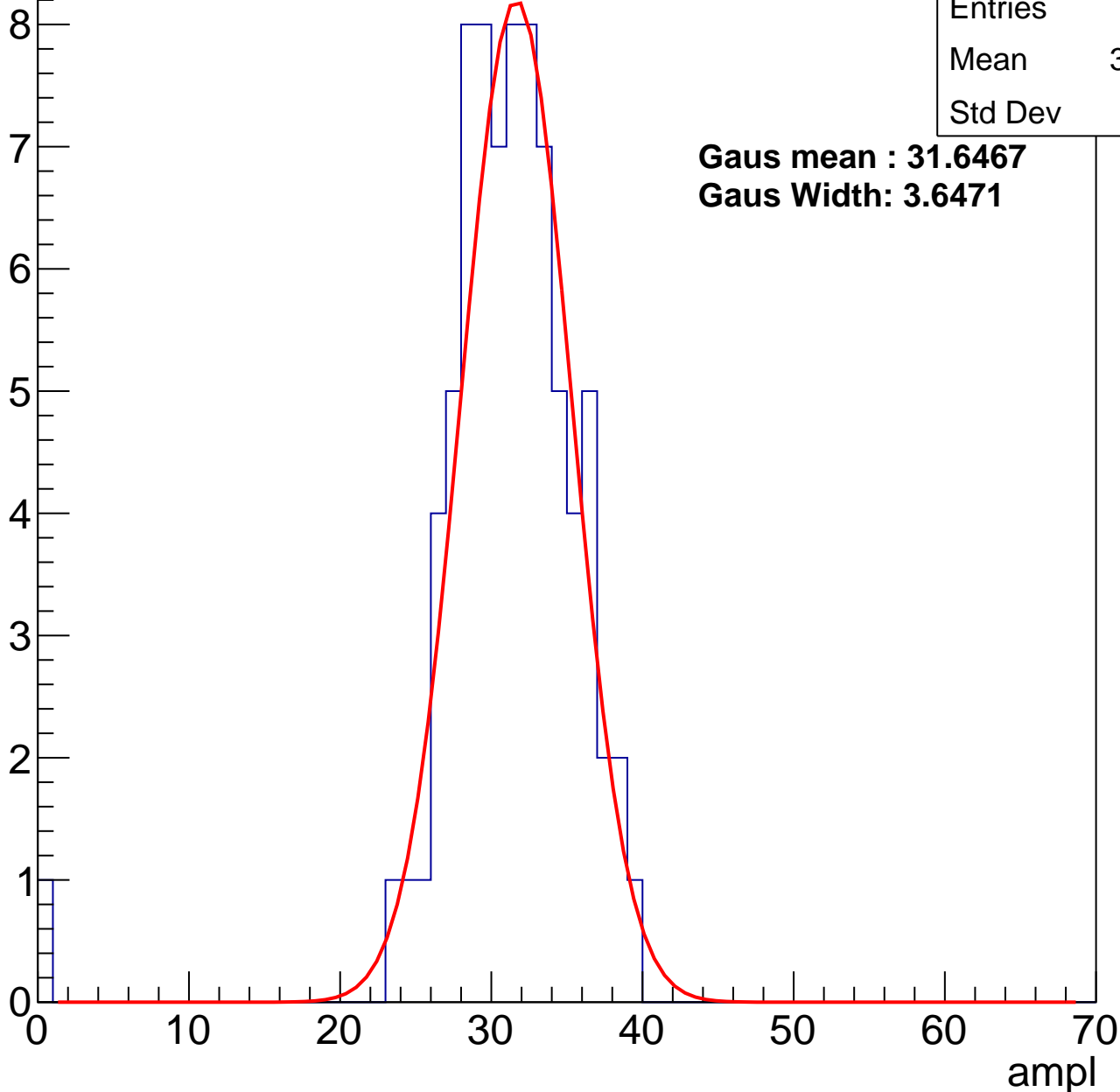
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	78
Mean	30.65
Std Dev	4.93

**Gaus mean : 31.6467**

**Gaus Width: 3.6471**



# B0L000S, U25-ch127, adc1

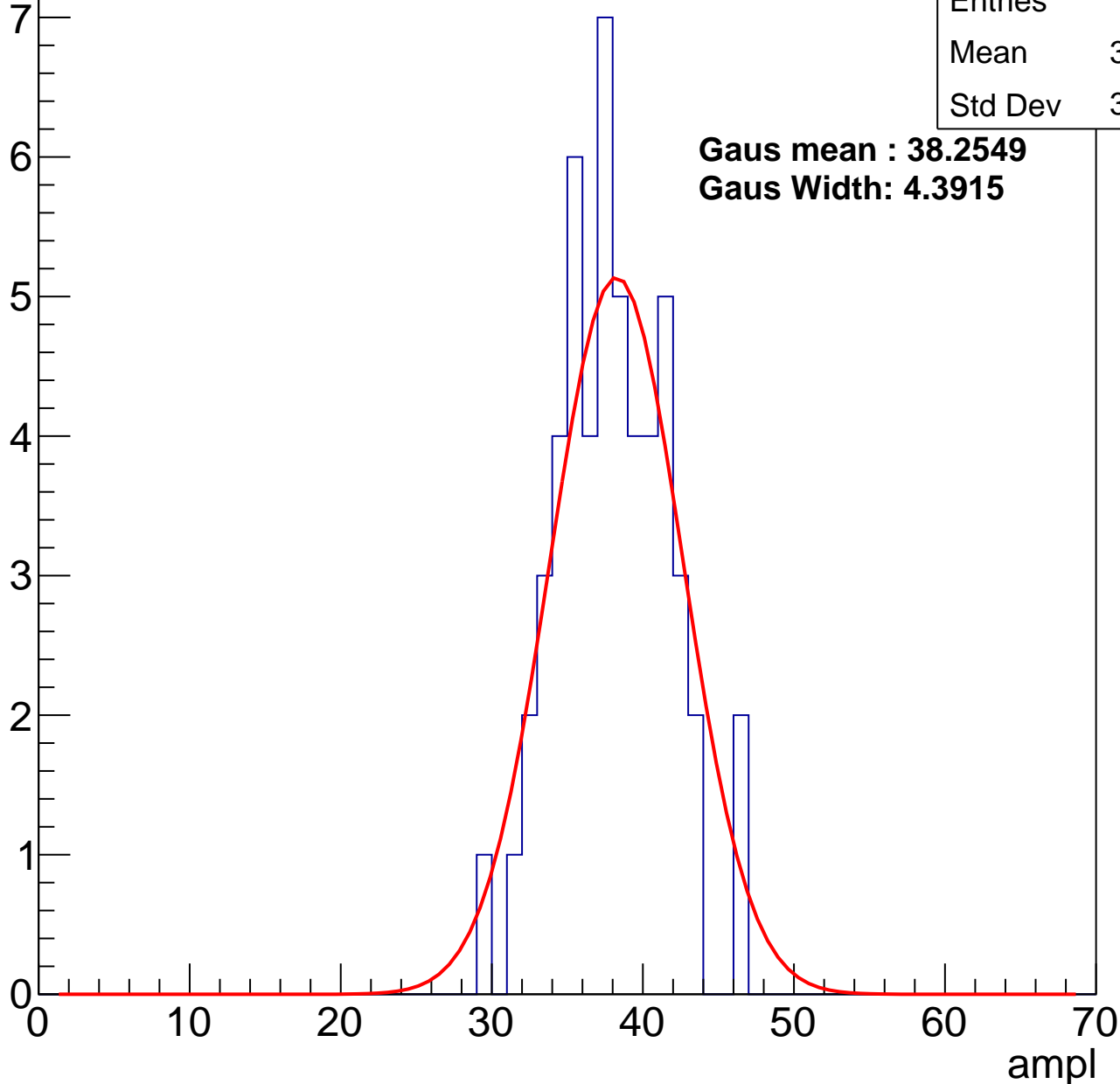
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	53
Mean	37.49
Std Dev	3.622

**Gaus mean : 38.2549**

**Gaus Width: 4.3915**



# B0L000S, U25-ch127, adc2

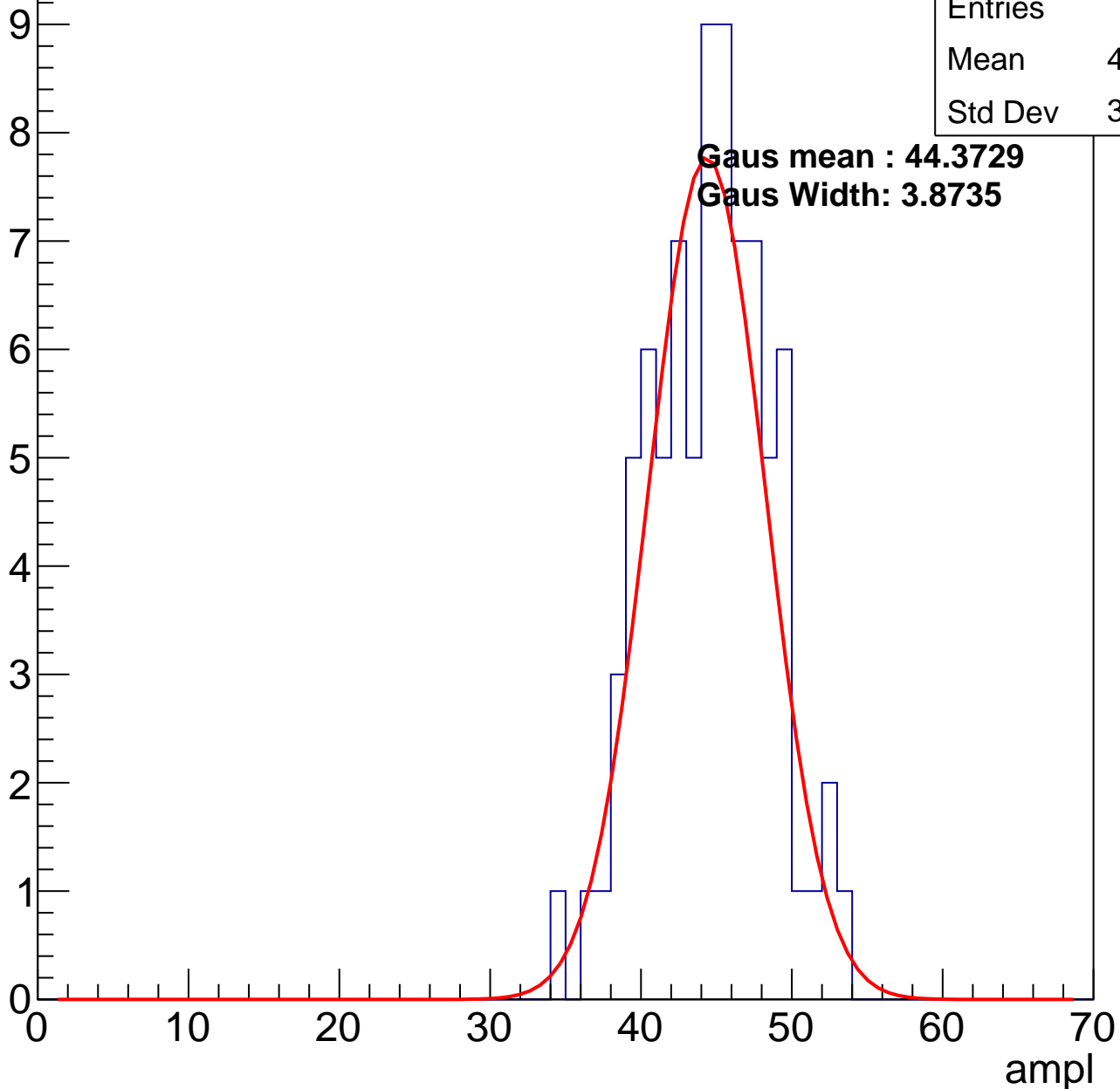
calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	82
Mean	44.07
Std Dev	3.894

**Gaus mean : 44.3729**

**Gaus Width: 3.8735**

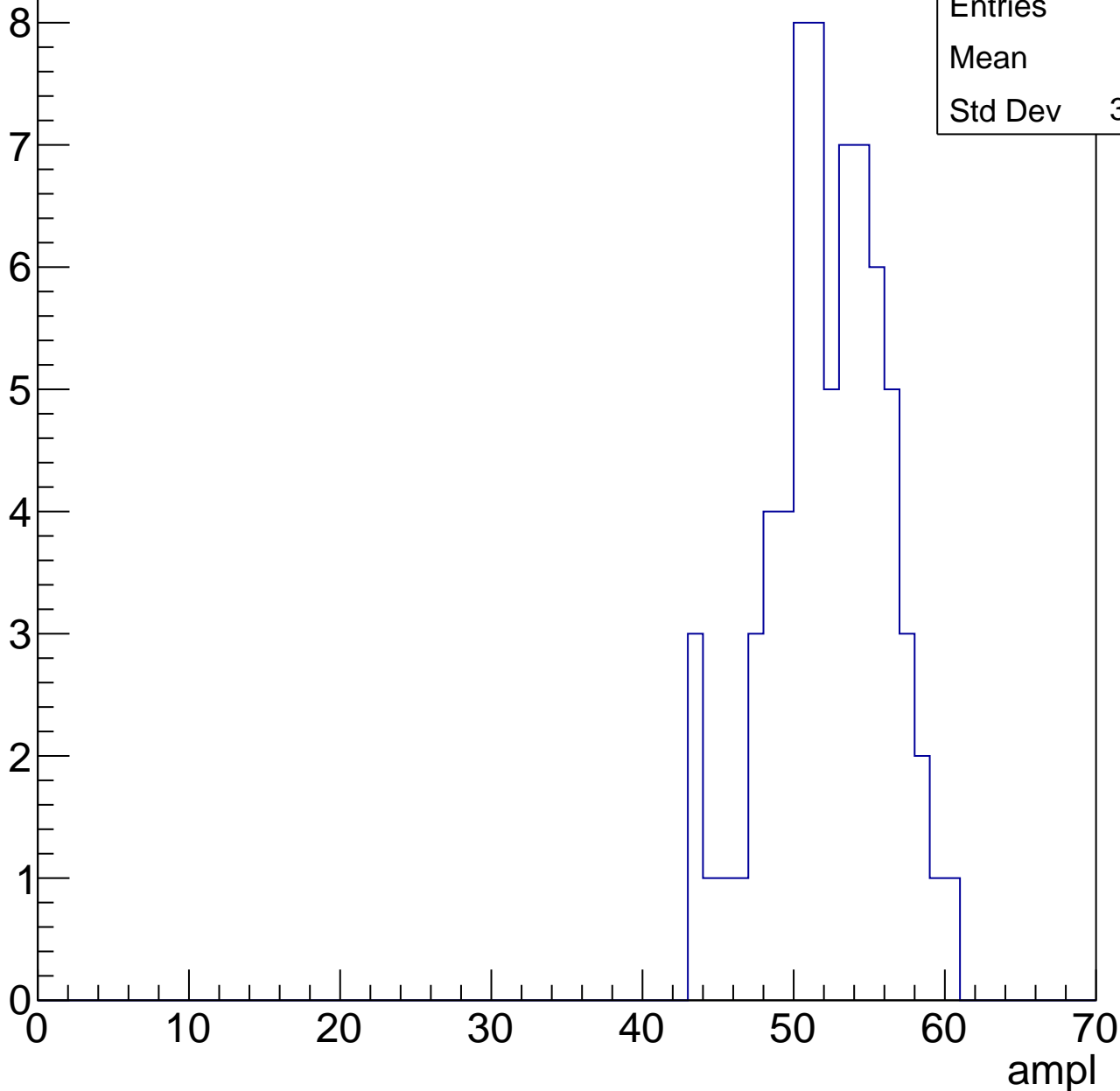


# B0L000S, U25-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	70
Mean	51.8
Std Dev	3.842

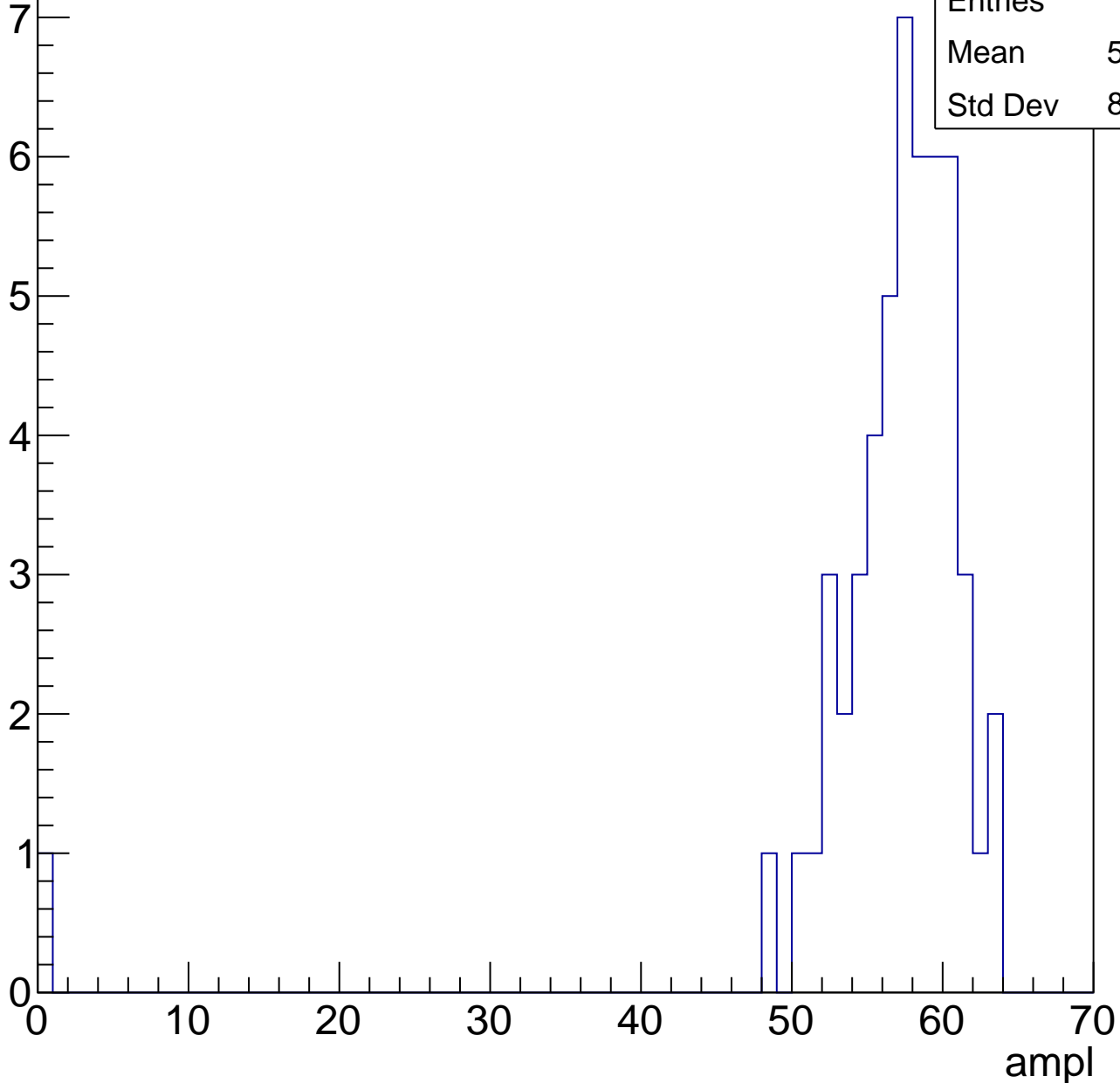


# B0L000S, U25-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

Entries	52
Mean	55.87
Std Dev	8.472

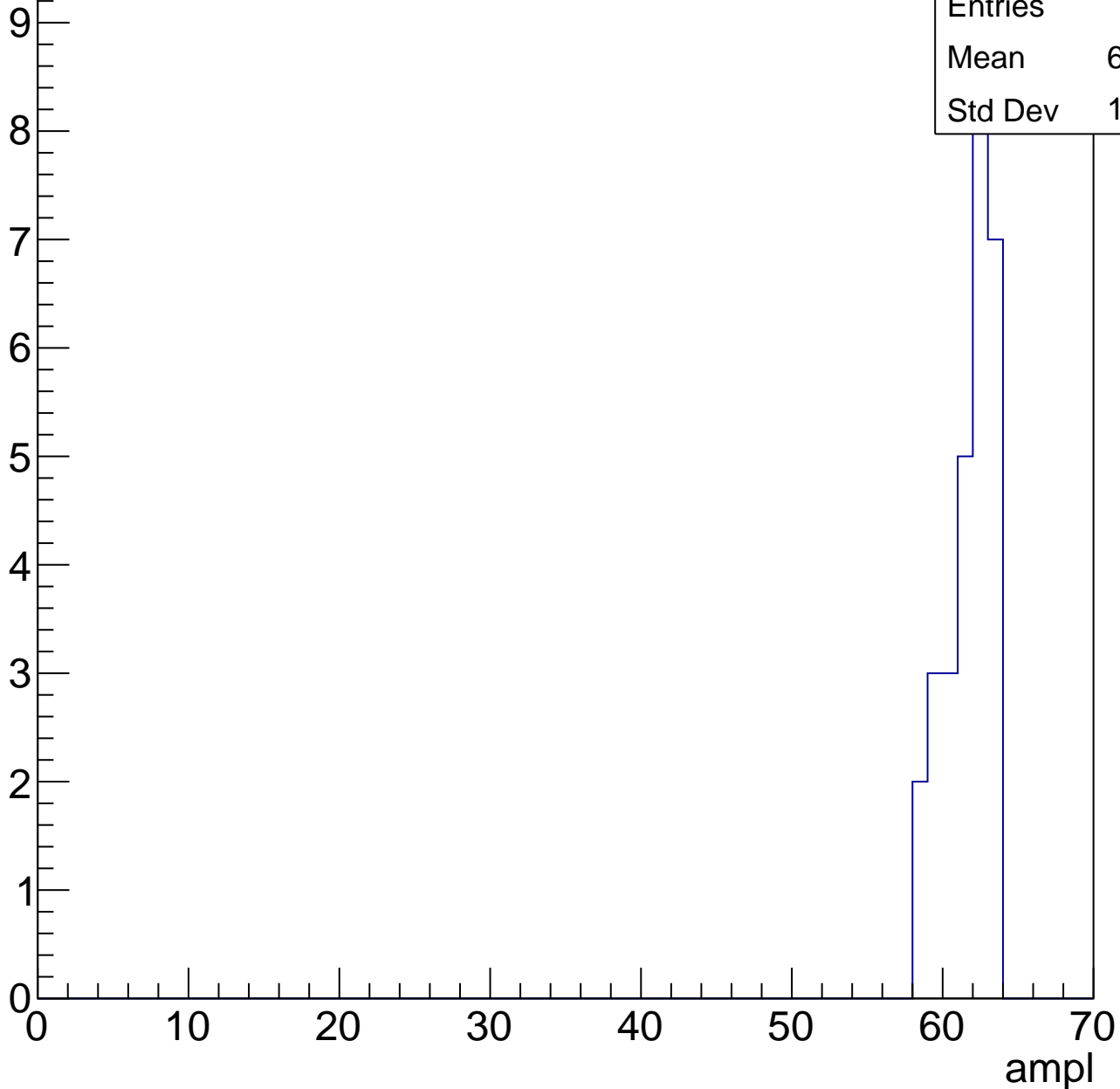


# B0L000S, U25-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

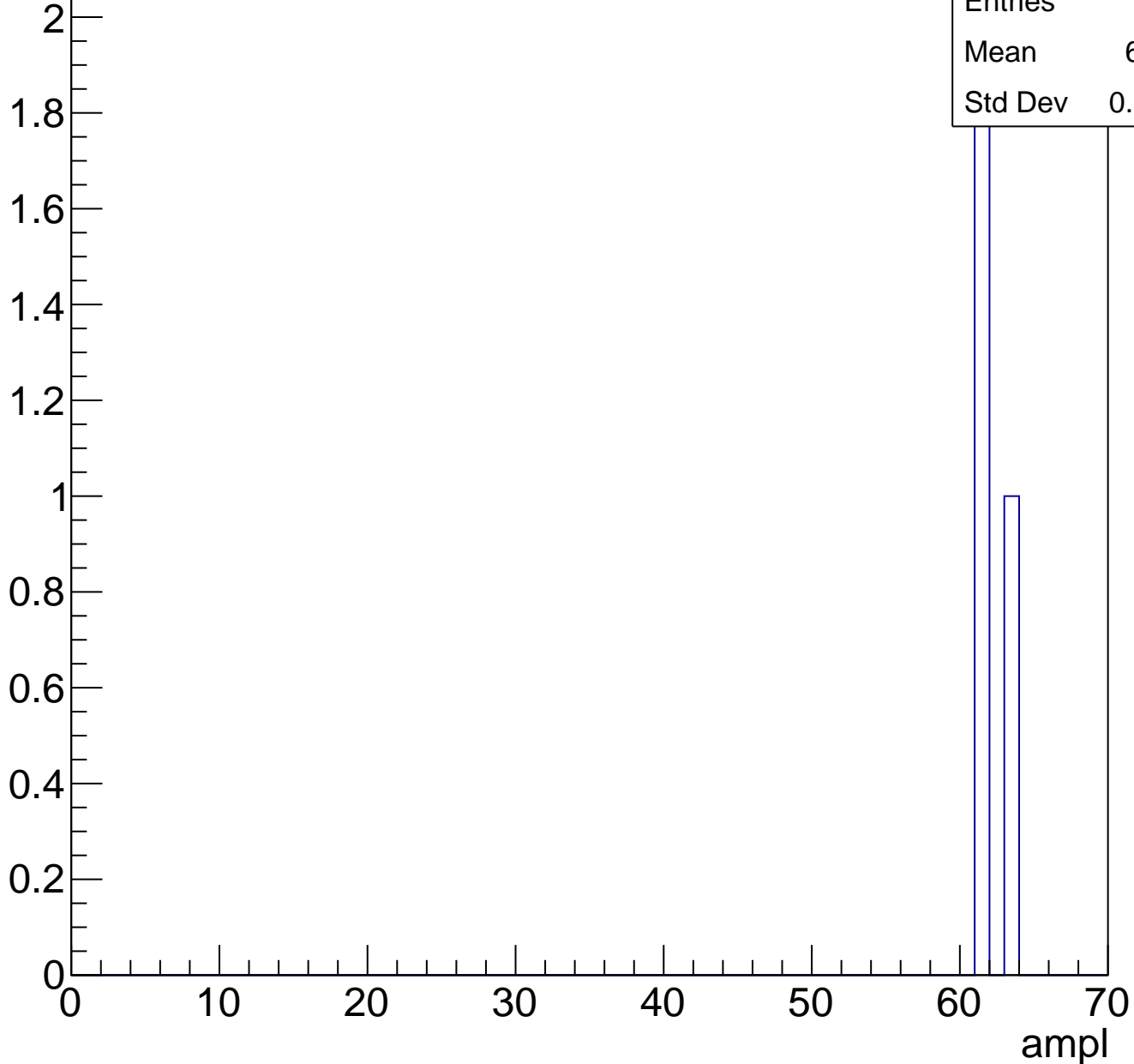
Entries	29
Mean	61.28
Std Dev	1.529



# B0L000S, U25-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



Entries	3
Mean	61.67
Std Dev	0.9428



# B0L000S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry



# B0L000S, U25-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#5, port B1

Entry

