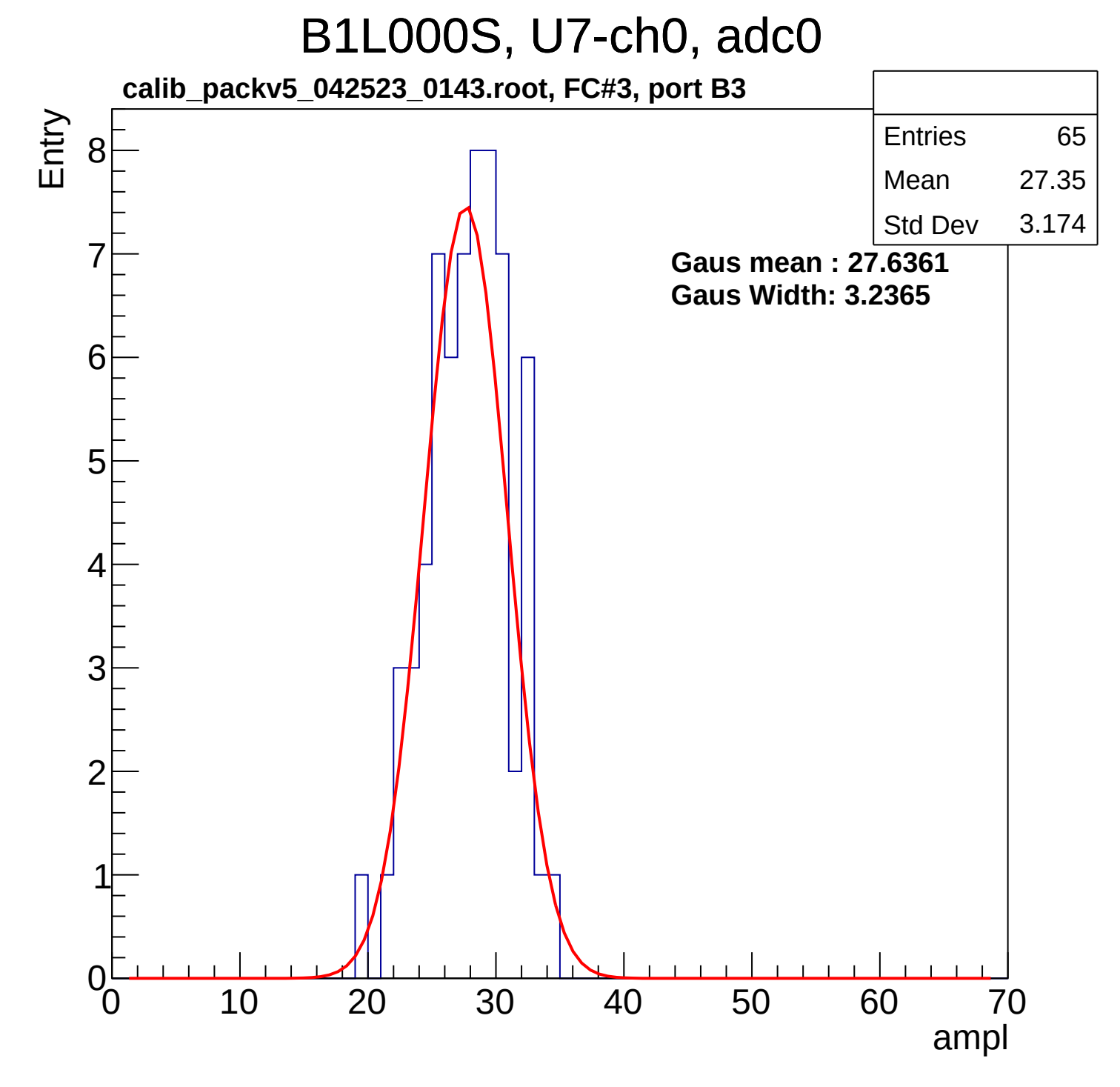
B1L000S, U7-ch0, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 27.35
Std Dev 3.174
Gaus mean : 27.6361
Gaus Width: 3.2365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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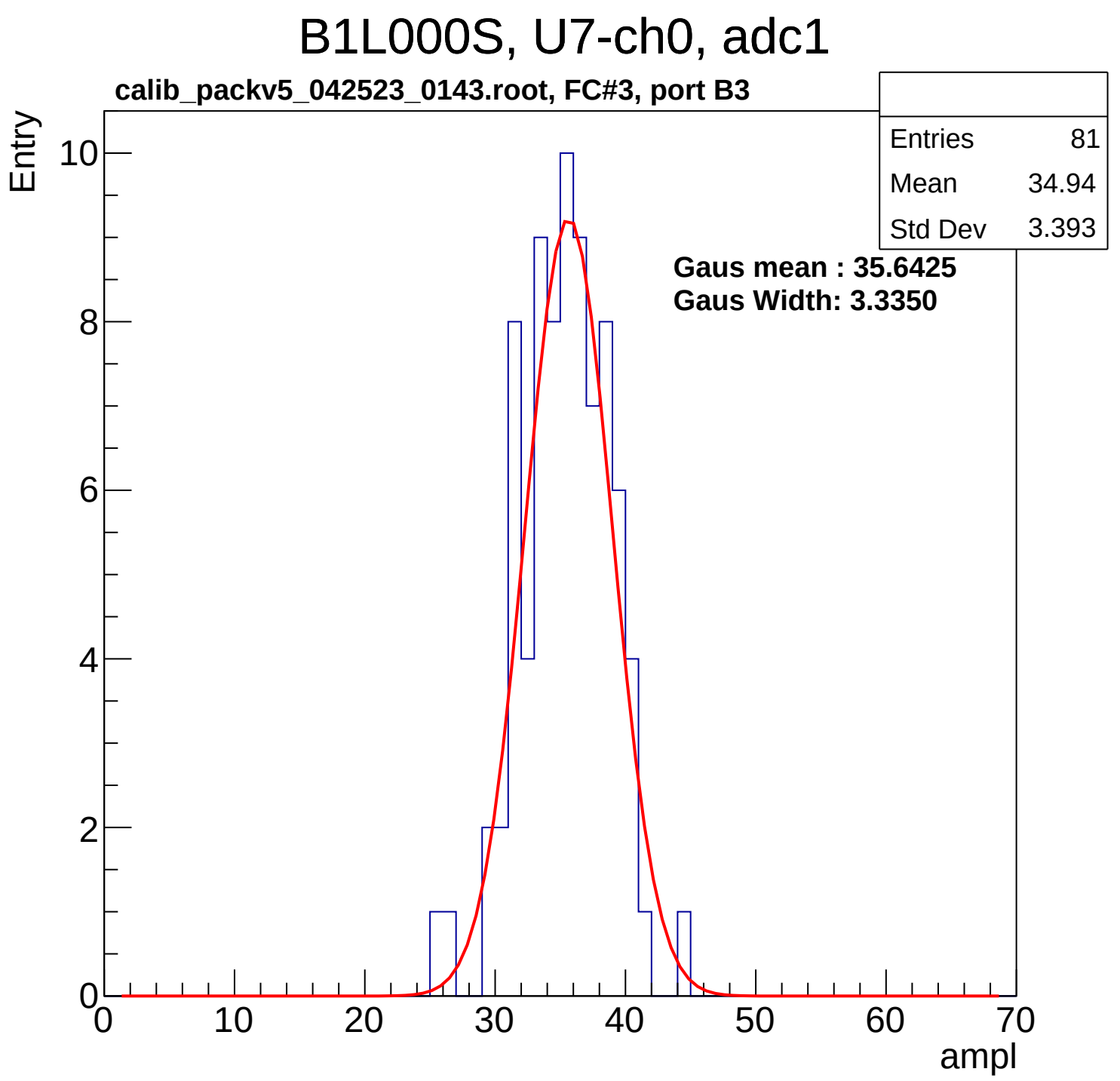
B1L000S, U7-ch0, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 34.94
Std Dev 3.393
Gaus mean : 35.6425
Gaus Width: 3.3350
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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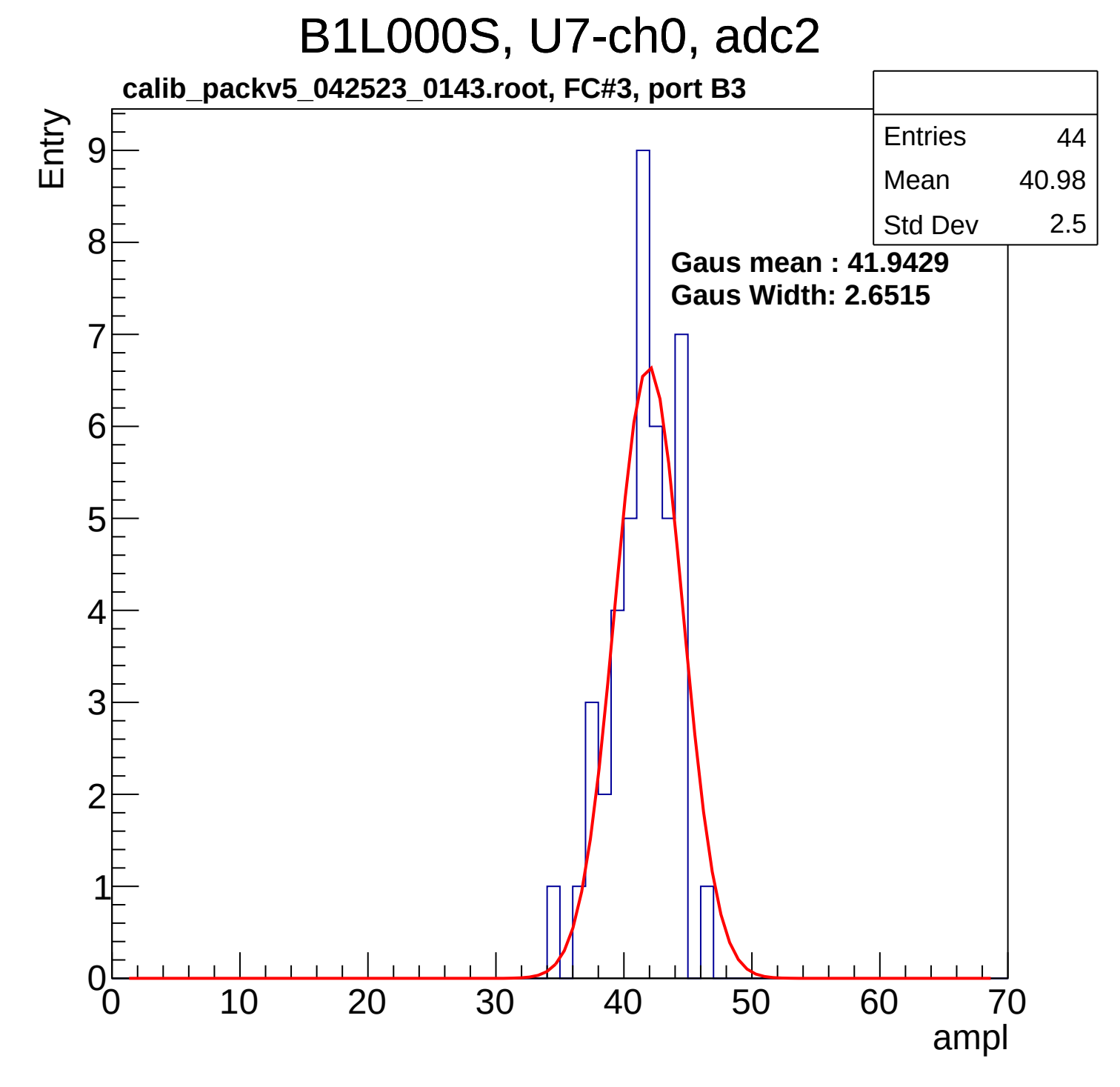
B1L000S, U7-ch0, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 40.98
Std Dev 2.5
Gaus mean : 41.9429
Gaus Width: 2.6515
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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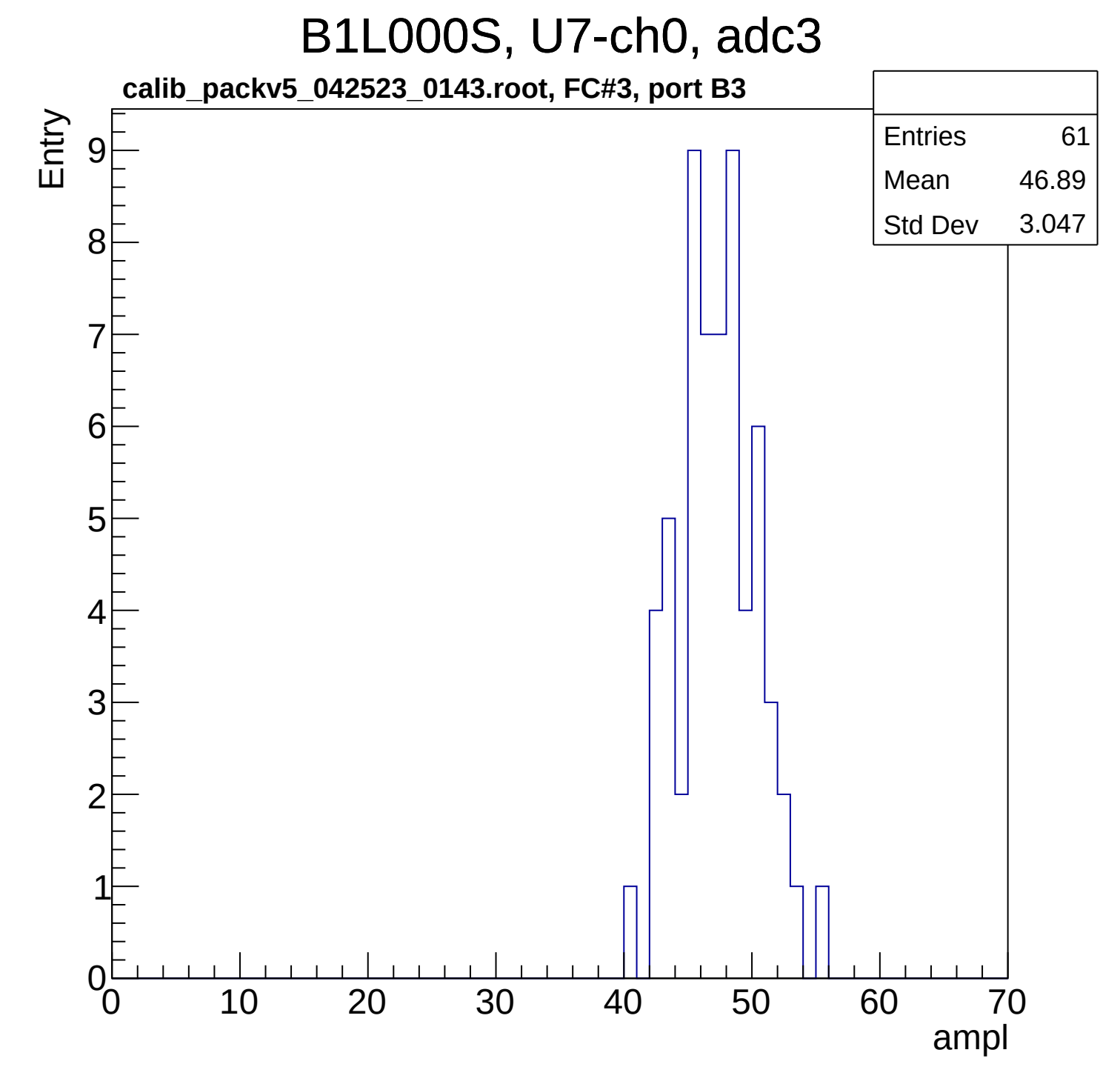
B1L000S, U7-ch0, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 46.89
Std Dev 3.047
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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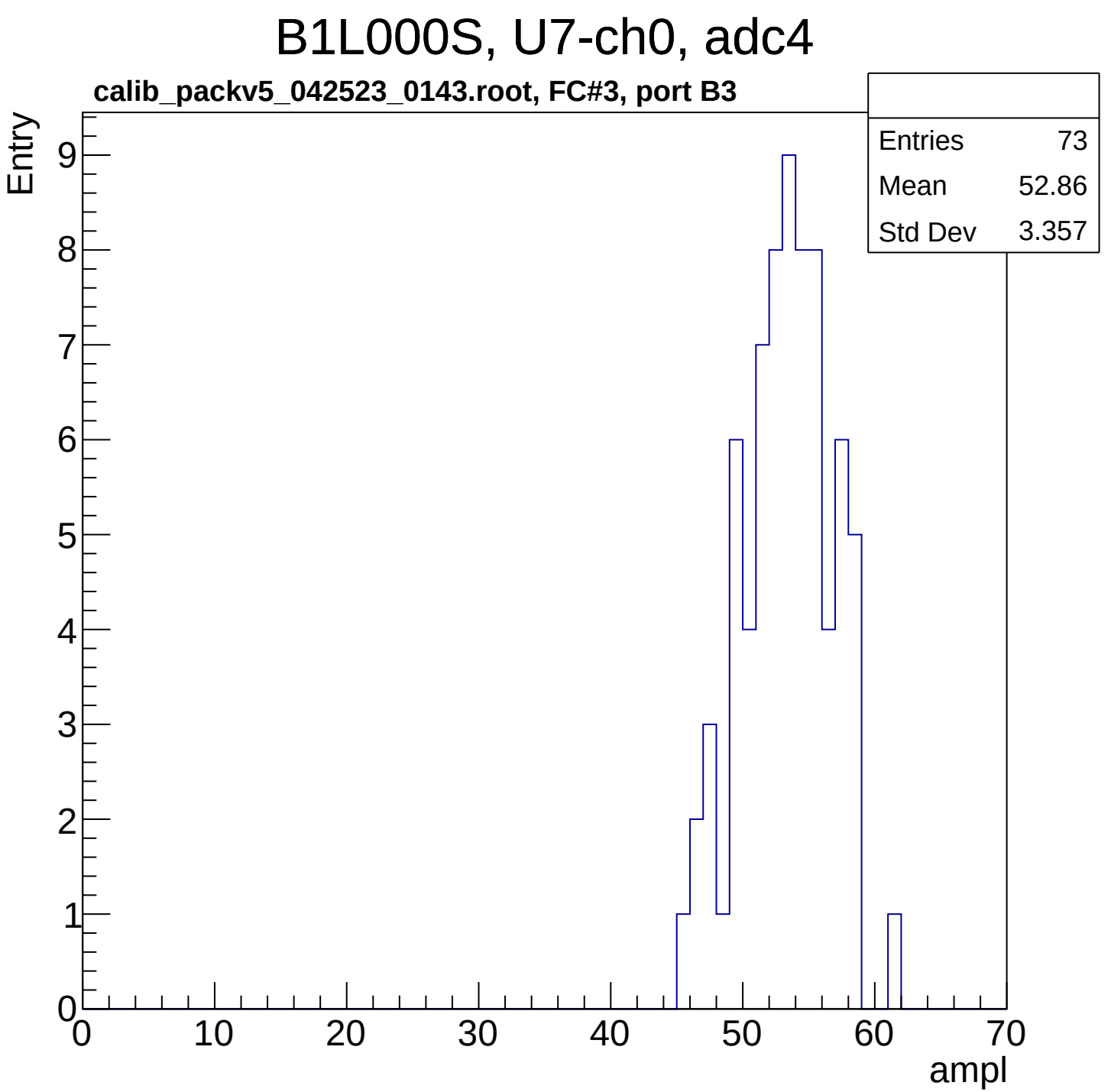
B1L000S, U7-ch0, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 52.86
Std Dev 3.357
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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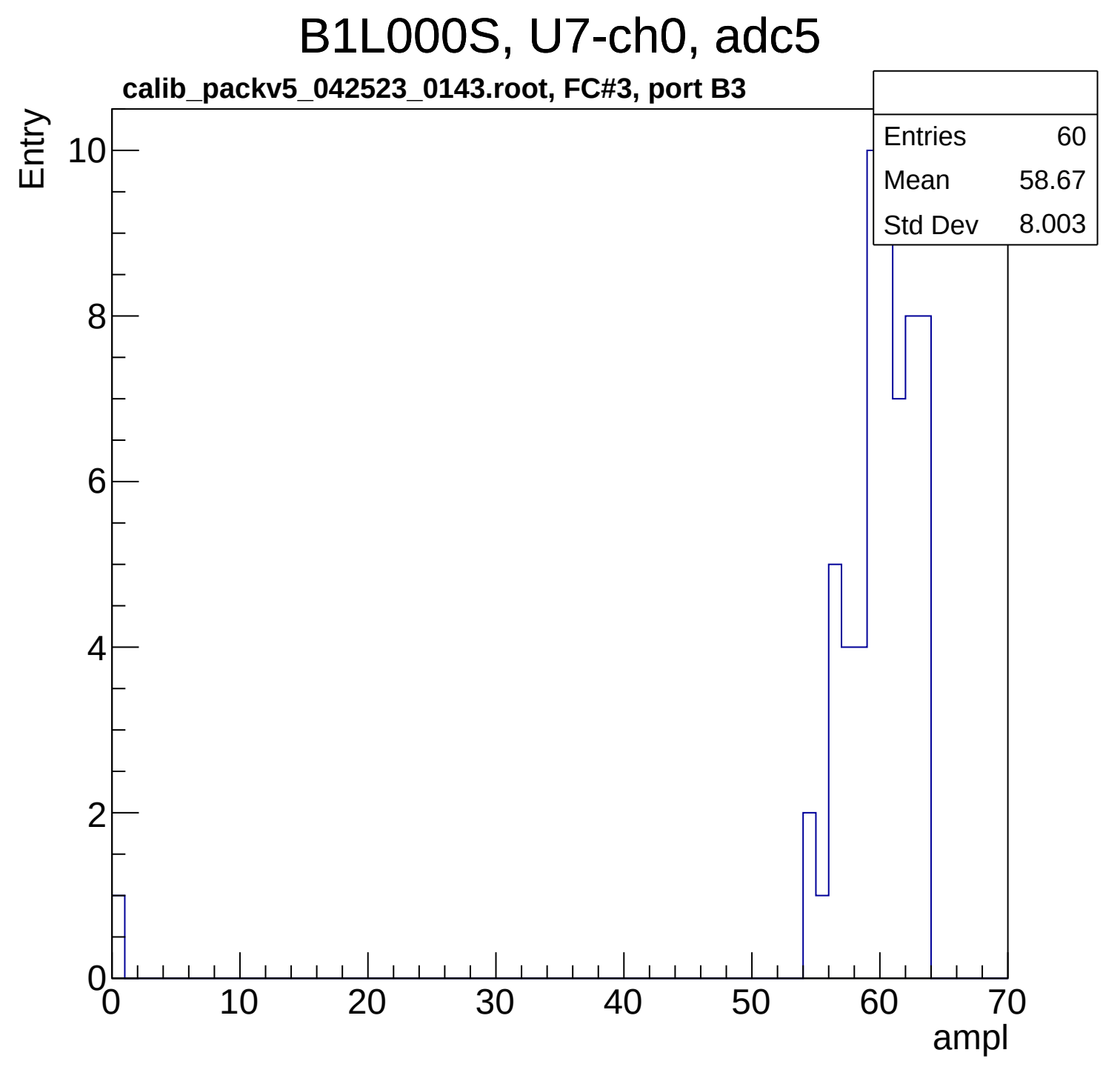
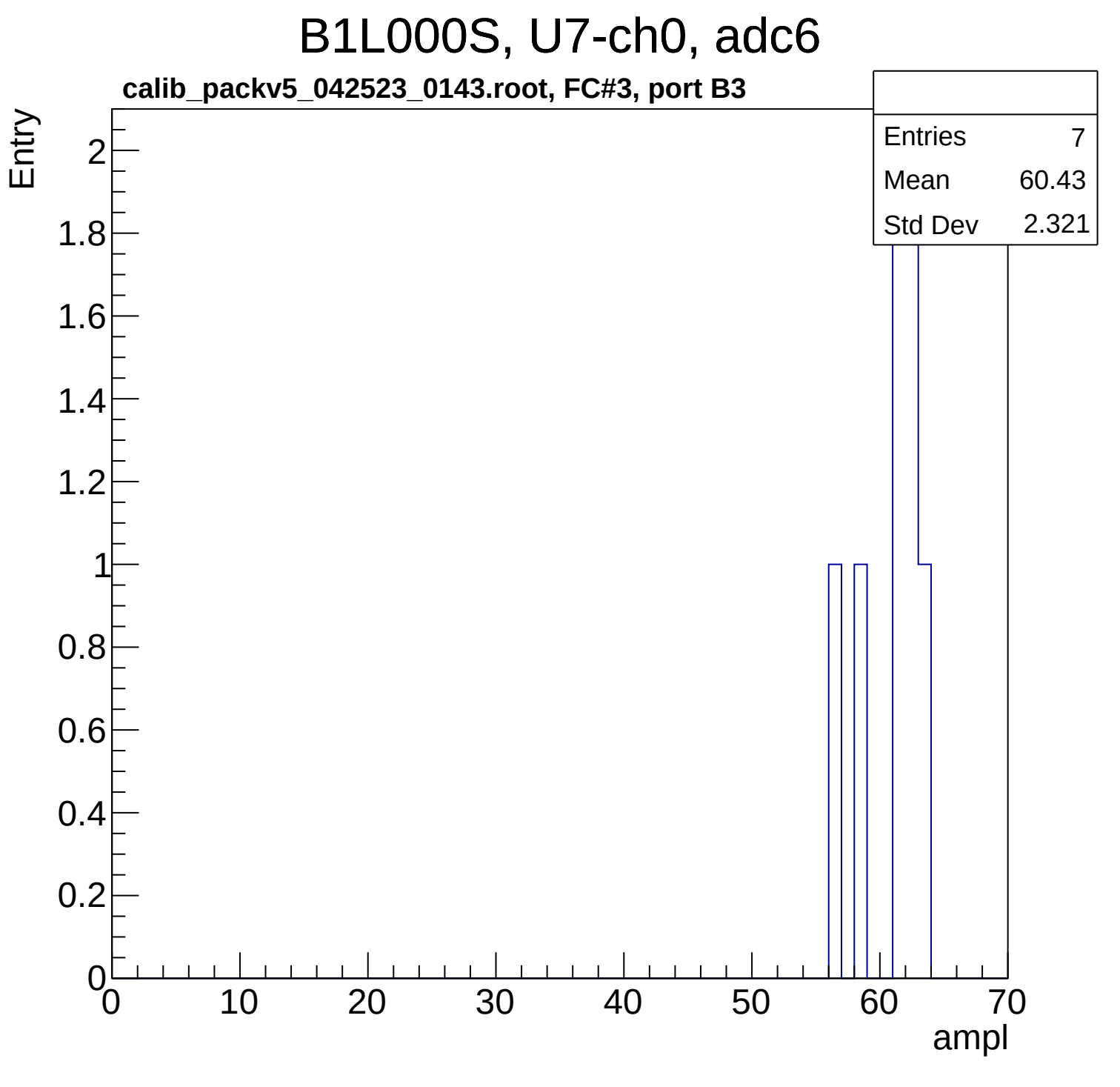

B1L000S, U7-ch0, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.67
Std Dev 8.003
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch0, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 60.43
Std Dev 2.321
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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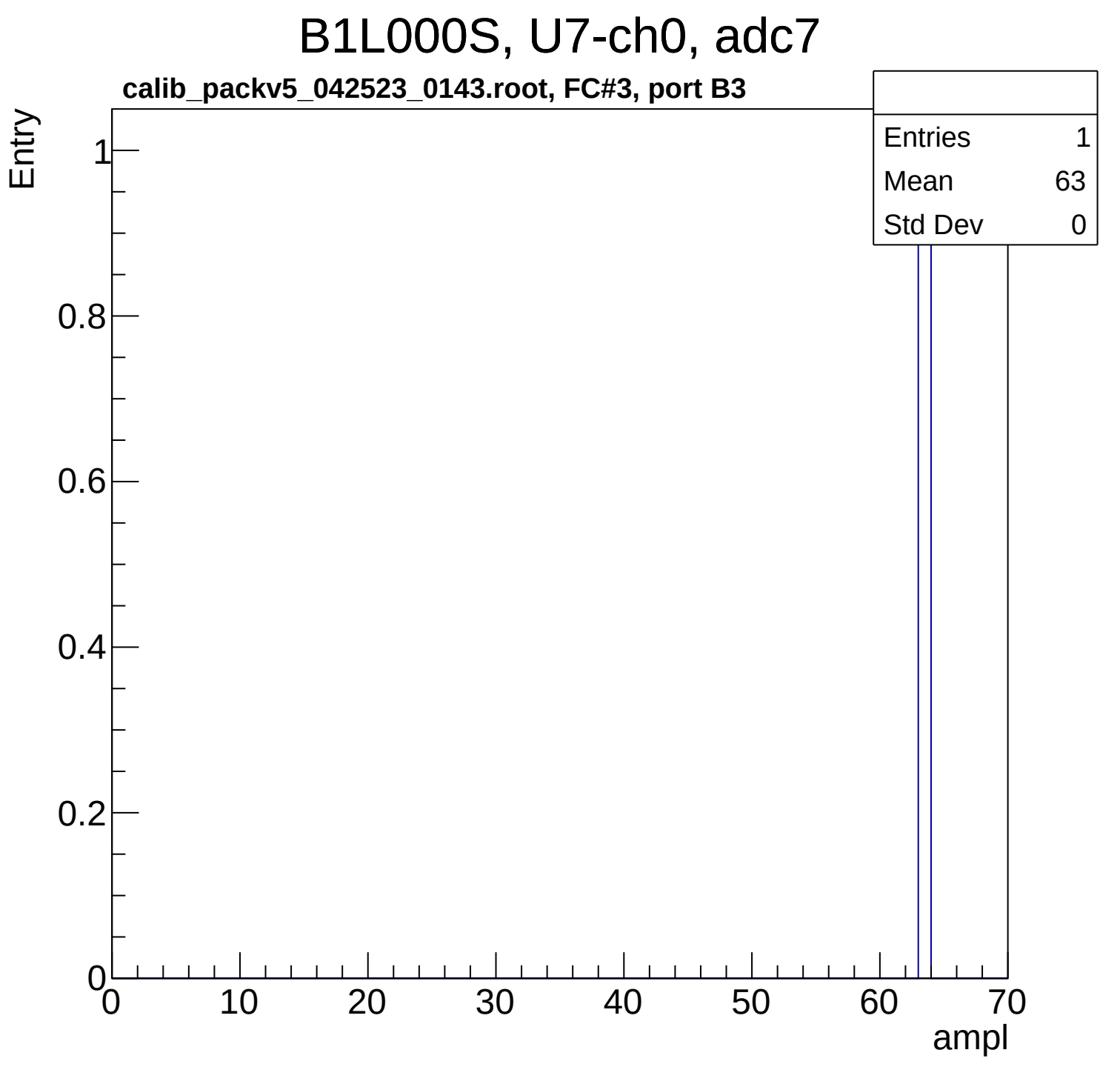
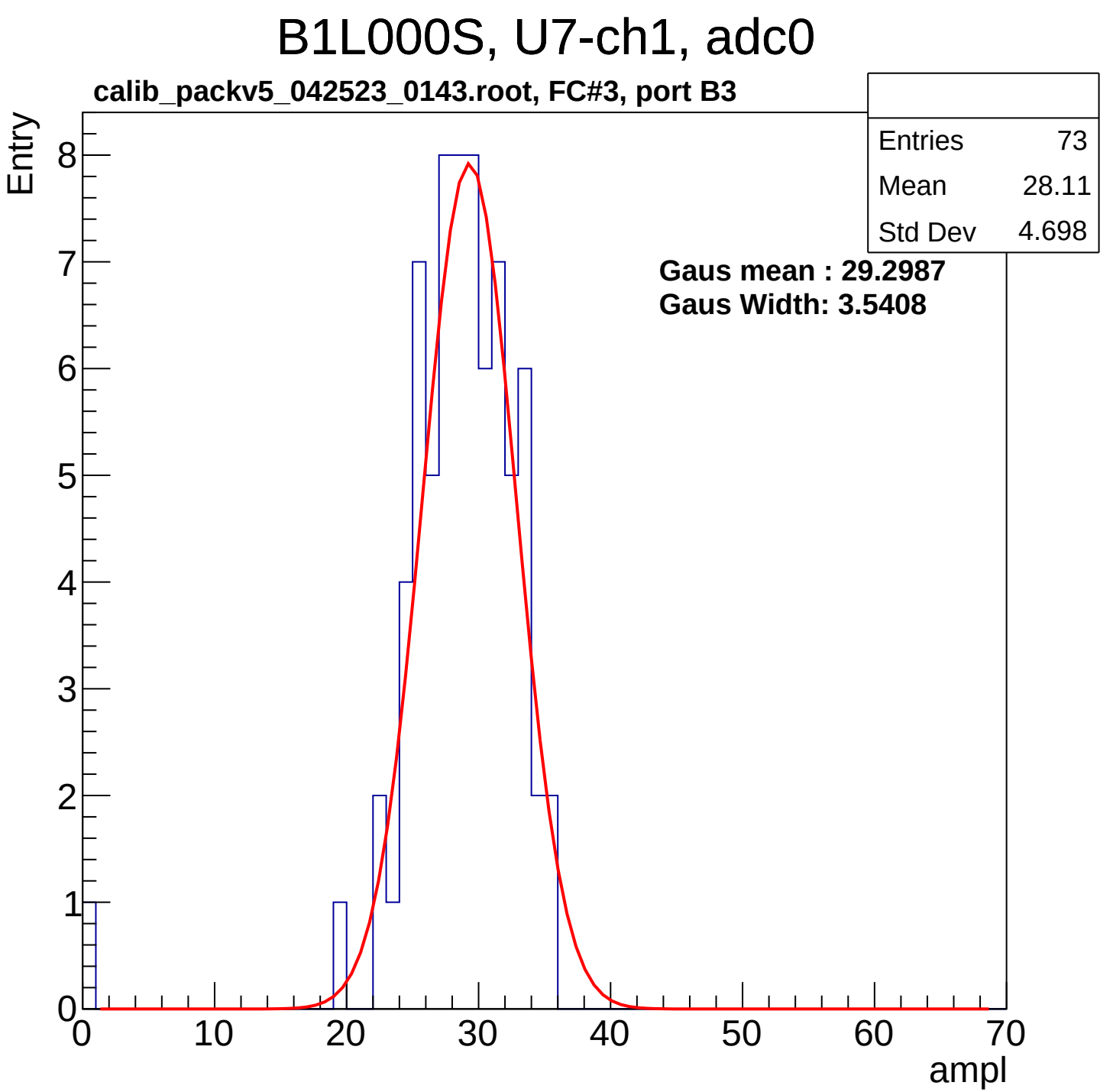
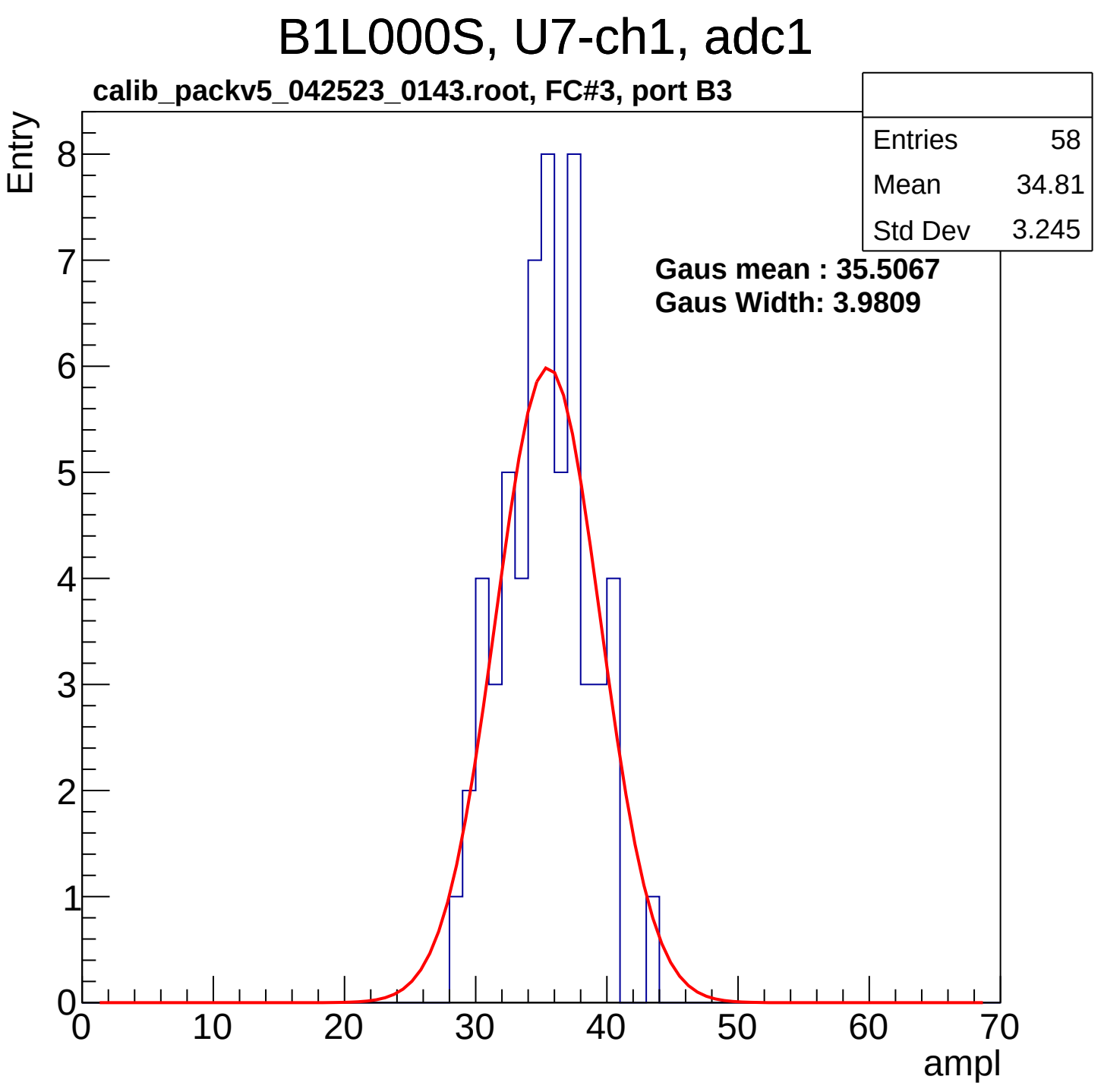
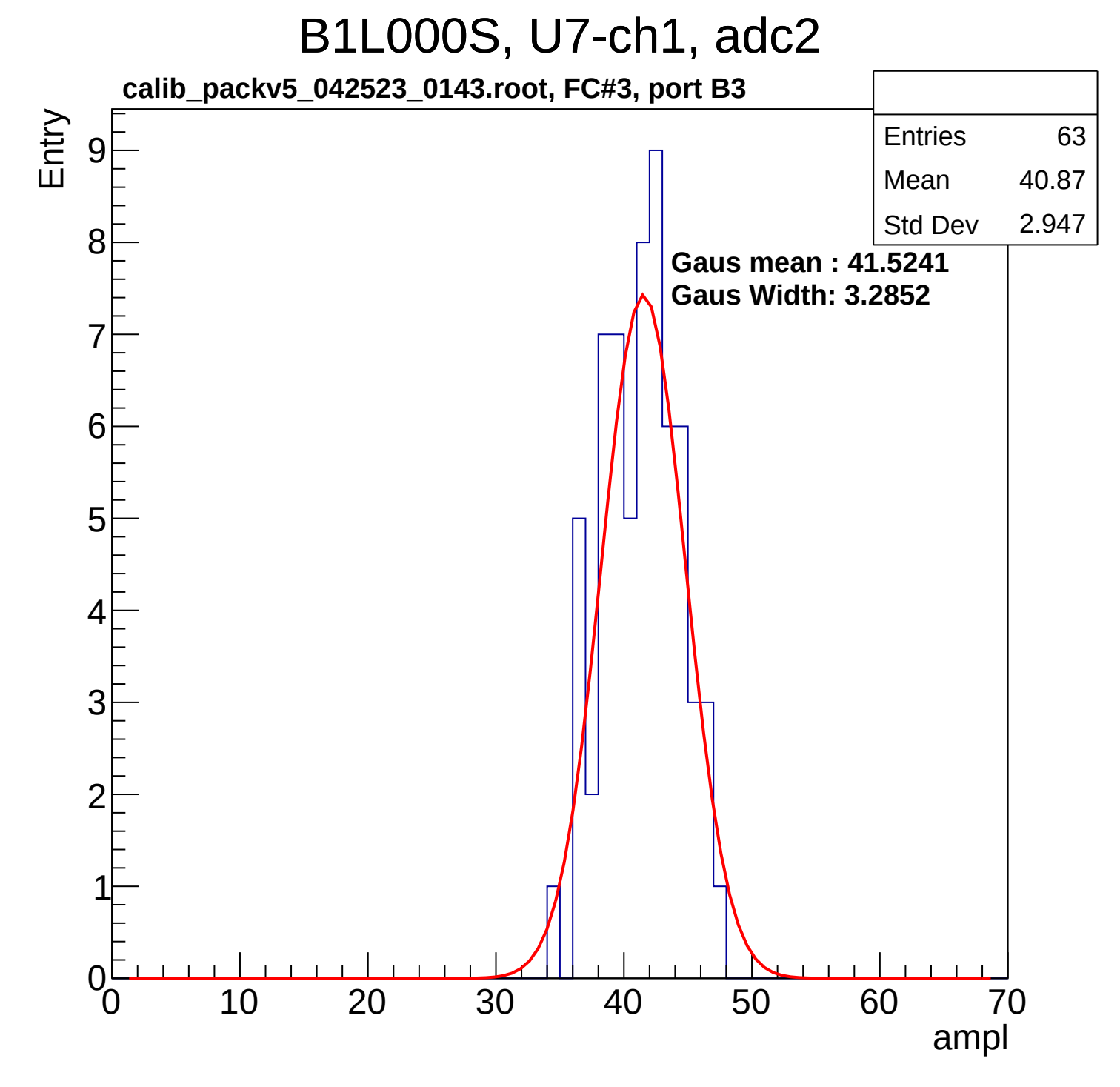

B1L000S, U7-ch0, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch1, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.11
Std Dev 4.698
Gaus mean : 29.2987
Gaus Width: 3.5408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch1, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.81
Std Dev 3.245
Gaus mean : 35.5067
Gaus Width: 3.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch1, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 40.87
Std Dev 2.947
Gaus mean : 41.5241
Gaus Width: 3.2852
Entry
ampl
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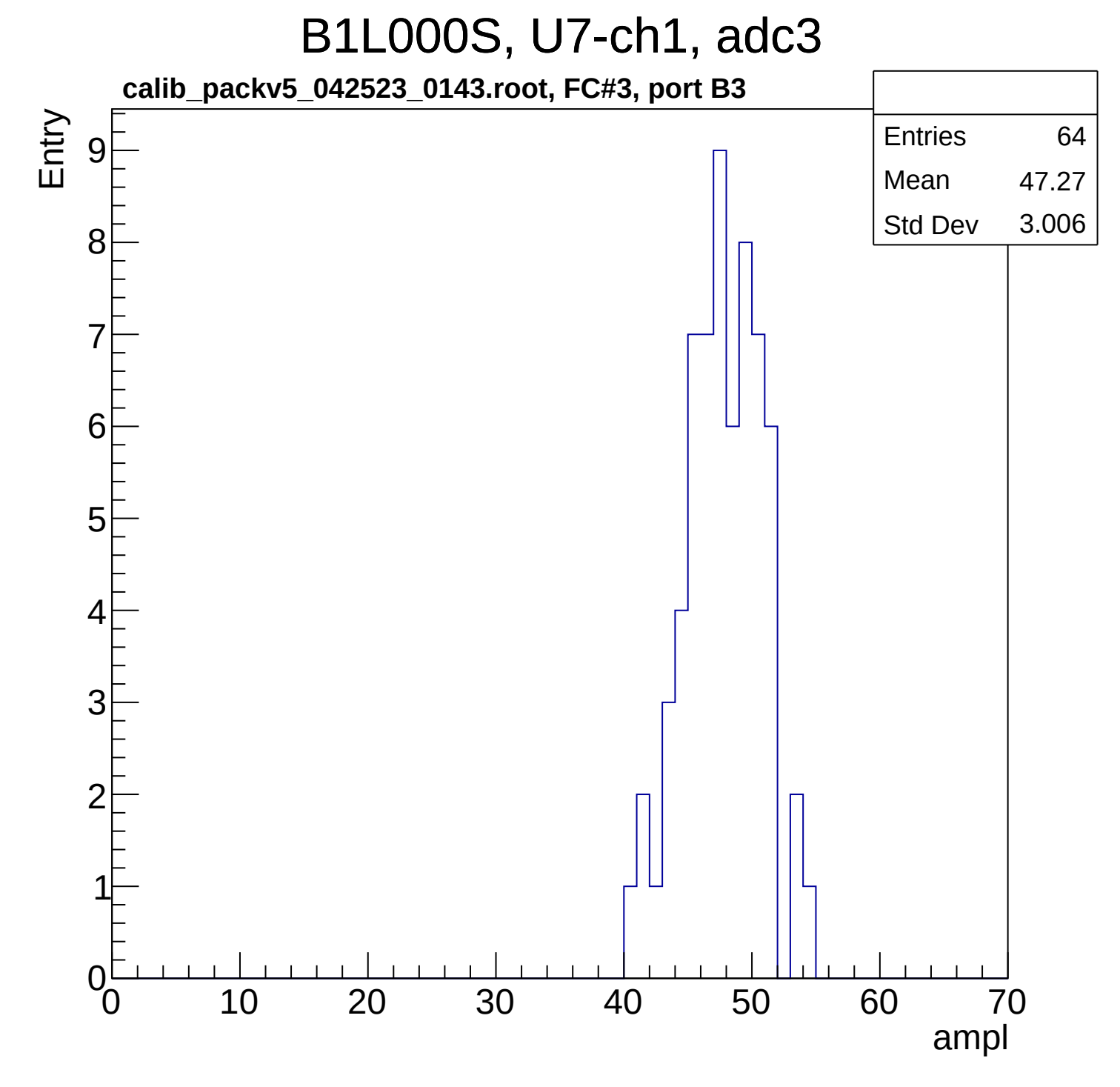
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch1, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 47.27
Std Dev 3.006
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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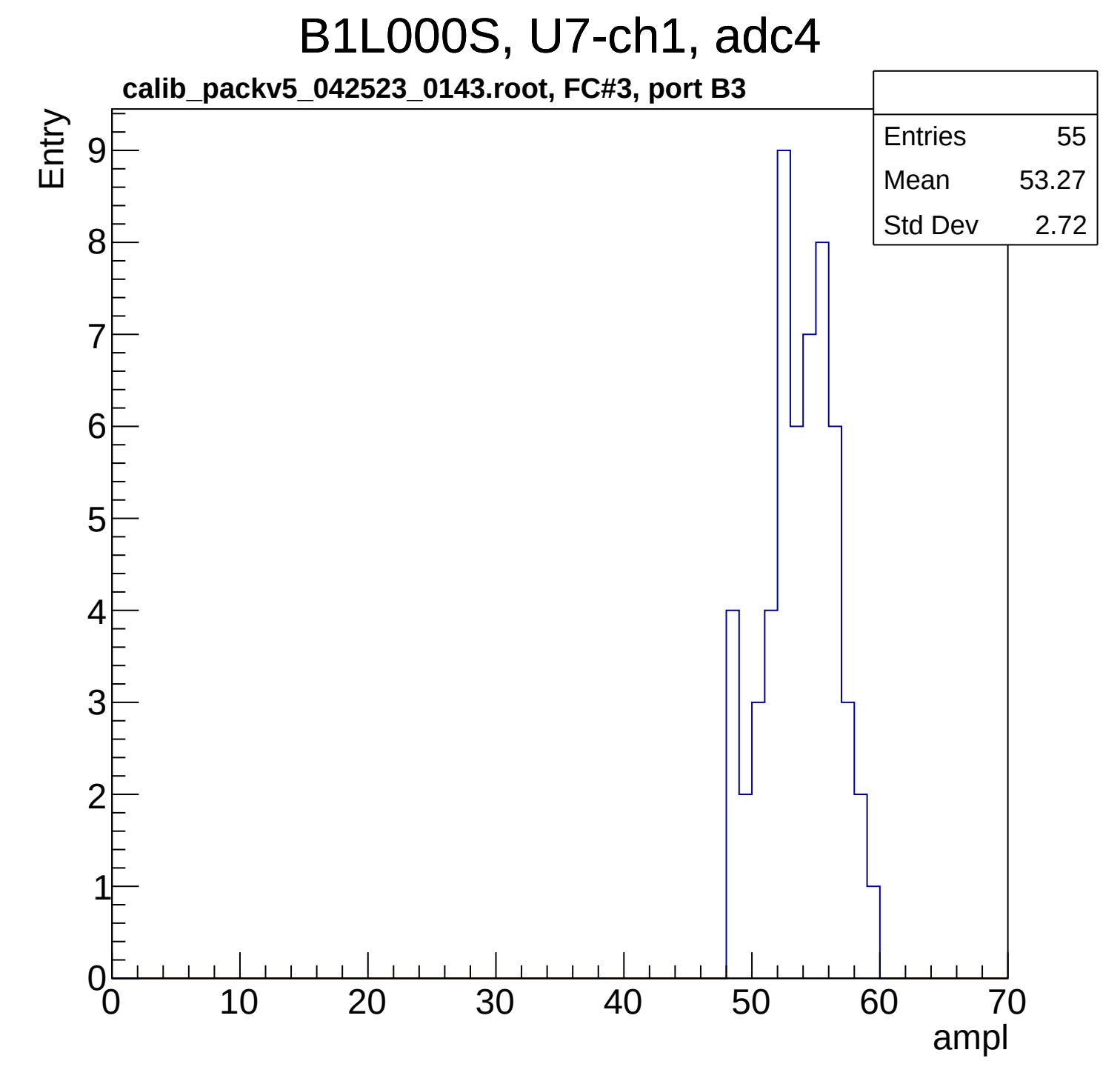
B1L000S, U7-ch1, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.27
Std Dev 2.72
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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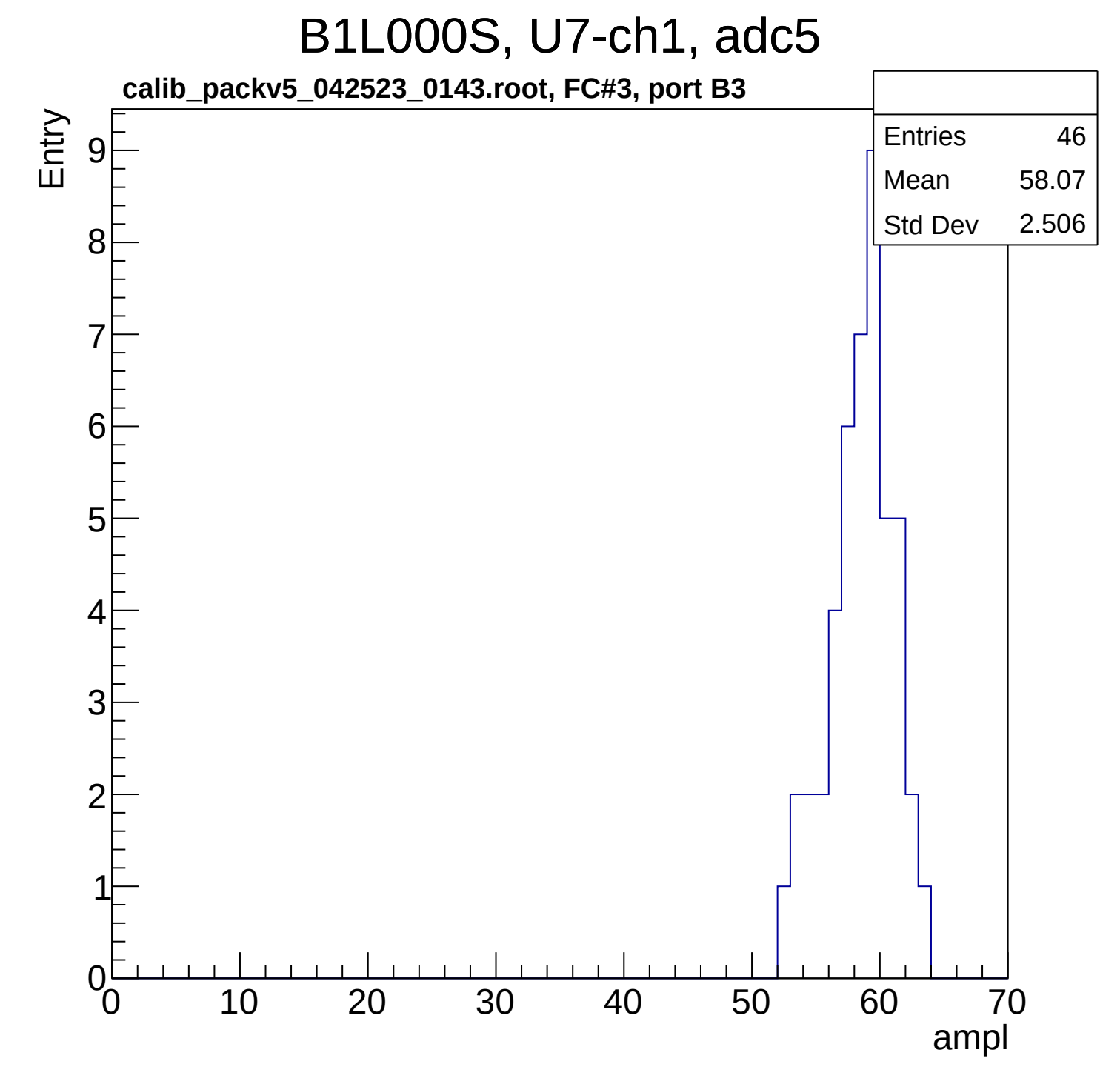
B1L000S, U7-ch1, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 58.07
Std Dev 2.506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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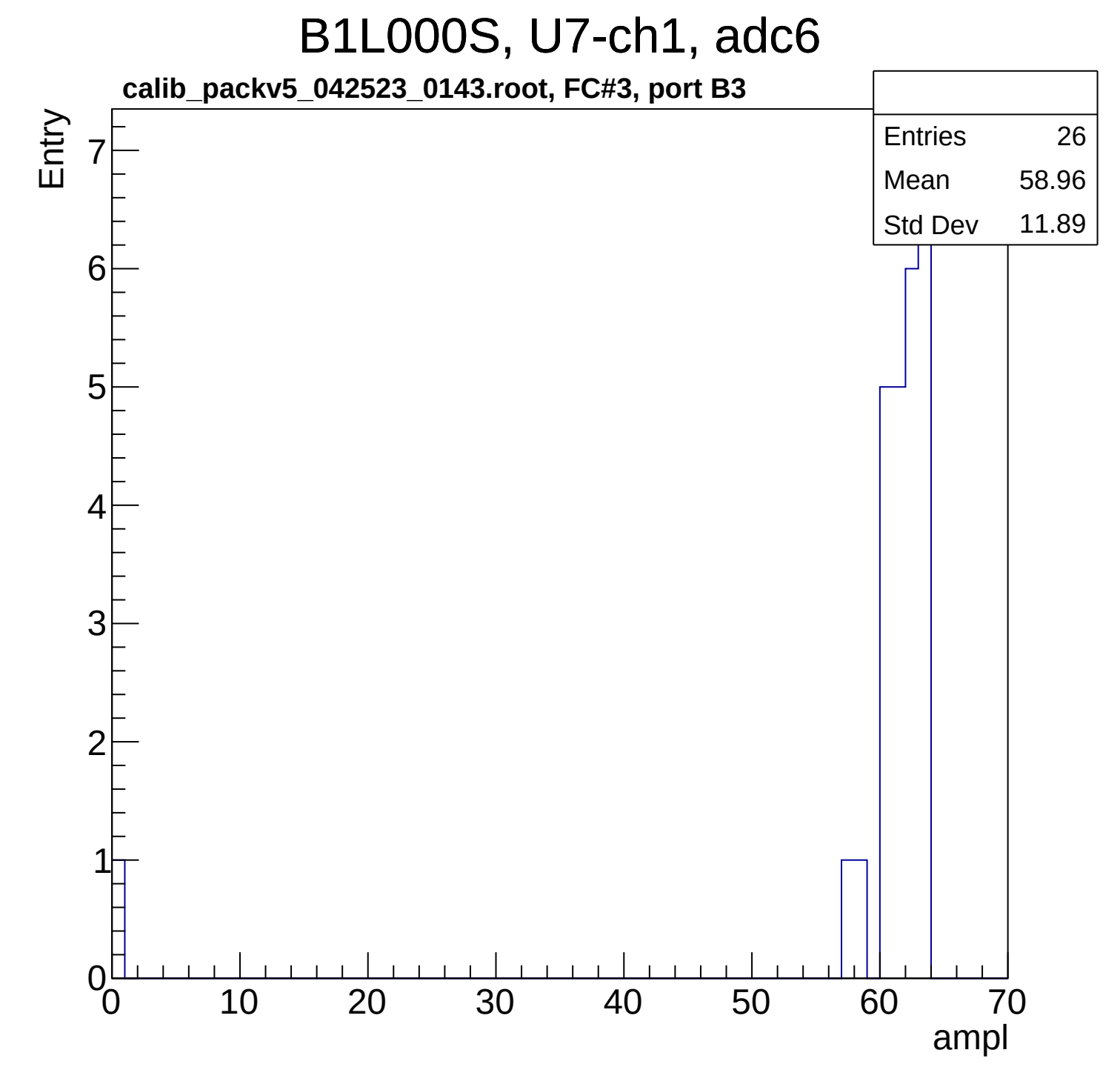
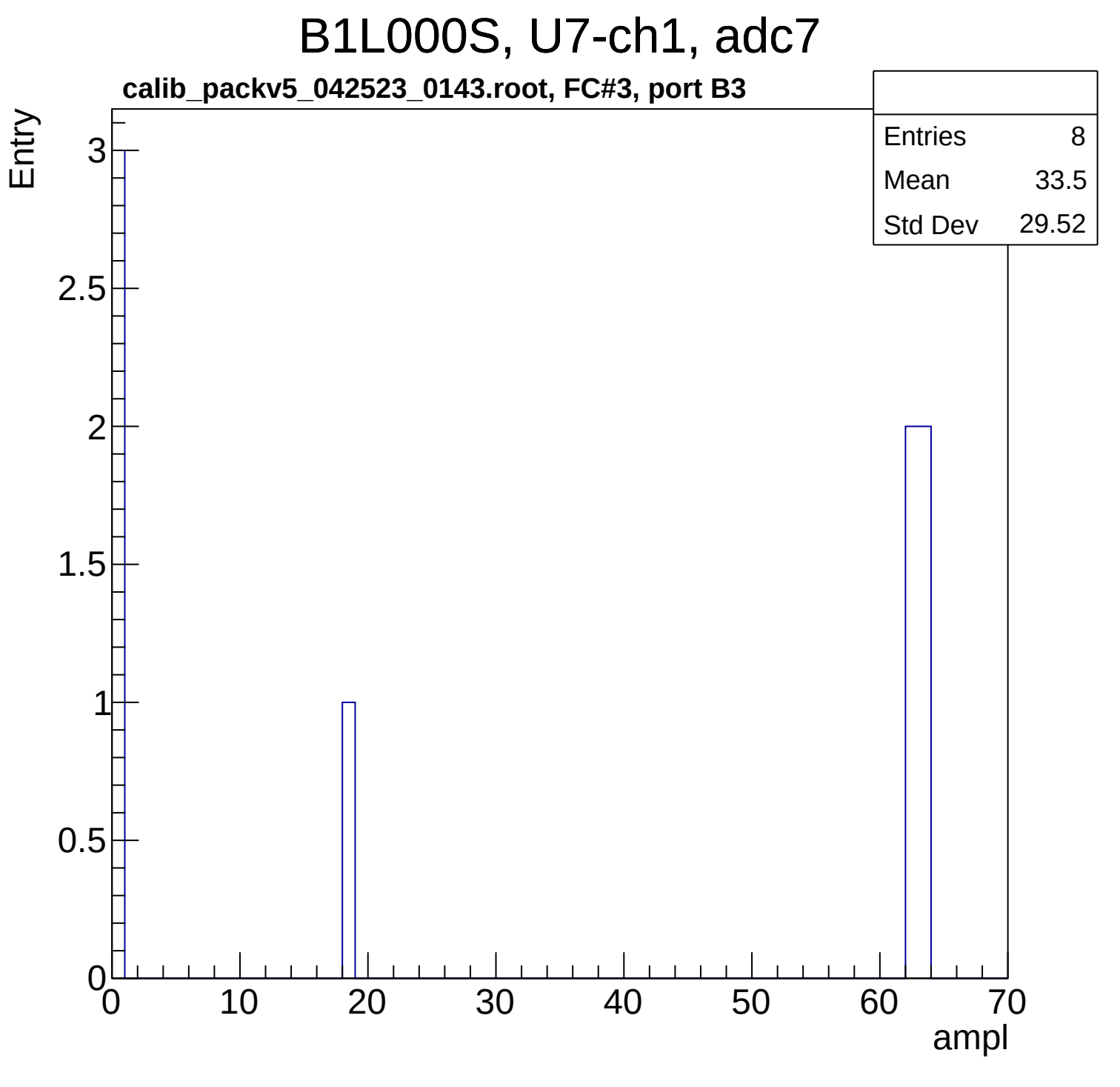

B1L000S, U7-ch1, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 58.96
Std Dev 11.89
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch1, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 33.5
Std Dev 29.52
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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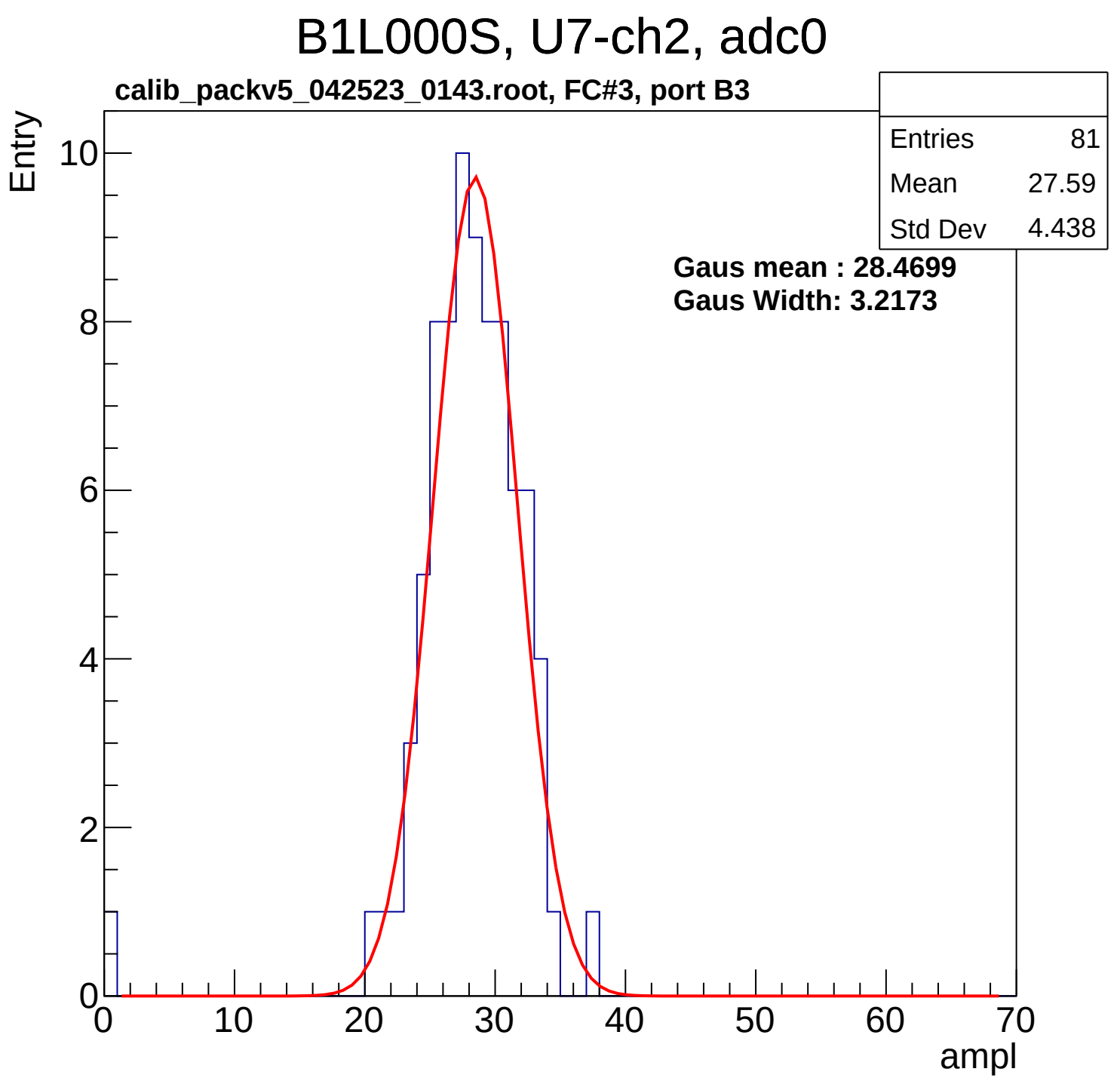
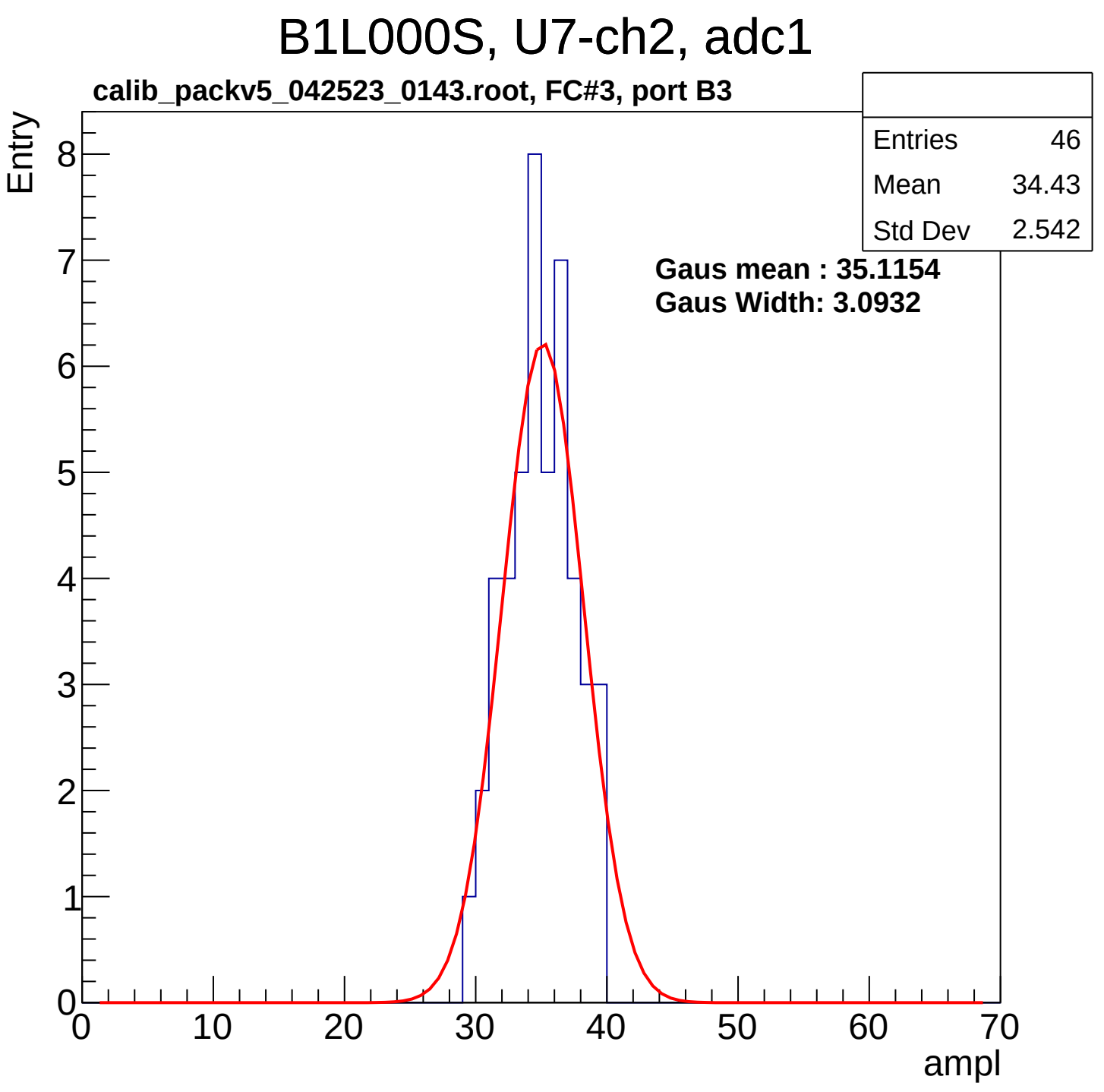

B1L000S, U7-ch2, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 27.59
Std Dev 4.438
Gaus mean : 28.4699
Gaus Width: 3.2173
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch2, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 34.43
Std Dev 2.542
Gaus mean : 35.1154
Gaus Width: 3.0932
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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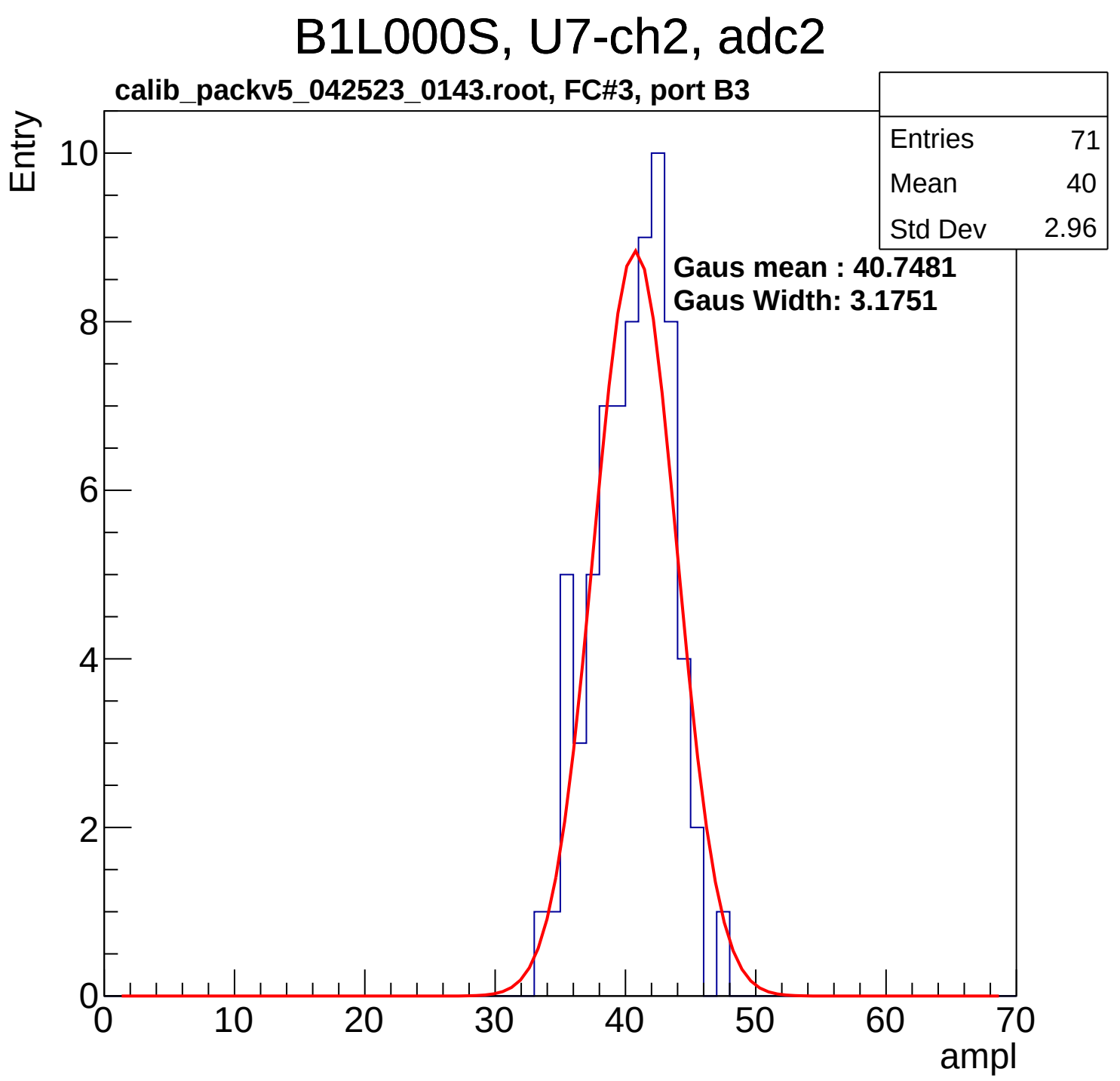
B1L000S, U7-ch2, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 40
Std Dev 2.96
Gaus mean : 40.7481
Gaus Width: 3.1751
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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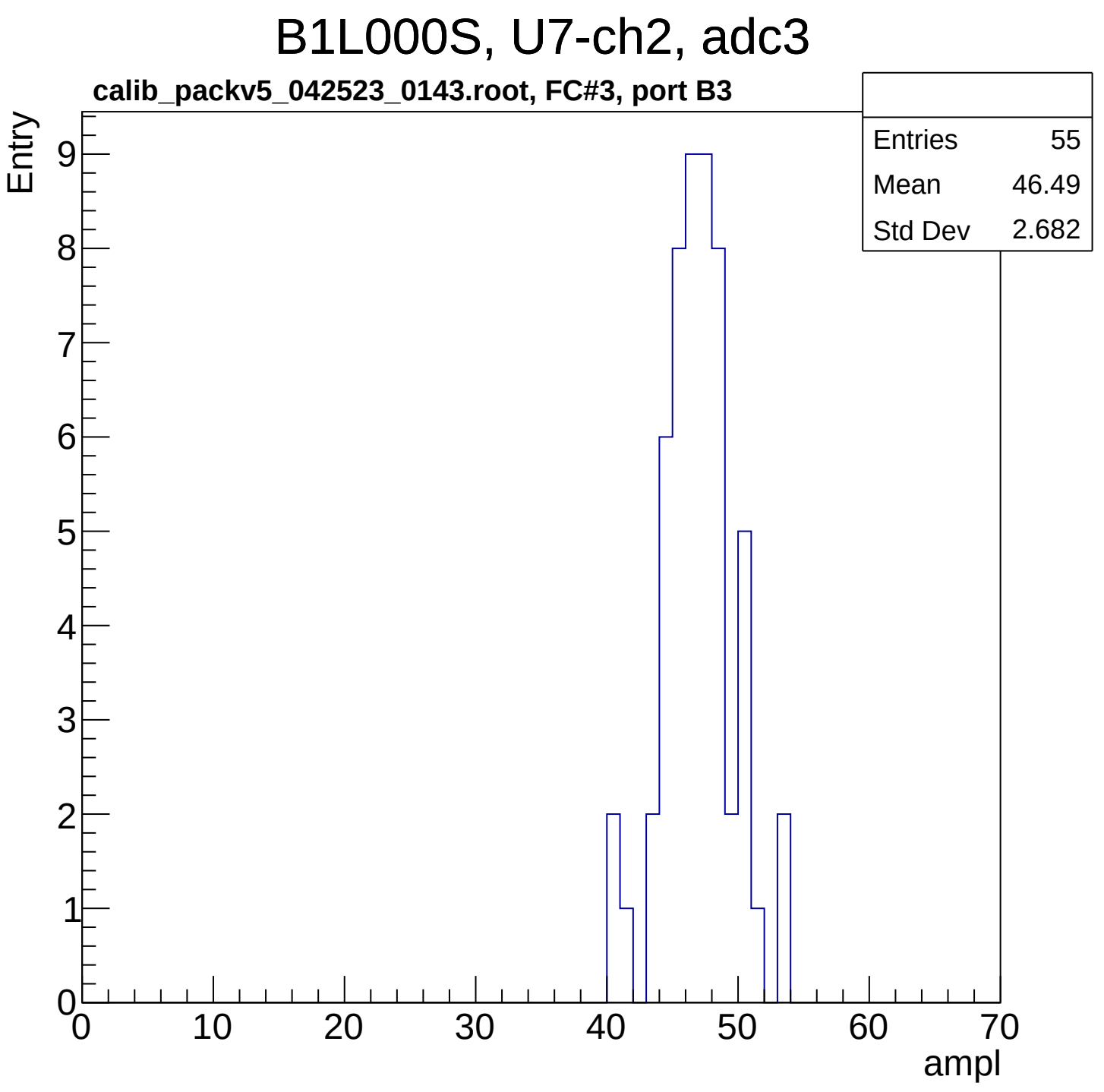
B1L000S, U7-ch2, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 46.49
Std Dev 2.682
Entry
ampl
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3
4
5
6
7
8
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0
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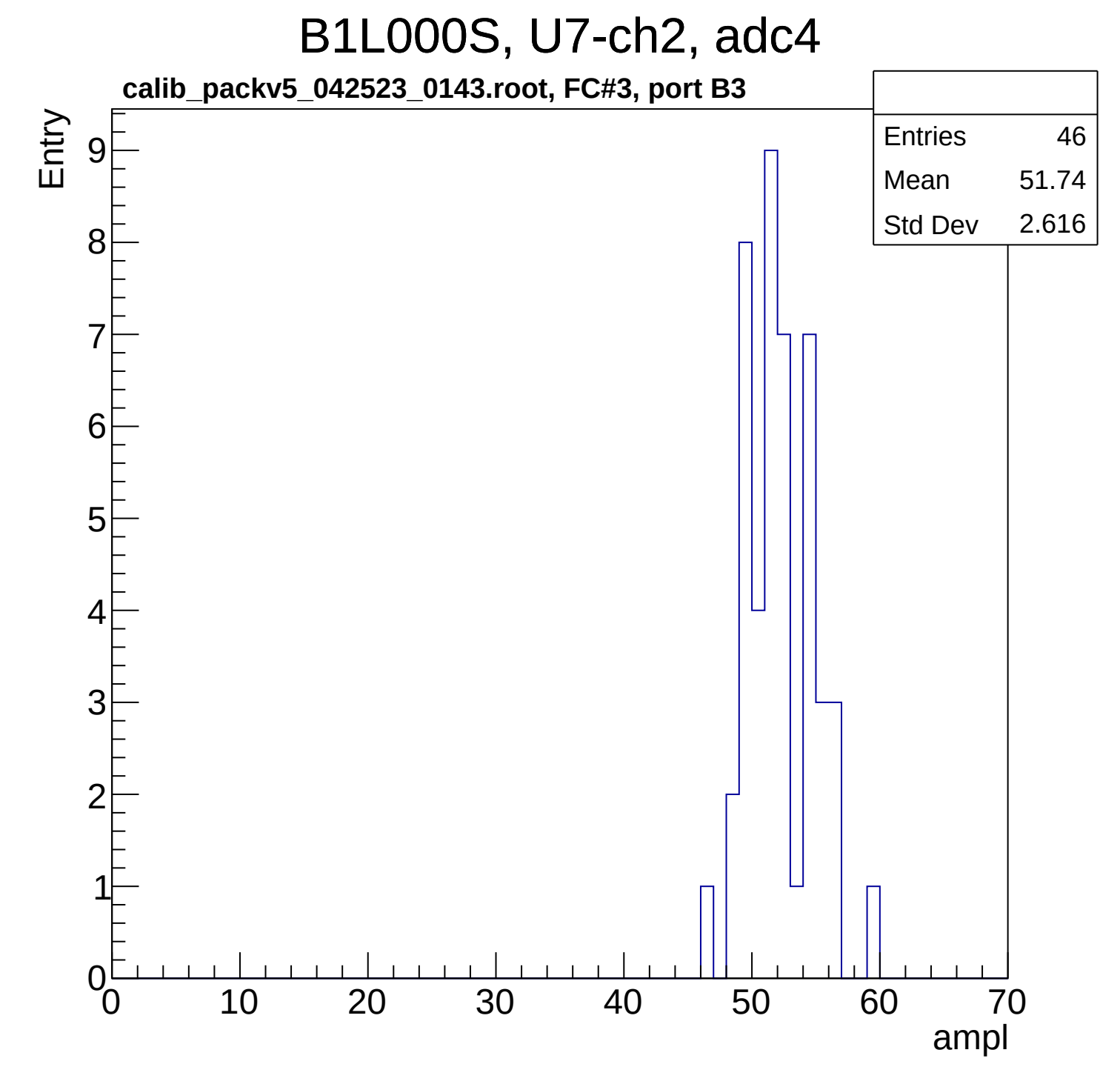
B1L000S, U7-ch2, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 51.74
Std Dev 2.616
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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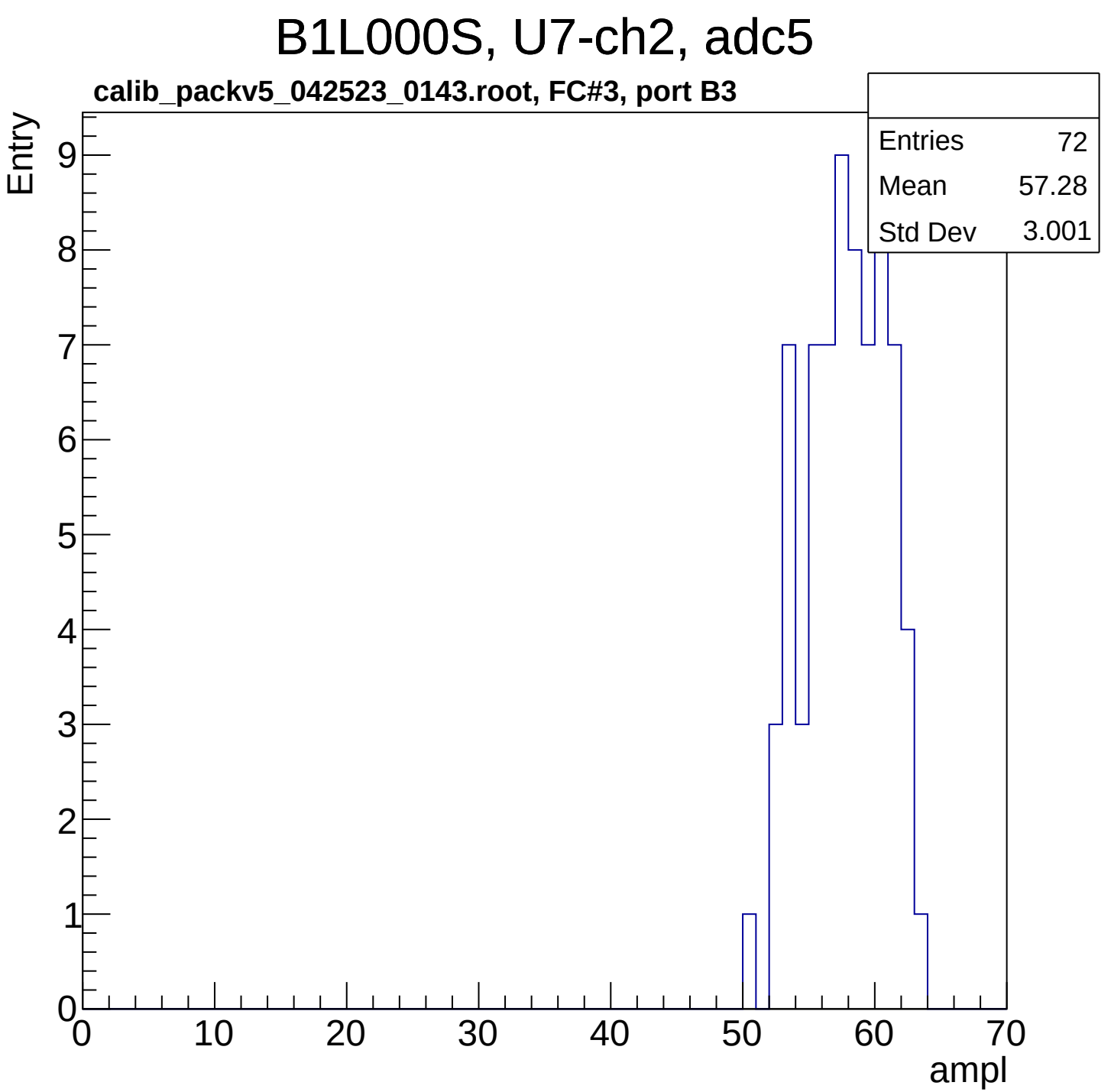
B1L000S, U7-ch2, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 57.28
Std Dev 3.001
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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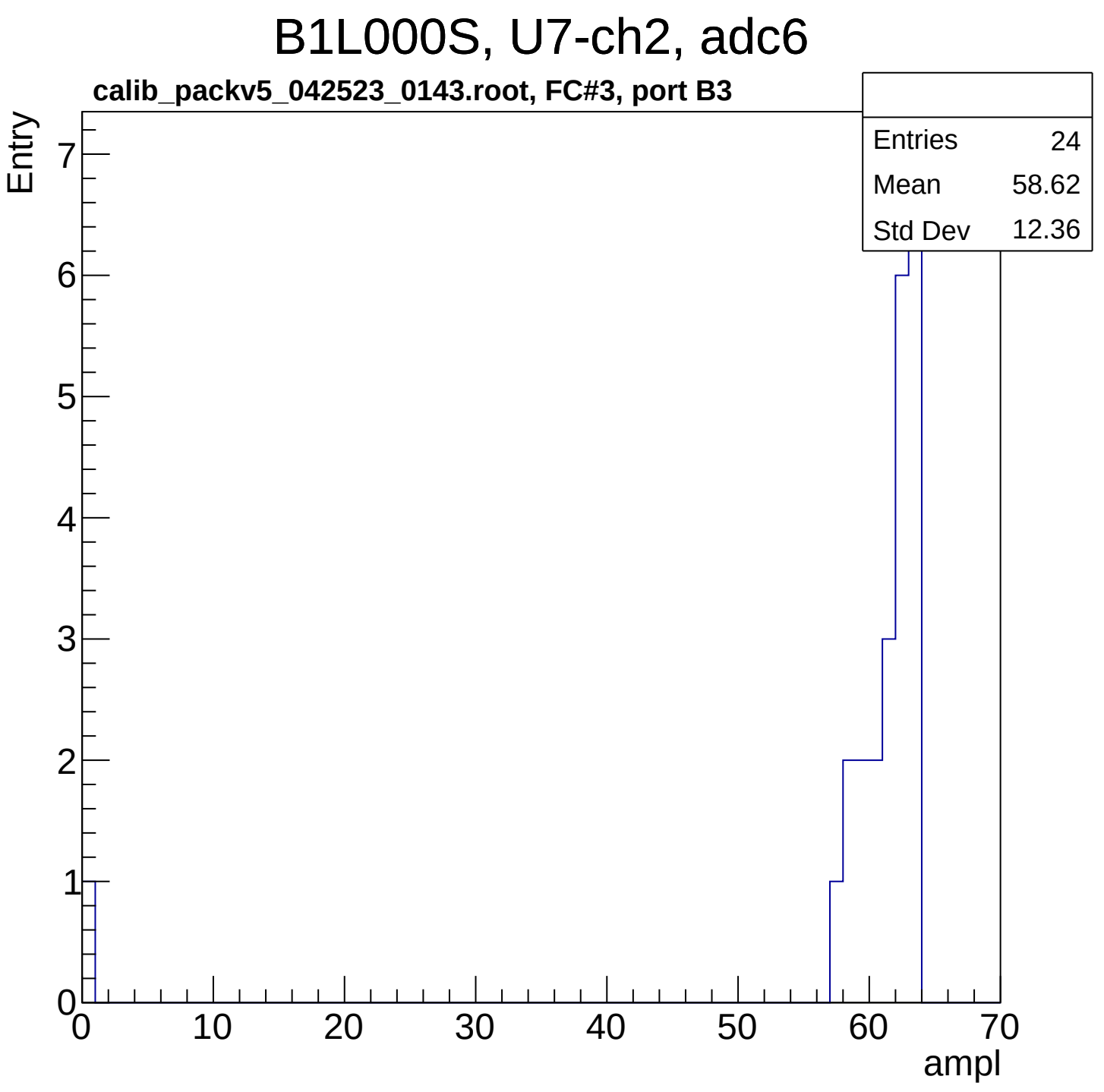
B1L000S, U7-ch2, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 58.62
Std Dev 12.36
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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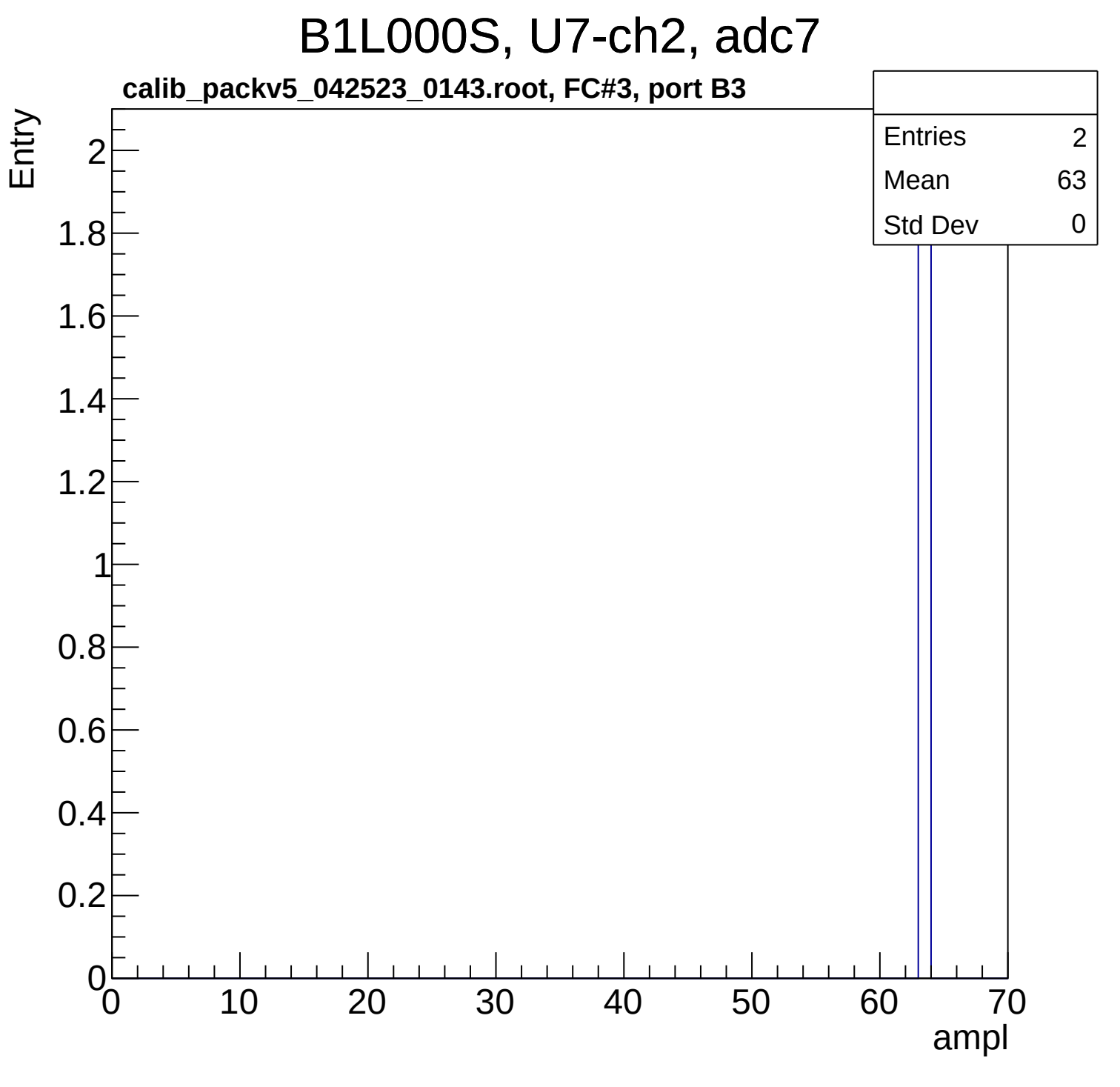
B1L000S, U7-ch2, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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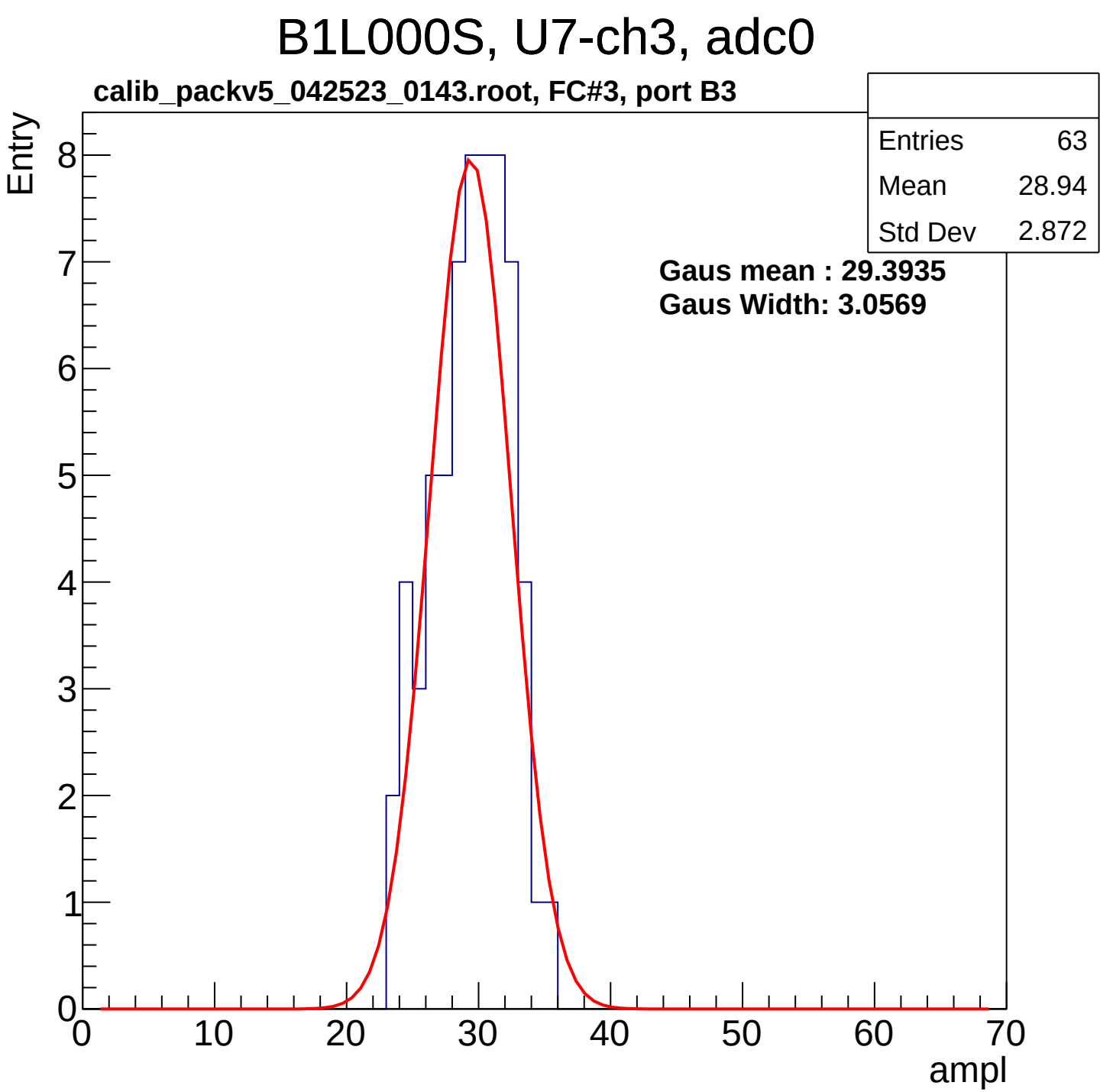
B1L000S, U7-ch3, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 28.94
Std Dev 2.872
Gaus mean : 29.3935
Gaus Width: 3.0569
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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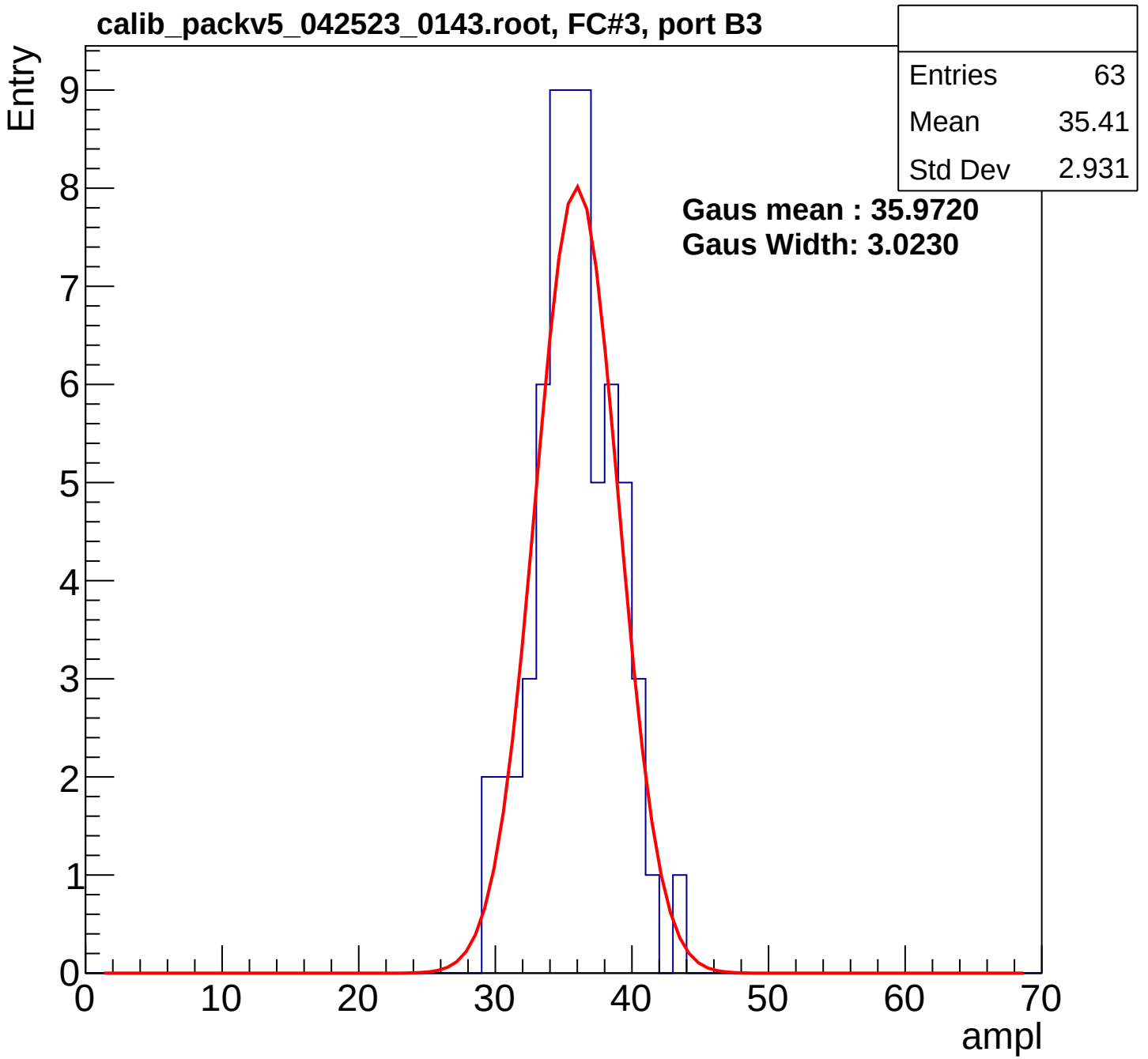
B1L000S, U7-ch3, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 35.41
Std Dev 2.931
Gaus mean : 35.9720
Gaus Width: 3.0230
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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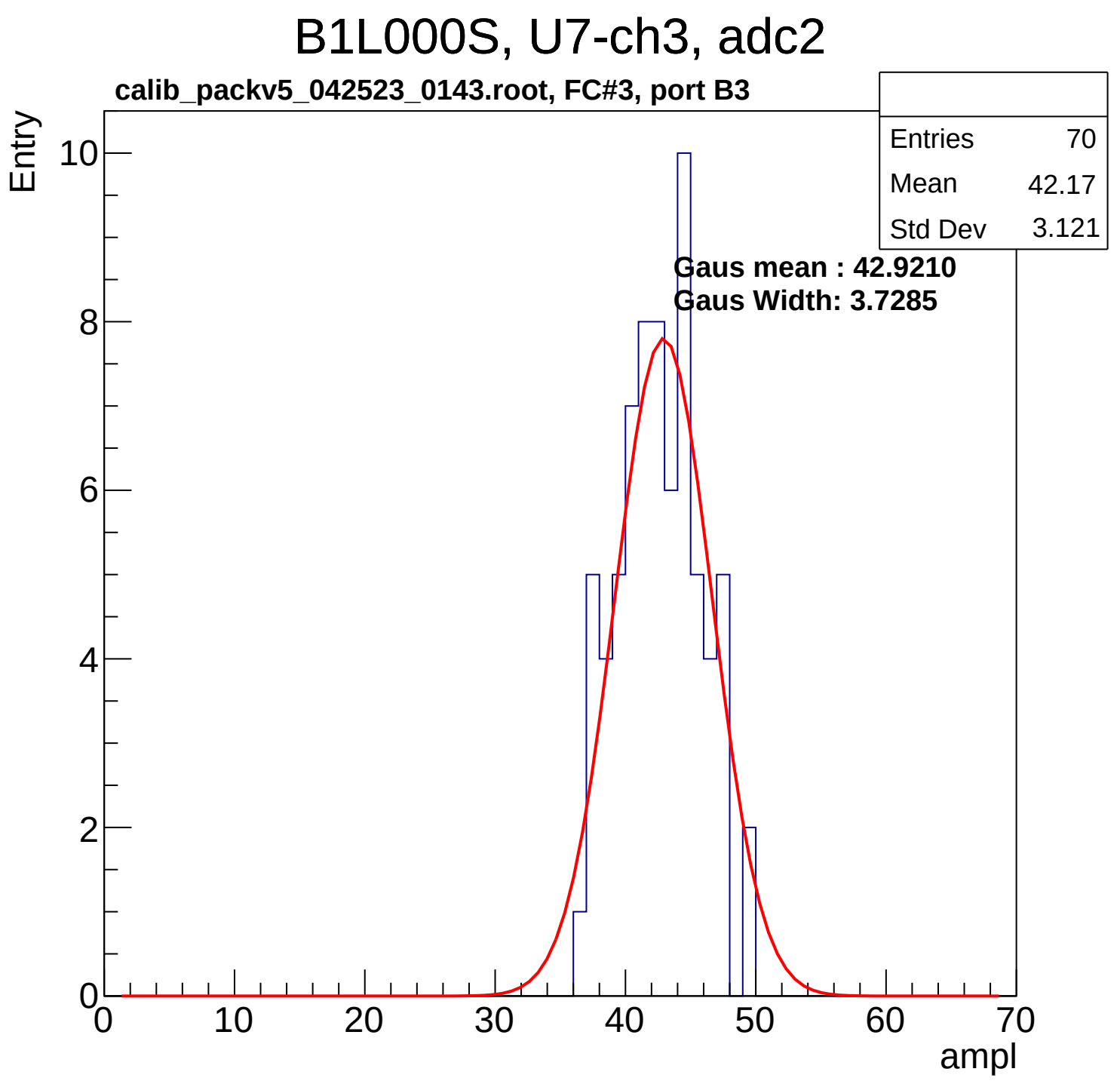
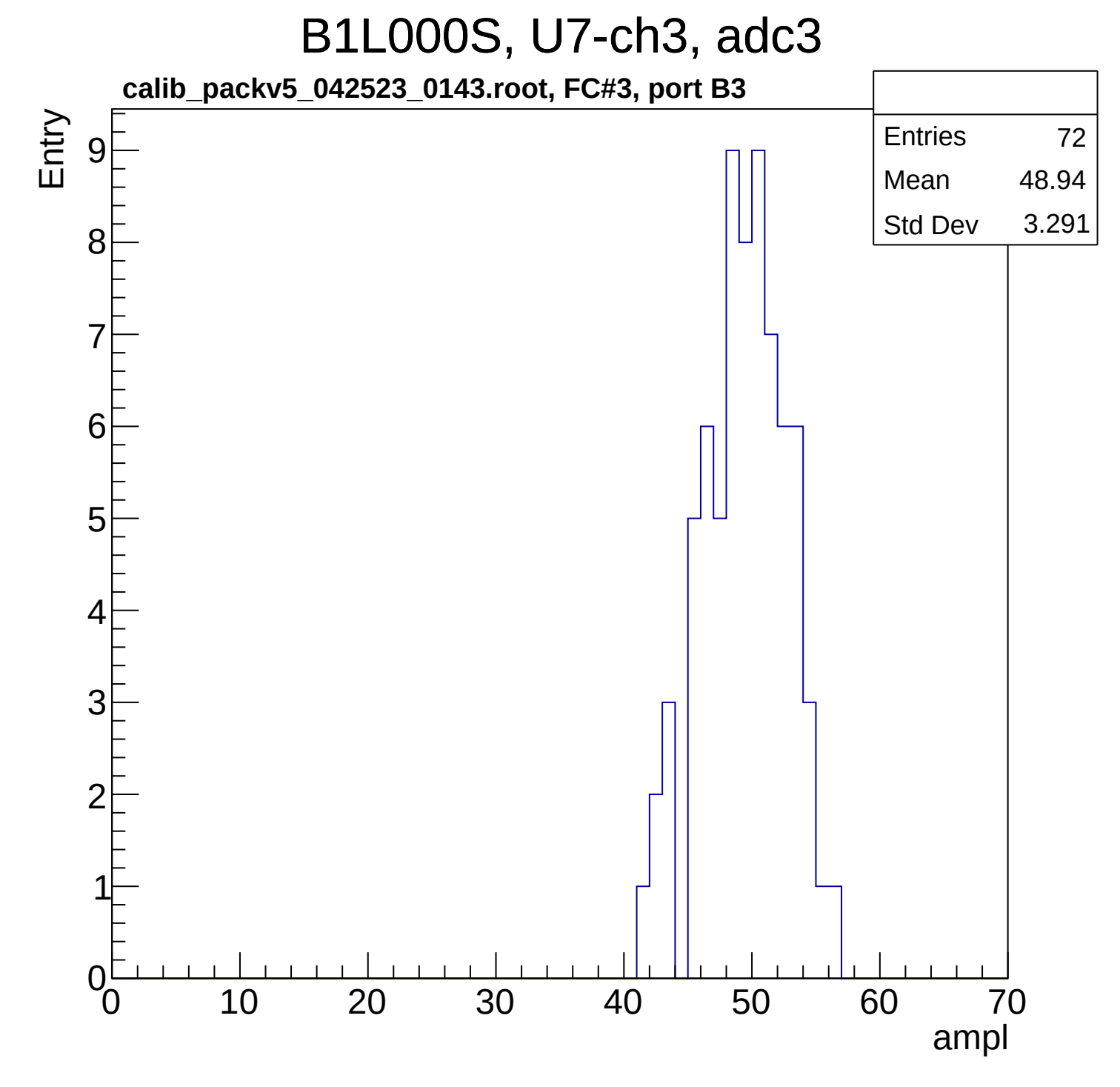

B1L000S, U7-ch3, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.17
Std Dev 3.121
Gaus mean : 42.9210
Gaus Width: 3.7285
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch3, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.94
Std Dev 3.291
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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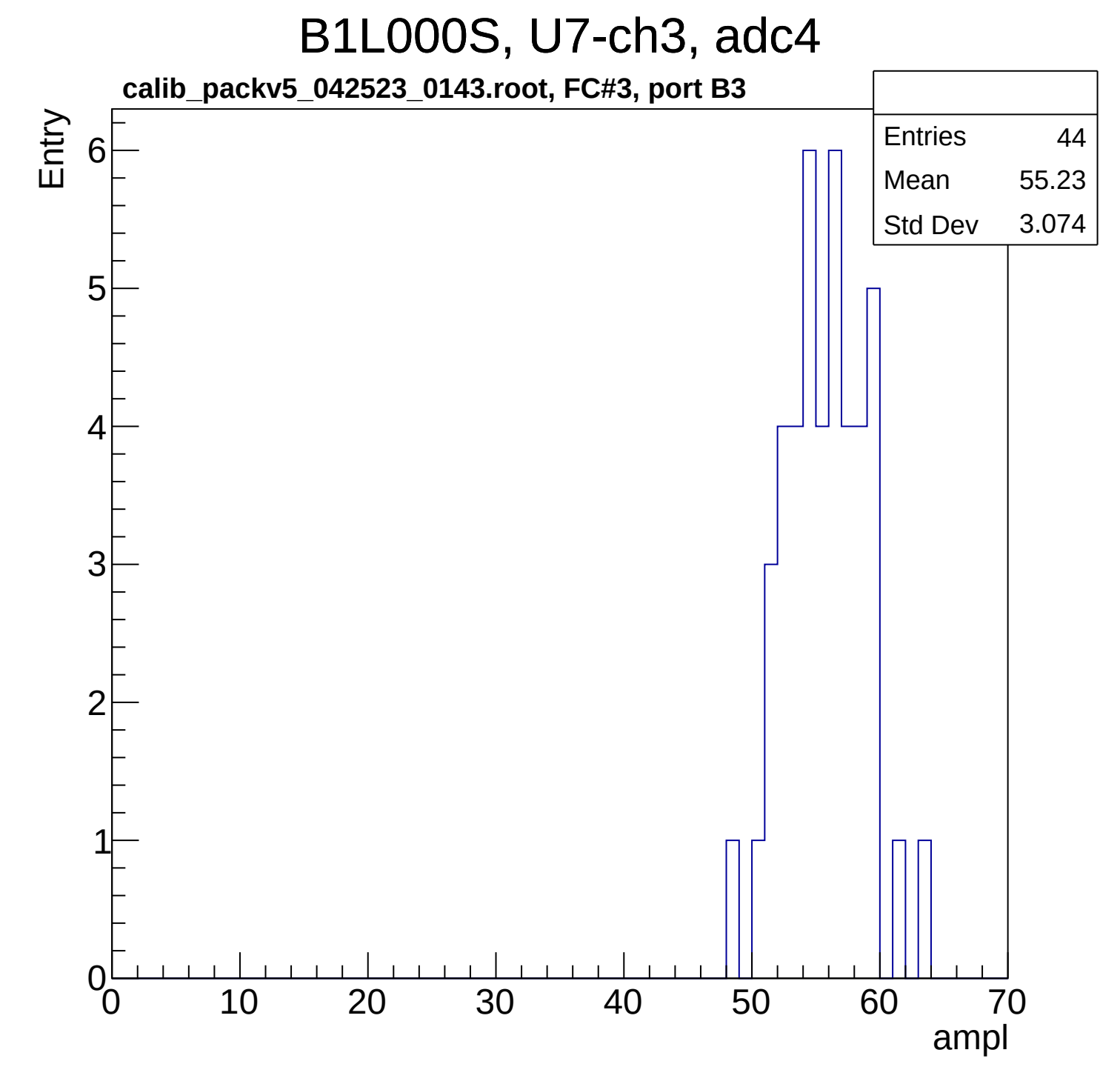
B1L000S, U7-ch3, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 55.23
Std Dev 3.074
Entry
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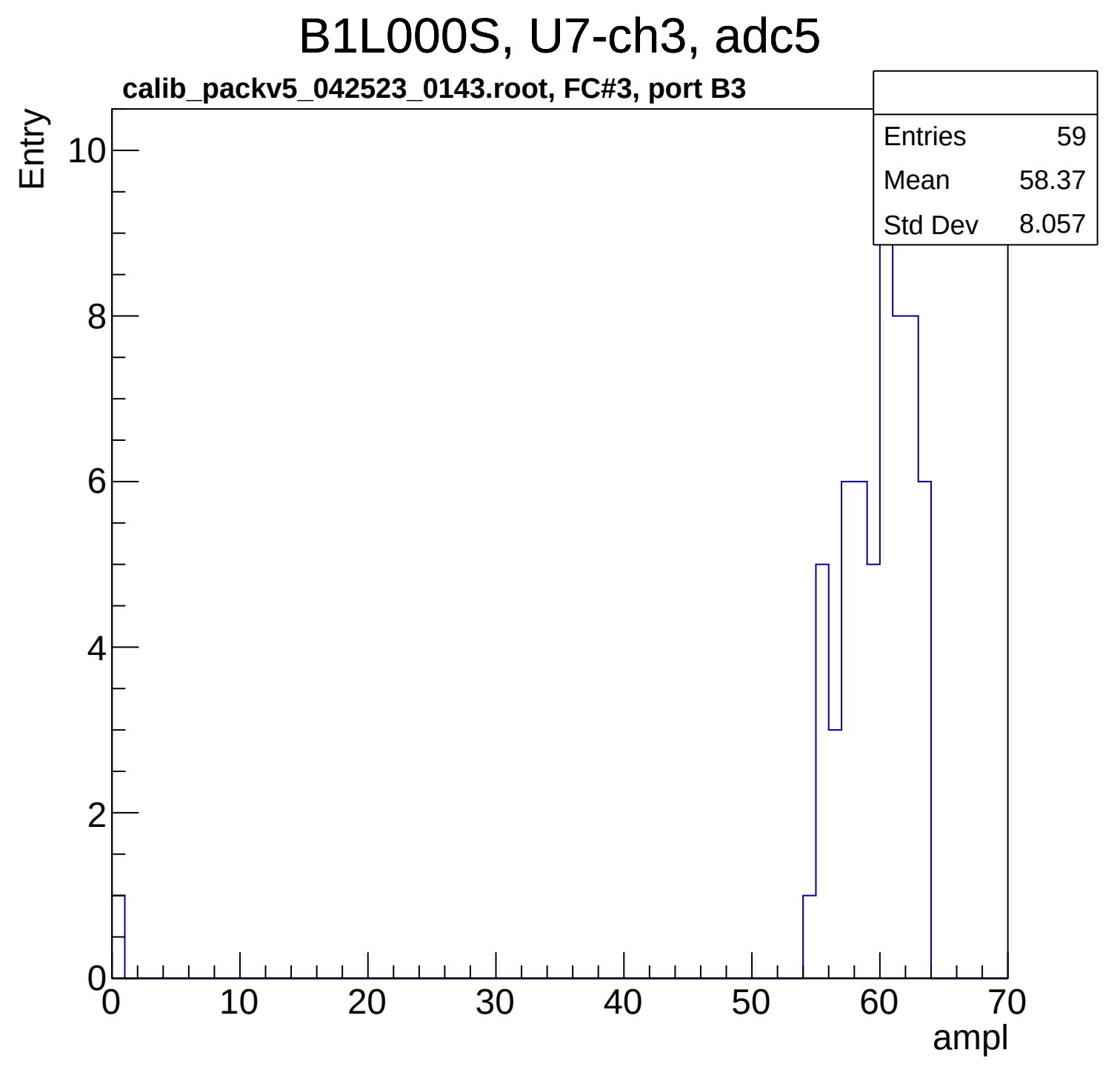
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch3, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.37
Std Dev 8.057
Entry
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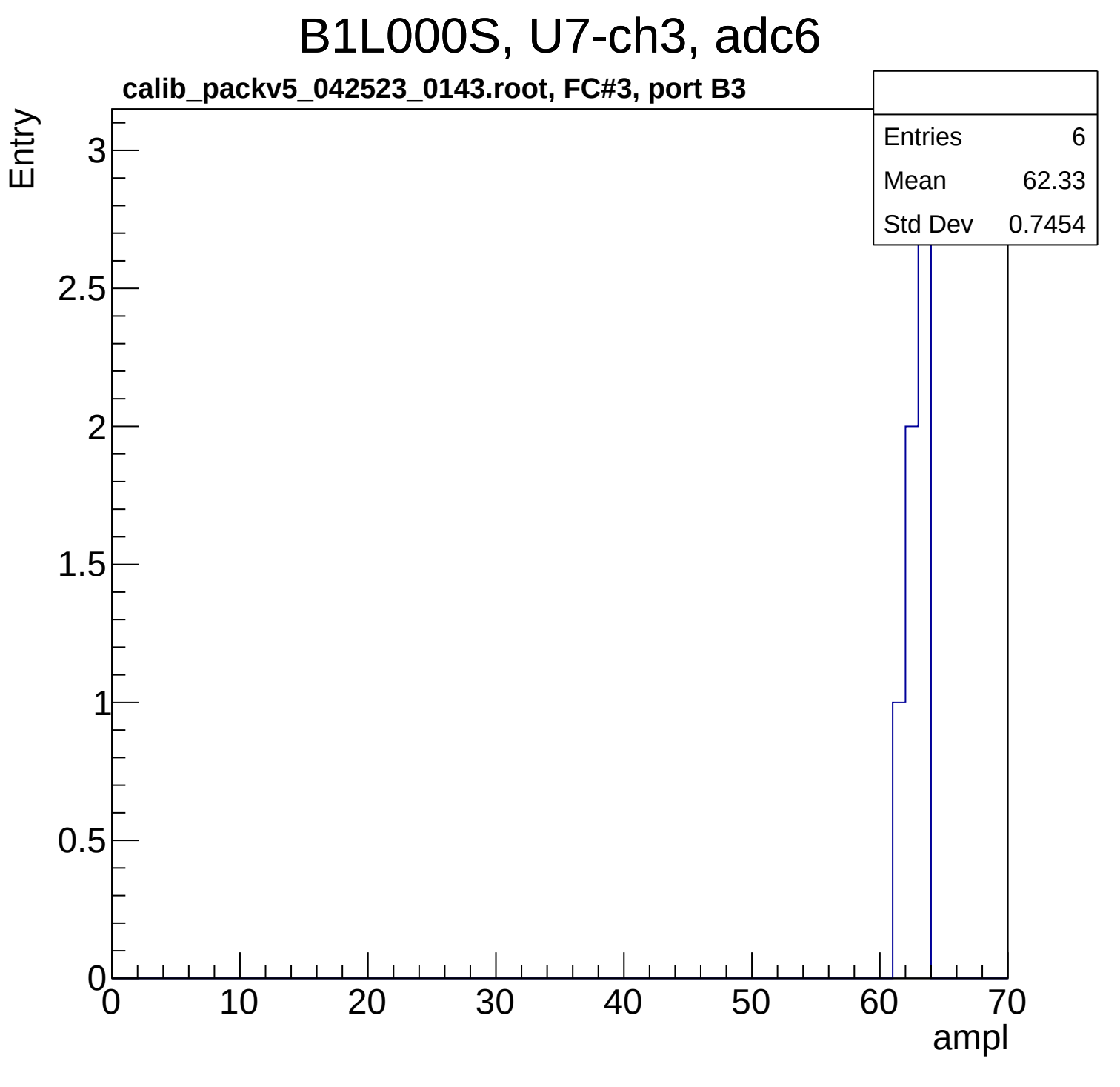
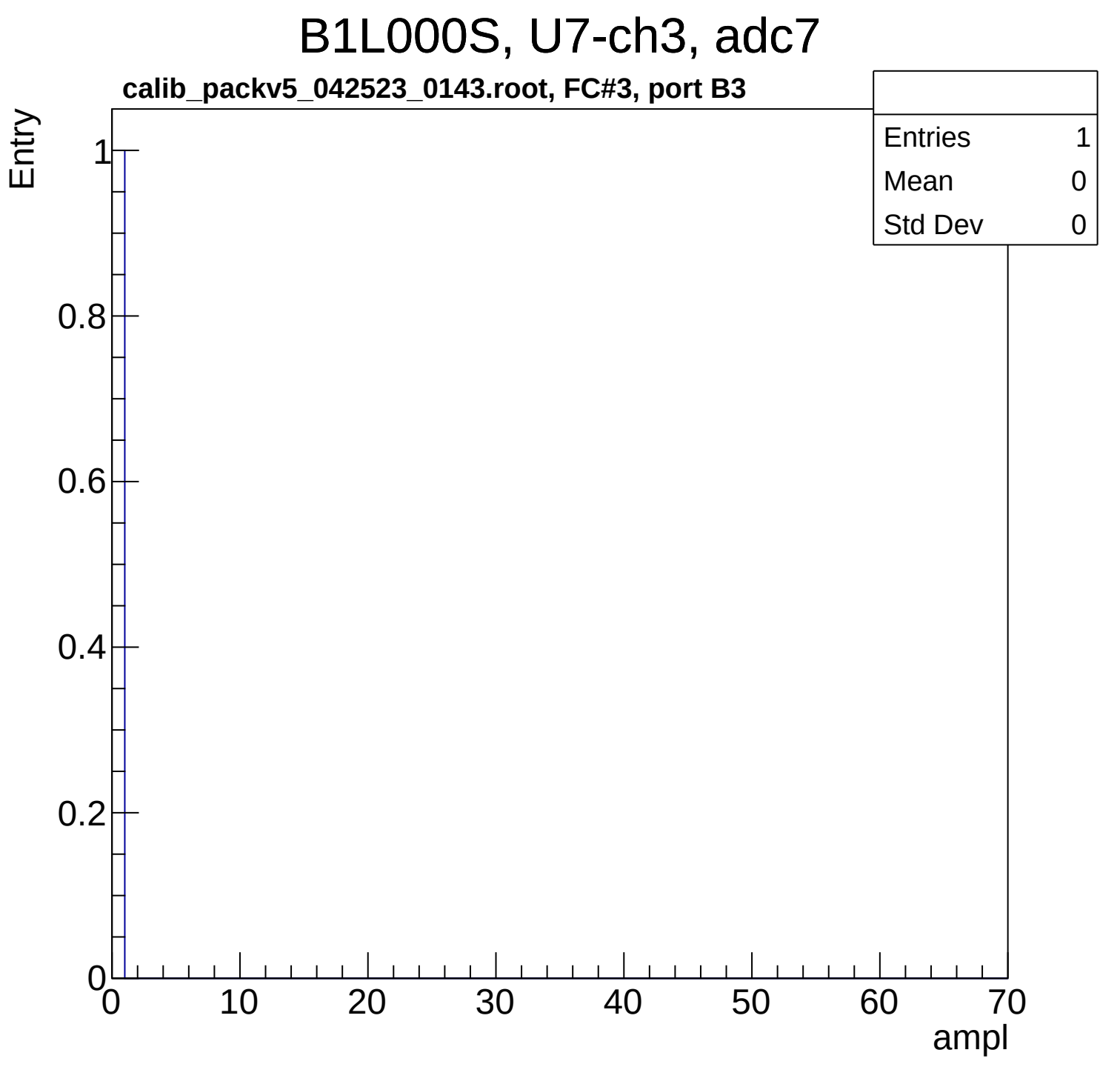
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch3, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch3, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
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0
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10
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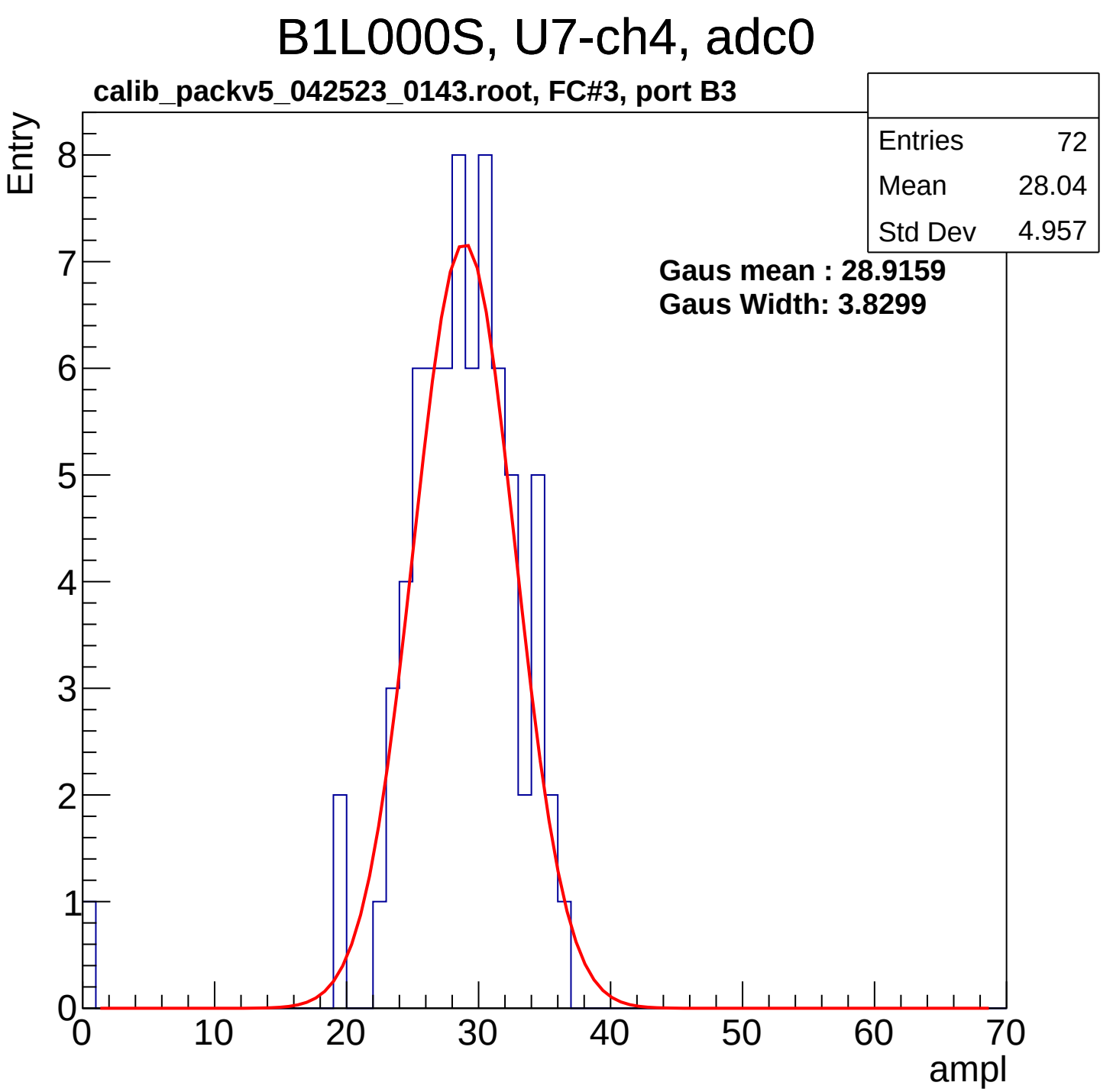
70
ampl


B1L000S, U7-ch4, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 28.04
Std Dev 4.957
Gaus mean : 28.9159
Gaus Width: 3.8299
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
50
60
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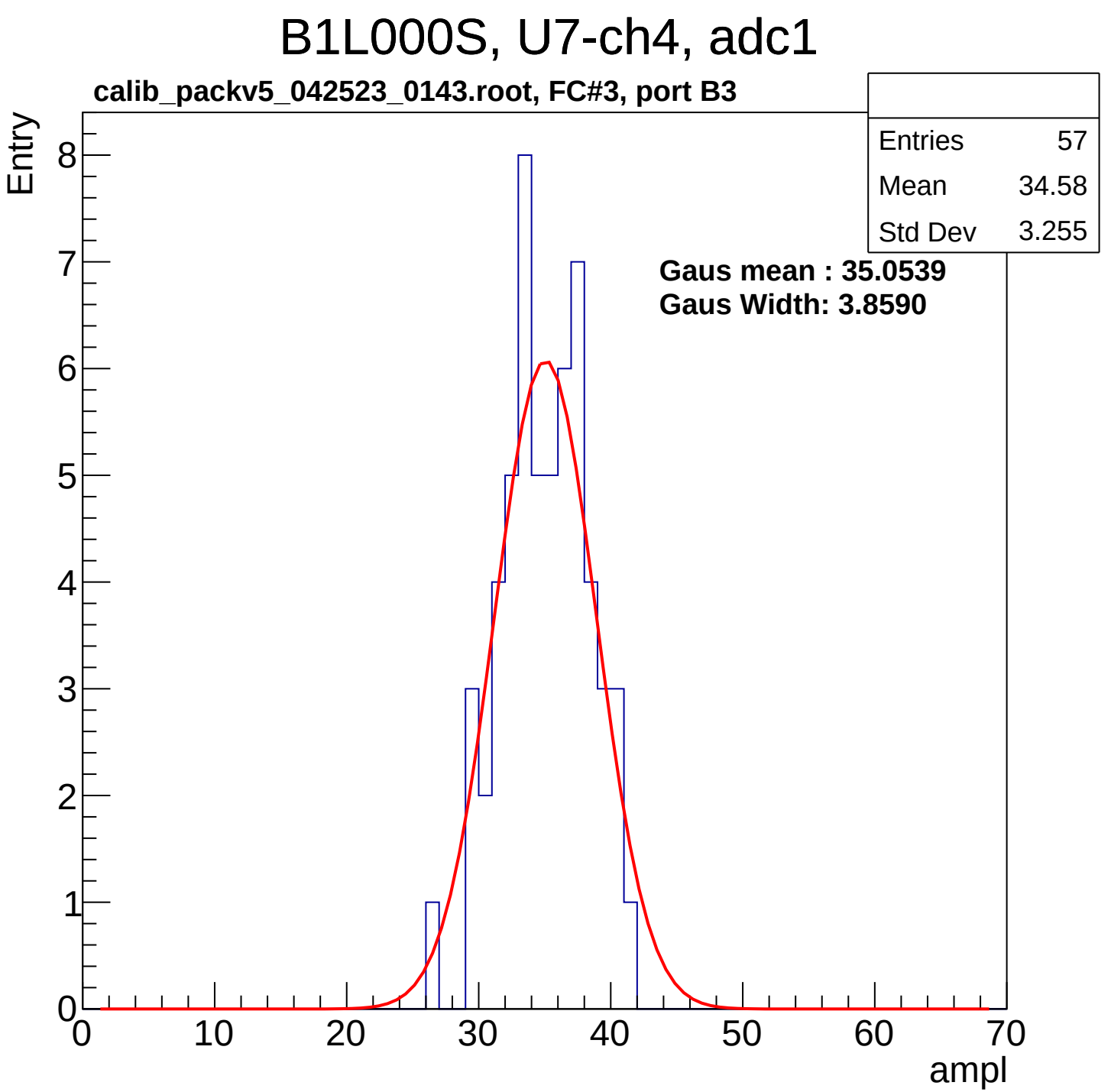
B1L000S, U7-ch4, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 34.58
Std Dev 3.255
Gaus mean : 35.0539
Gaus Width: 3.8590
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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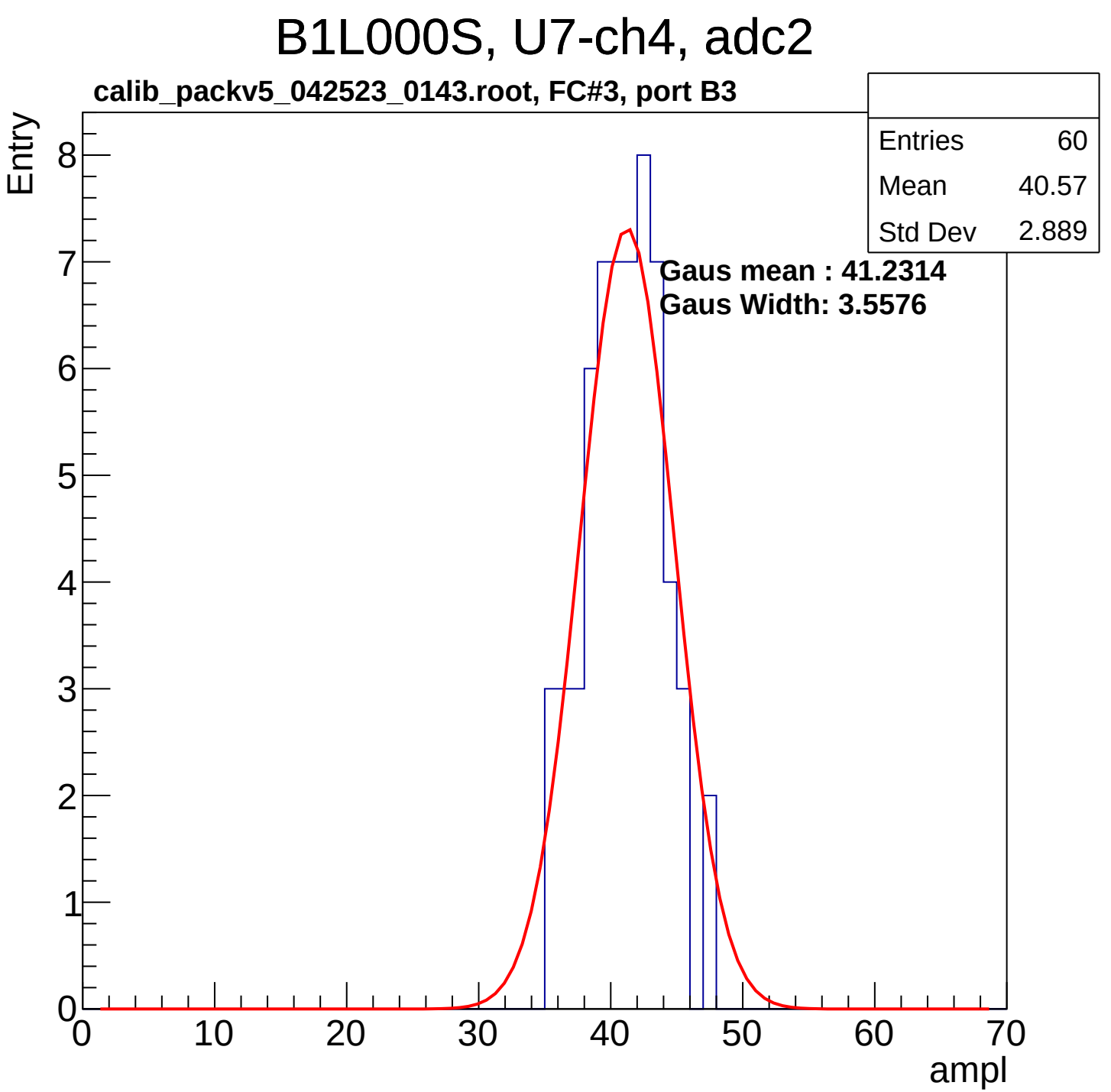
B1L000S, U7-ch4, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 40.57
Std Dev 2.889
Gaus mean : 41.2314
Gaus Width: 3.5576
Entry
ampl
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1
2
3
4
5
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0
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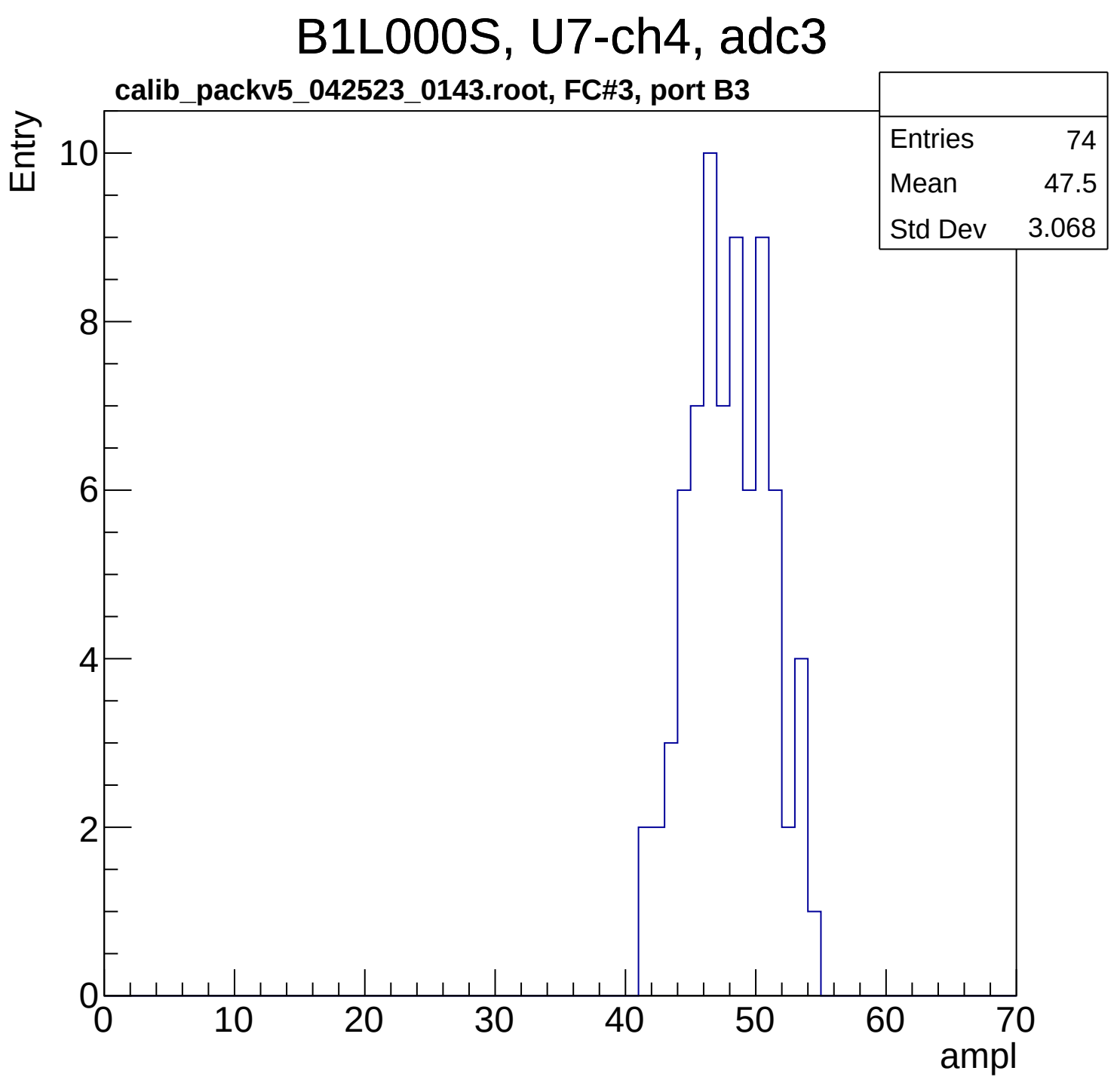
B1L000S, U7-ch4, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 47.5
Std Dev 3.068
Entry
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2
4
6
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10
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10
20
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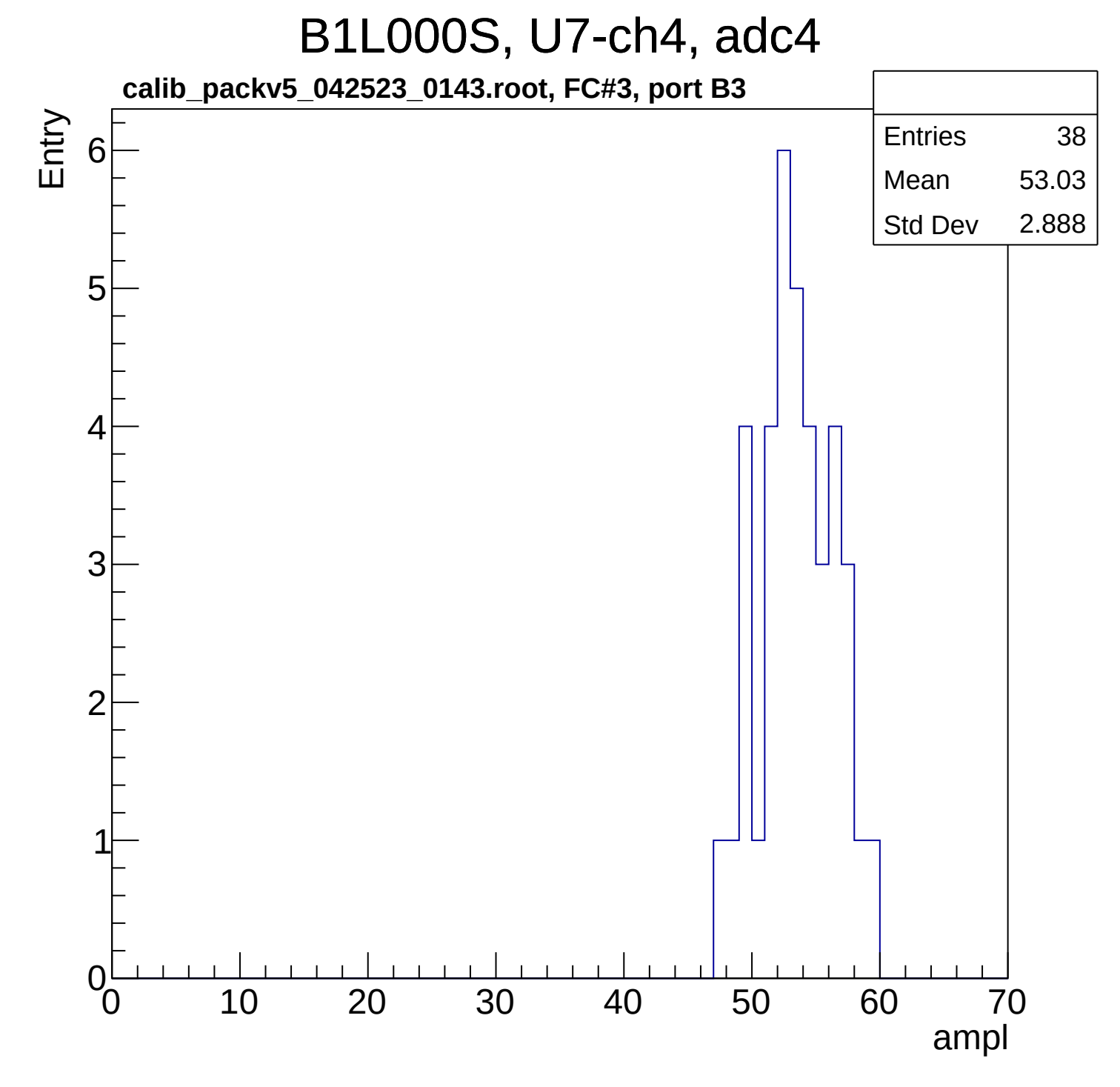
50
60
70
ampl


B1L000S, U7-ch4, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 38
Mean 53.03
Std Dev 2.888
Entry
0
1
2
3
4
5
6
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10
20
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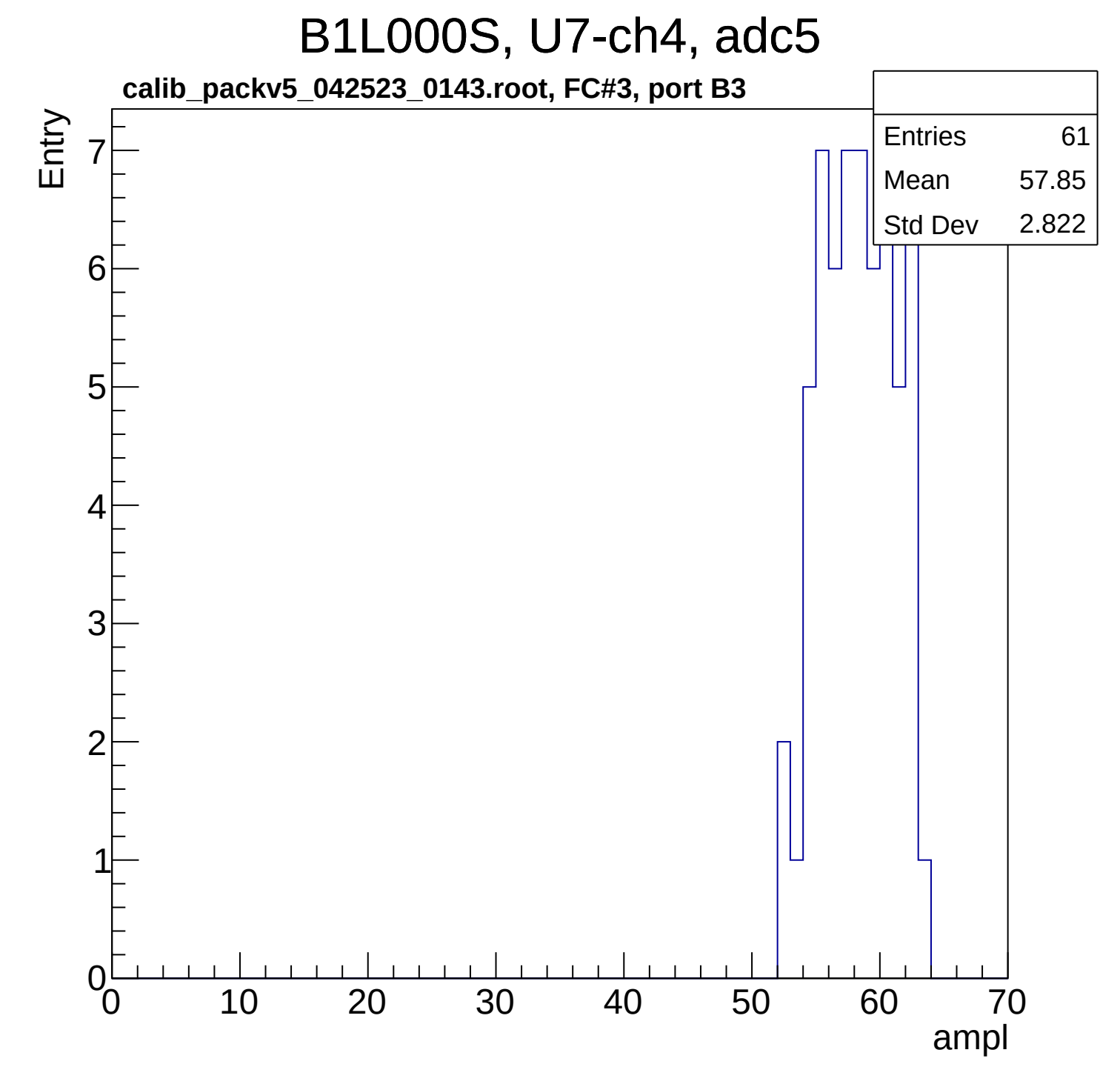
40
50
60
70
ampl


B1L000S, U7-ch4, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.85
Std Dev 2.822
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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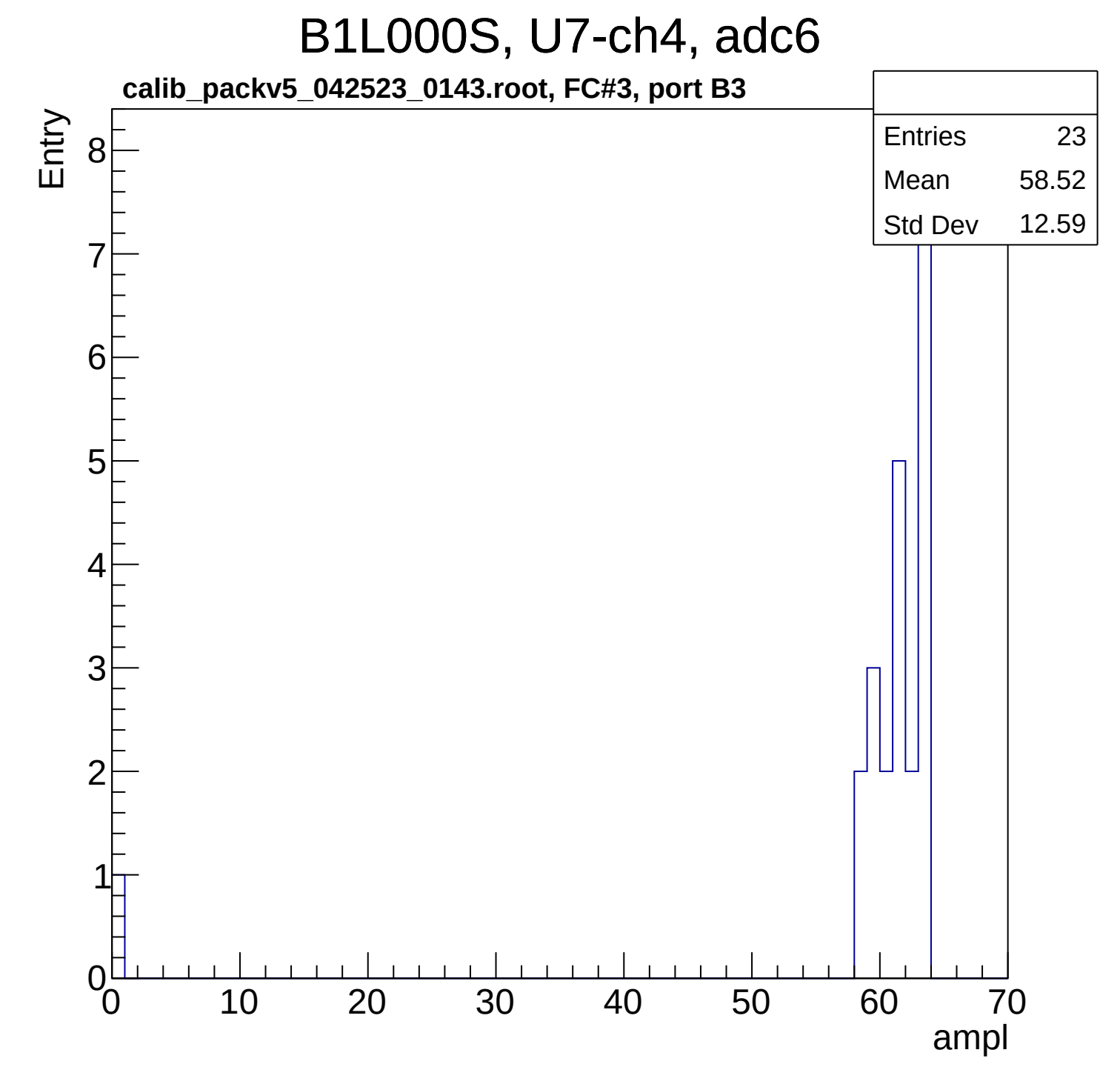
B1L000S, U7-ch4, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 58.52
Std Dev 12.59
Entry
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1
2
3
4
5
6
7
8
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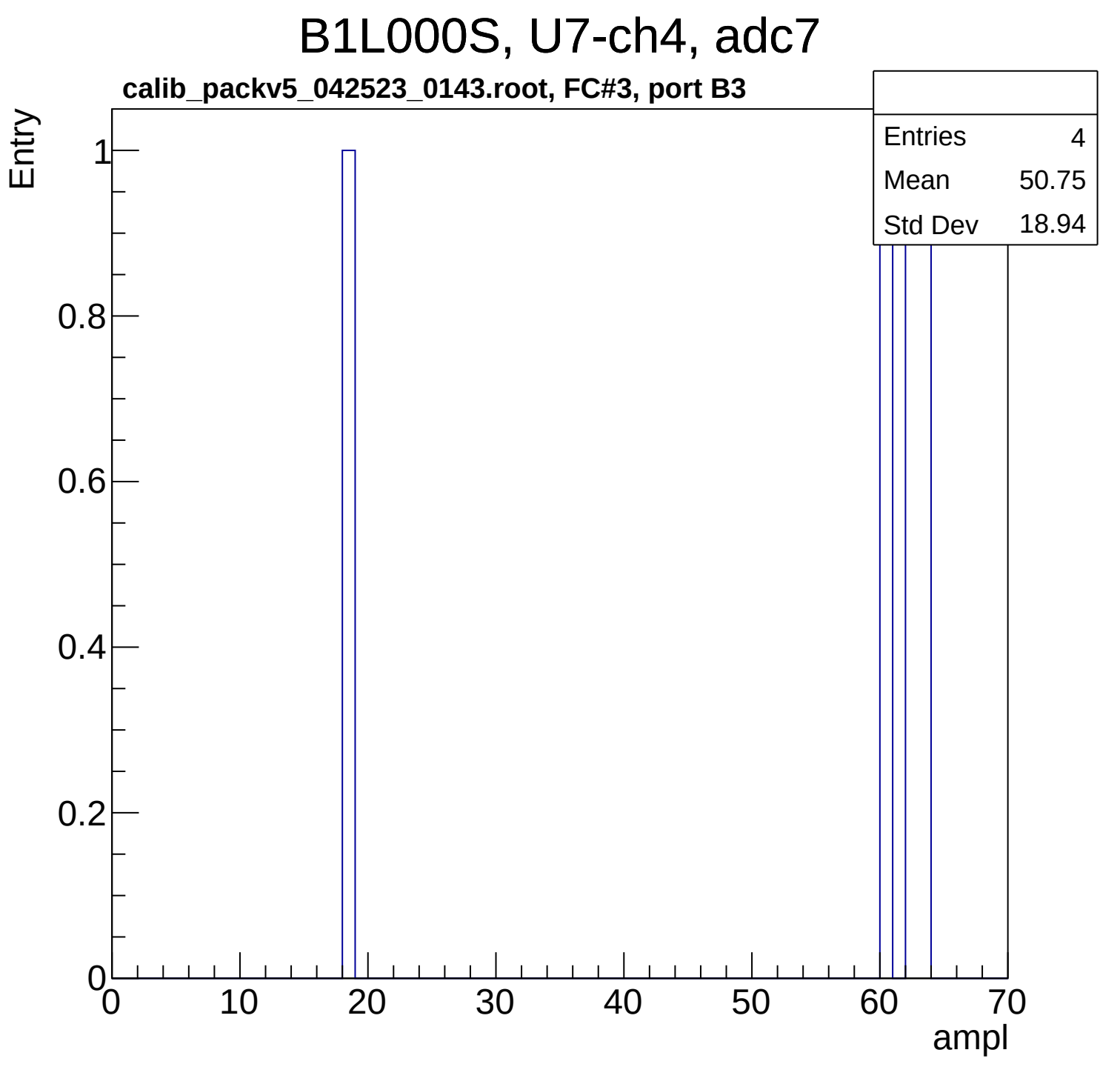
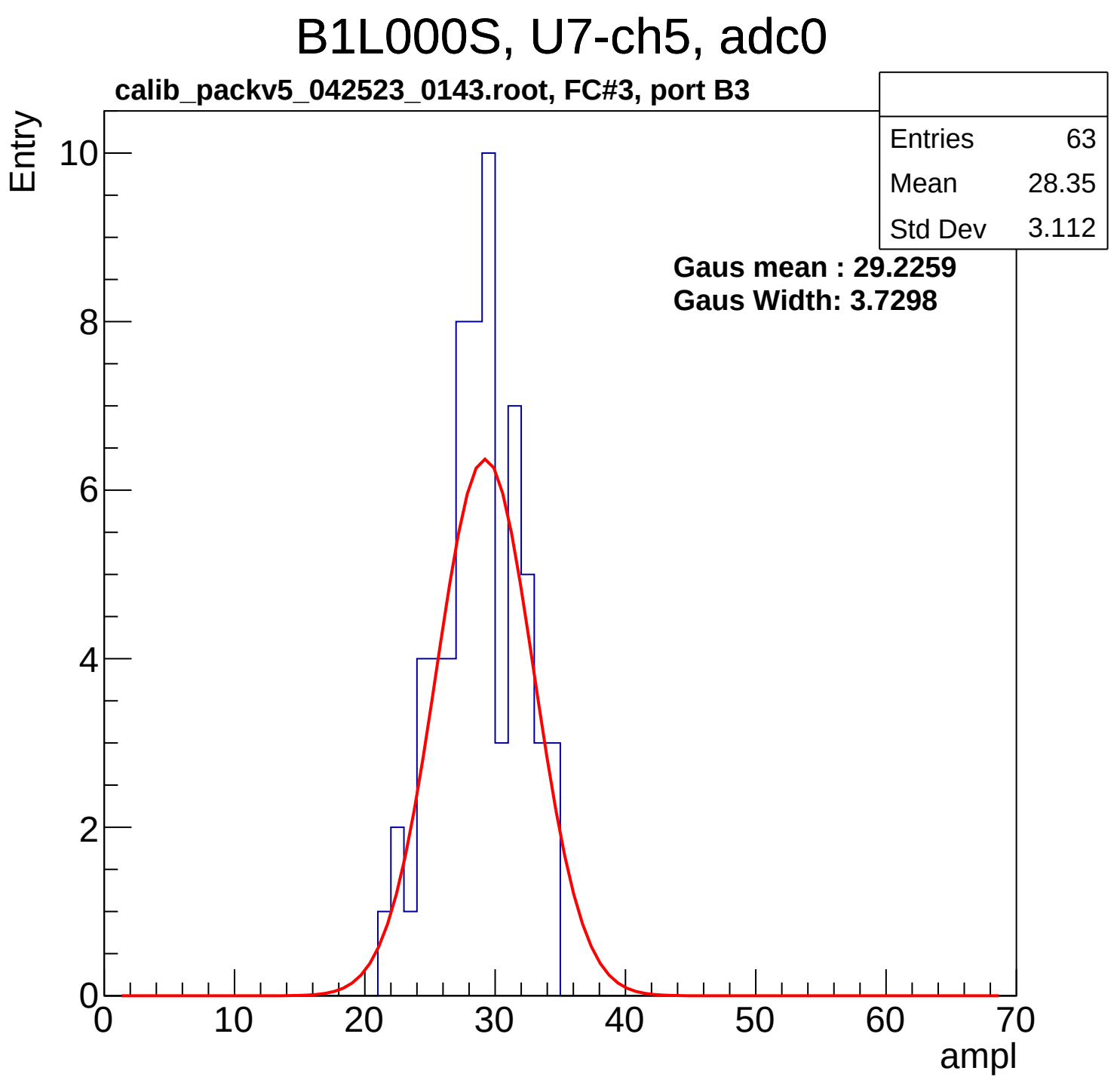
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch4, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 50.75
Std Dev 18.94
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U7-ch5, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 28.35
Std Dev 3.112
Gaus mean : 29.2259
Gaus Width: 3.7298
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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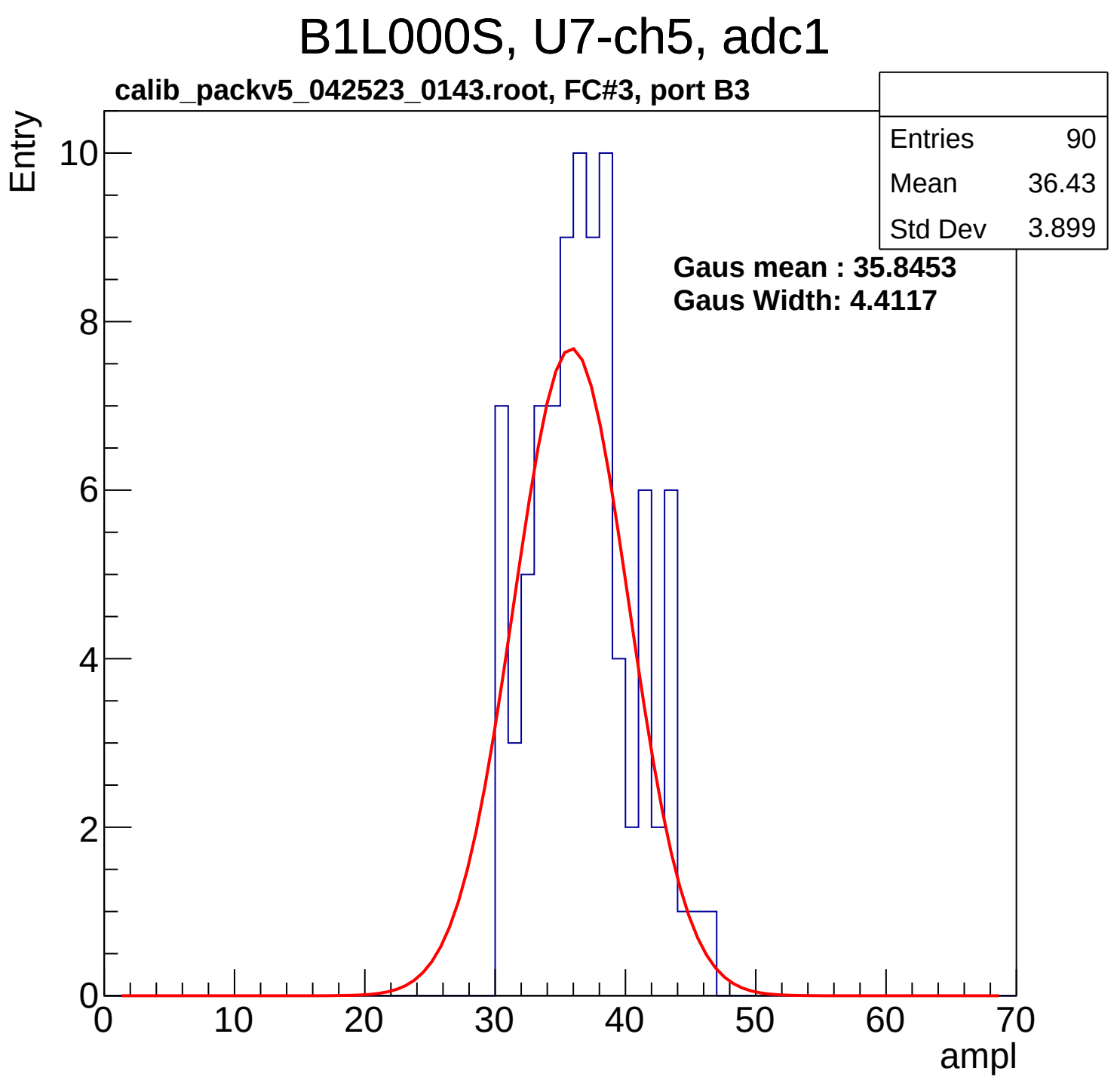
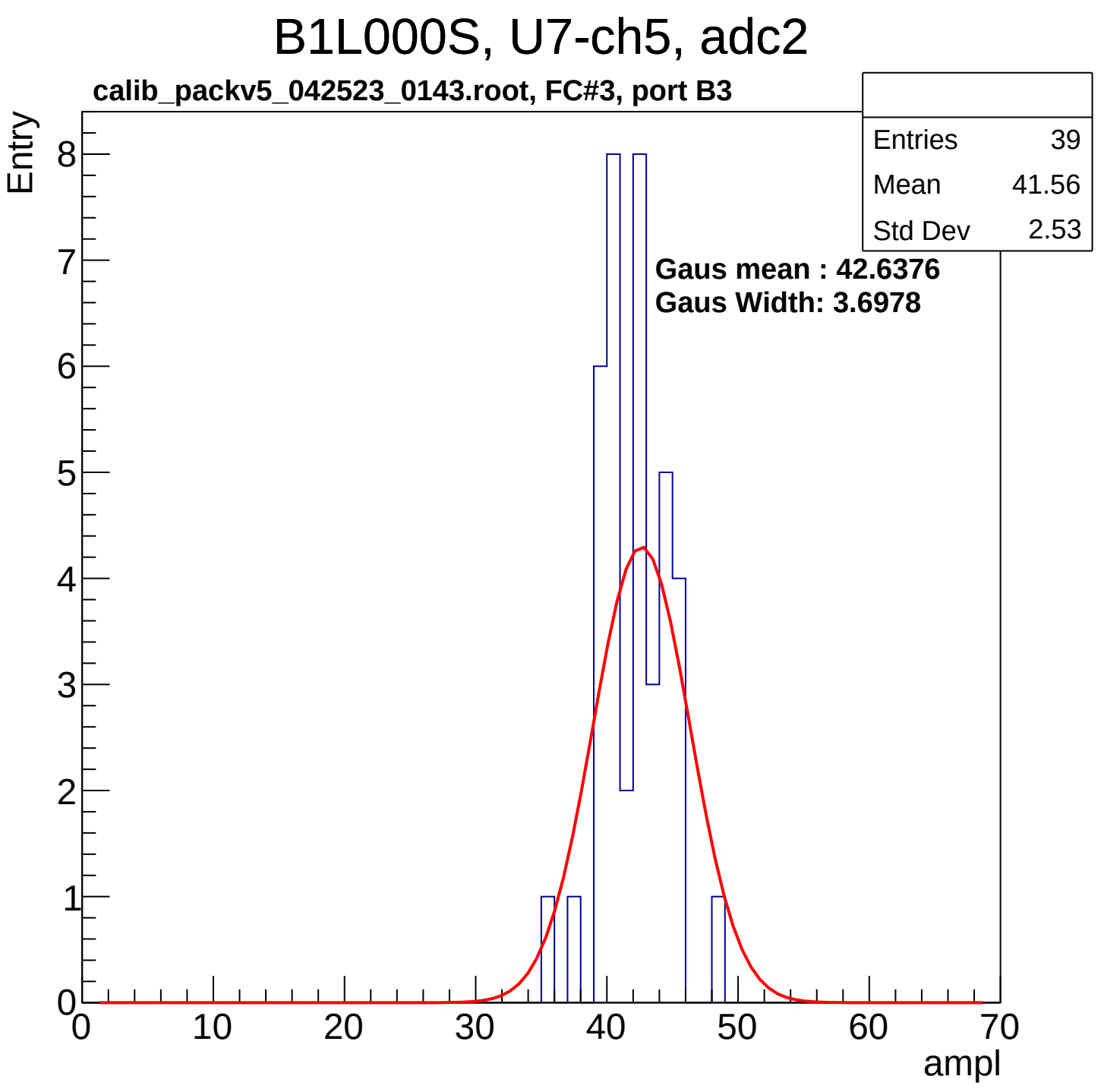

B1L000S, U7-ch5, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 90
Mean 36.43
Std Dev 3.899
Gaus mean : 35.8453
Gaus Width: 4.4117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch5, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 41.56
Std Dev 2.53
Gaus mean : 42.6376
Gaus Width: 3.6978
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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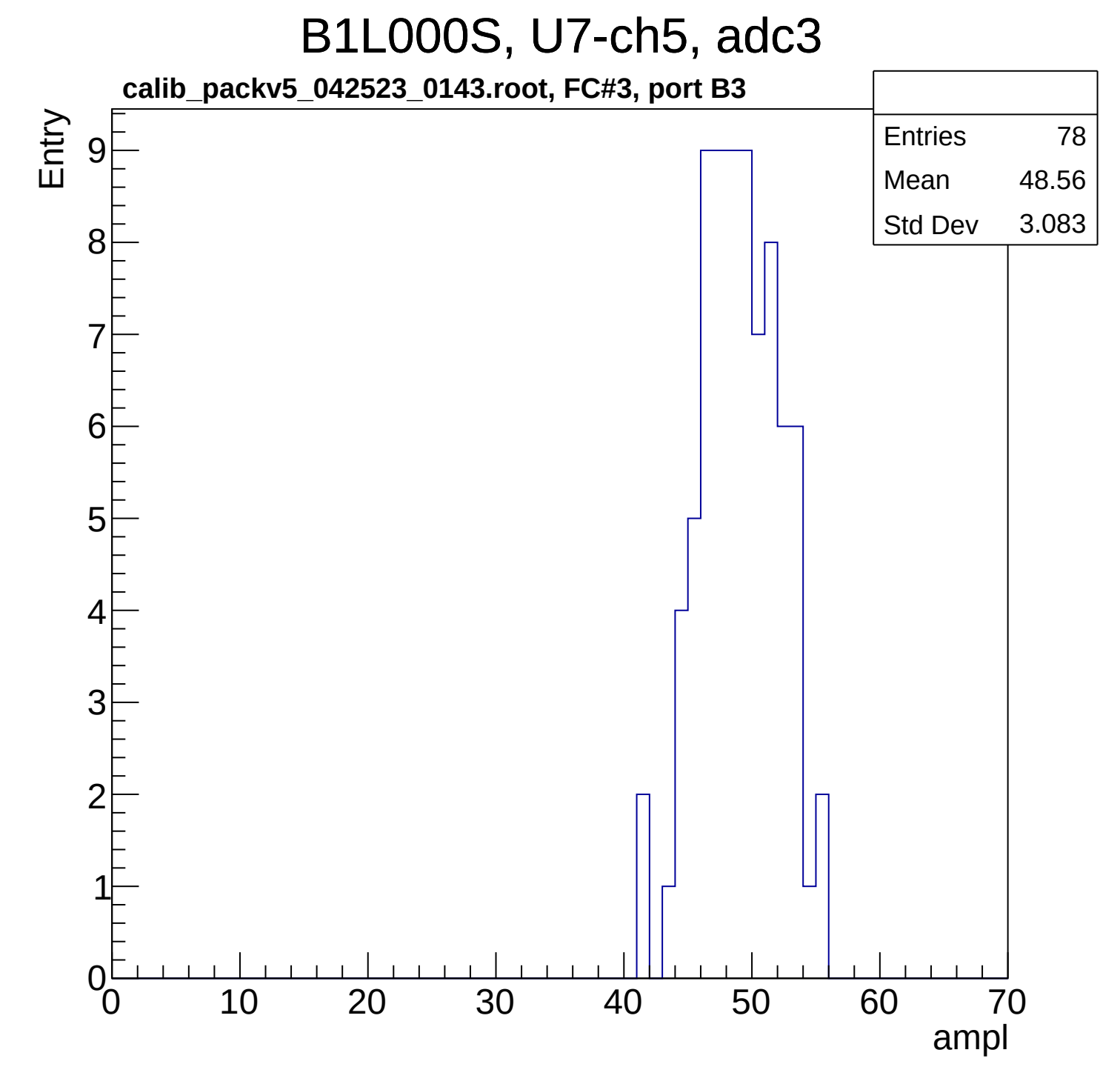
B1L000S, U7-ch5, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 48.56
Std Dev 3.083
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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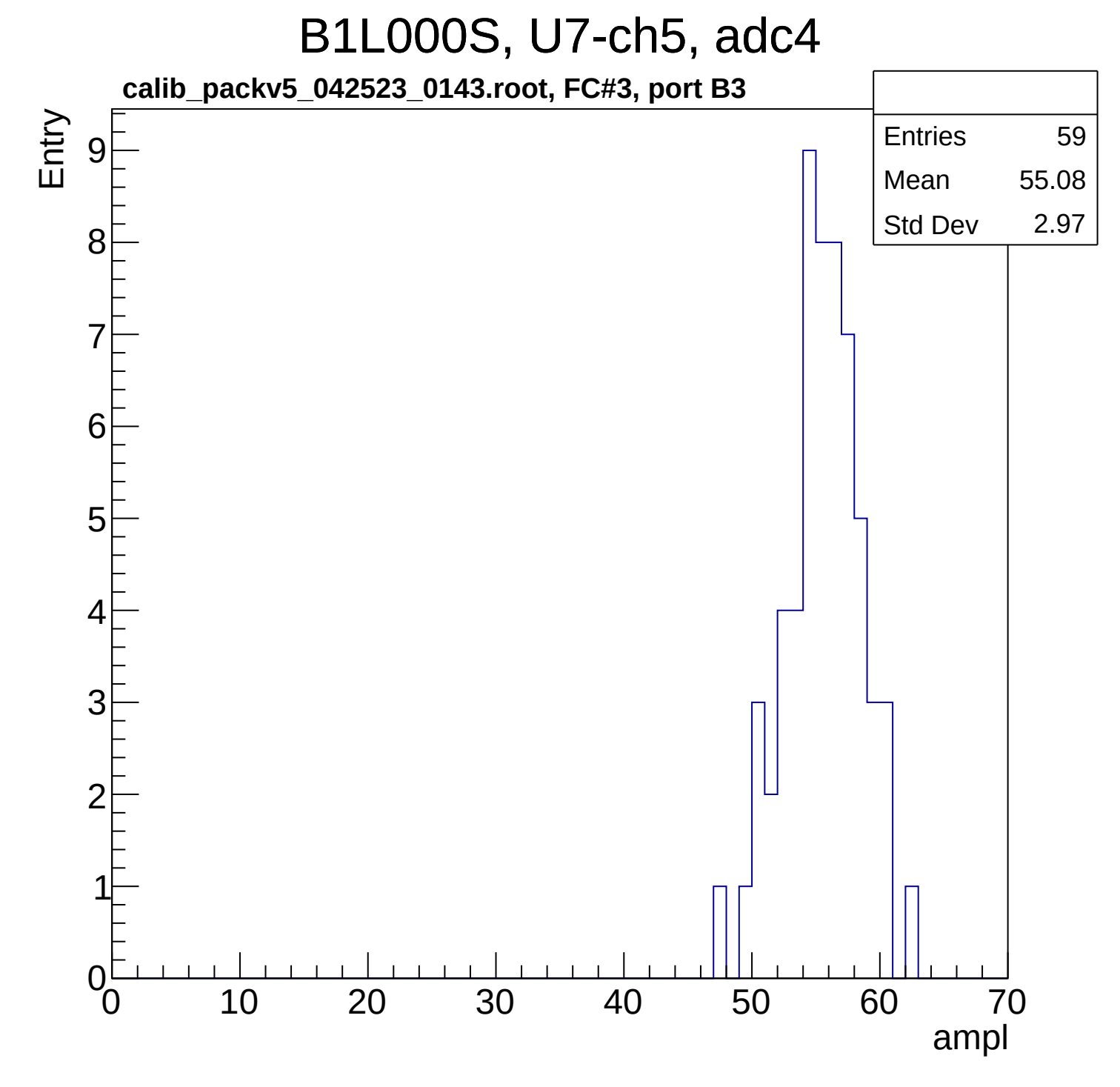
B1L000S, U7-ch5, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 55.08
Std Dev 2.97
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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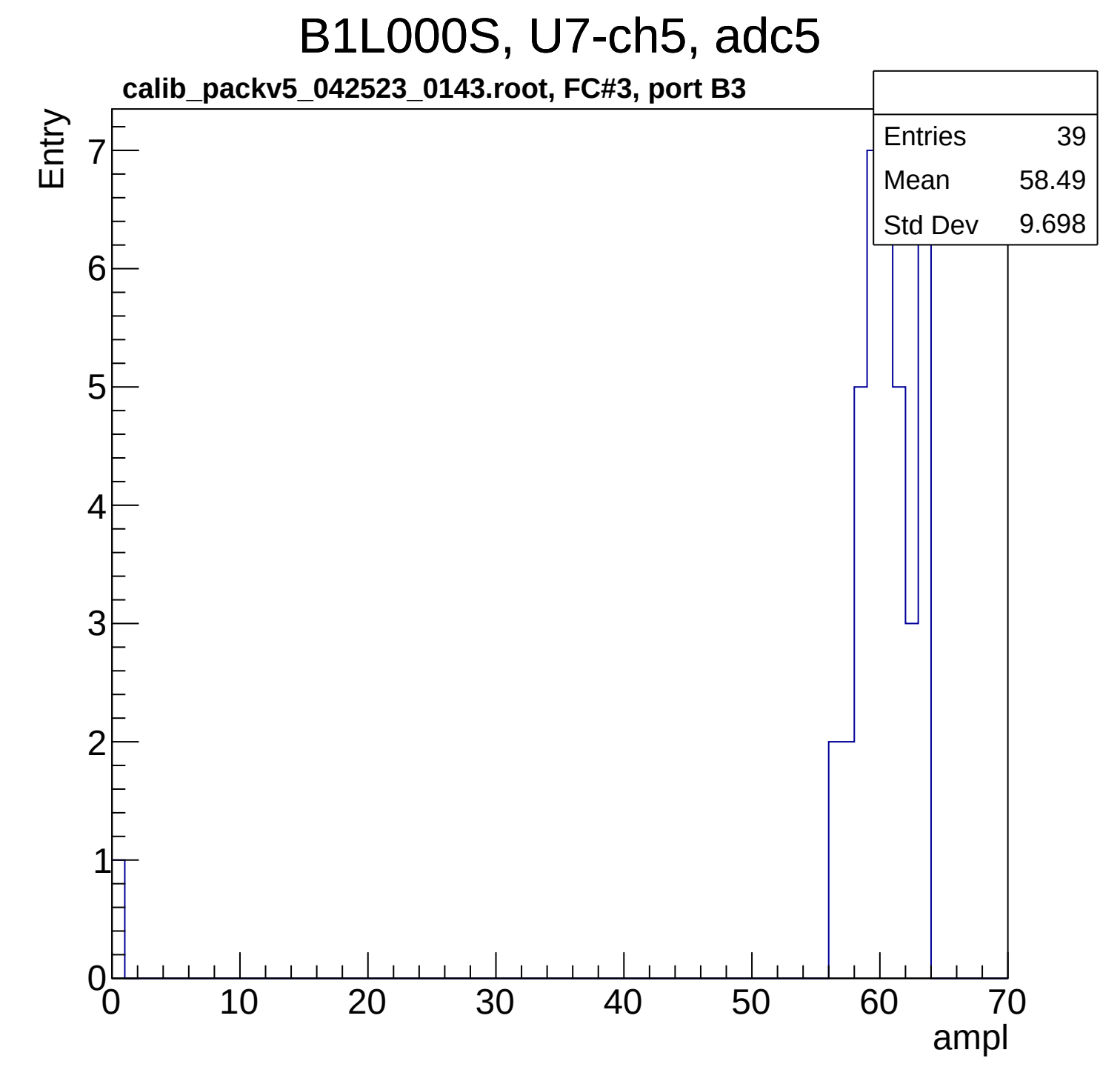
B1L000S, U7-ch5, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 58.49
Std Dev 9.698
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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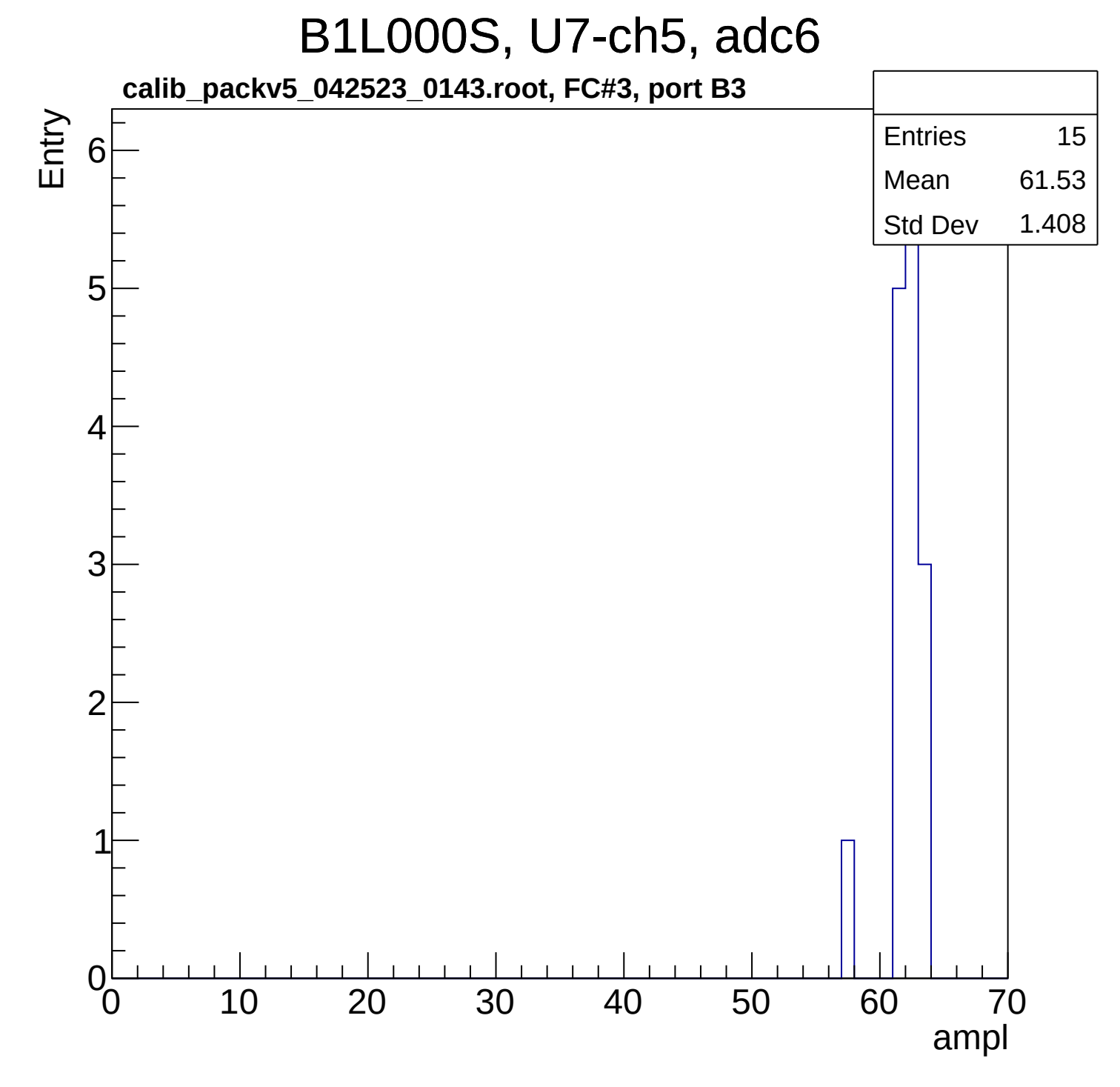
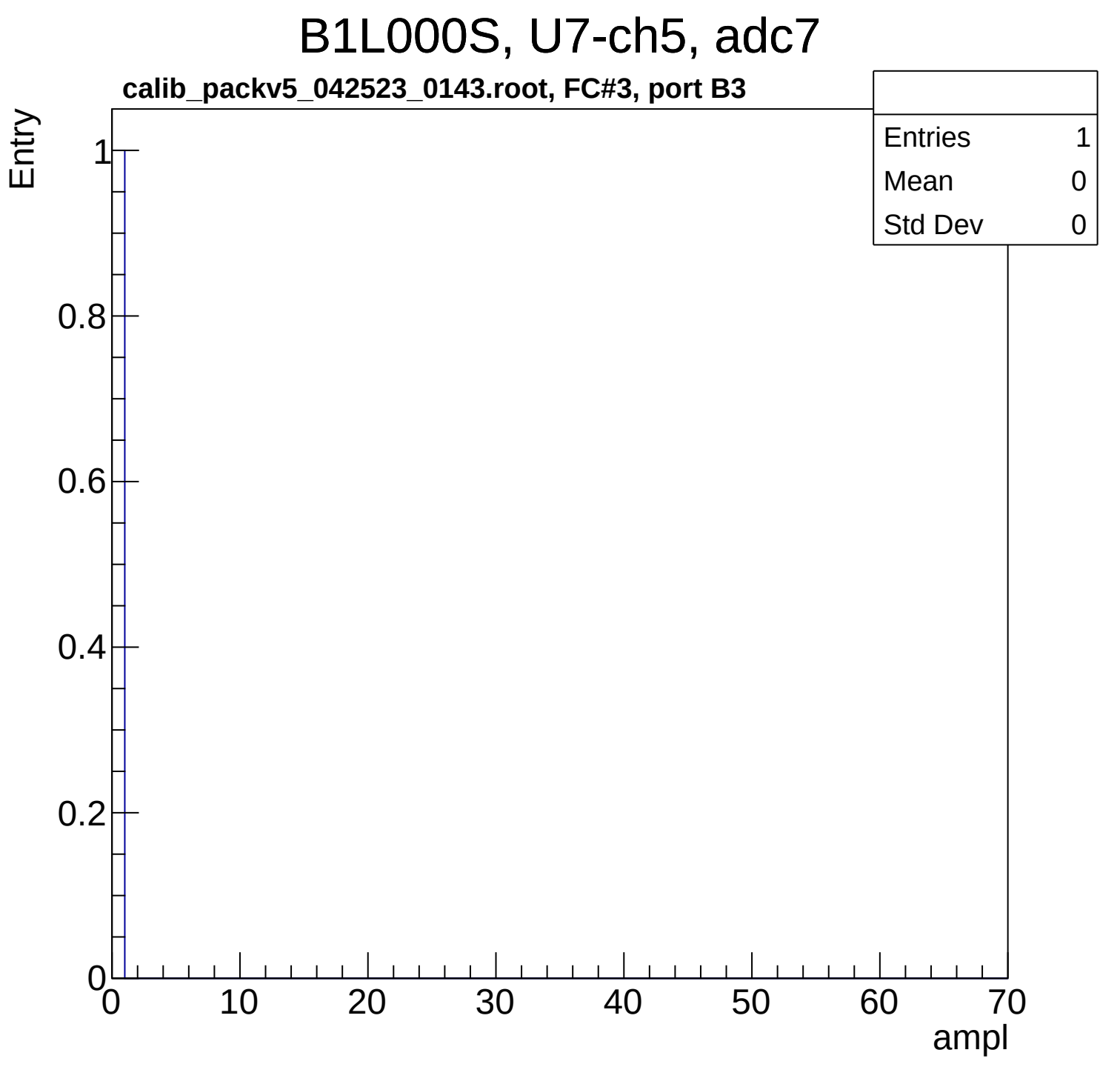
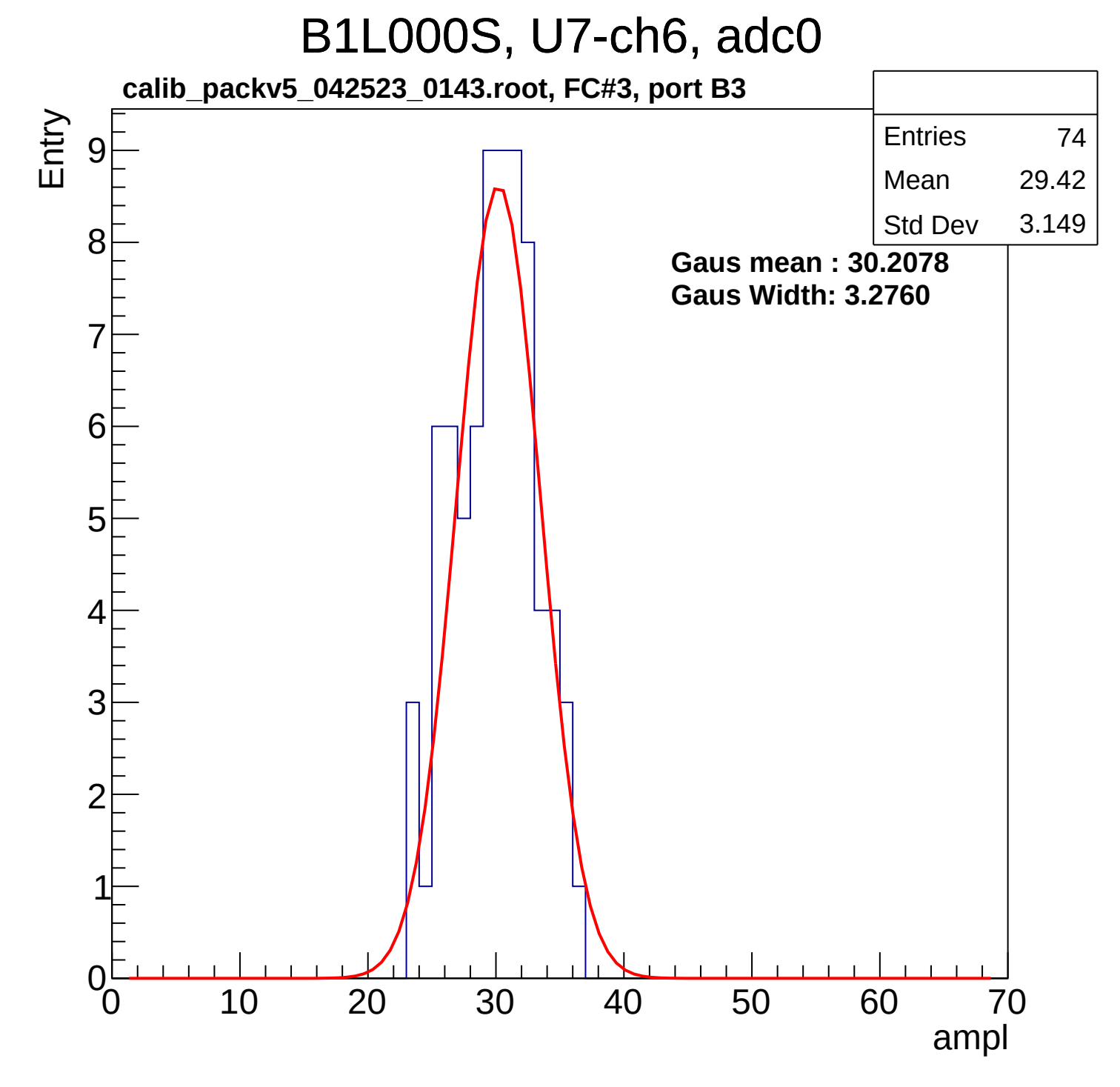

B1L000S, U7-ch5, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.53
Std Dev 1.408
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U7-ch5, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch6, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.42
Std Dev 3.149
Gaus mean : 30.2078
Gaus Width: 3.2760
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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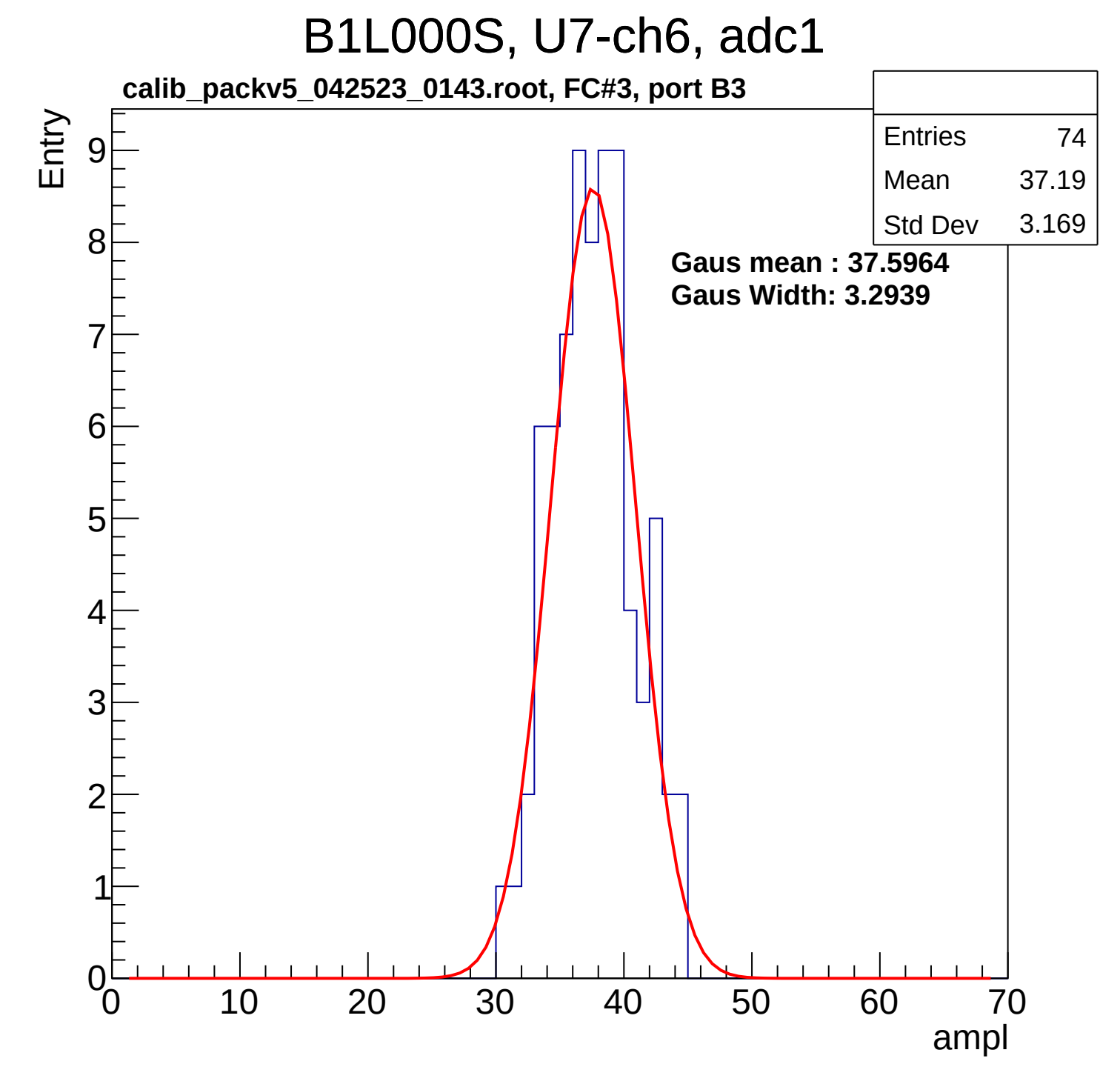
B1L000S, U7-ch6, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 37.19
Std Dev 3.169
Gaus mean : 37.5964
Gaus Width: 3.2939
Entry
ampl
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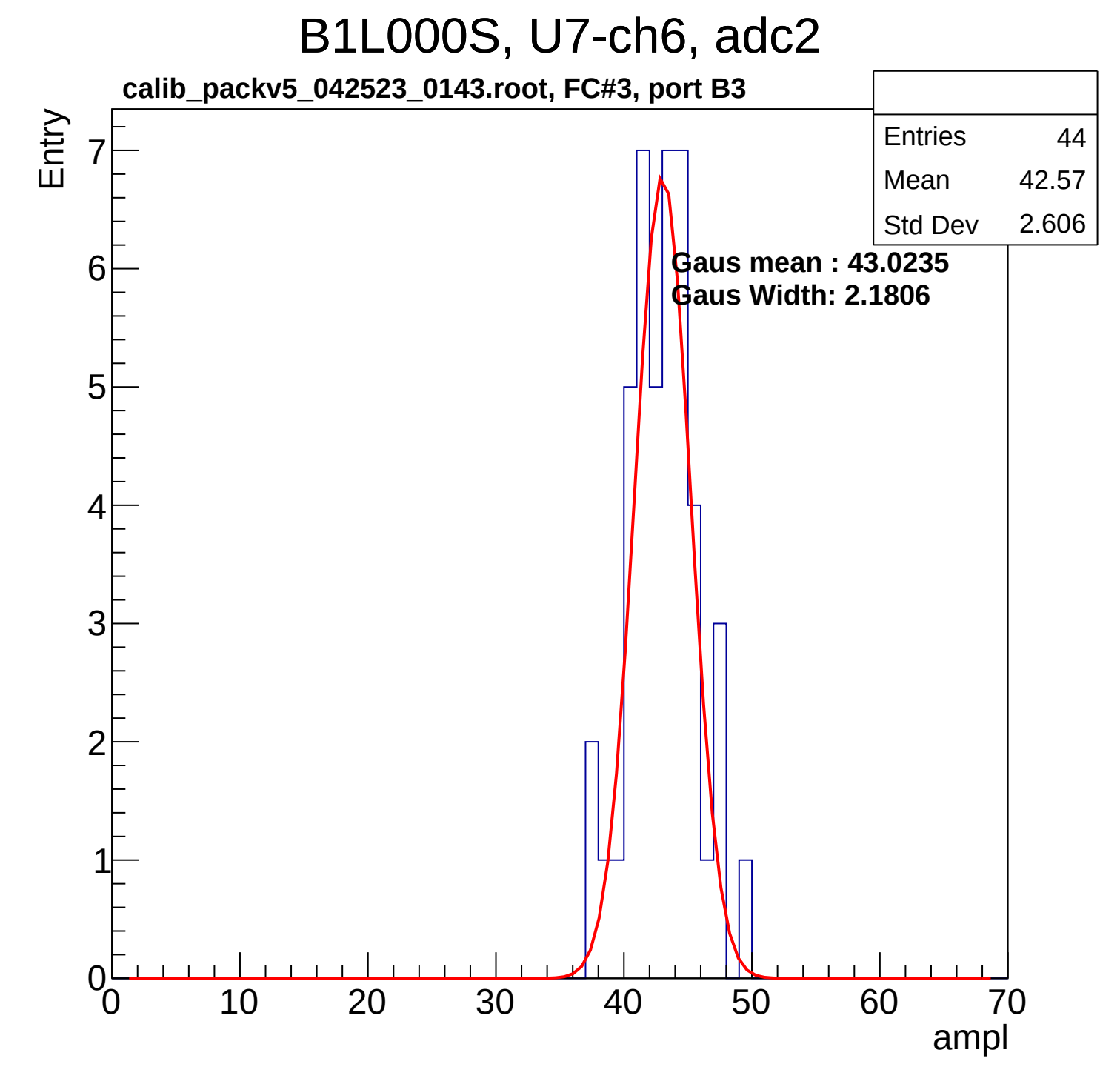
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch6, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 42.57
Std Dev 2.606
Gaus mean : 43.0235
Gaus Width: 2.1806
Entry
ampl
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1
2
3
4
5
6
7
0
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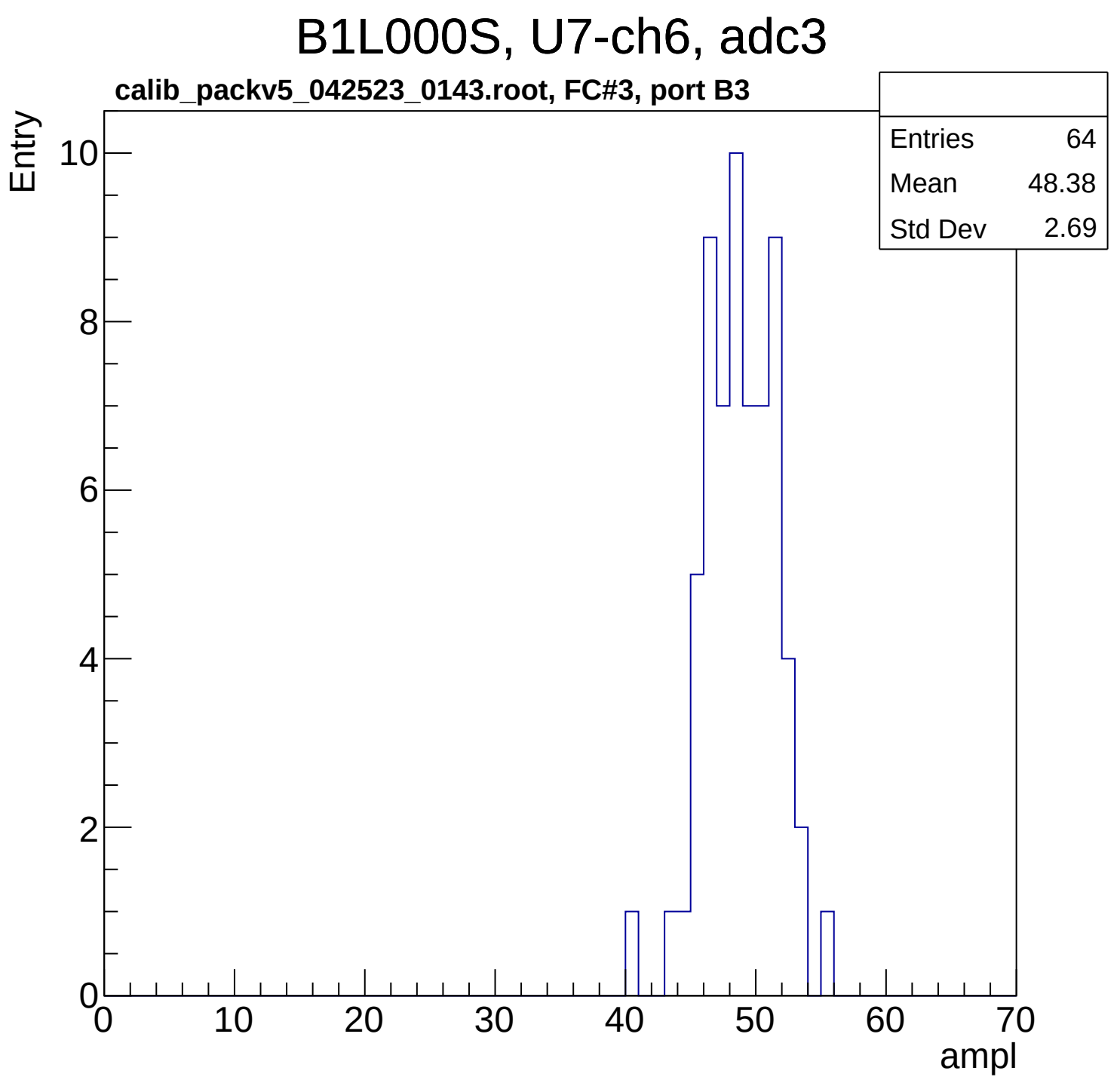
40
50
60
70


B1L000S, U7-ch6, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 48.38
Std Dev 2.69
Entry
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10
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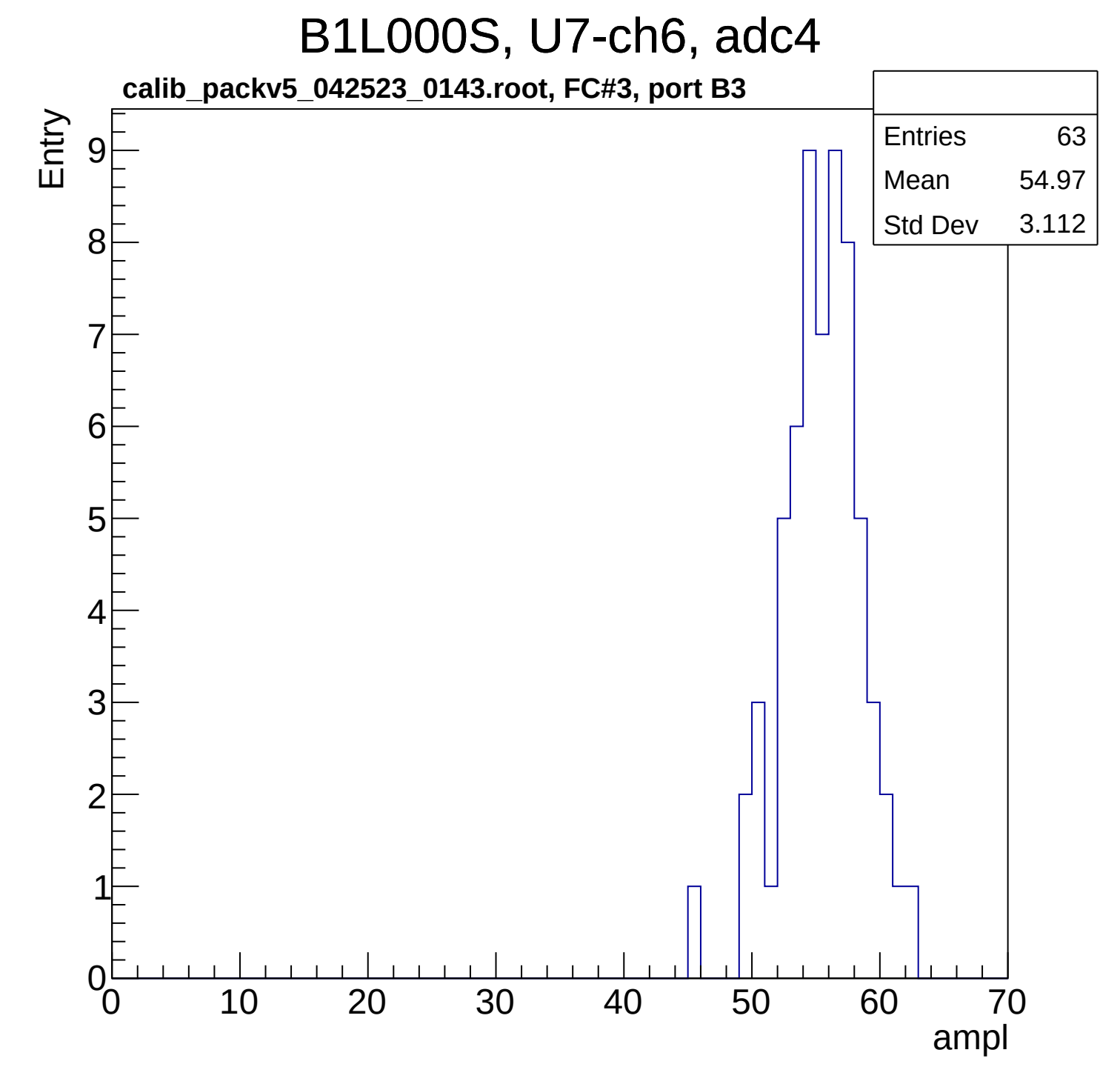
50
60
70
ampl


B1L000S, U7-ch6, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 54.97
Std Dev 3.112
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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60
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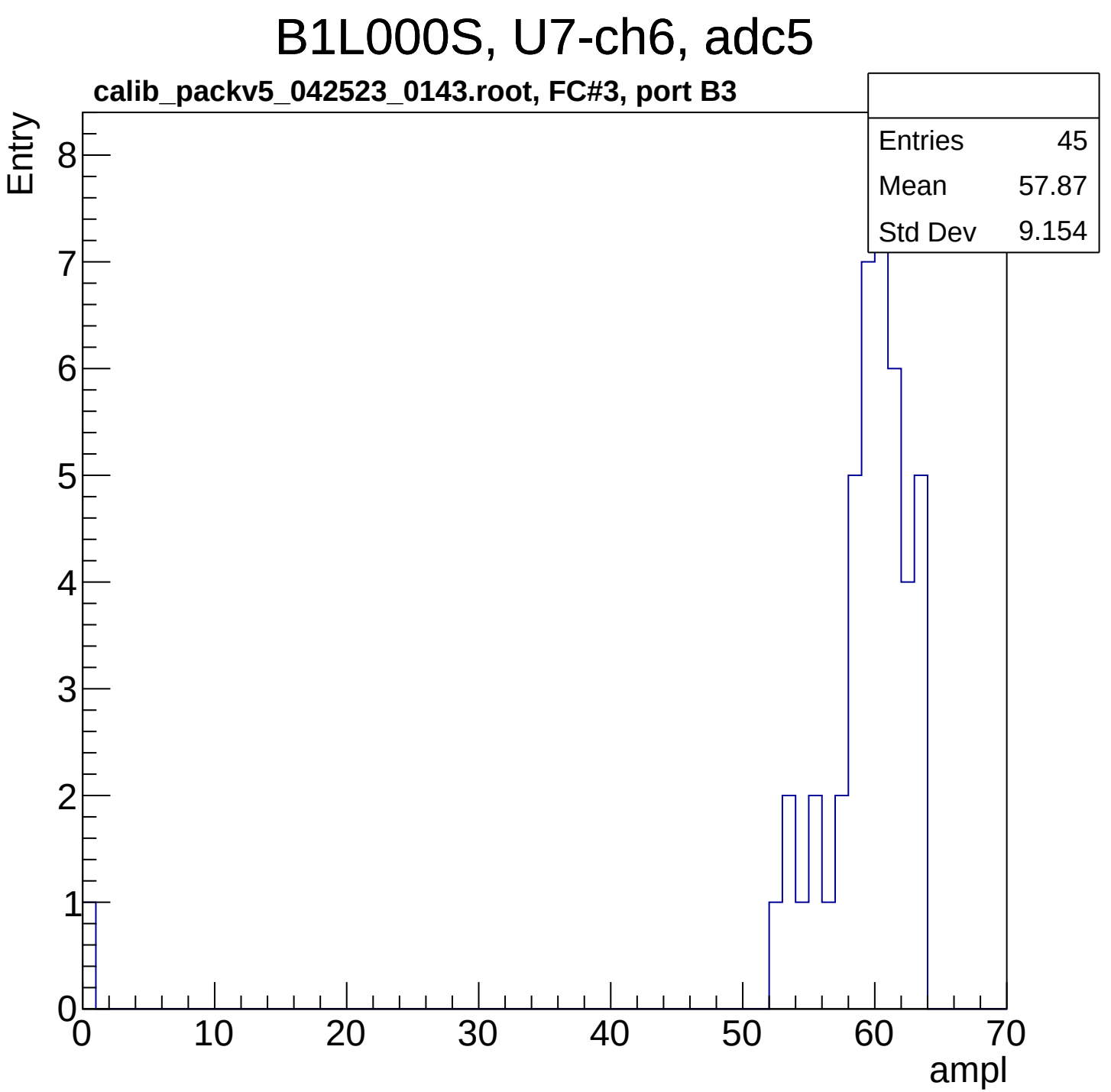
B1L000S, U7-ch6, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 57.87
Std Dev 9.154
Entry
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2
3
4
5
6
7
8
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10
20
30
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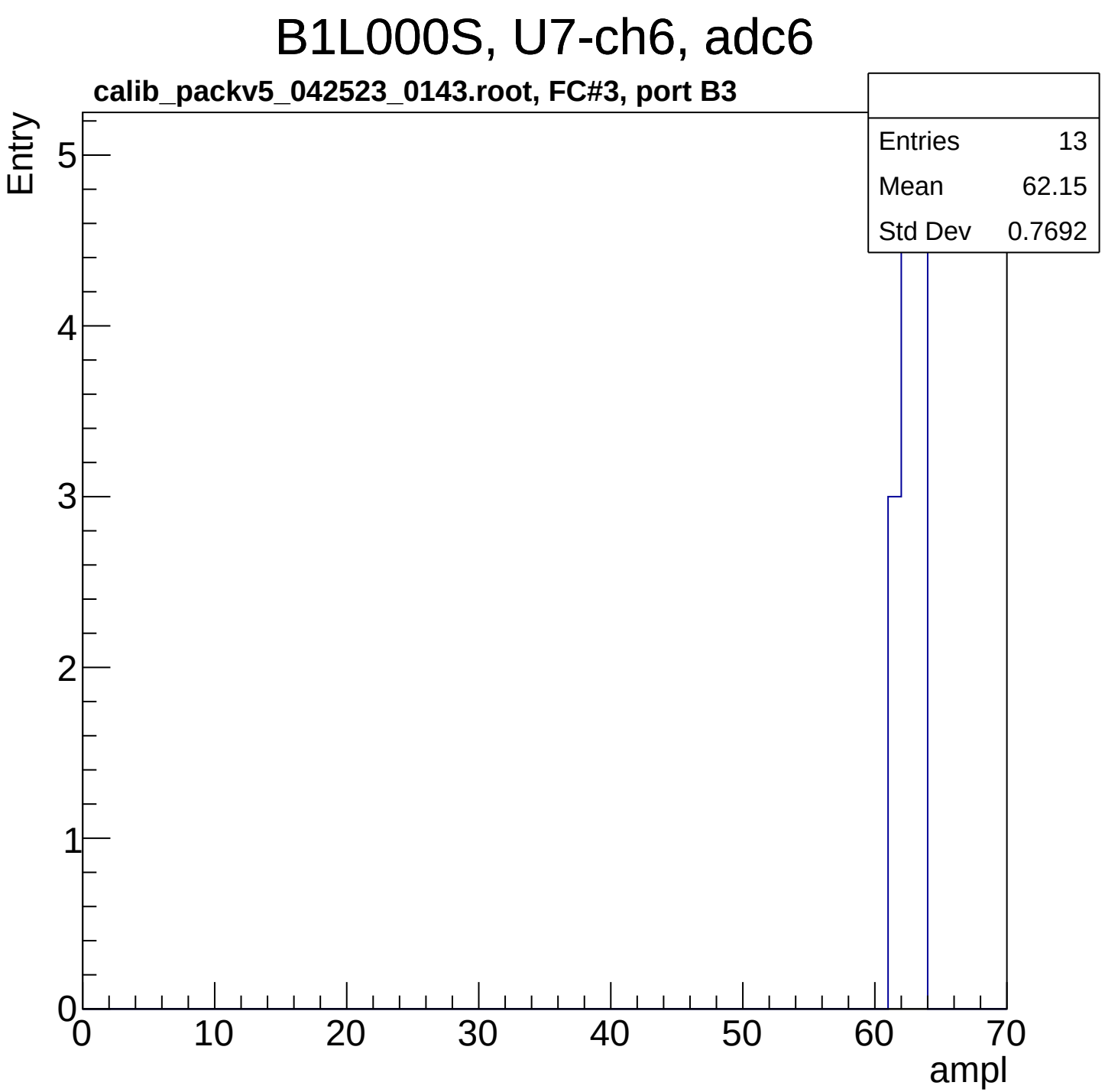
50
60
70
ampl


B1L000S, U7-ch6, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 62.15
Std Dev 0.7692
Entry
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3
4
5
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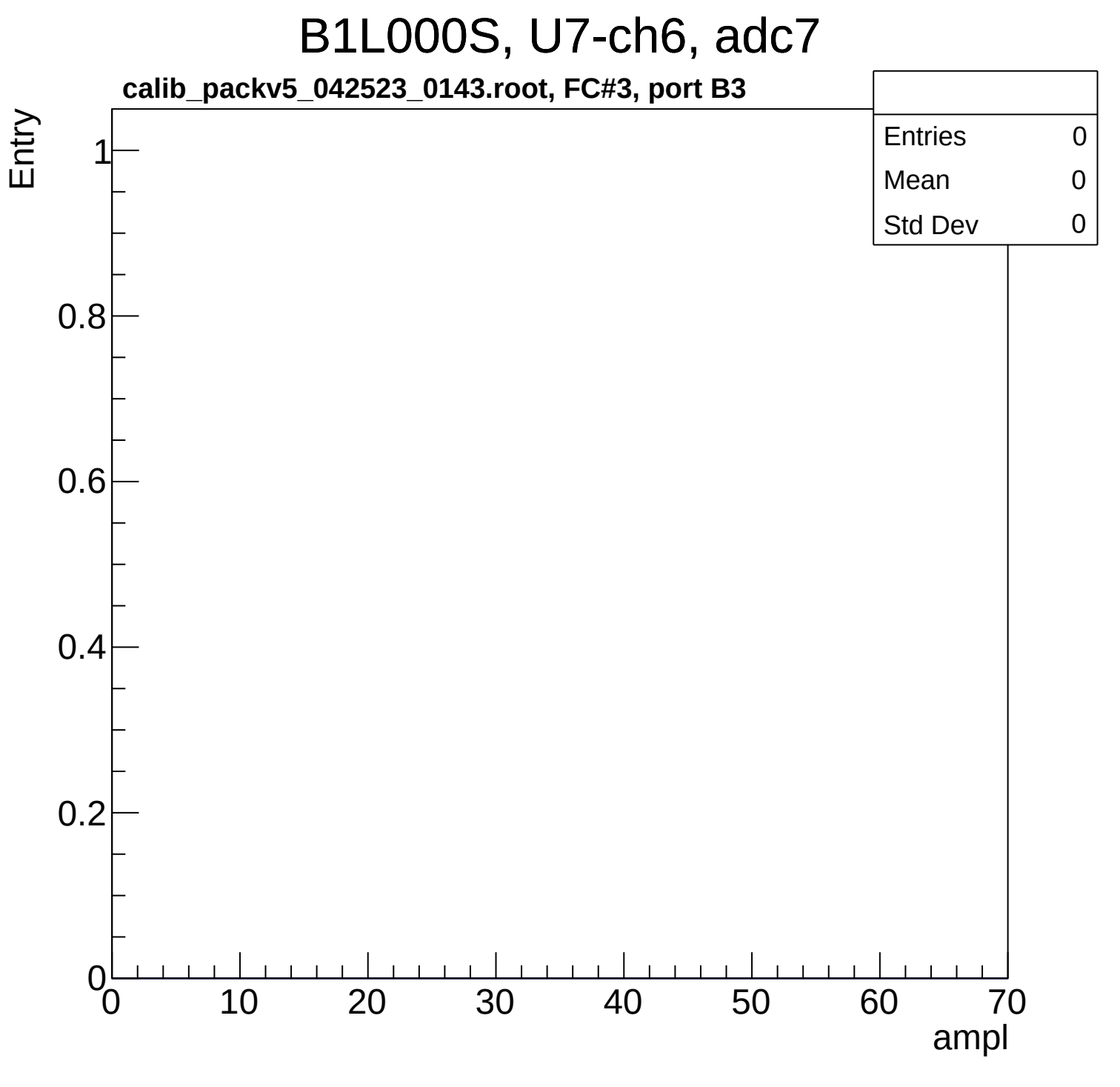
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch6, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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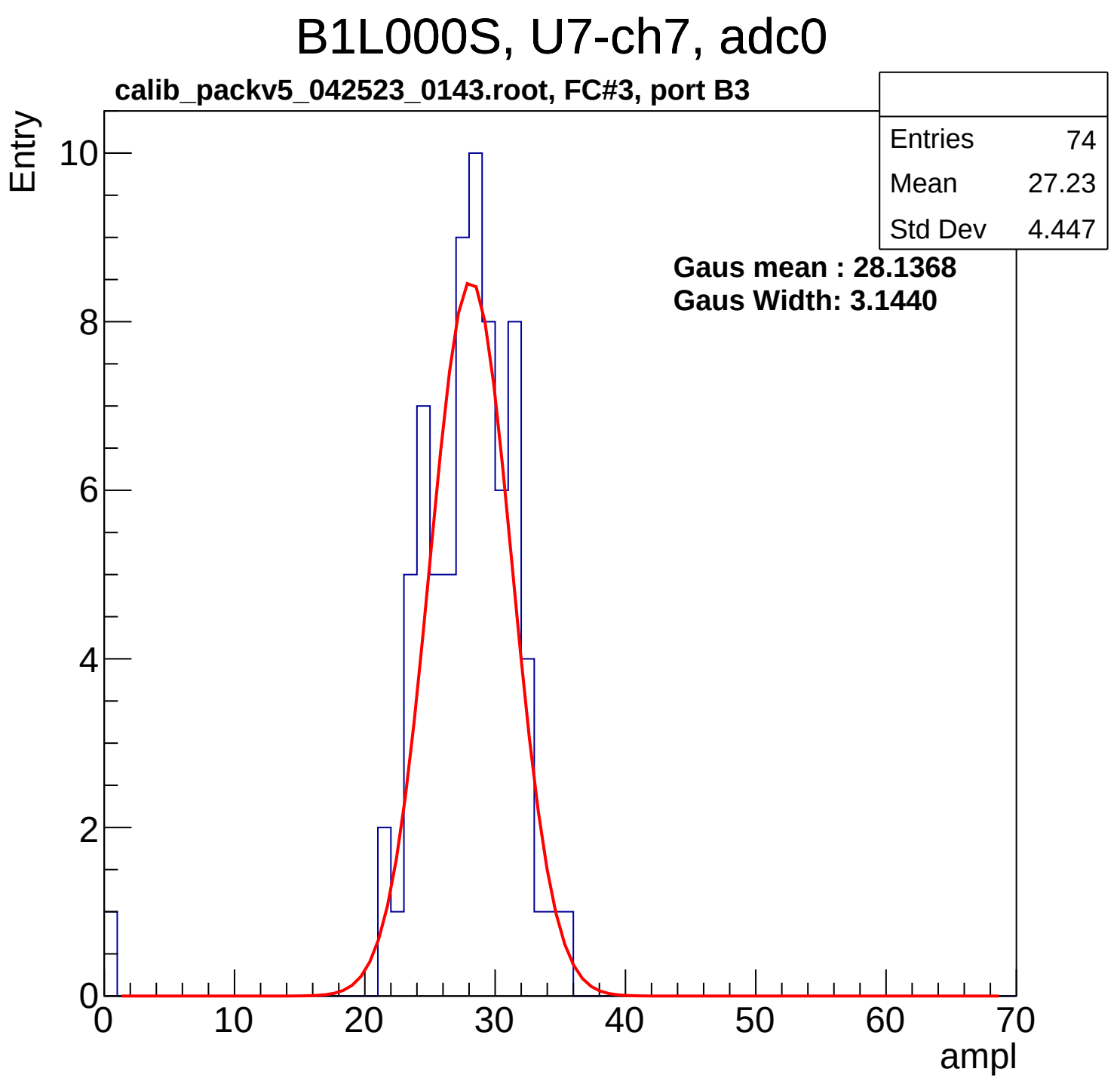
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch7, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 27.23
Std Dev 4.447
Gaus mean : 28.1368
Gaus Width: 3.1440
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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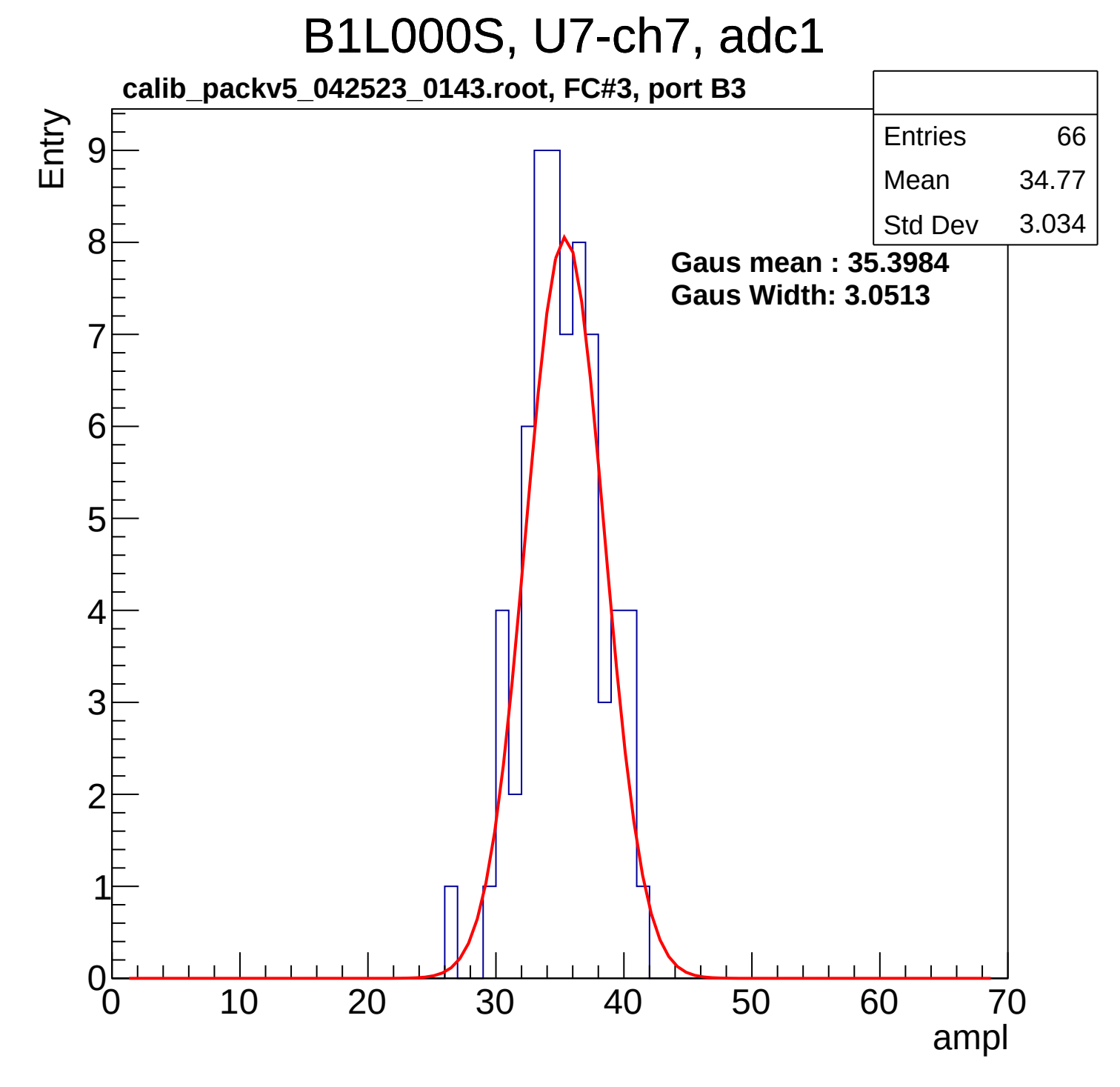
B1L000S, U7-ch7, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.77
Std Dev 3.034
Gaus mean : 35.3984
Gaus Width: 3.0513
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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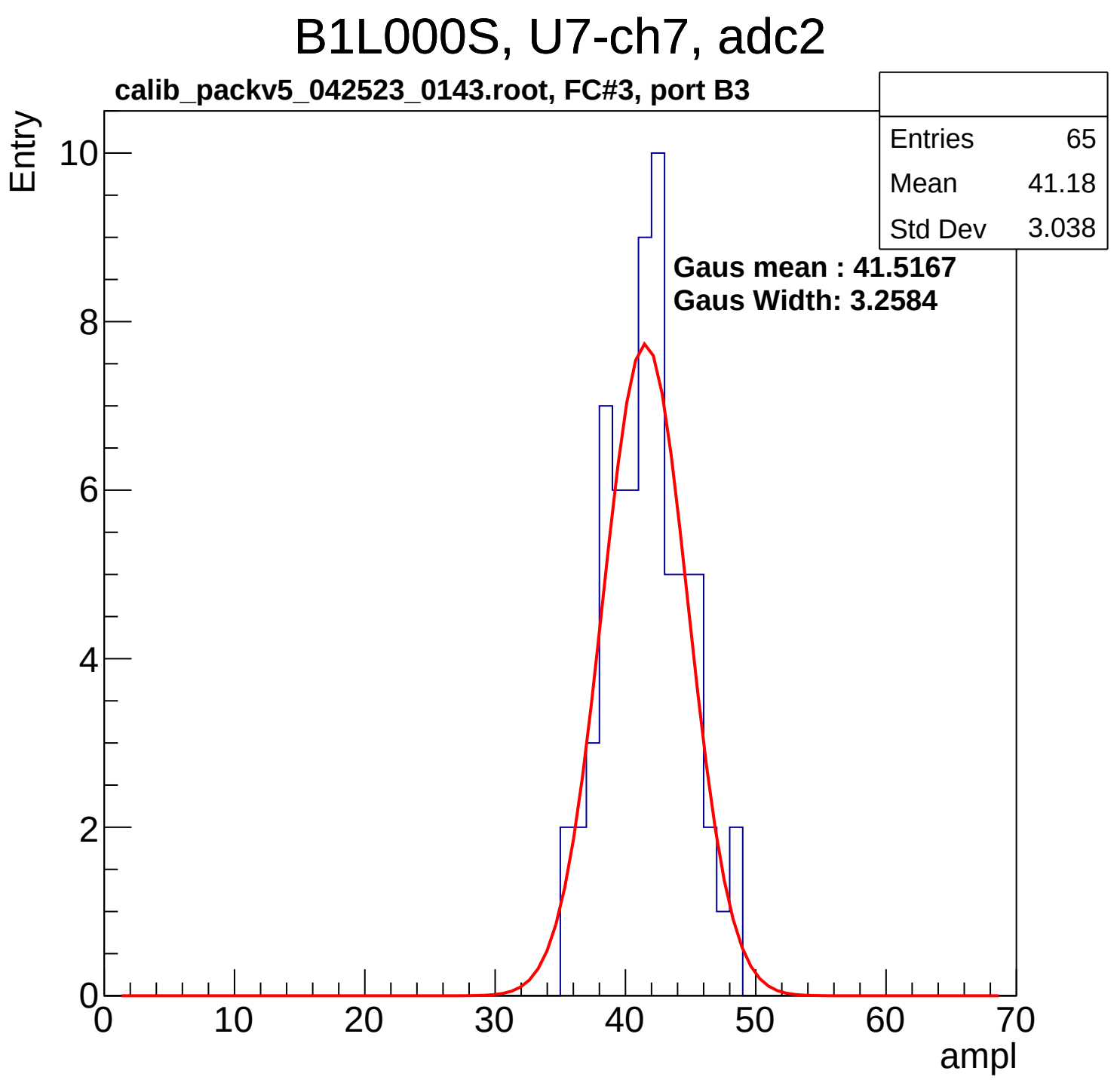
B1L000S, U7-ch7, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.18
Std Dev 3.038
Gaus mean : 41.5167
Gaus Width: 3.2584
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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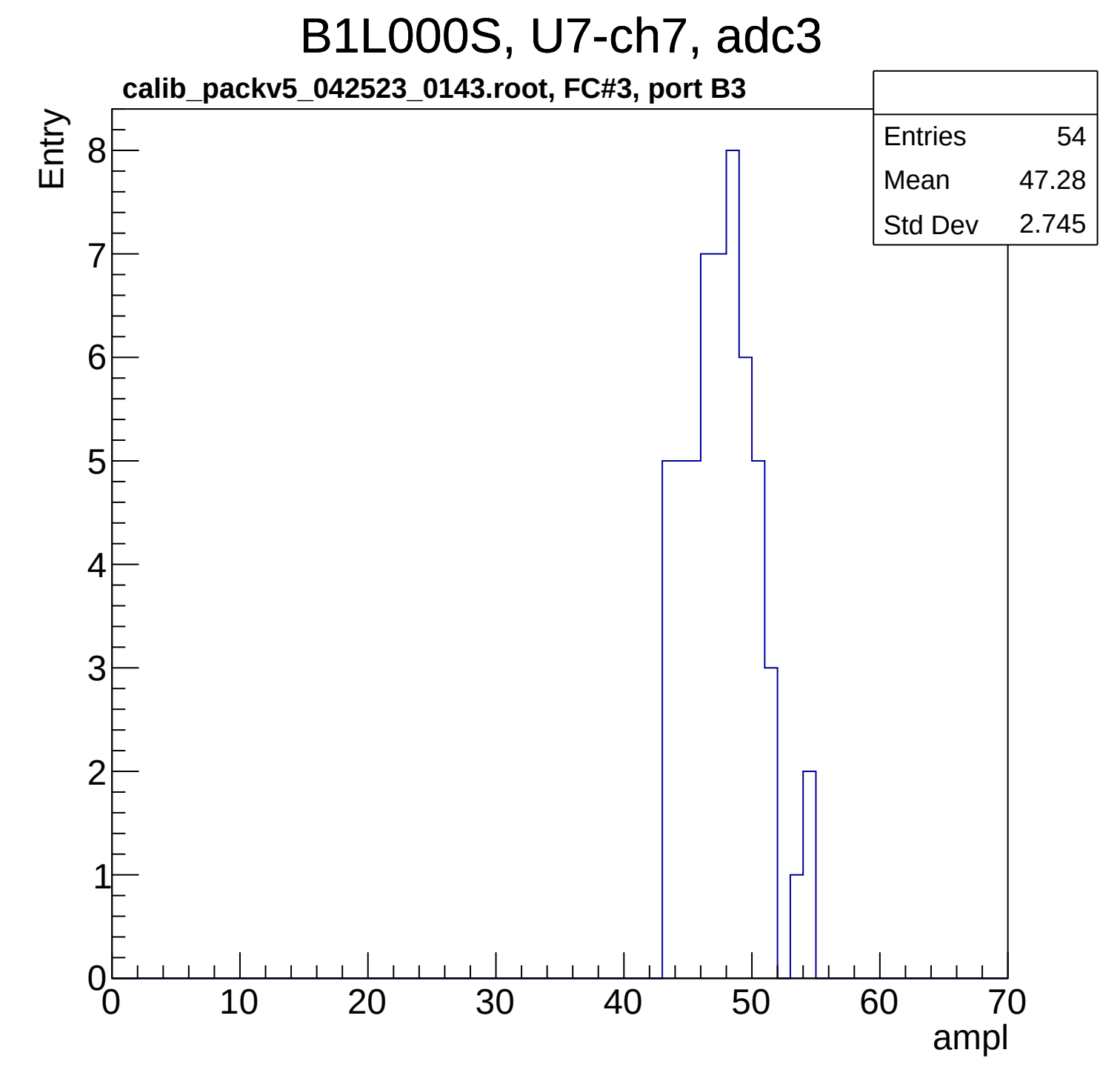
B1L000S, U7-ch7, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.28
Std Dev 2.745
Entry
ampl
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3
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5
6
7
8
0
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20
30
40
50
60
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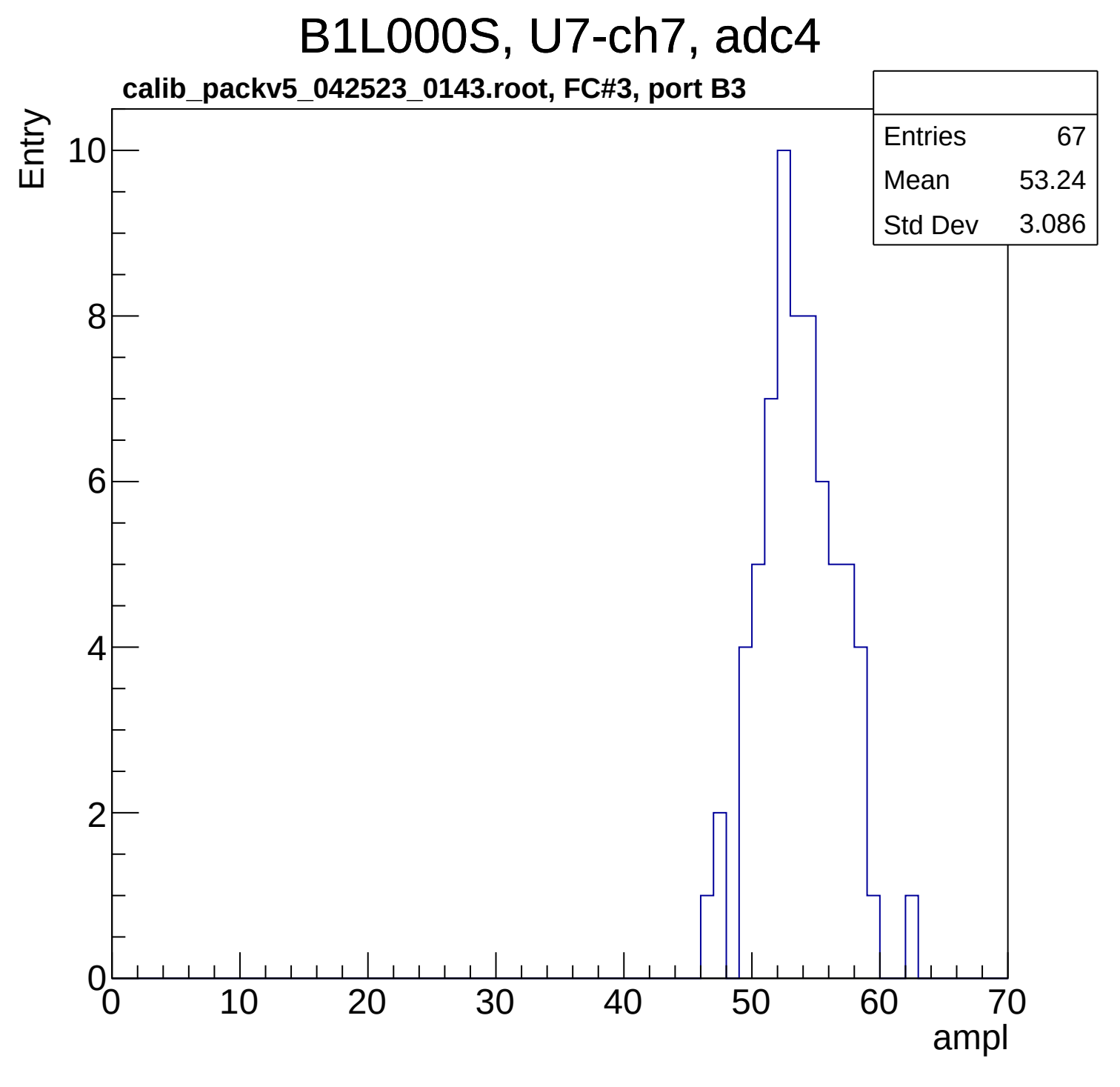
B1L000S, U7-ch7, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 53.24
Std Dev 3.086
Entry
ampl
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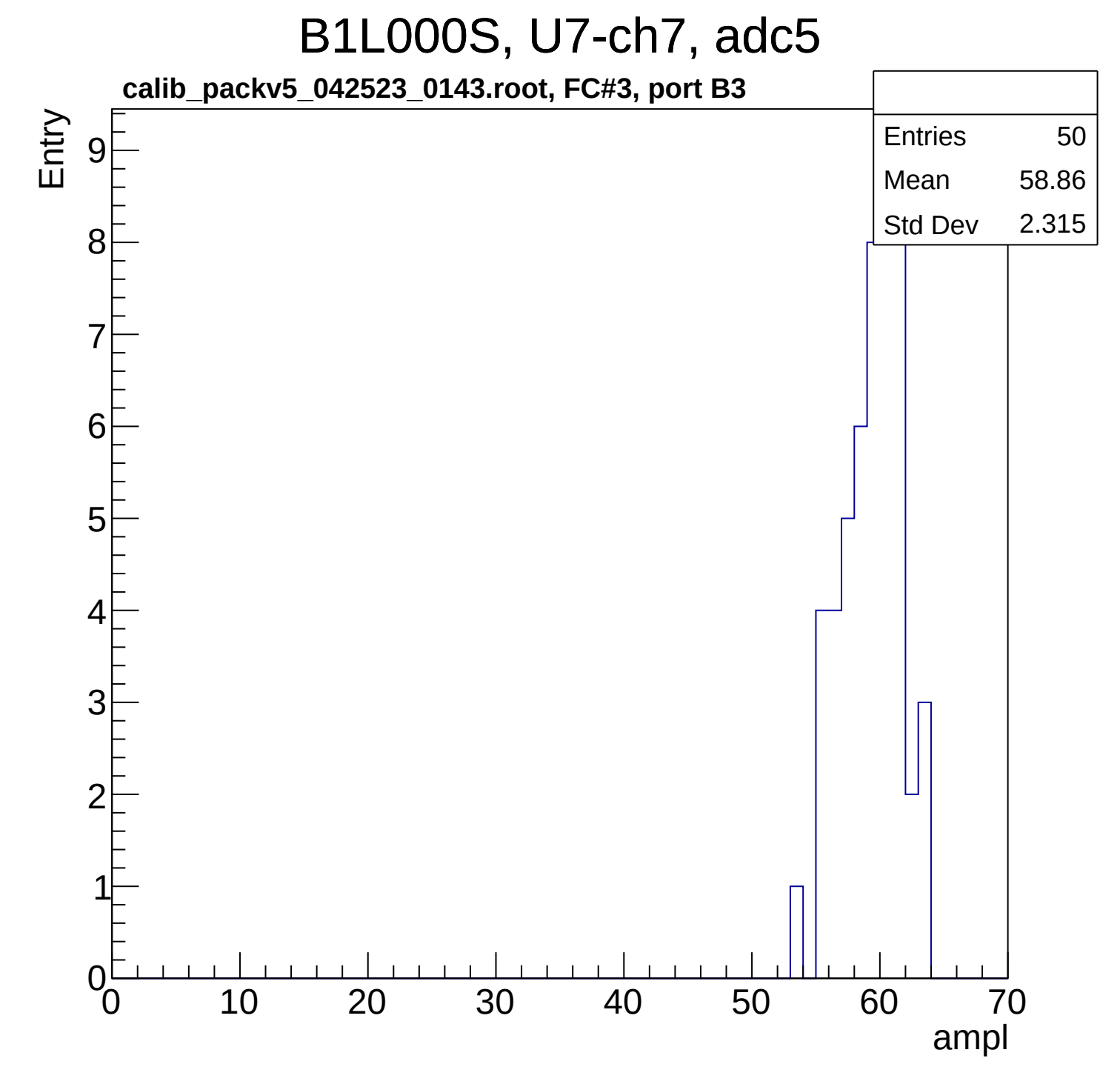
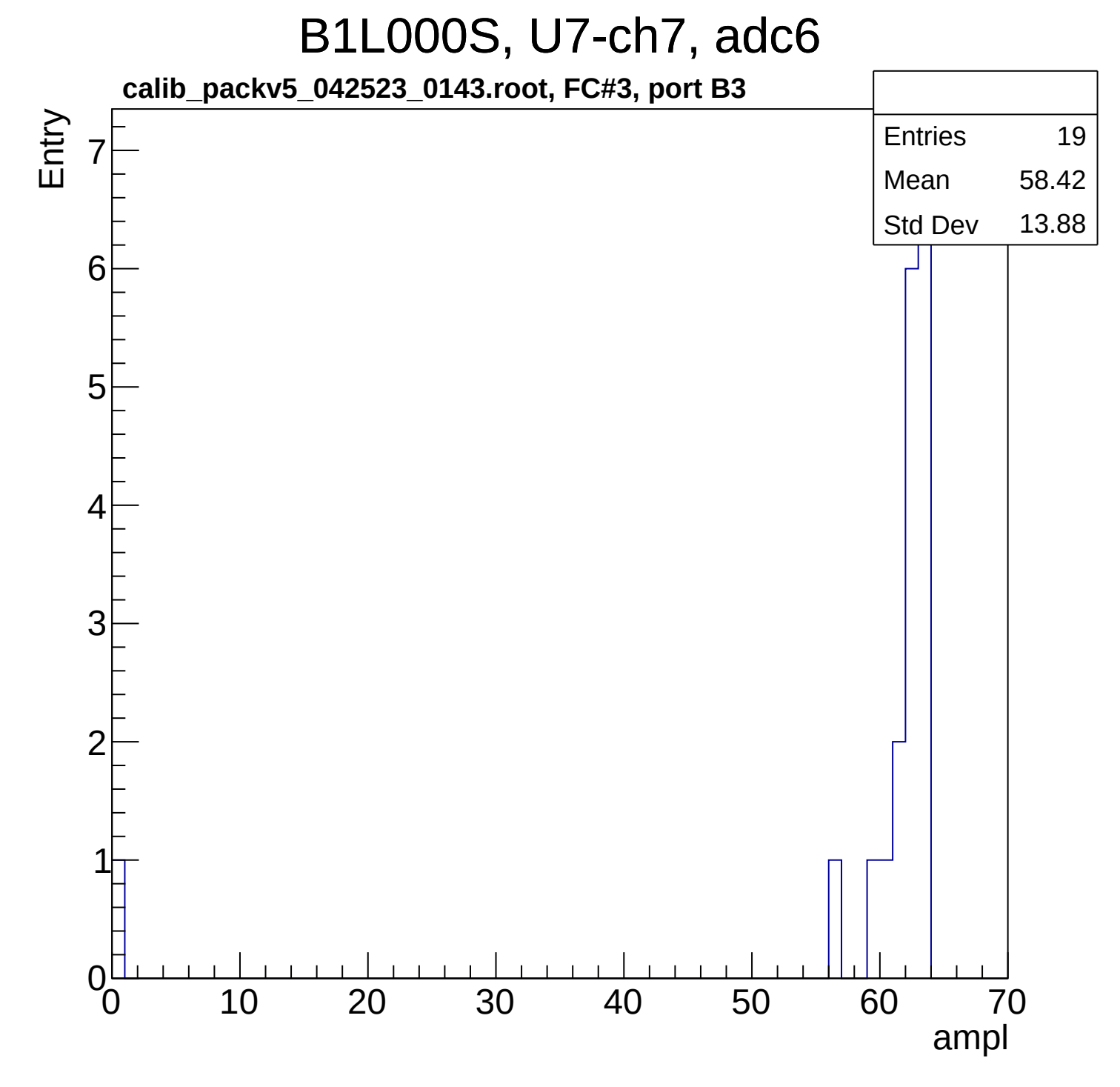
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch7, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.86
Std Dev 2.315
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch7, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.42
Std Dev 13.88
Entry
0
1
2
3
4
5
6
7
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10
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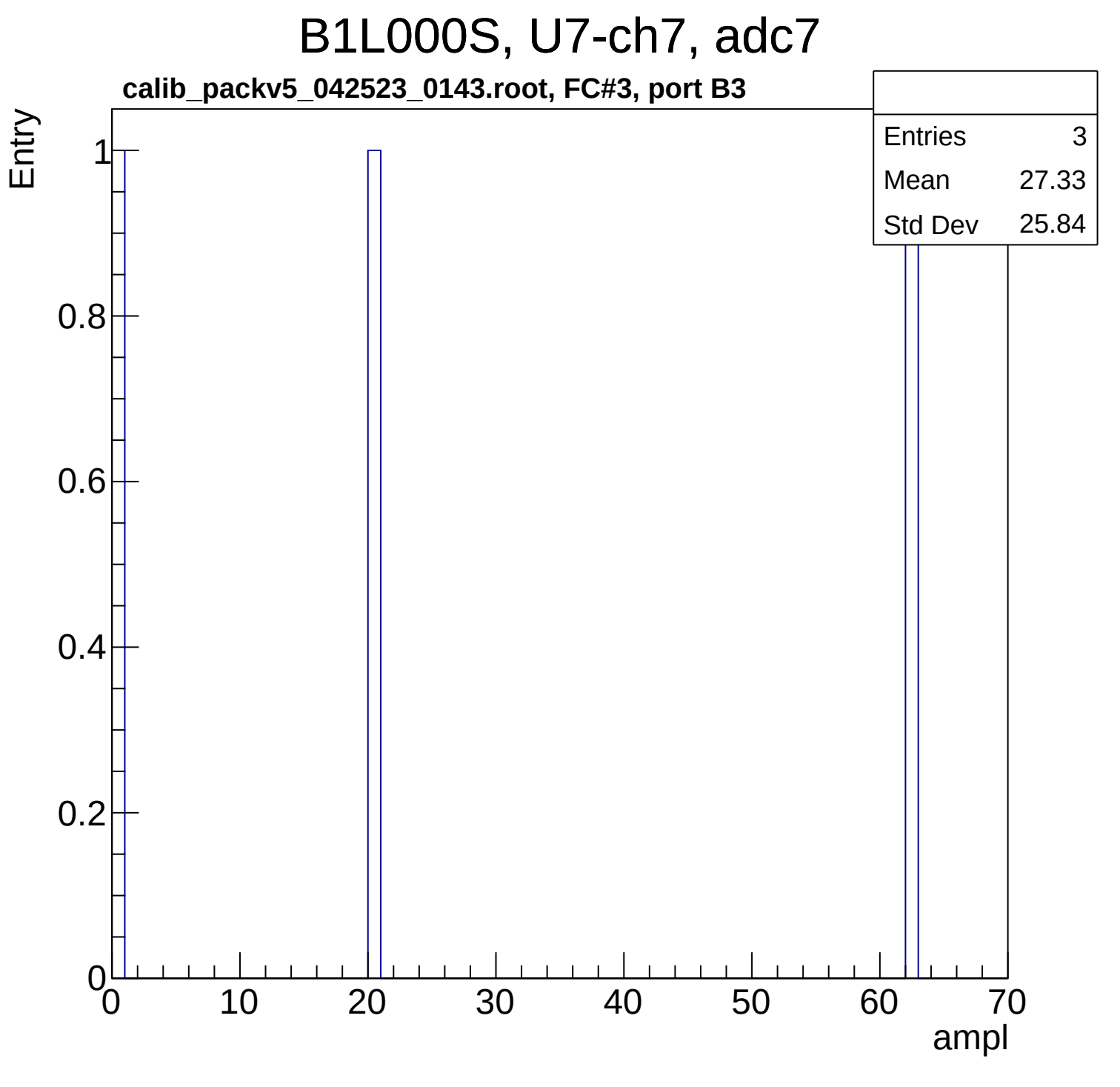
ampl


B1L000S, U7-ch7, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 27.33
Std Dev 25.84
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
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60
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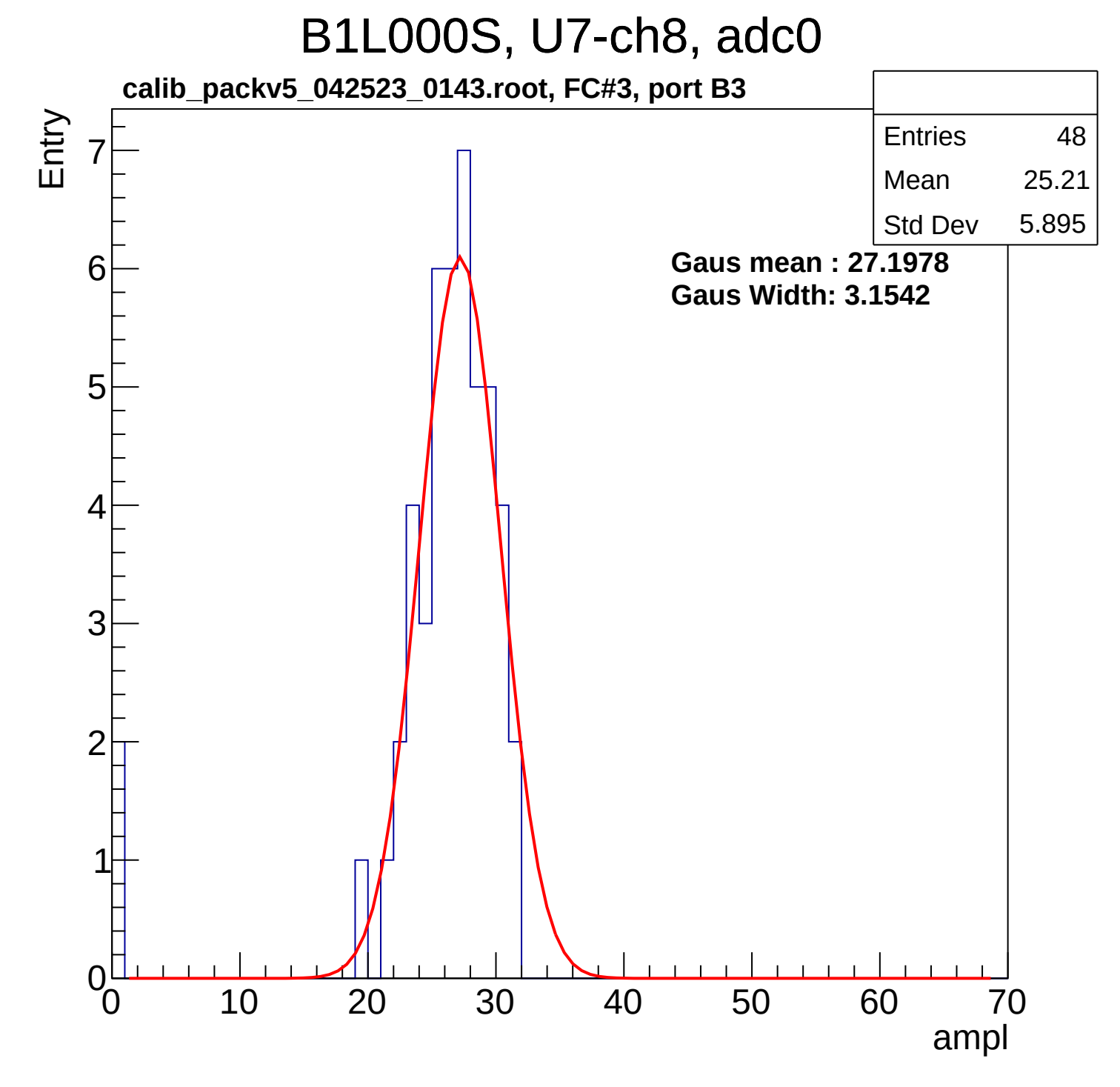
B1L000S, U7-ch8, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 25.21
Std Dev 5.895
Gaus mean : 27.1978
Gaus Width: 3.1542
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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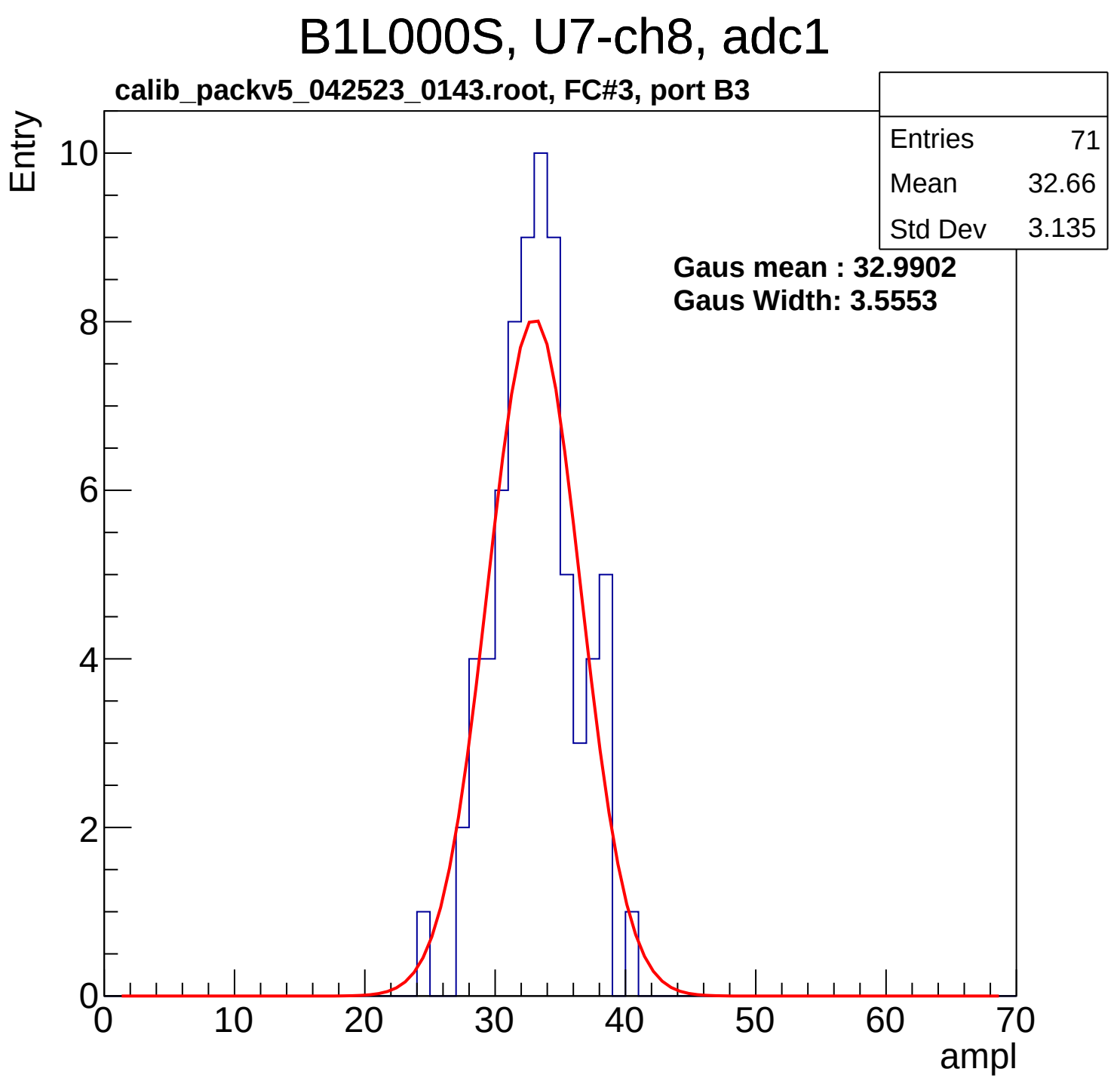
B1L000S, U7-ch8, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 32.66
Std Dev 3.135
Gaus mean : 32.9902
Gaus Width: 3.5553
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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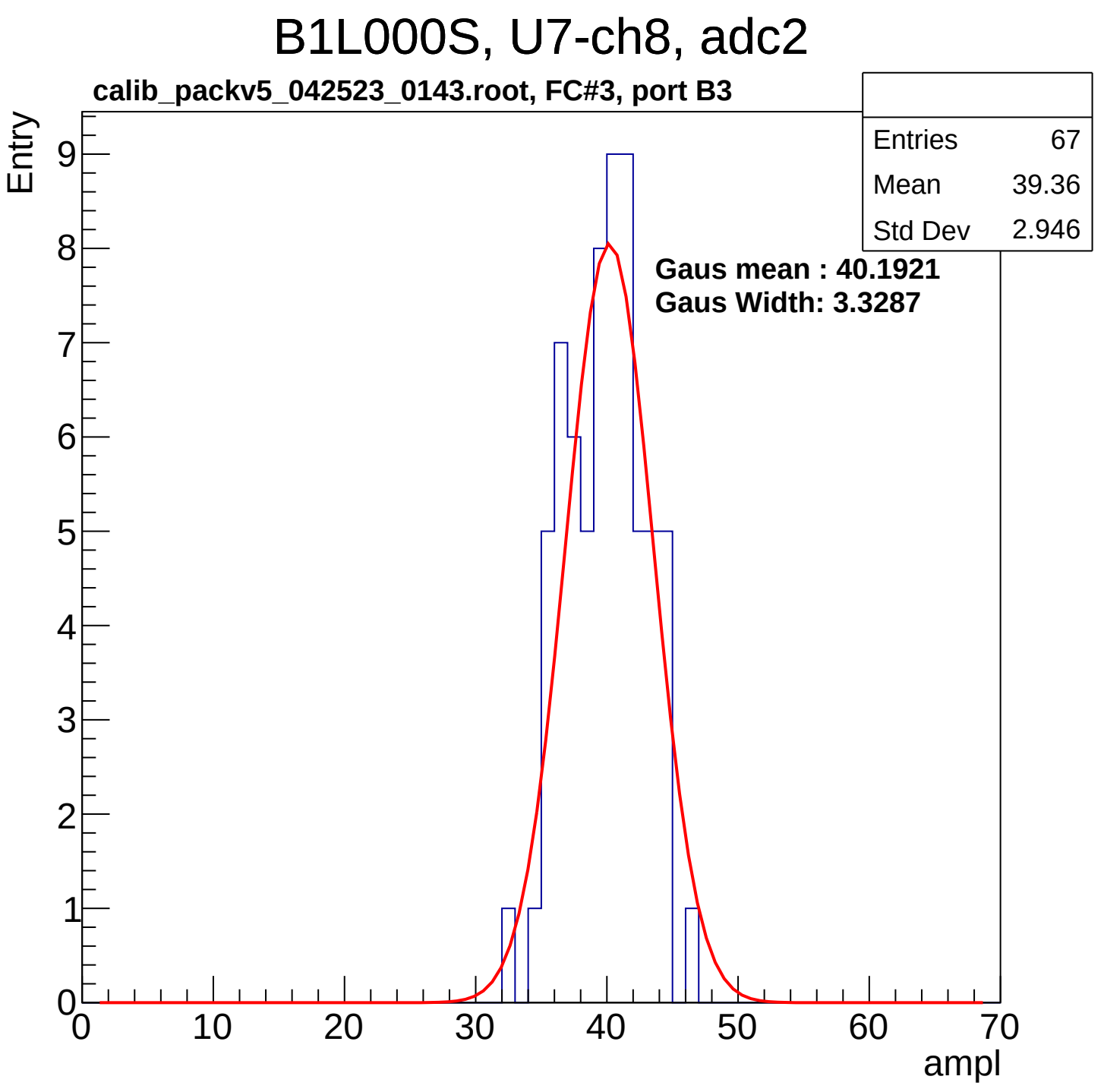
B1L000S, U7-ch8, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 39.36
Std Dev 2.946
Gaus mean : 40.1921
Gaus Width: 3.3287
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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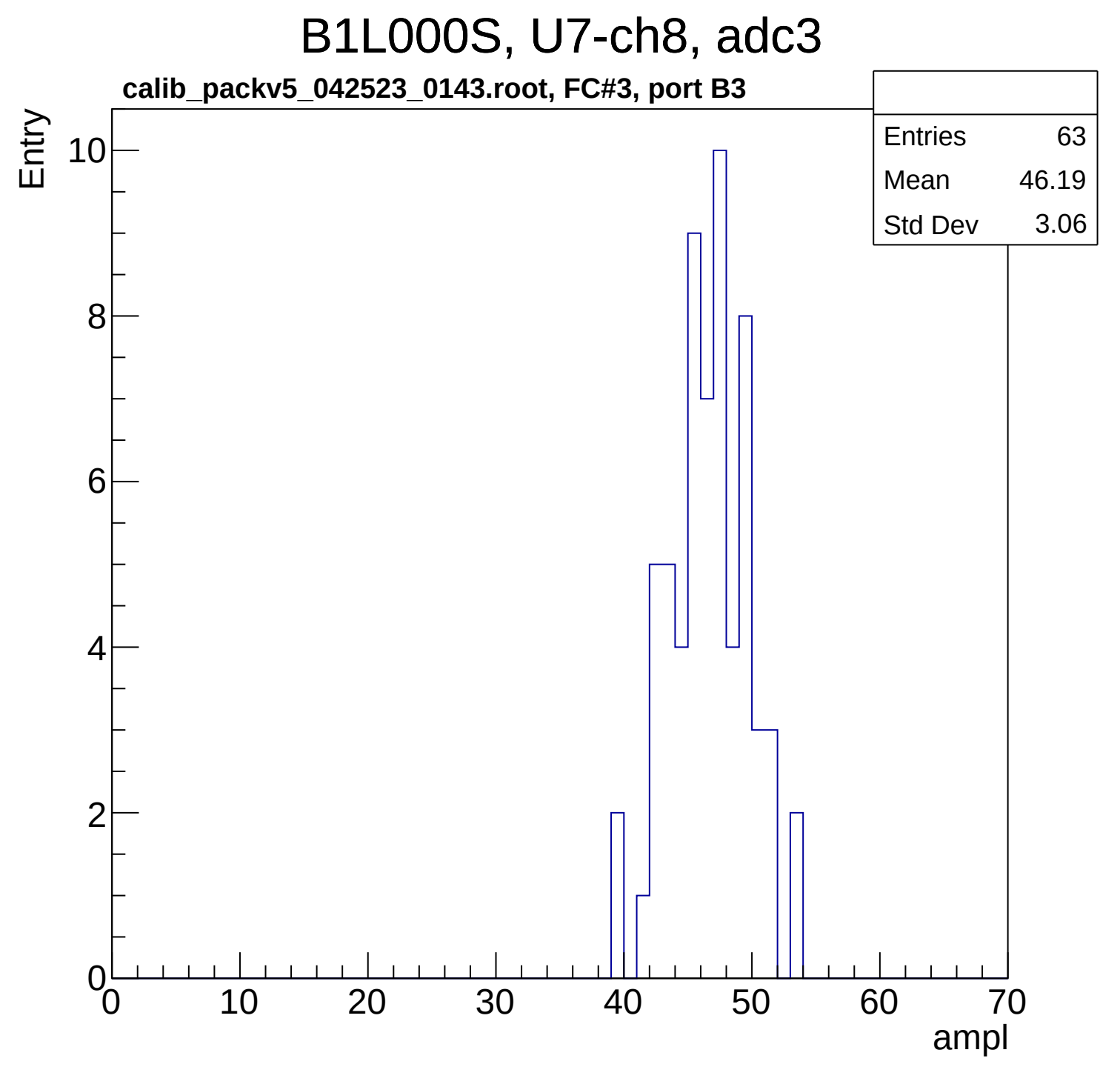
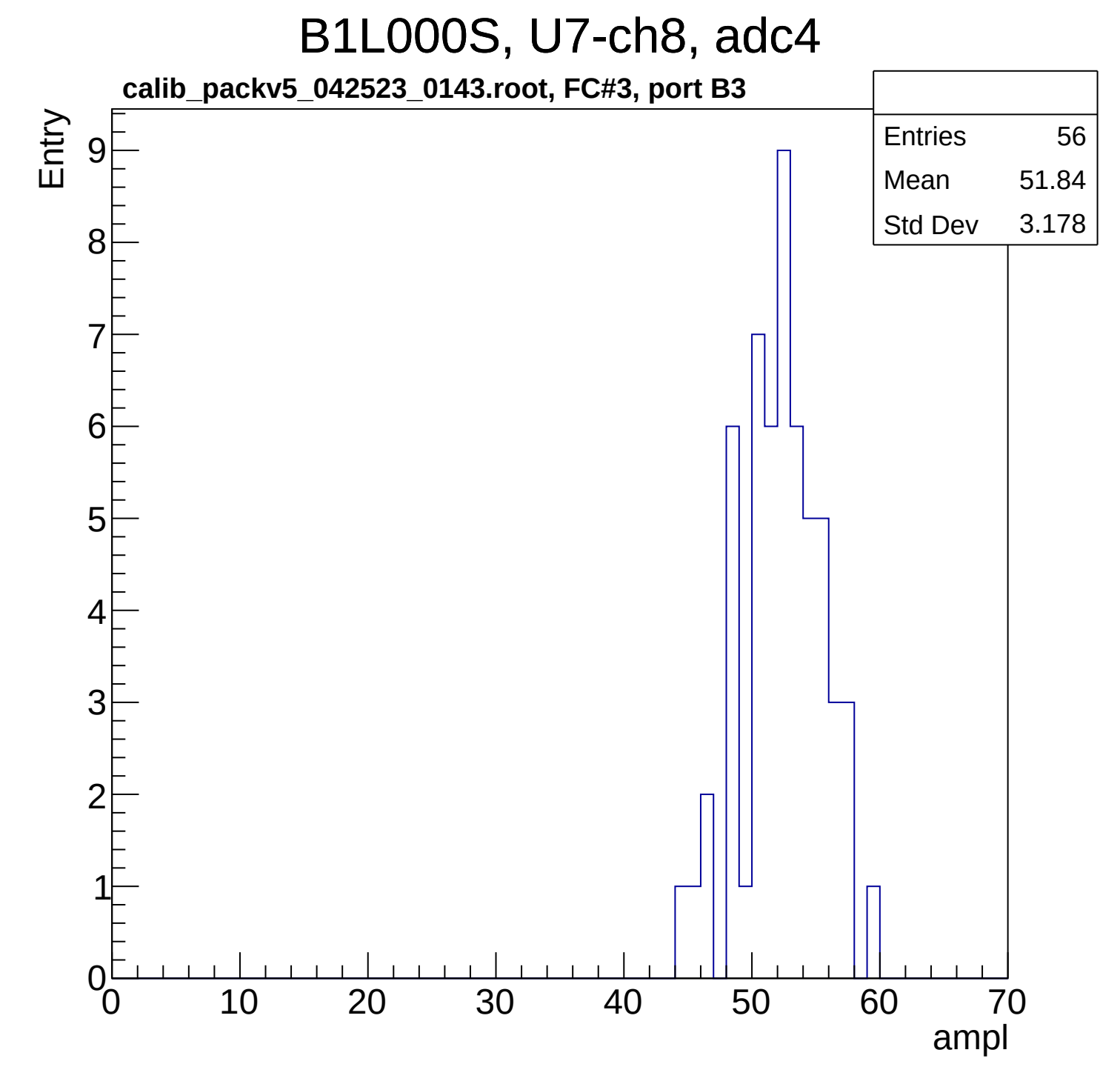

B1L000S, U7-ch8, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 46.19
Std Dev 3.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch8, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 51.84
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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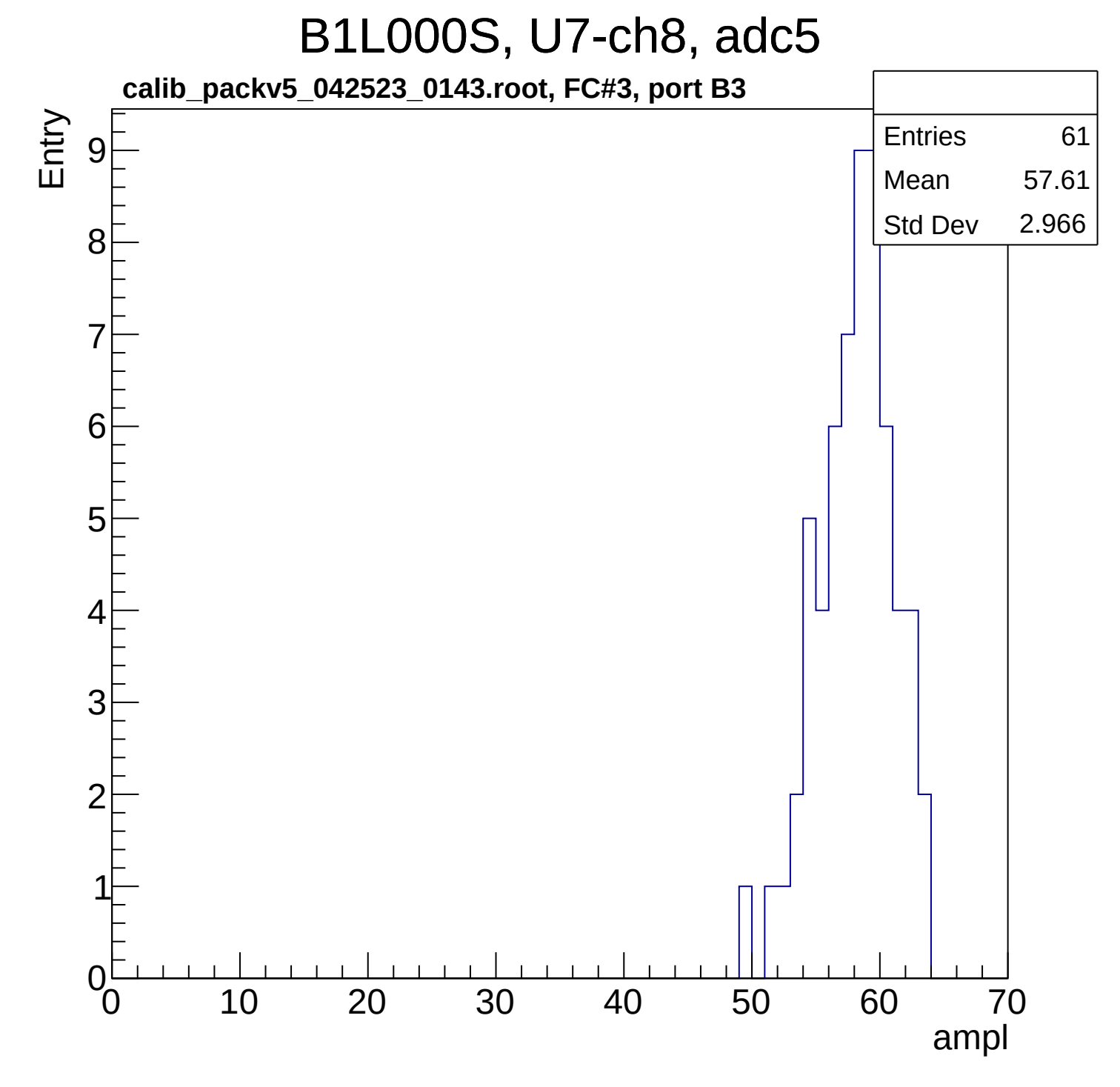
B1L000S, U7-ch8, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.61
Std Dev 2.966
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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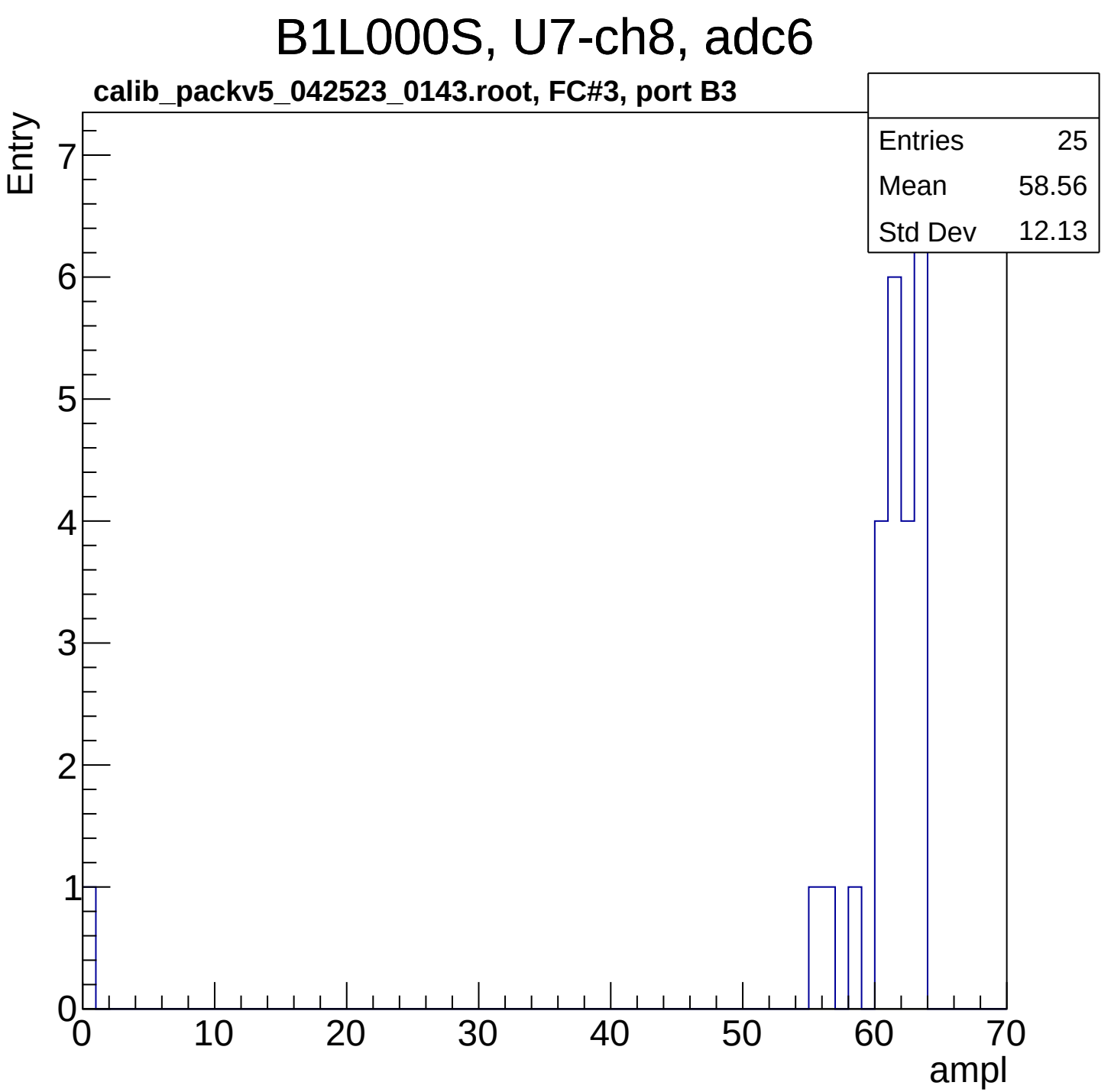
B1L000S, U7-ch8, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.56
Std Dev 12.13
Entry
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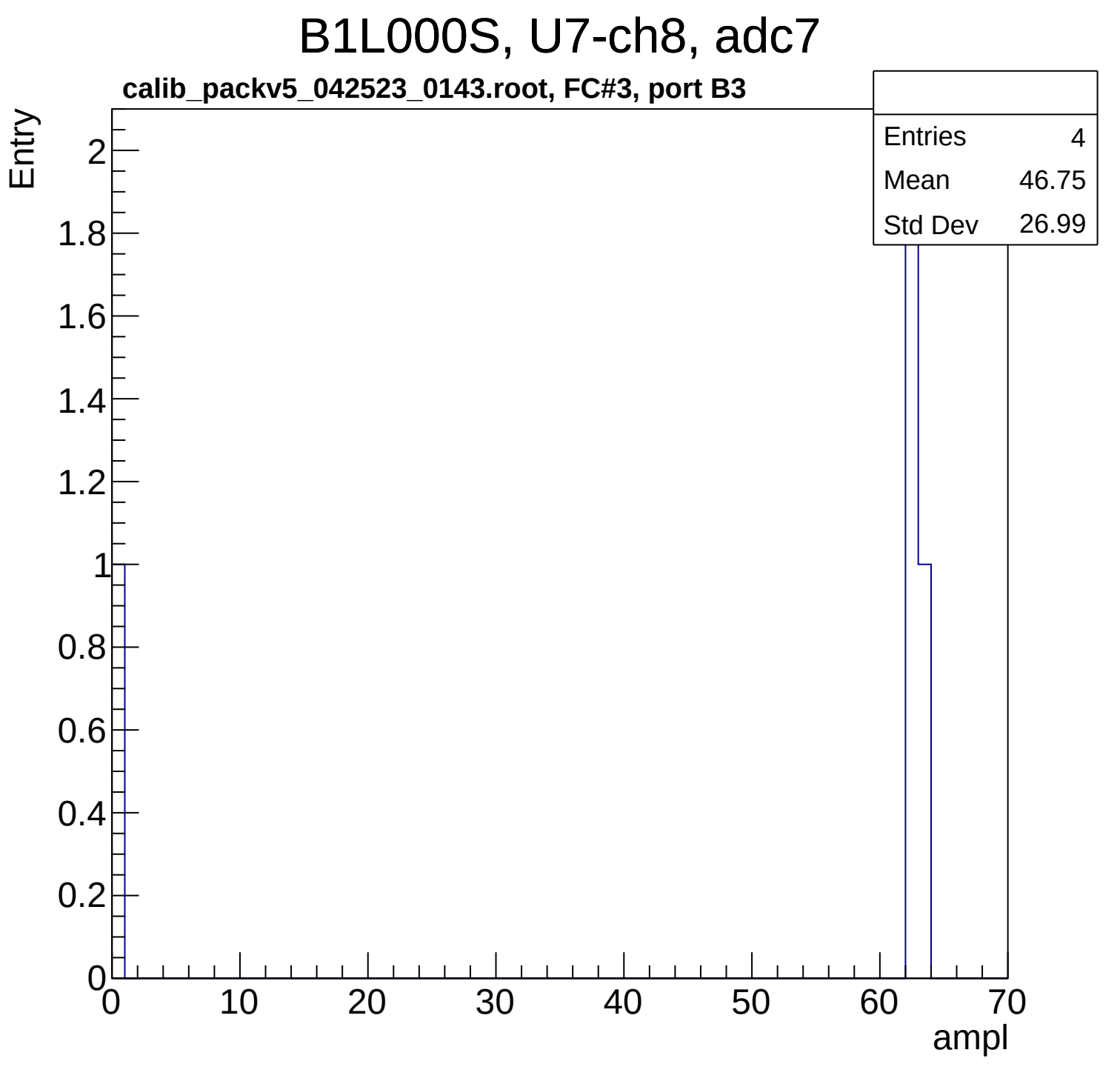
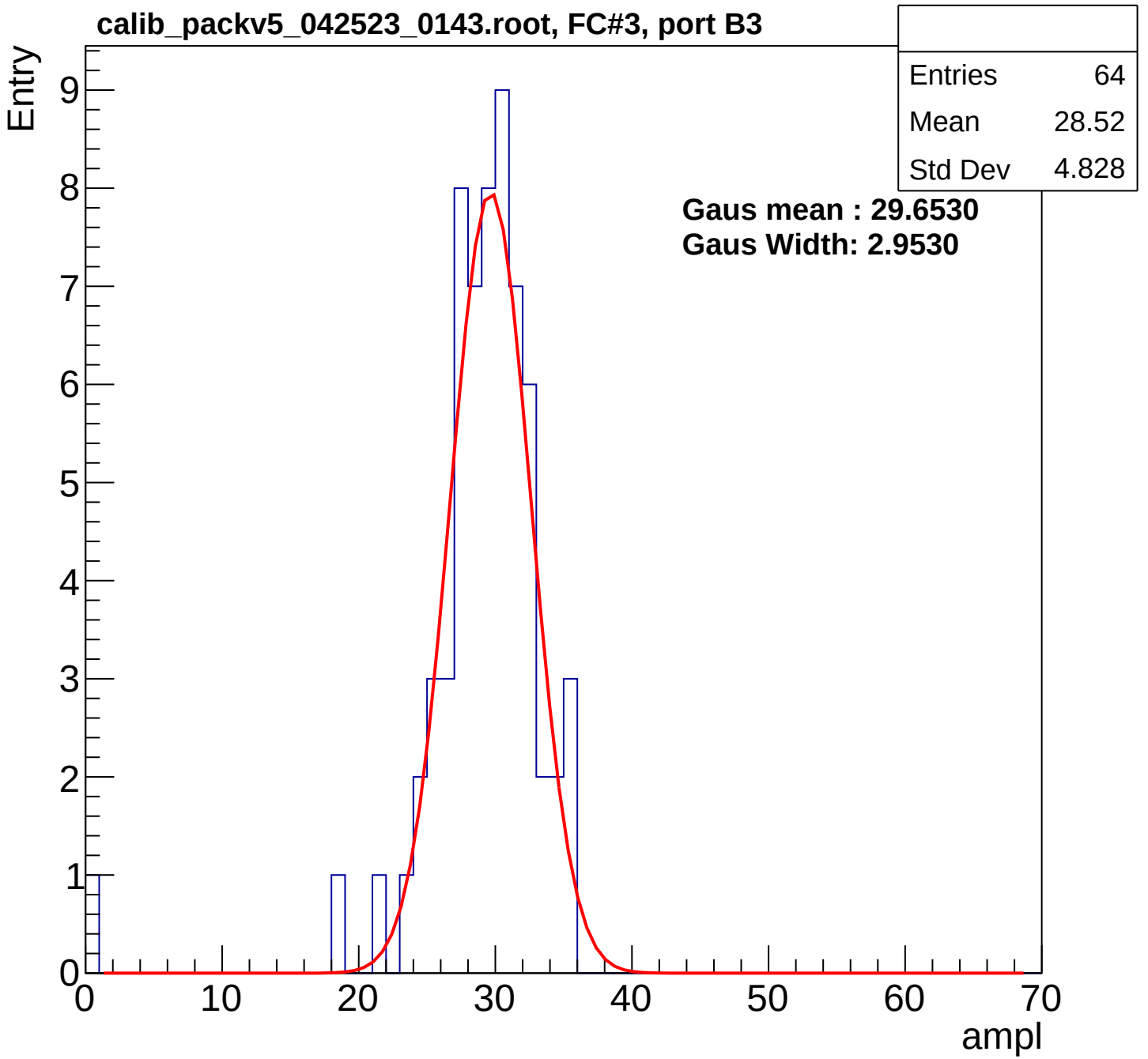
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch8, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch9, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.52
Std Dev 4.828
Gaus mean : 29.6530
Gaus Width: 2.9530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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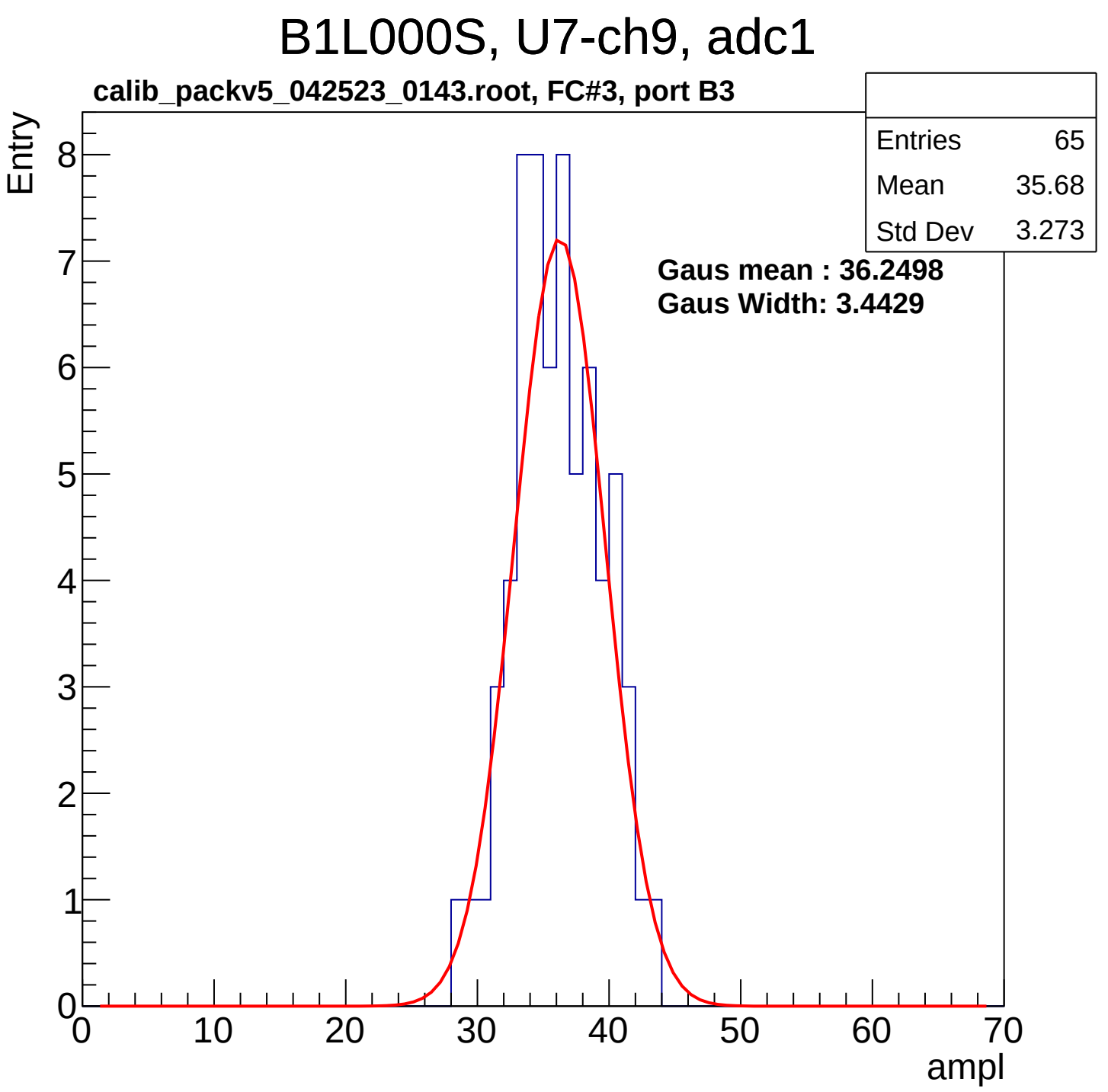
B1L000S, U7-ch9, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.68
Std Dev 3.273
Gaus mean : 36.2498
Gaus Width: 3.4429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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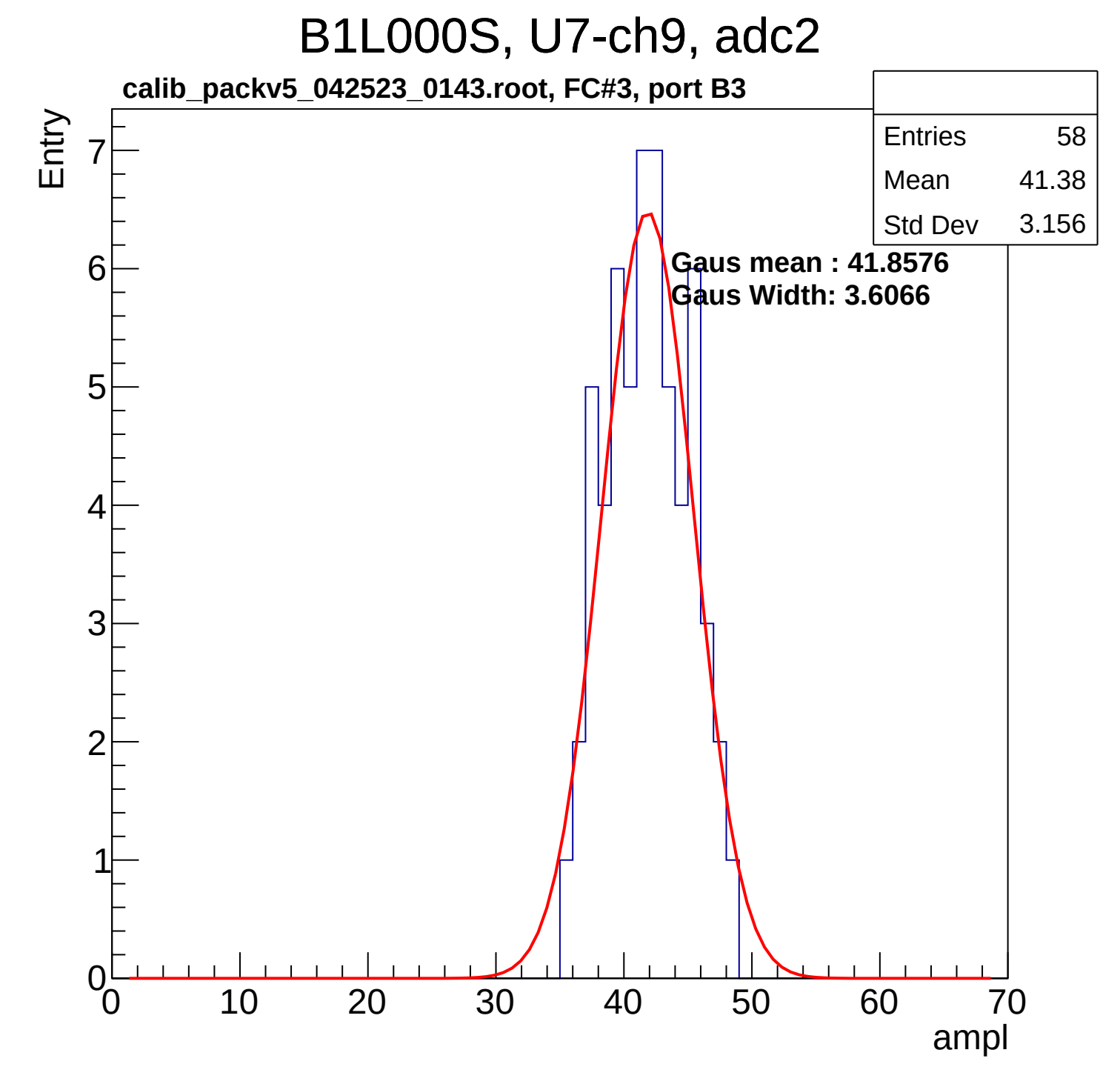
B1L000S, U7-ch9, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.38
Std Dev 3.156
Gaus mean : 41.8576
Gaus Width: 3.6066
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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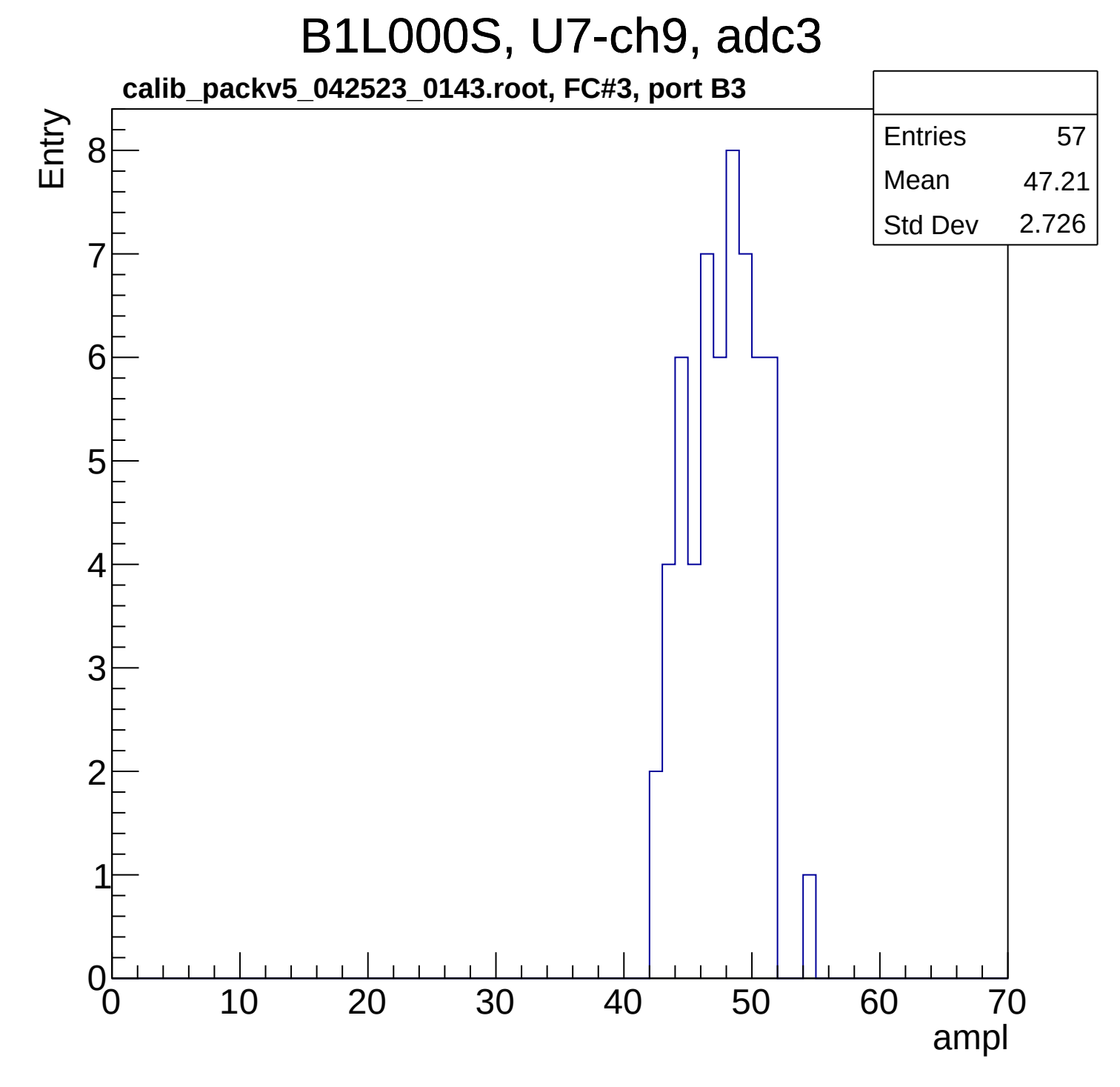
B1L000S, U7-ch9, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.21
Std Dev 2.726
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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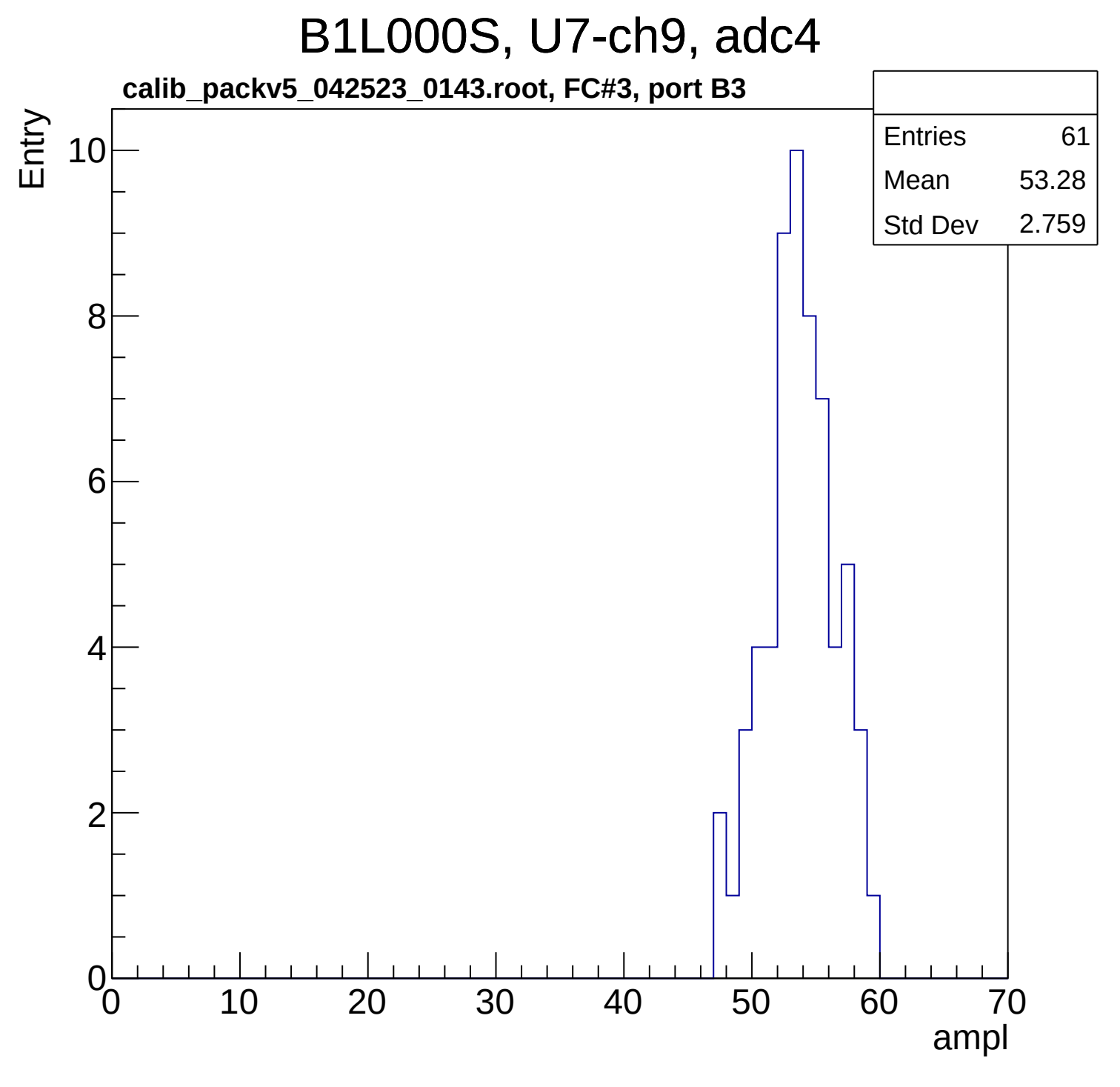
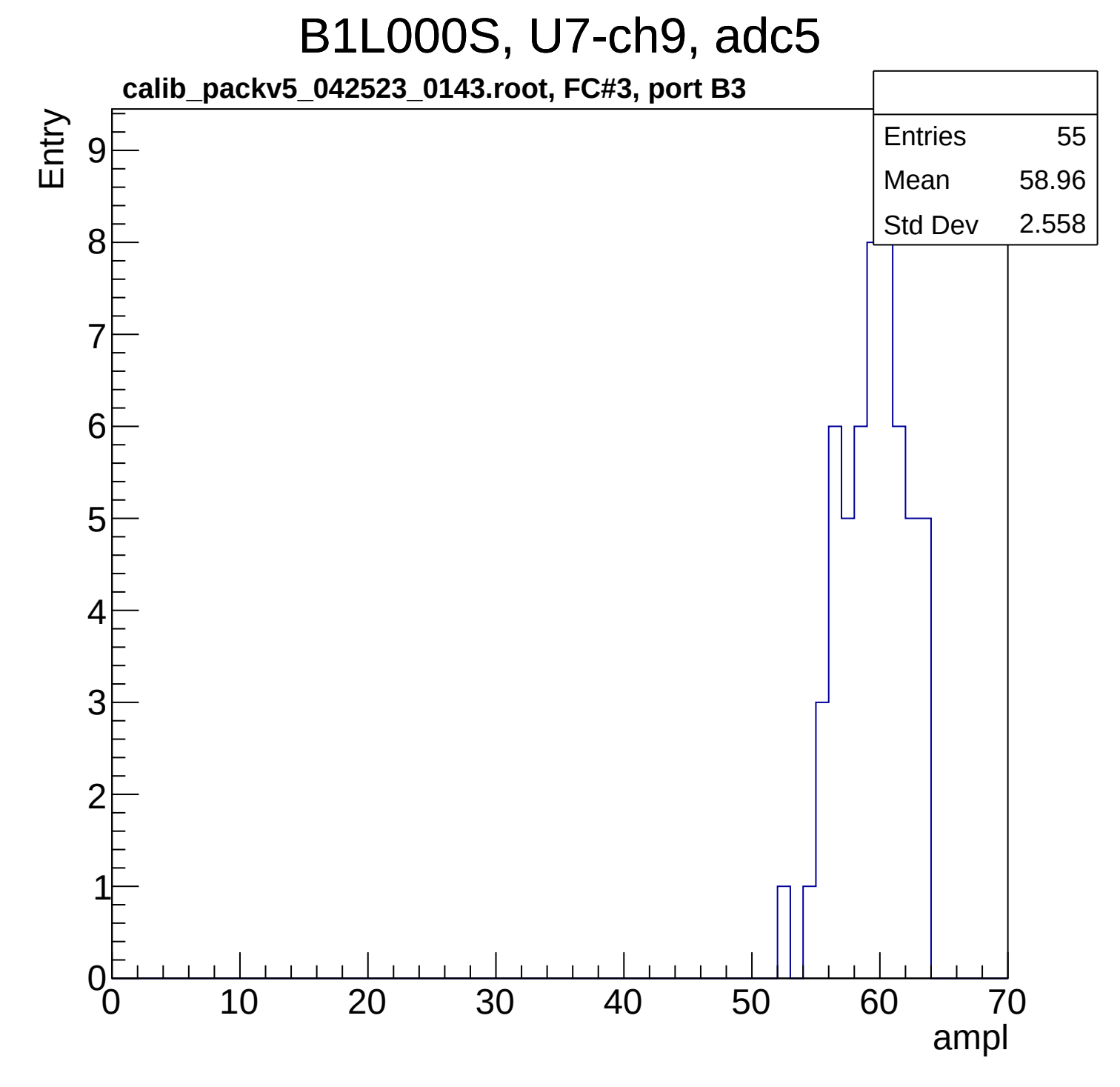

B1L000S, U7-ch9, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 53.28
Std Dev 2.759
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch9, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.96
Std Dev 2.558
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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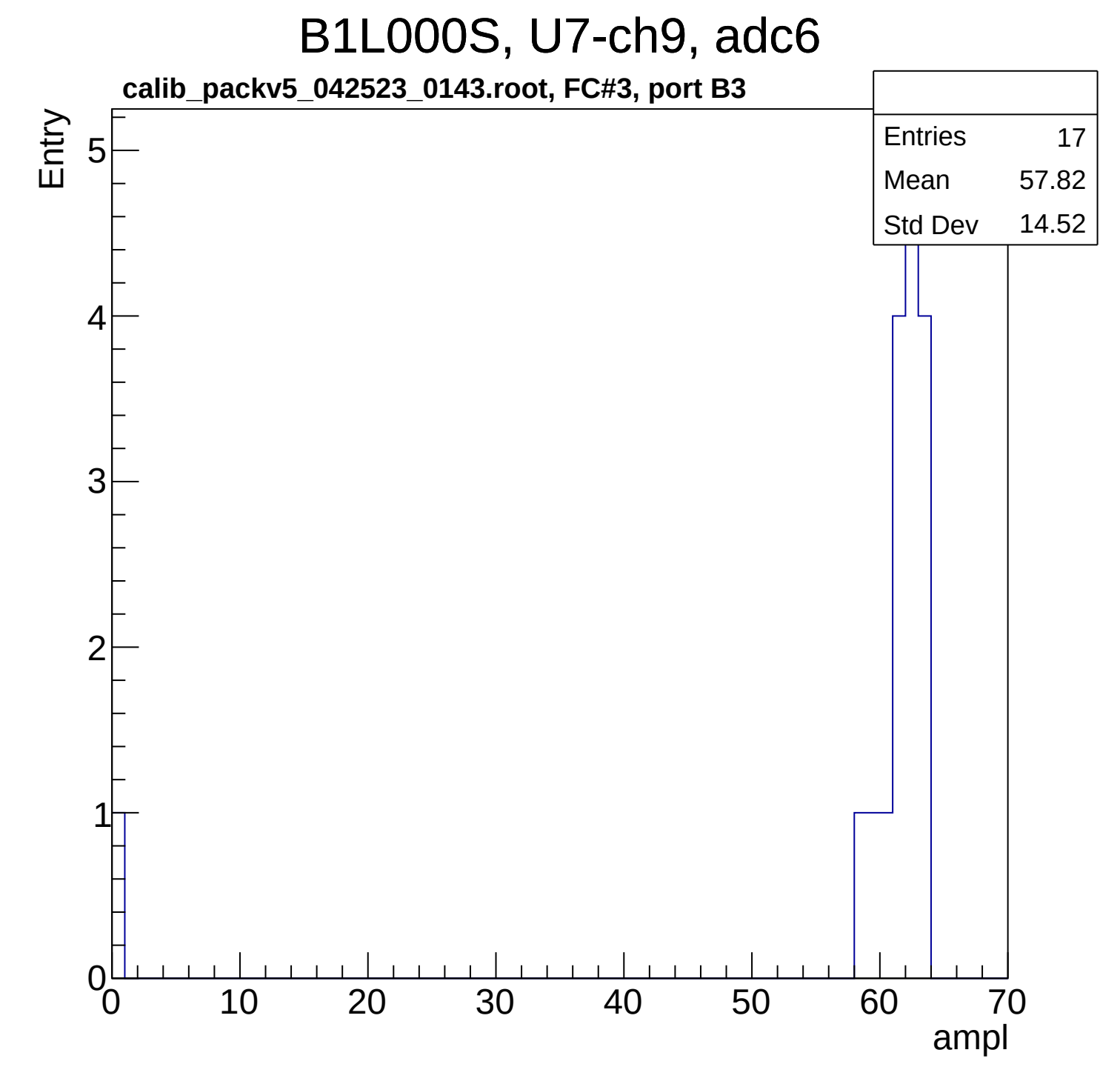
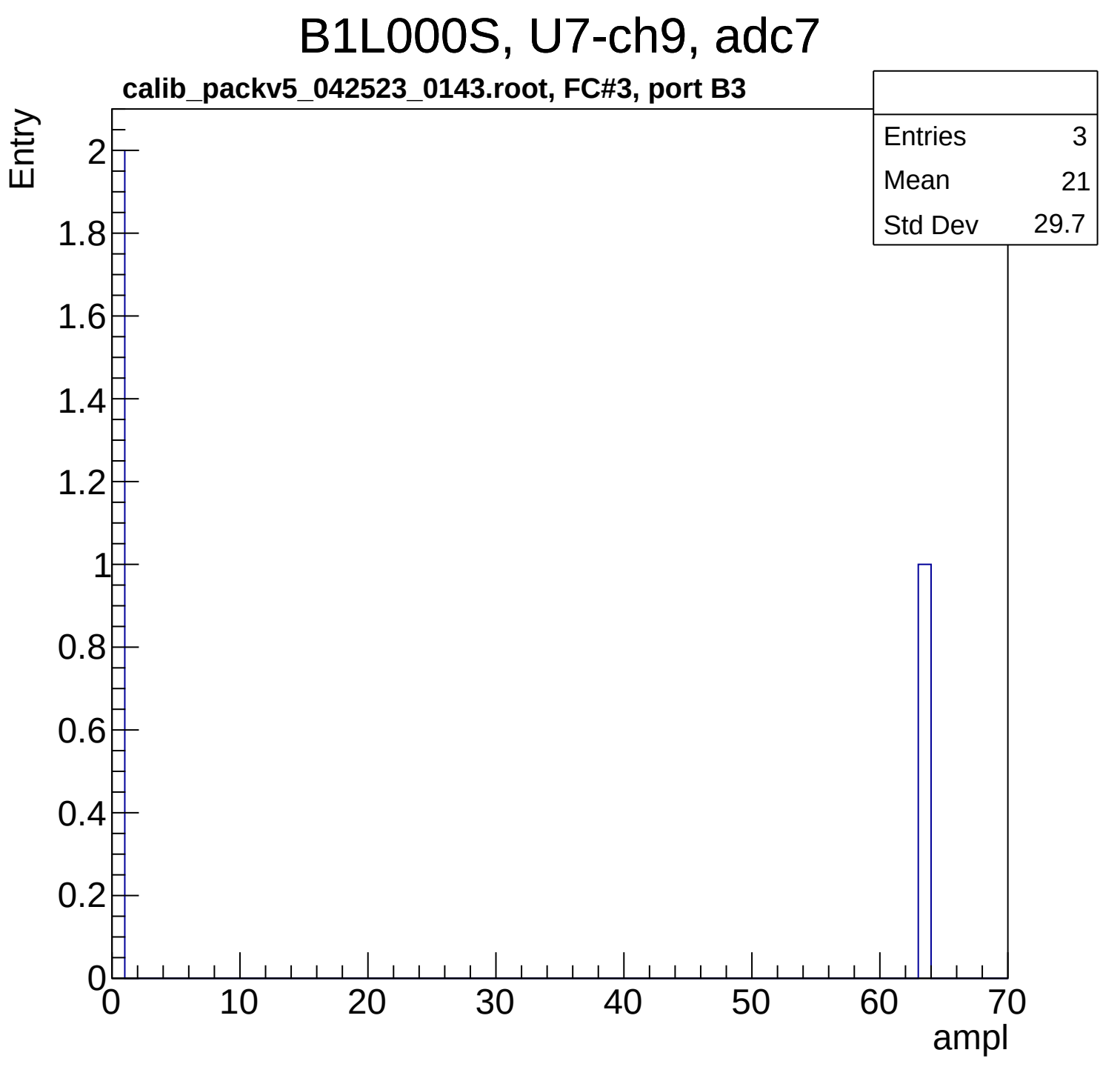
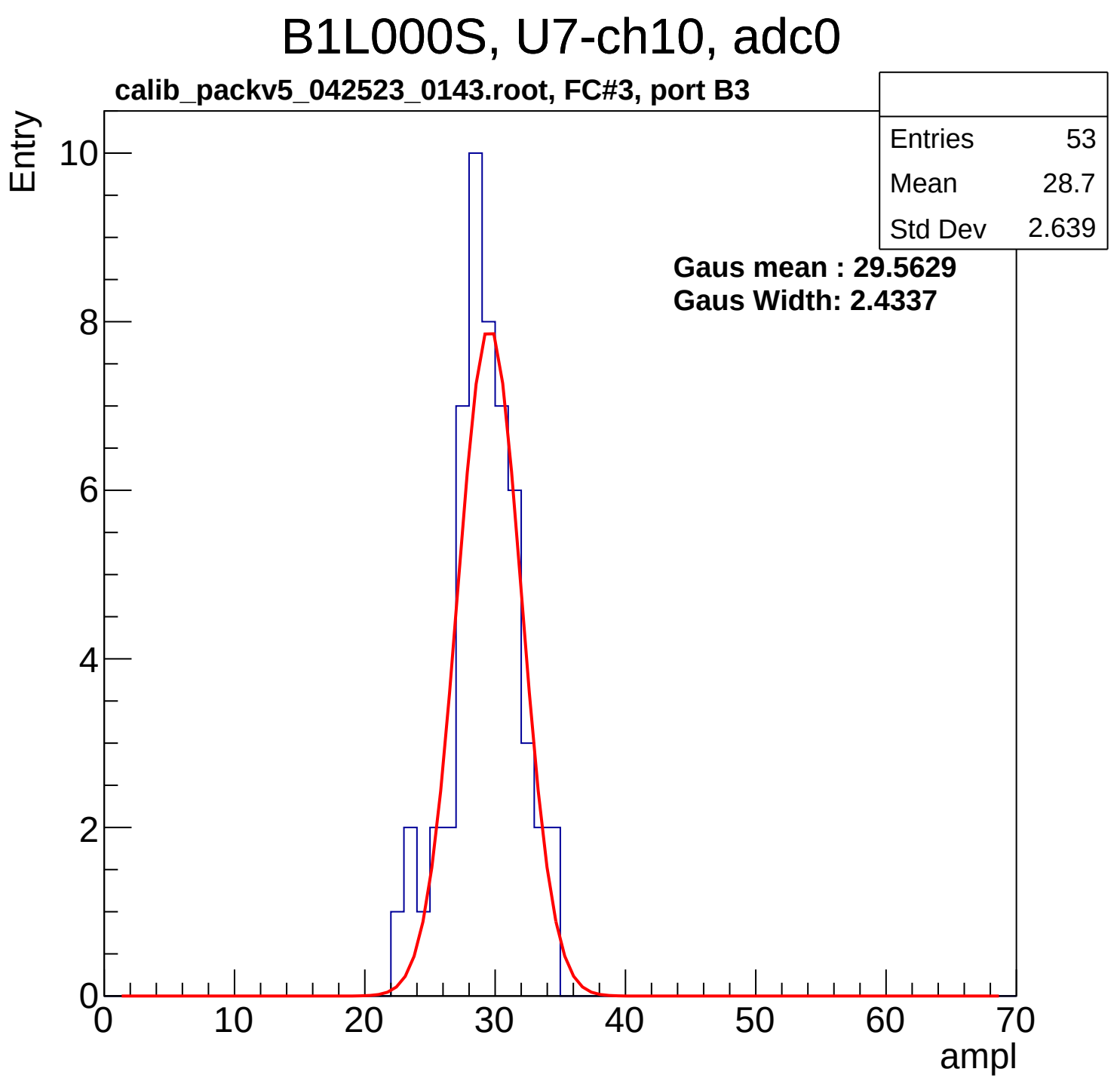

B1L000S, U7-ch9, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 57.82
Std Dev 14.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch9, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch10, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 28.7
Std Dev 2.639
Gaus mean : 29.5629
Gaus Width: 2.4337
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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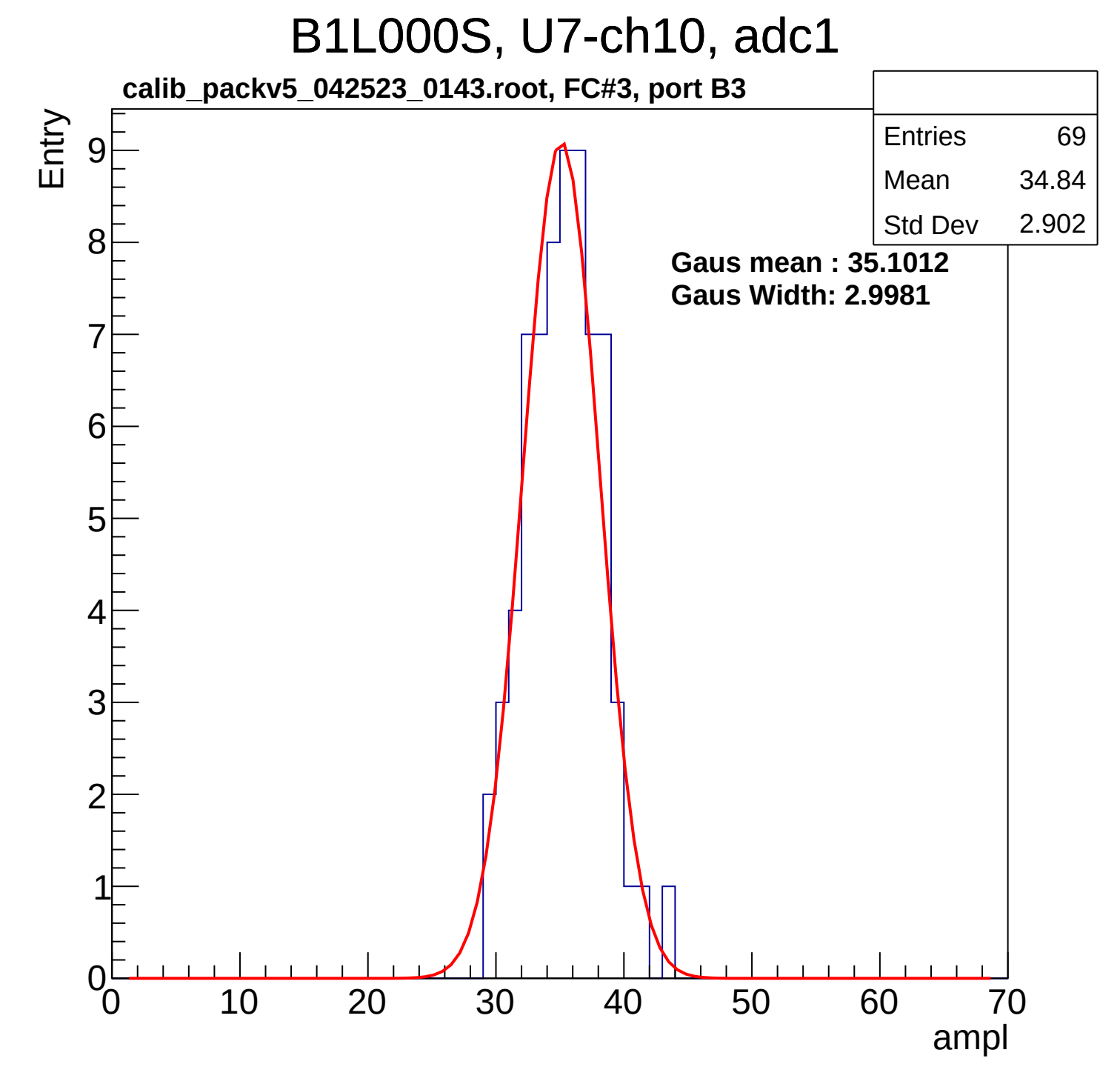
B1L000S, U7-ch10, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.84
Std Dev 2.902
Gaus mean : 35.1012
Gaus Width: 2.9981
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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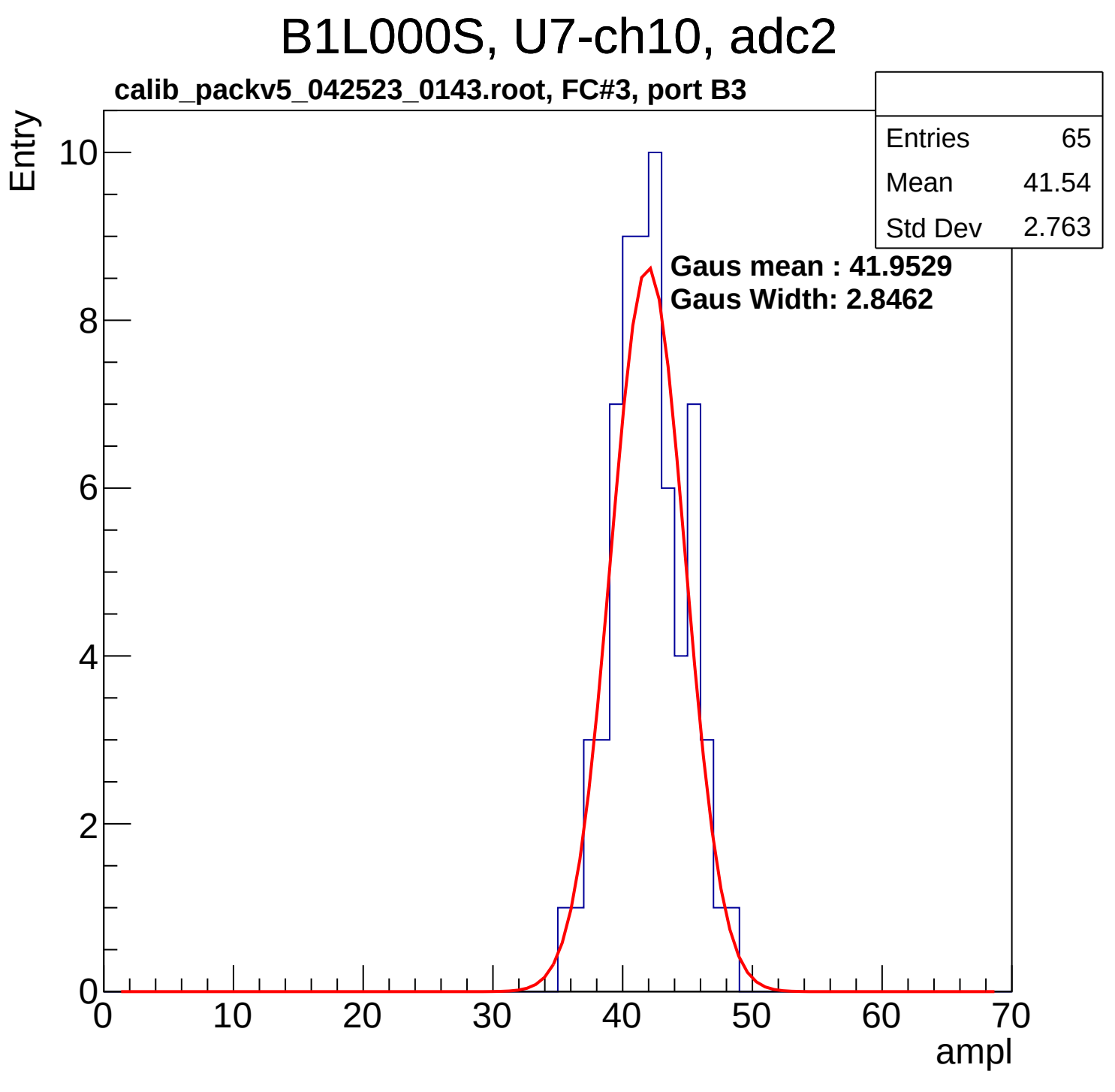
B1L000S, U7-ch10, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.54
Std Dev 2.763
Gaus mean : 41.9529
Gaus Width: 2.8462
Entry
ampl
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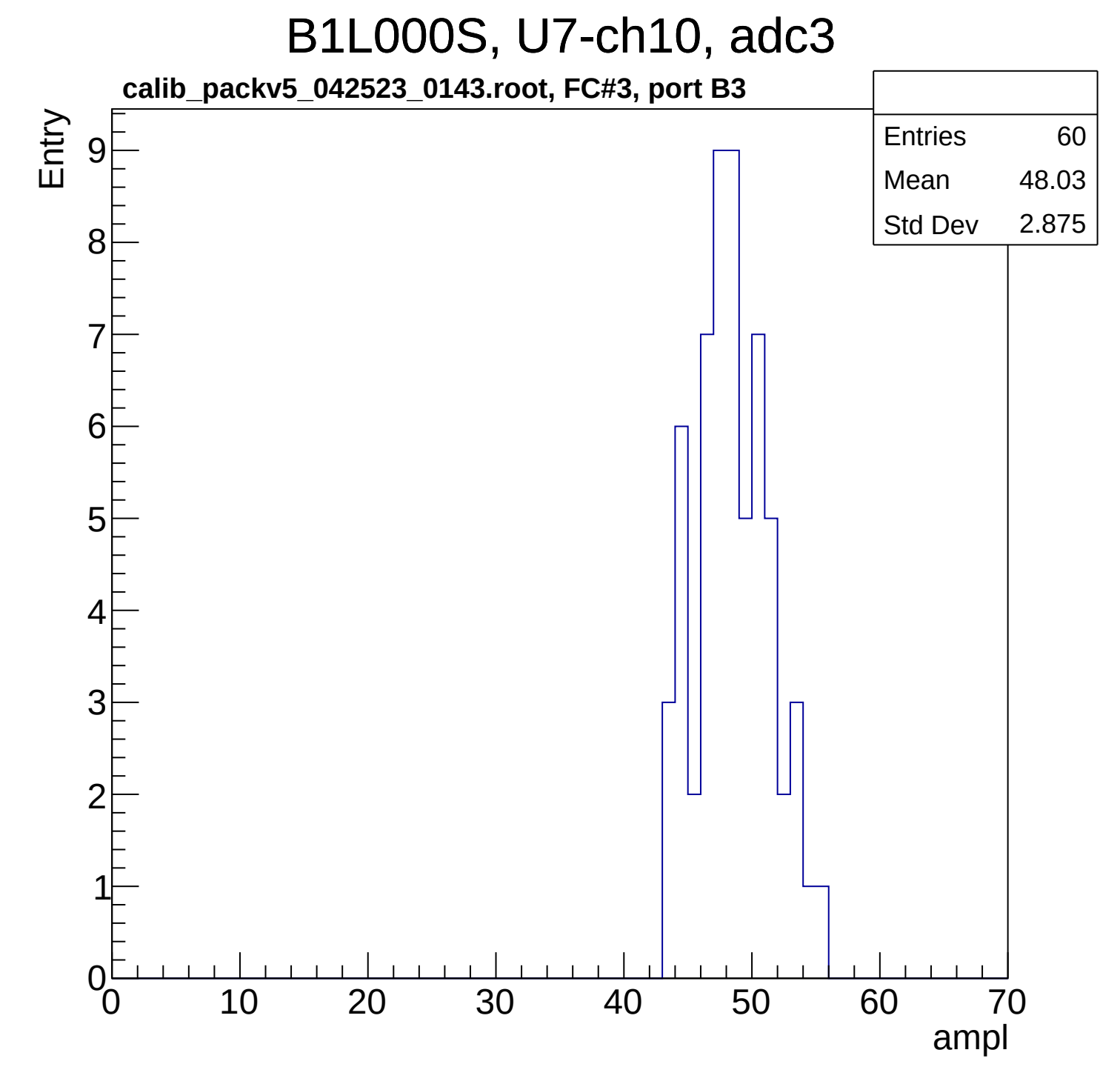
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch10, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.03
Std Dev 2.875
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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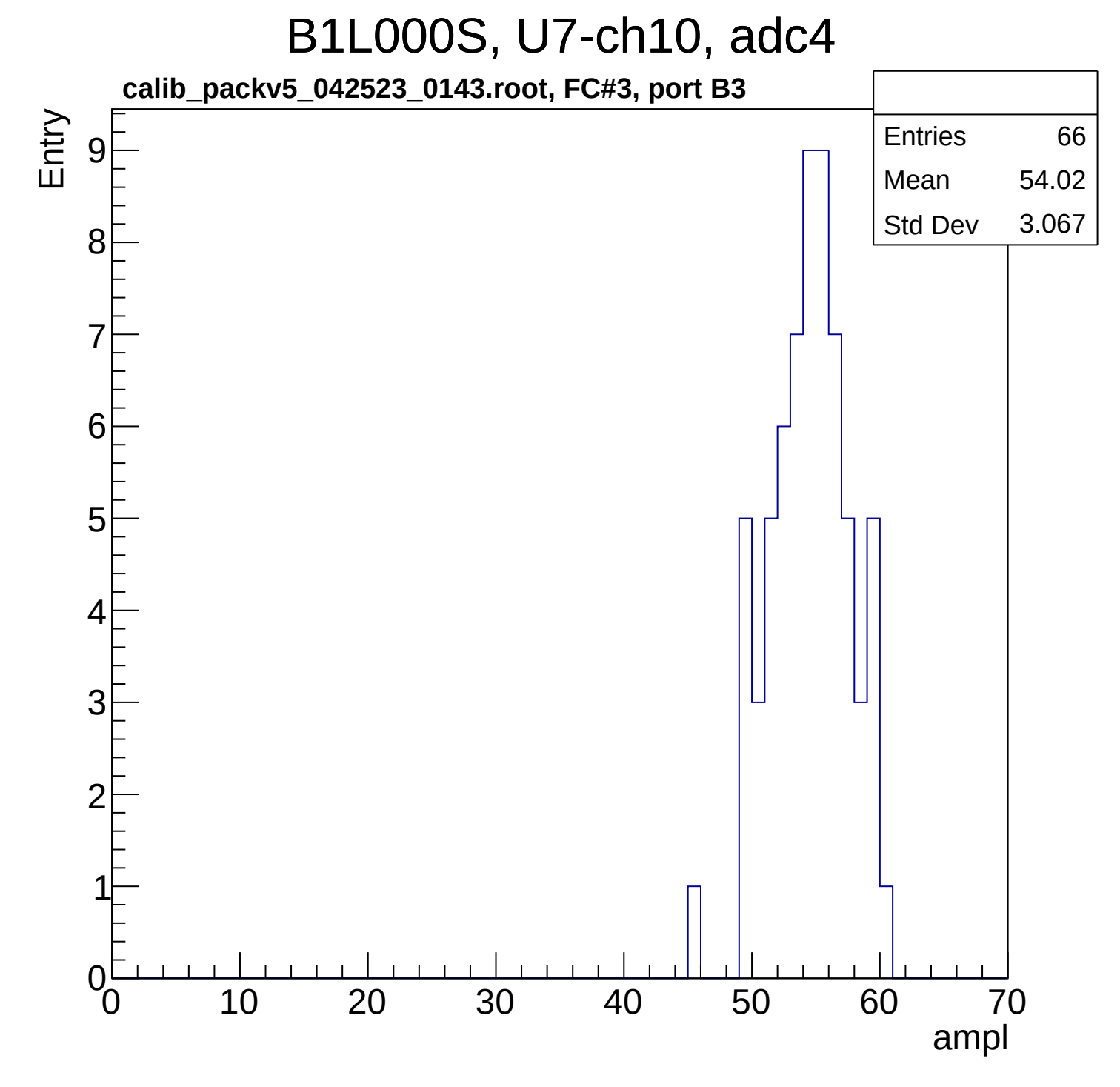
B1L000S, U7-ch10, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 54.02
Std Dev 3.067
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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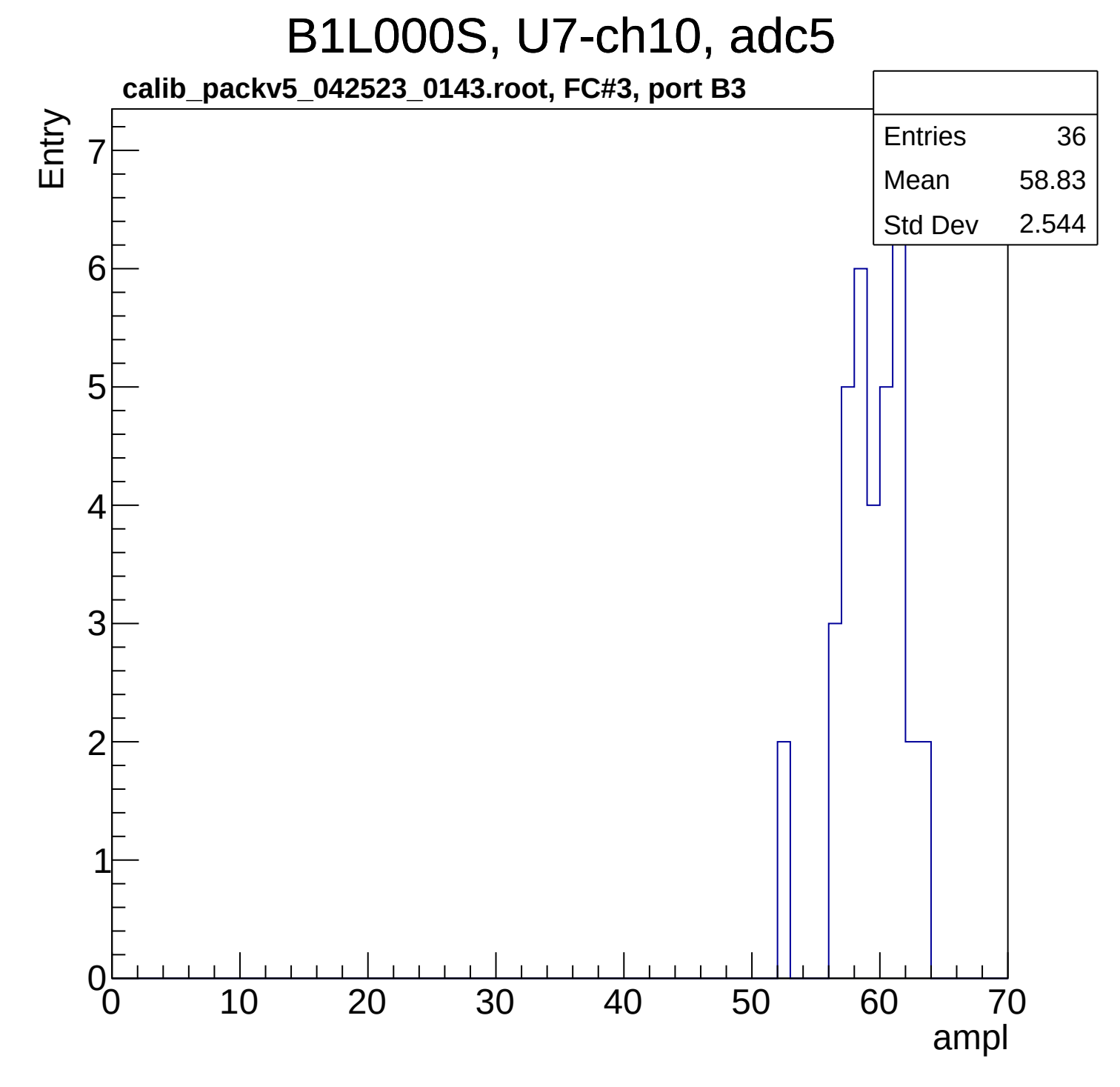
60
70


B1L000S, U7-ch10, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 58.83
Std Dev 2.544
Entry
ampl
0
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2
3
4
5
6
7
0
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20
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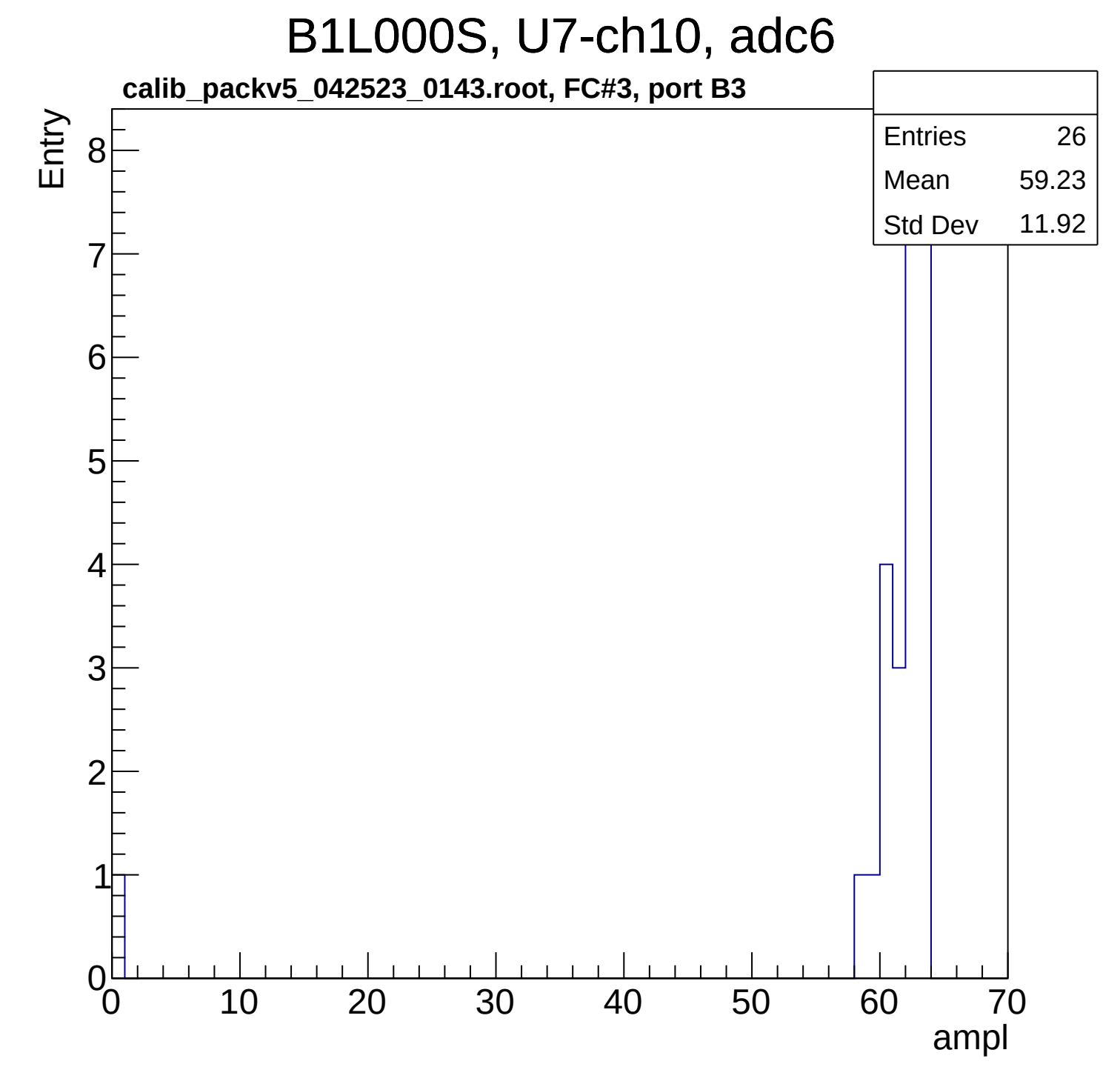
40
50
60
70


B1L000S, U7-ch10, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 59.23
Std Dev 11.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
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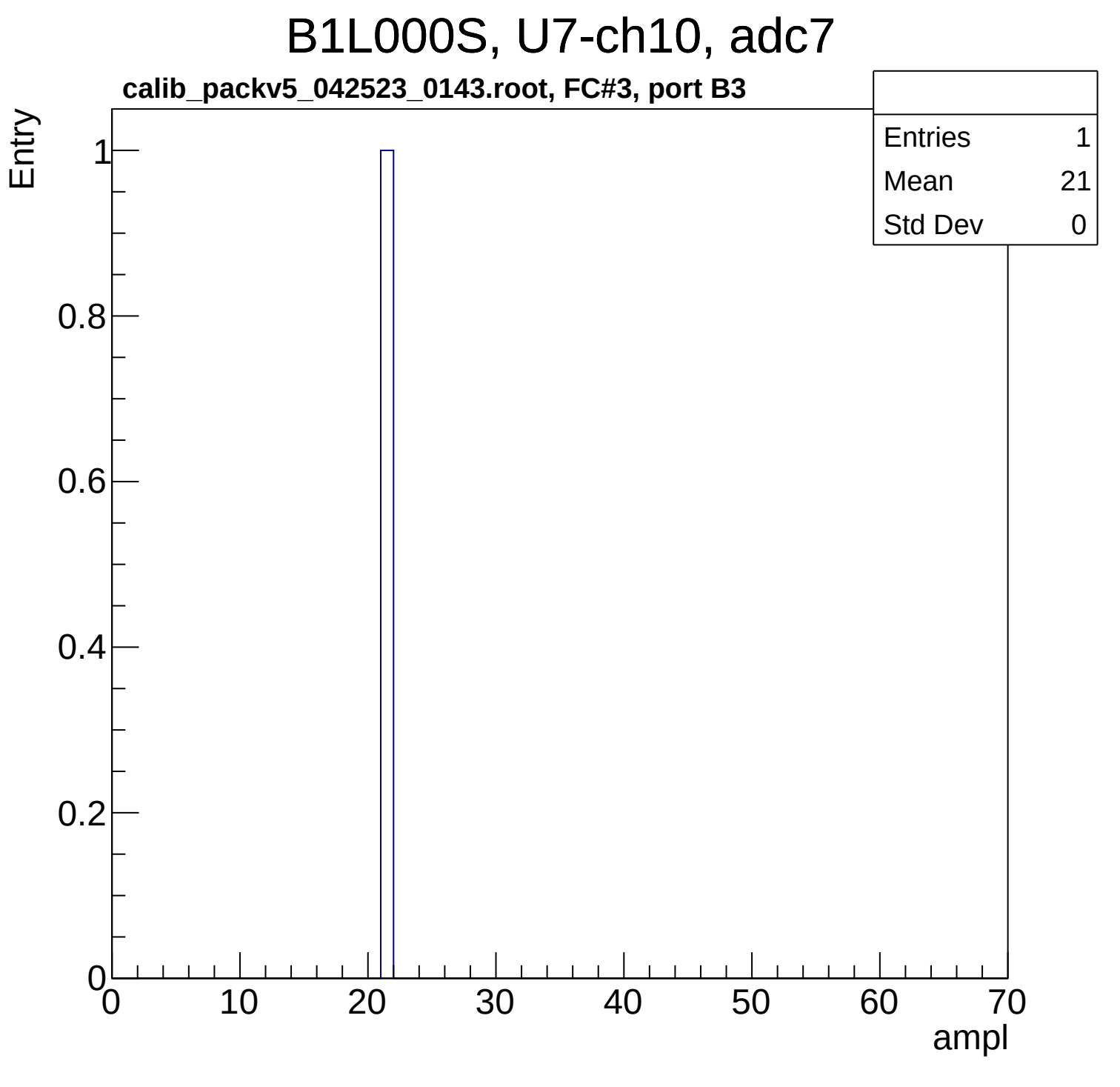
60
70


B1L000S, U7-ch10, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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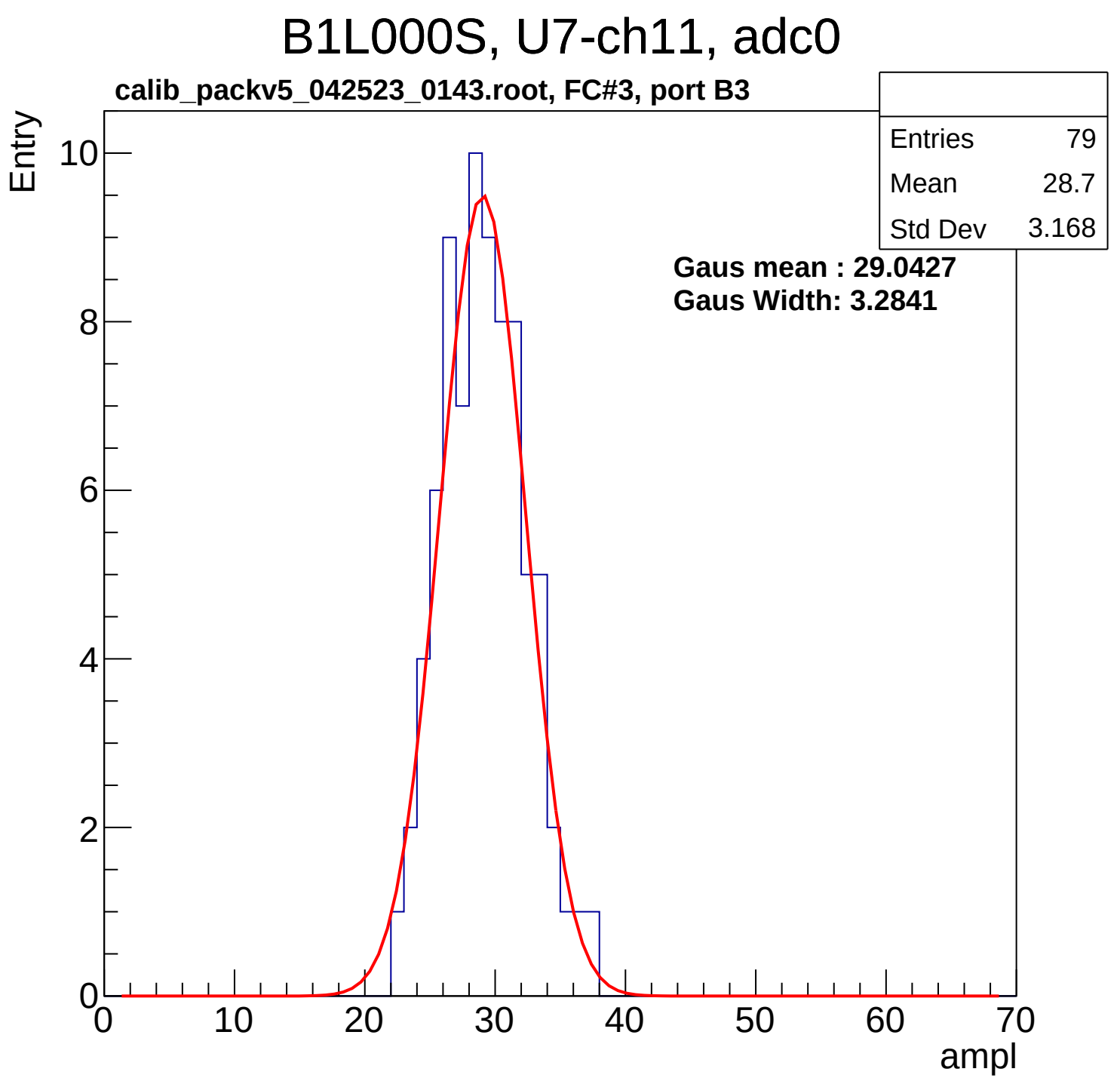
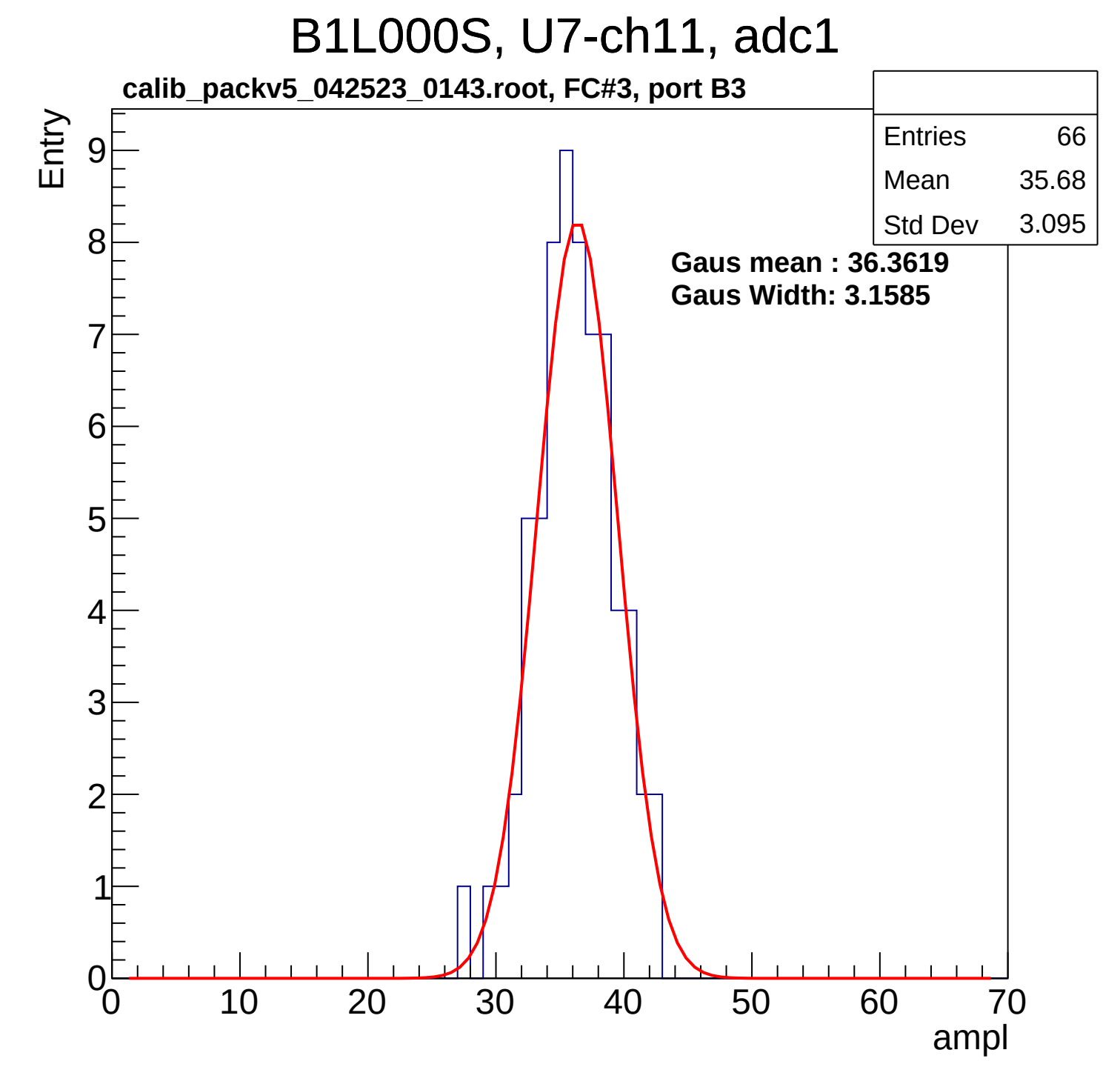
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch11, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 28.7
Std Dev 3.168
Gaus mean : 29.0427
Gaus Width: 3.2841
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch11, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 35.68
Std Dev 3.095
Gaus mean : 36.3619
Gaus Width: 3.1585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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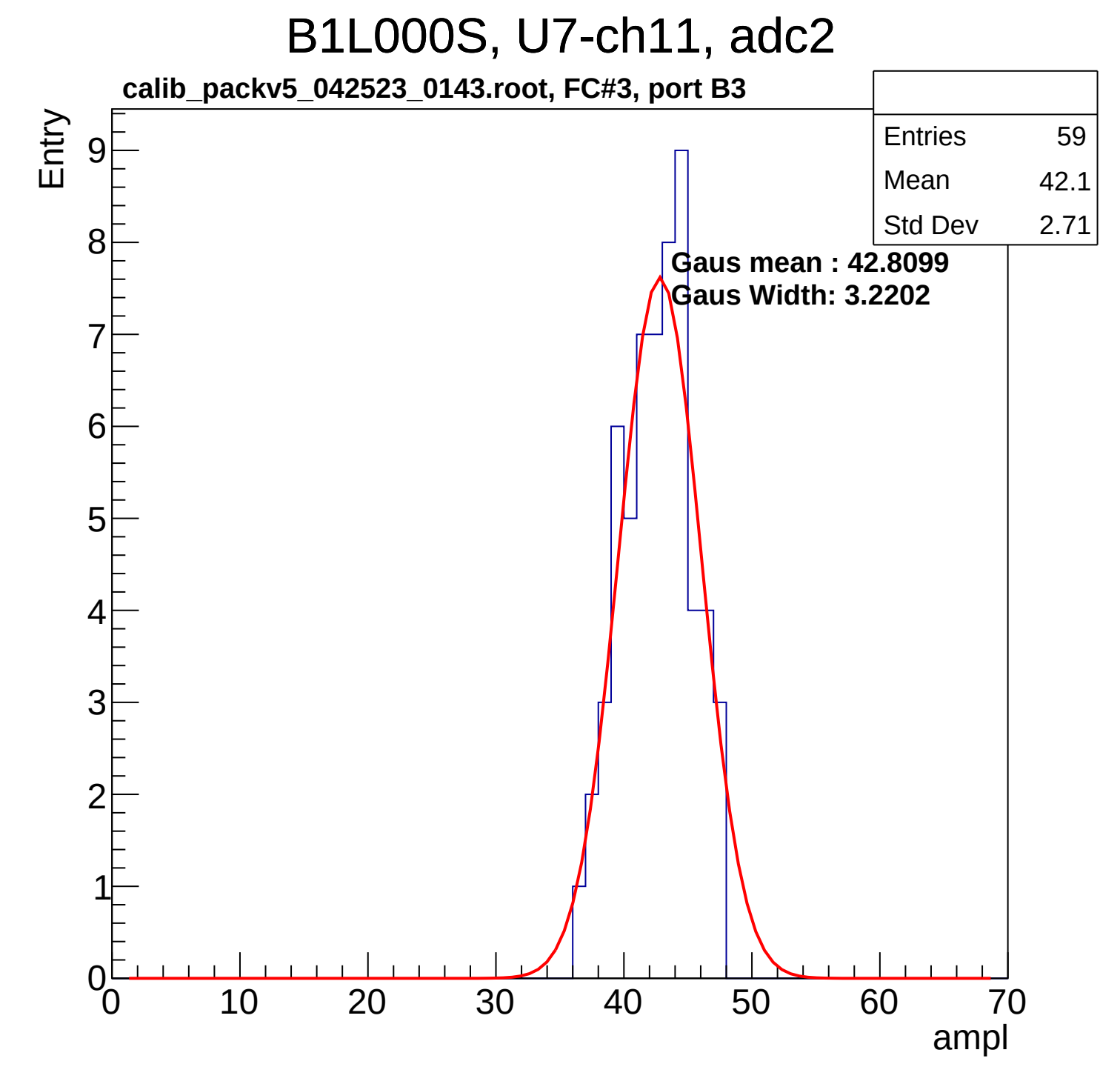
B1L000S, U7-ch11, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 42.1
Std Dev 2.71
Gaus mean : 42.8099
Gaus Width: 3.2202
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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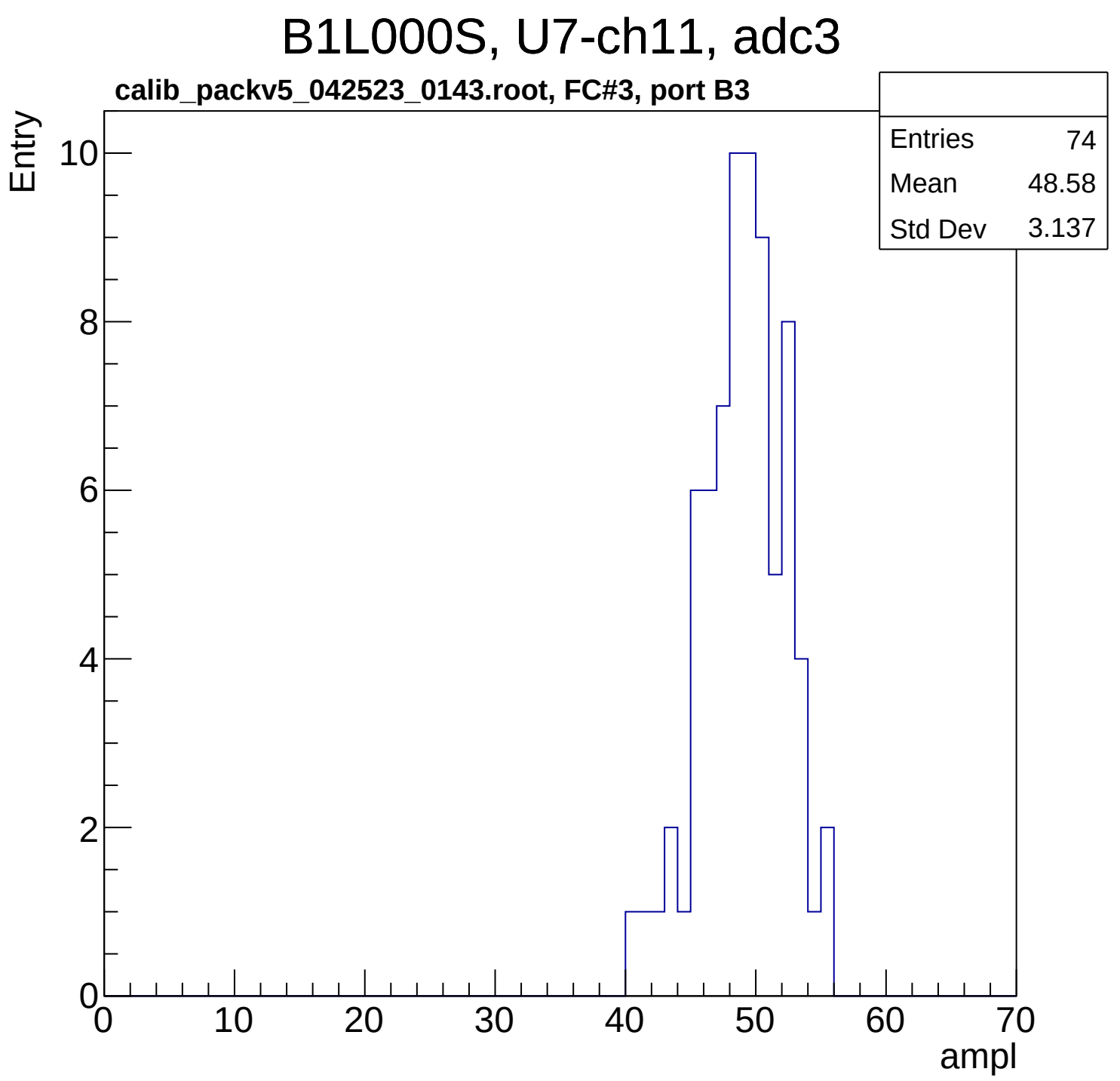
B1L000S, U7-ch11, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 48.58
Std Dev 3.137
Entry
ampl
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4
6
8
10
0
10
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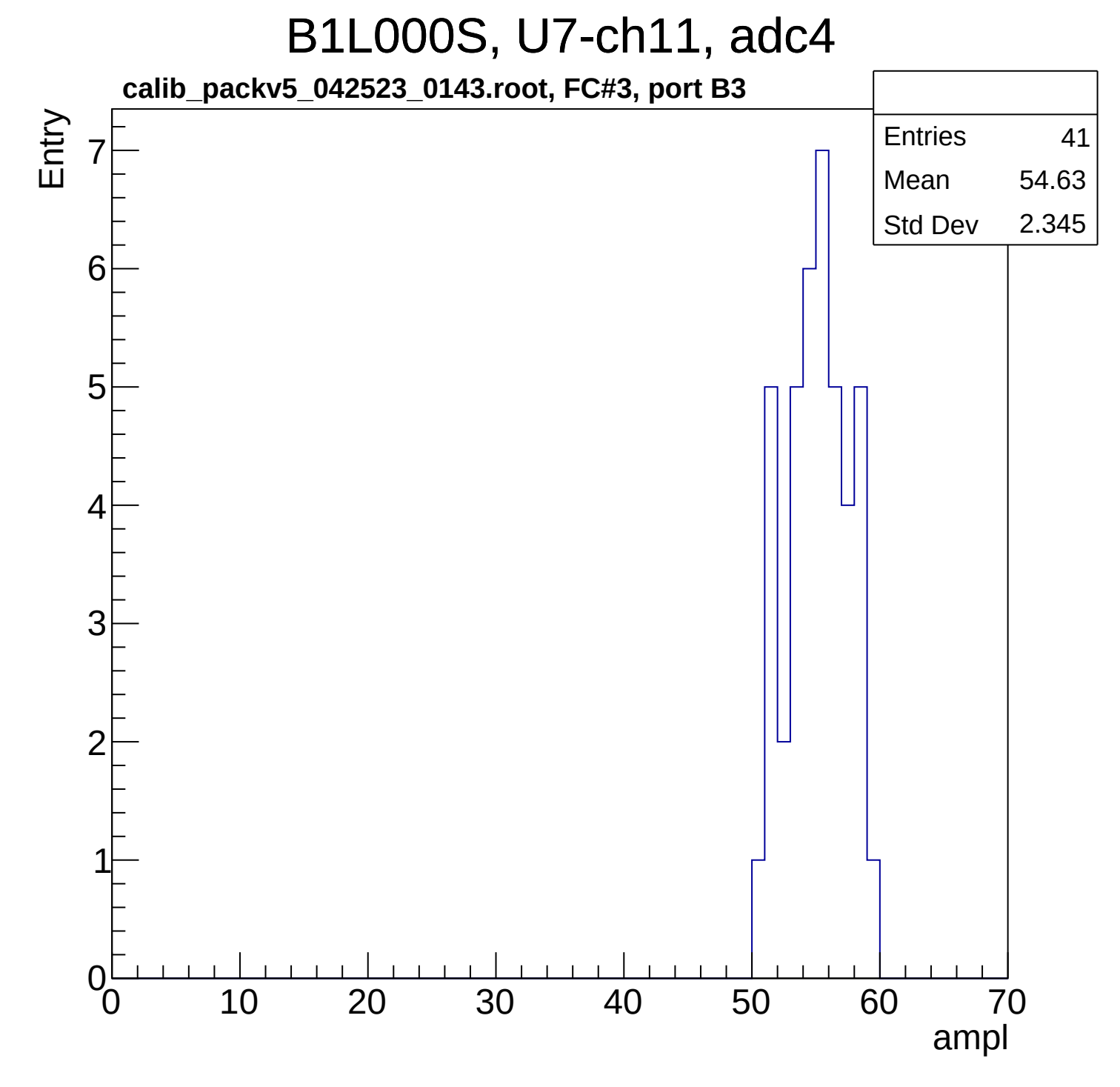
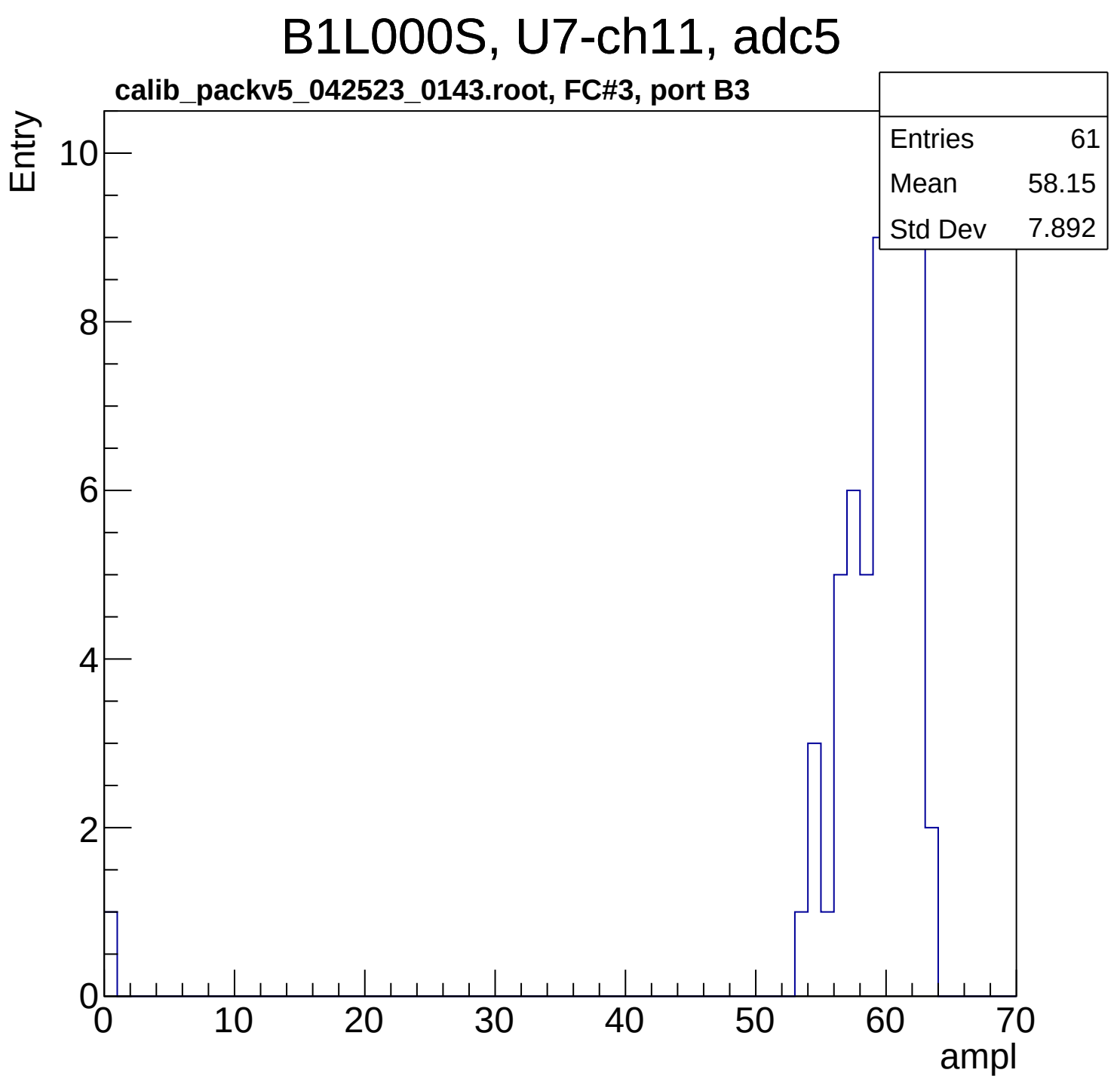
40
50
60
70


B1L000S, U7-ch11, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 54.63
Std Dev 2.345
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch11, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.15
Std Dev 7.892
Entry
0
2
4
6
8
10
0
10
20
30
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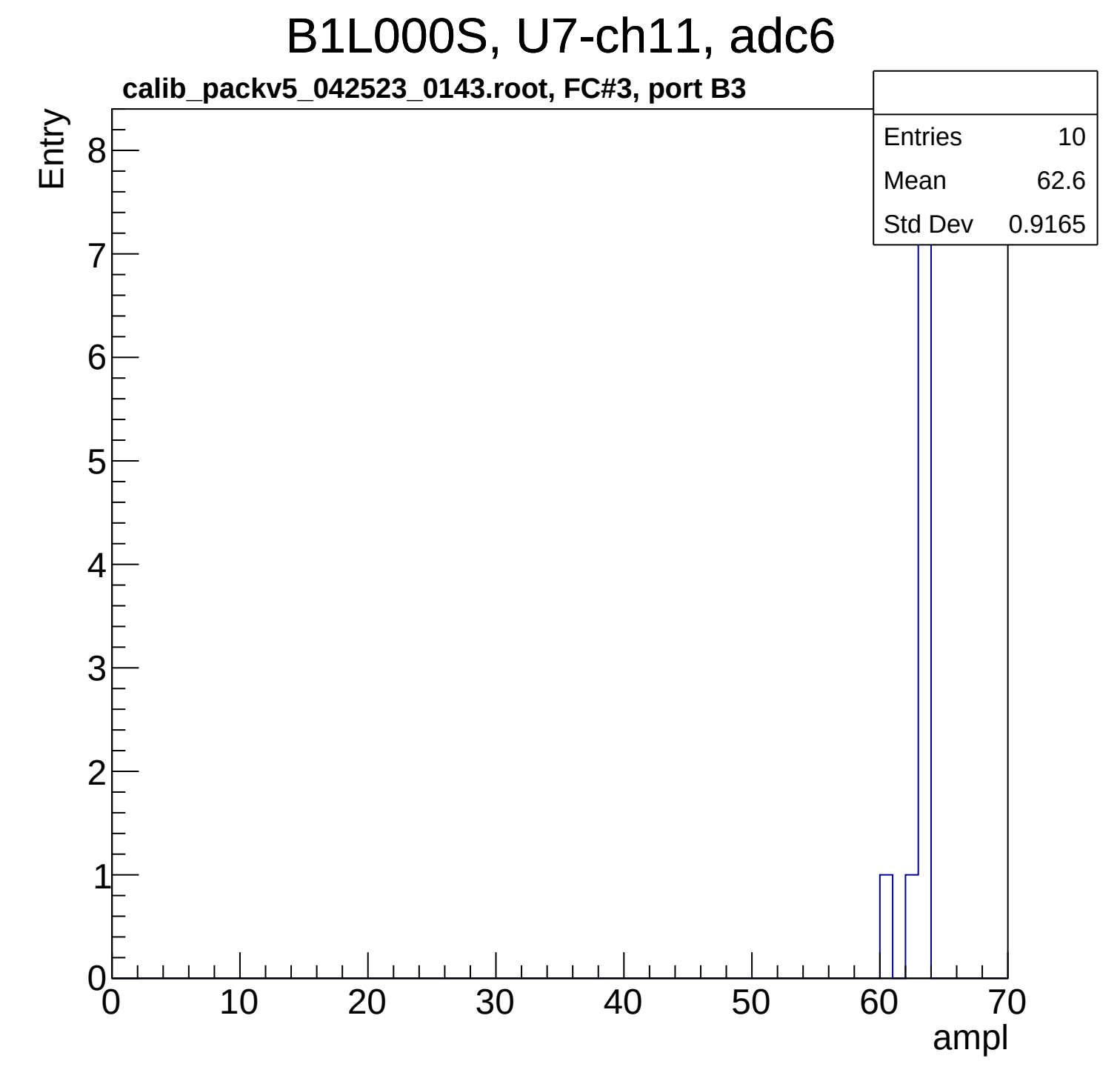
50
60
70
ampl


B1L000S, U7-ch11, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.6
Std Dev 0.9165
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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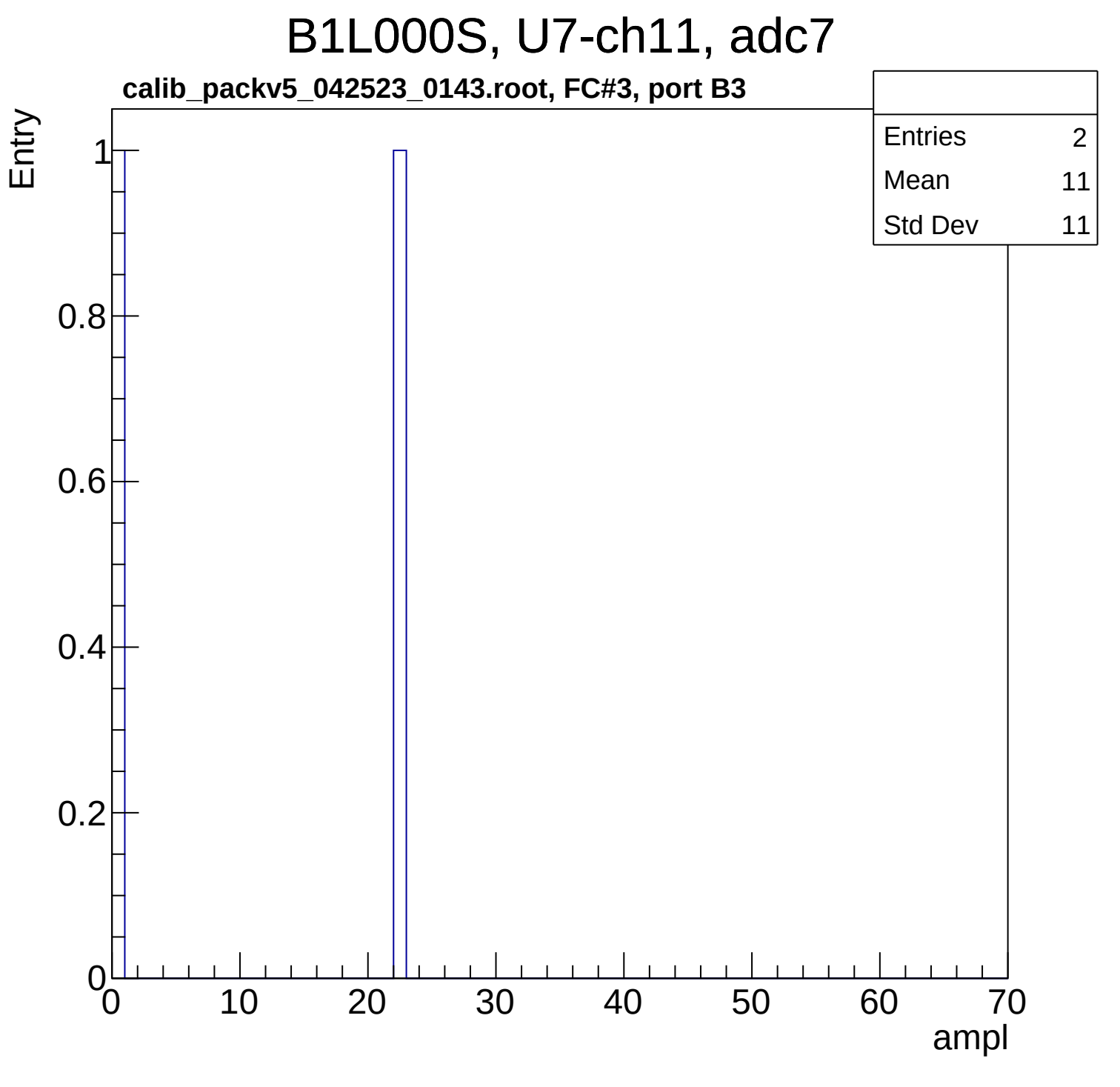
B1L000S, U7-ch11, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11
Std Dev 11
Entry
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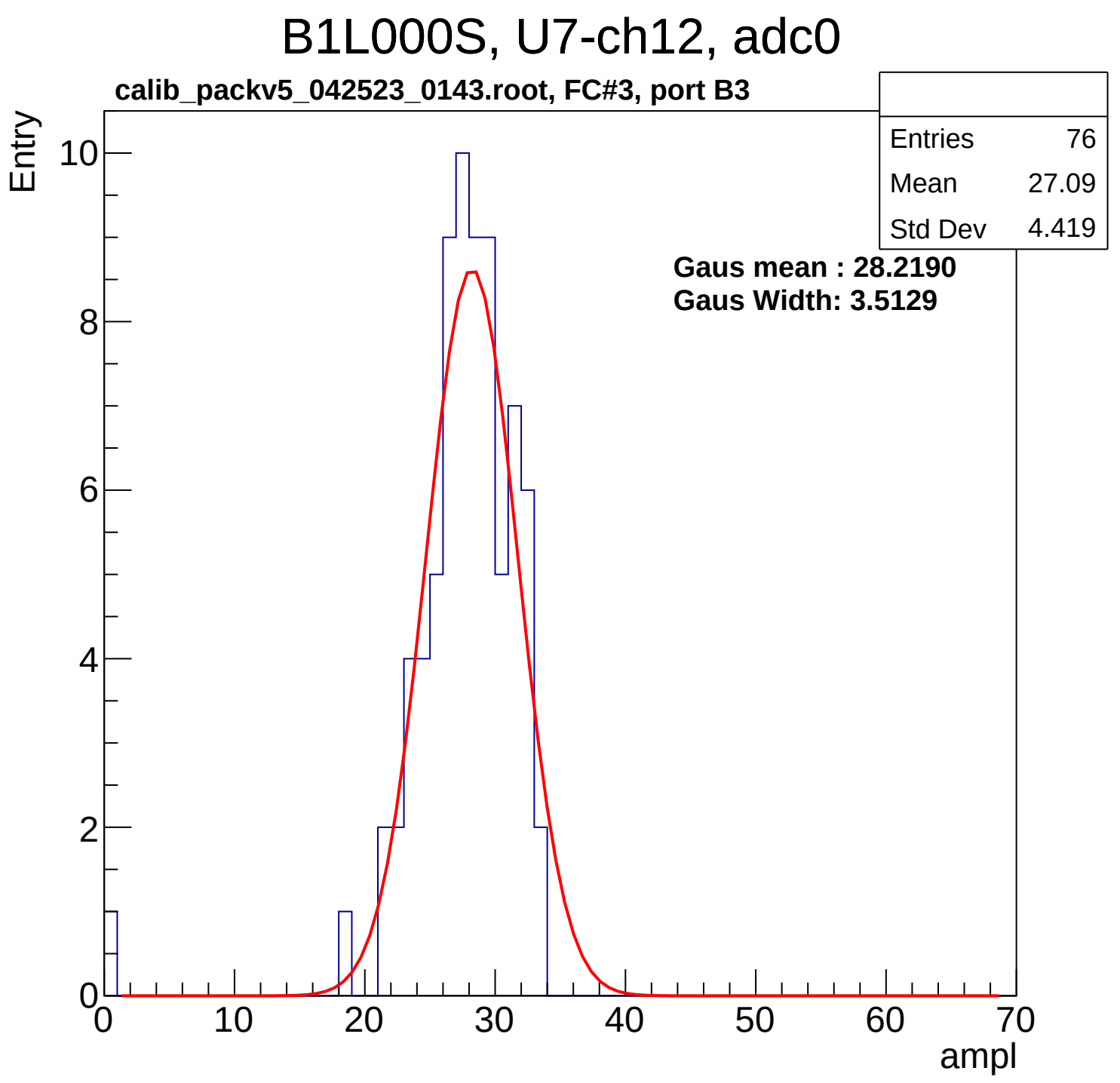
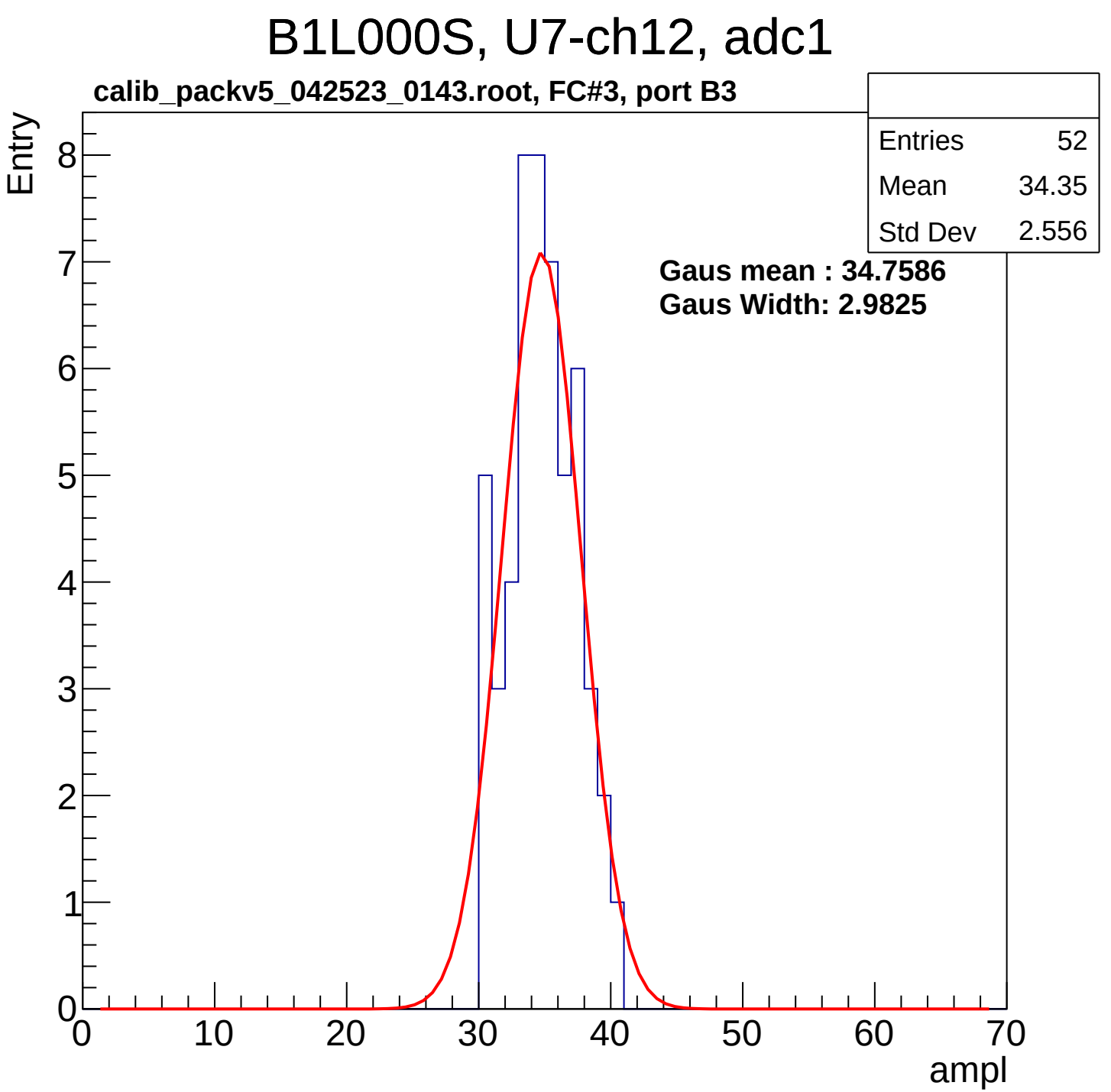
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch12, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 27.09
Std Dev 4.419
Gaus mean : 28.2190
Gaus Width: 3.5129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch12, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 34.35
Std Dev 2.556
Gaus mean : 34.7586
Gaus Width: 2.9825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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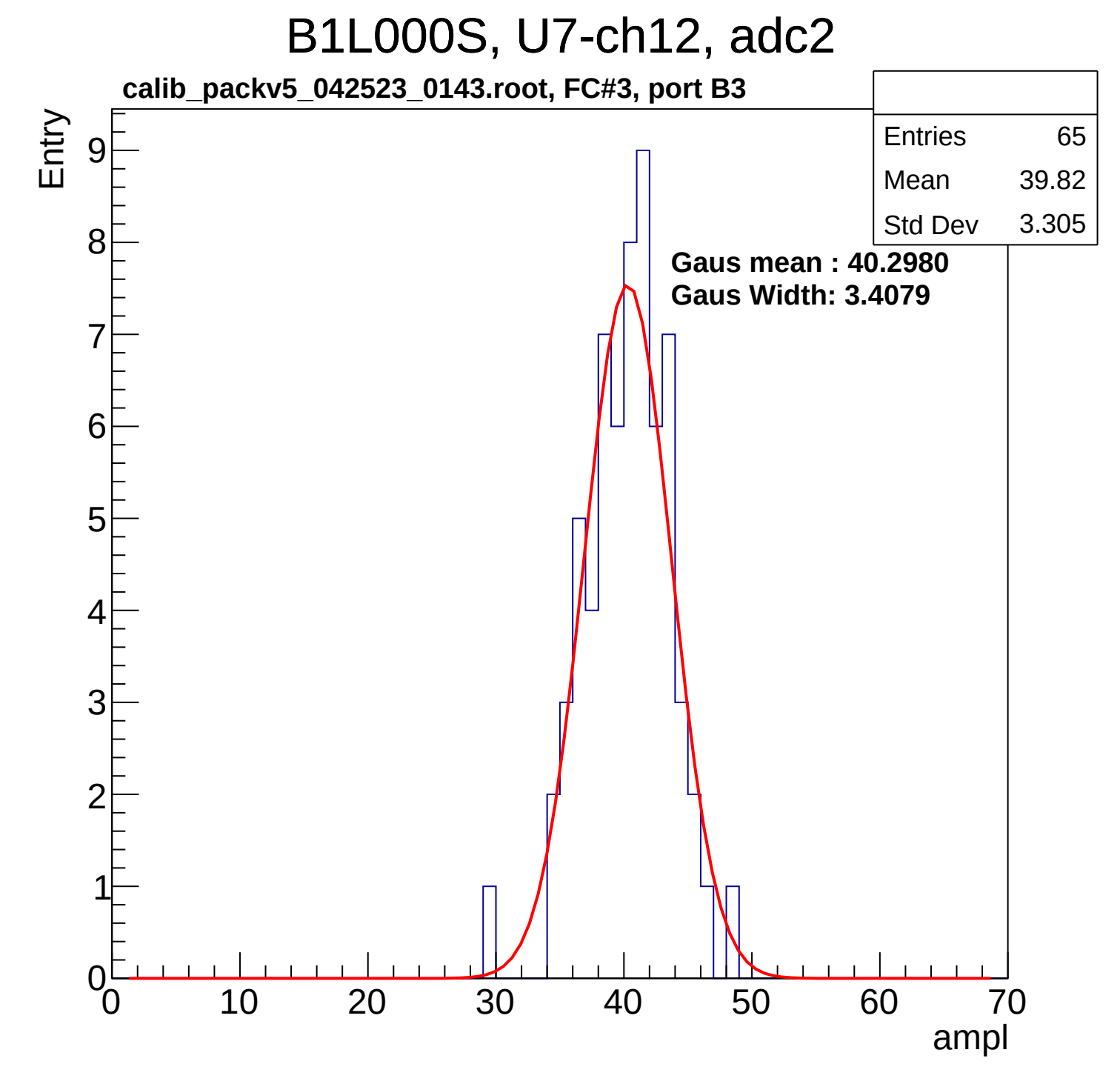
B1L000S, U7-ch12, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 39.82
Std Dev 3.305
Gaus mean : 40.2980
Gaus Width: 3.4079
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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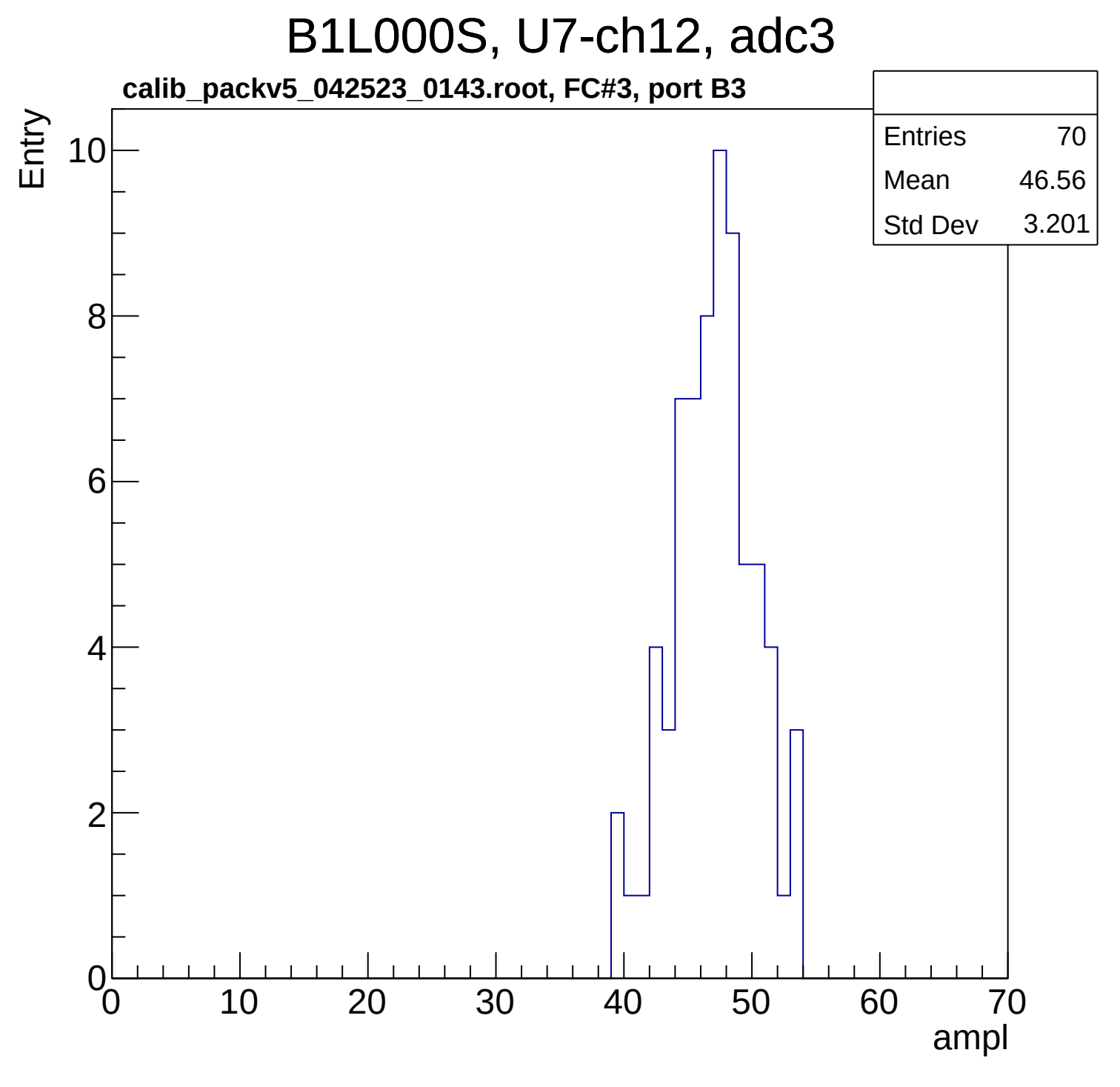
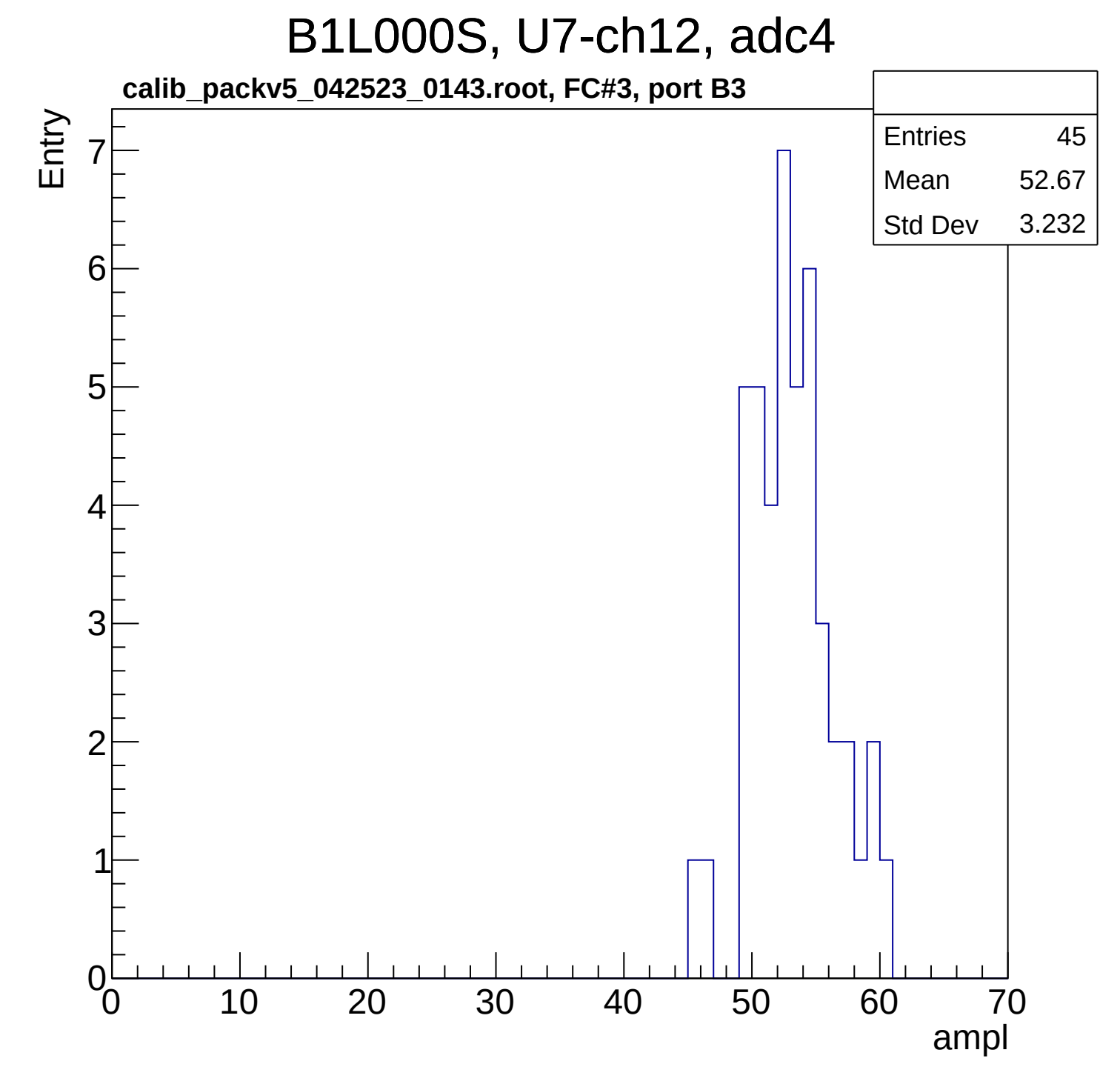

B1L000S, U7-ch12, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 46.56
Std Dev 3.201
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch12, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 52.67
Std Dev 3.232
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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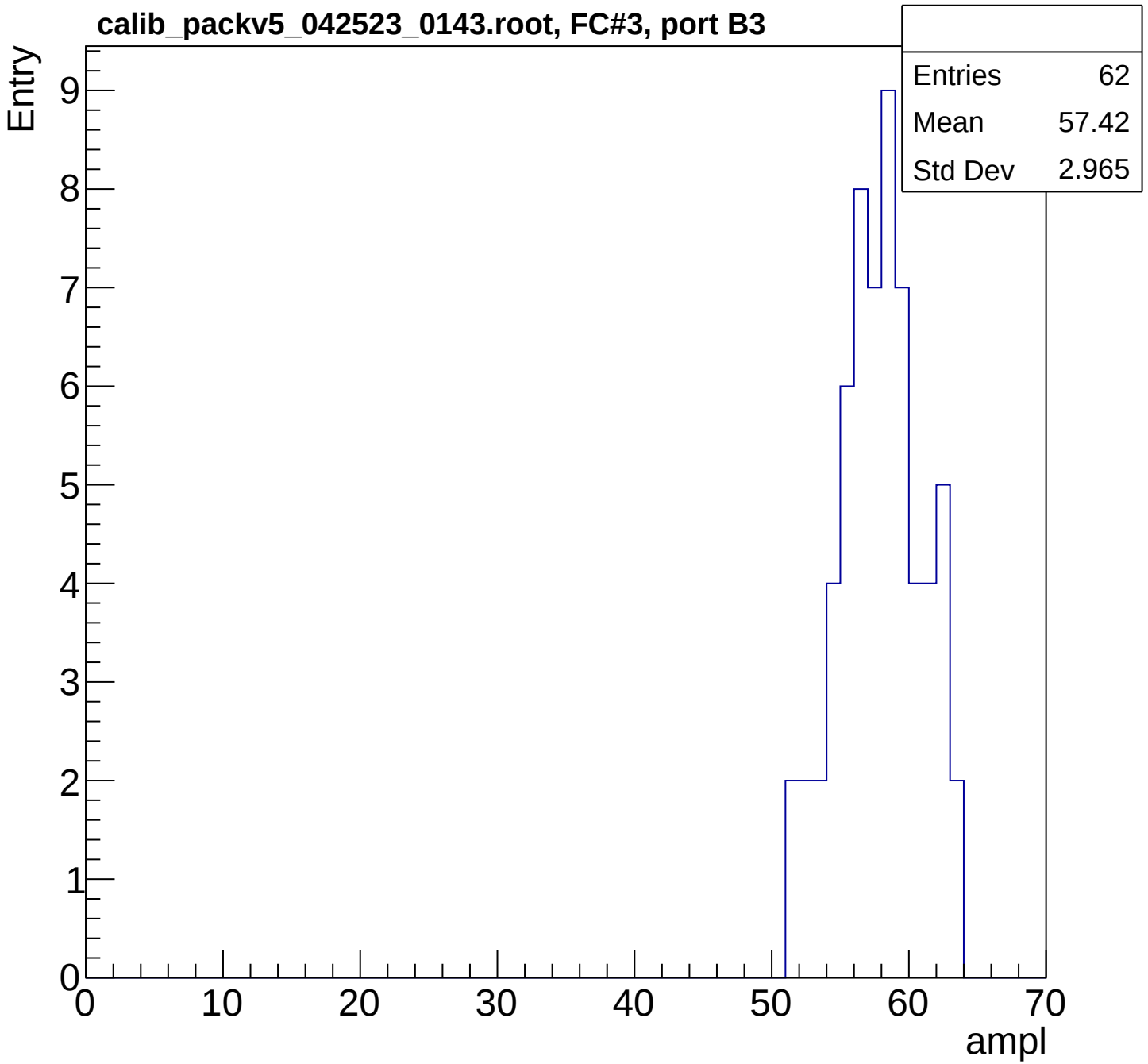
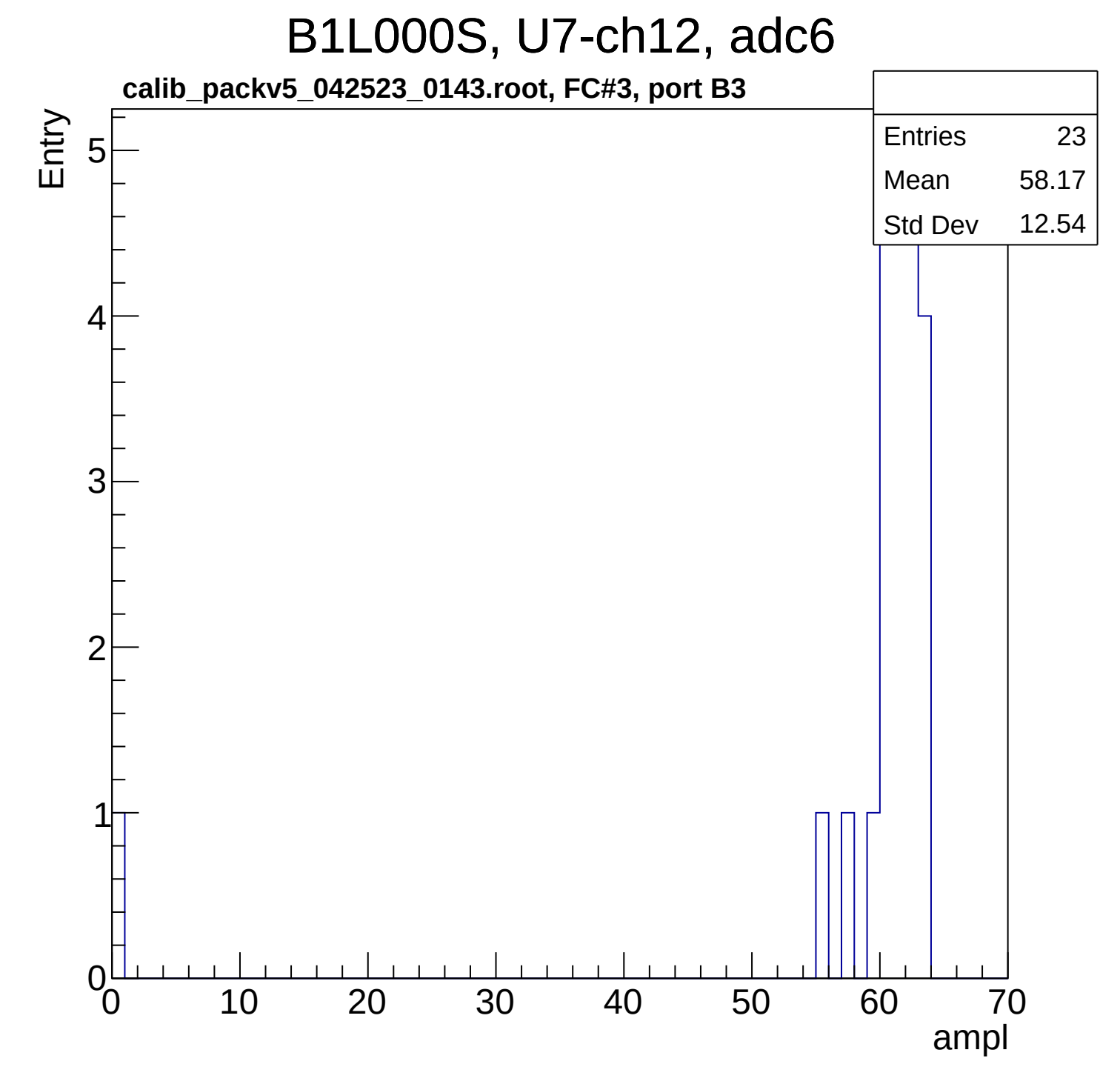
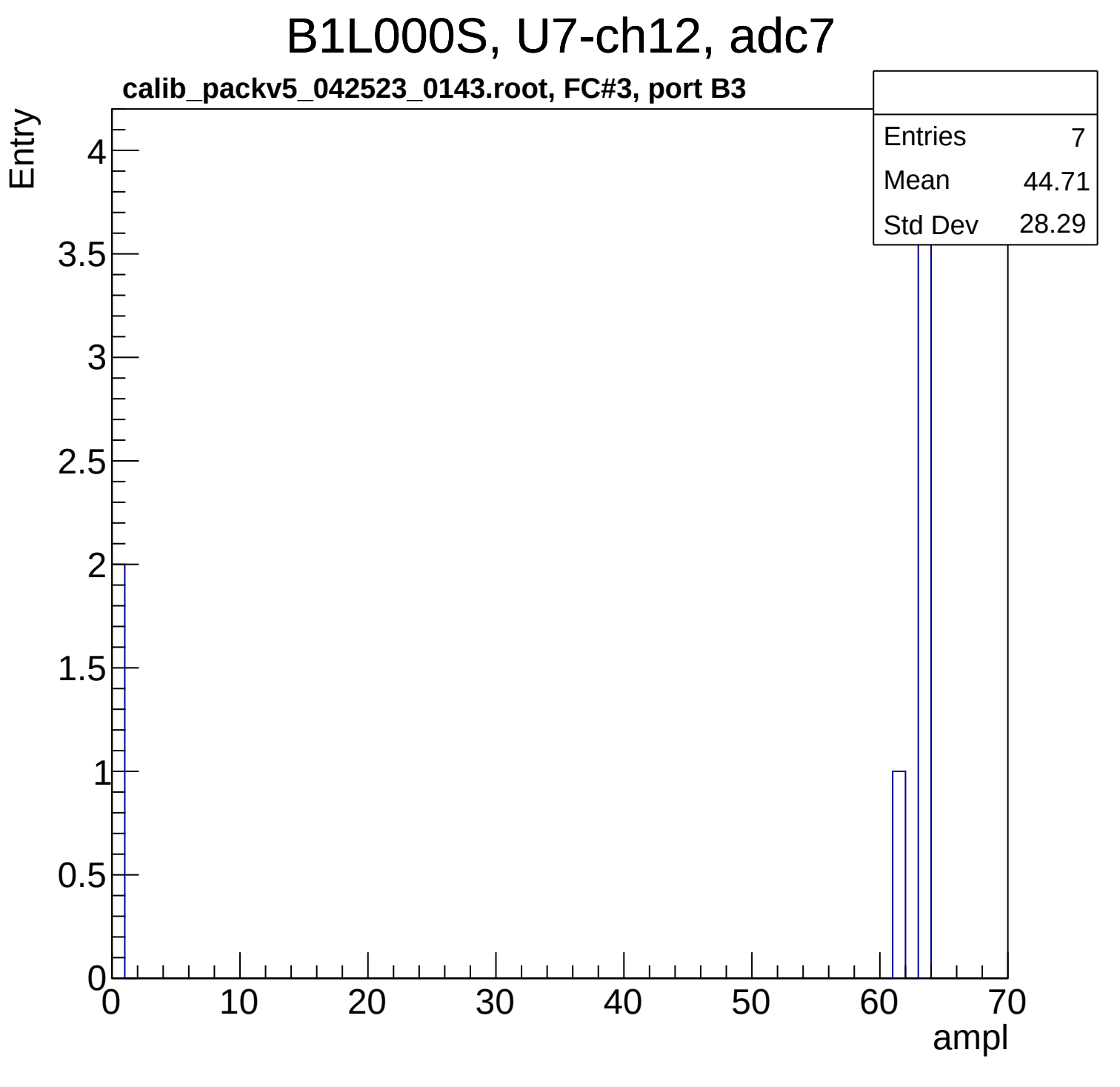

B1L000S, U7-ch12, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.42
Std Dev 2.965
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch12, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 23
Mean 58.17
Std Dev 12.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch12, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 44.71
Std Dev 28.29
Entry
ampl
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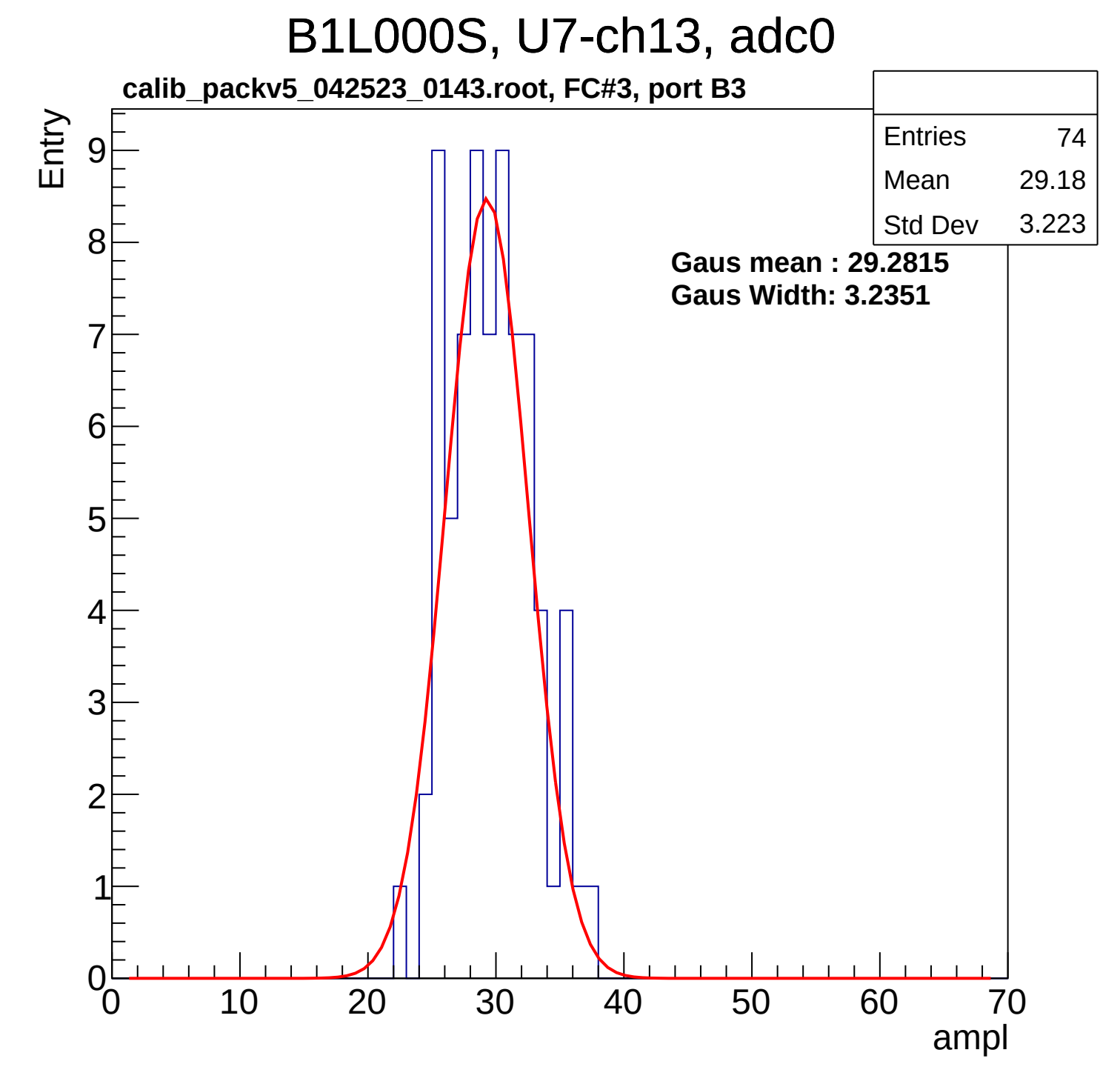
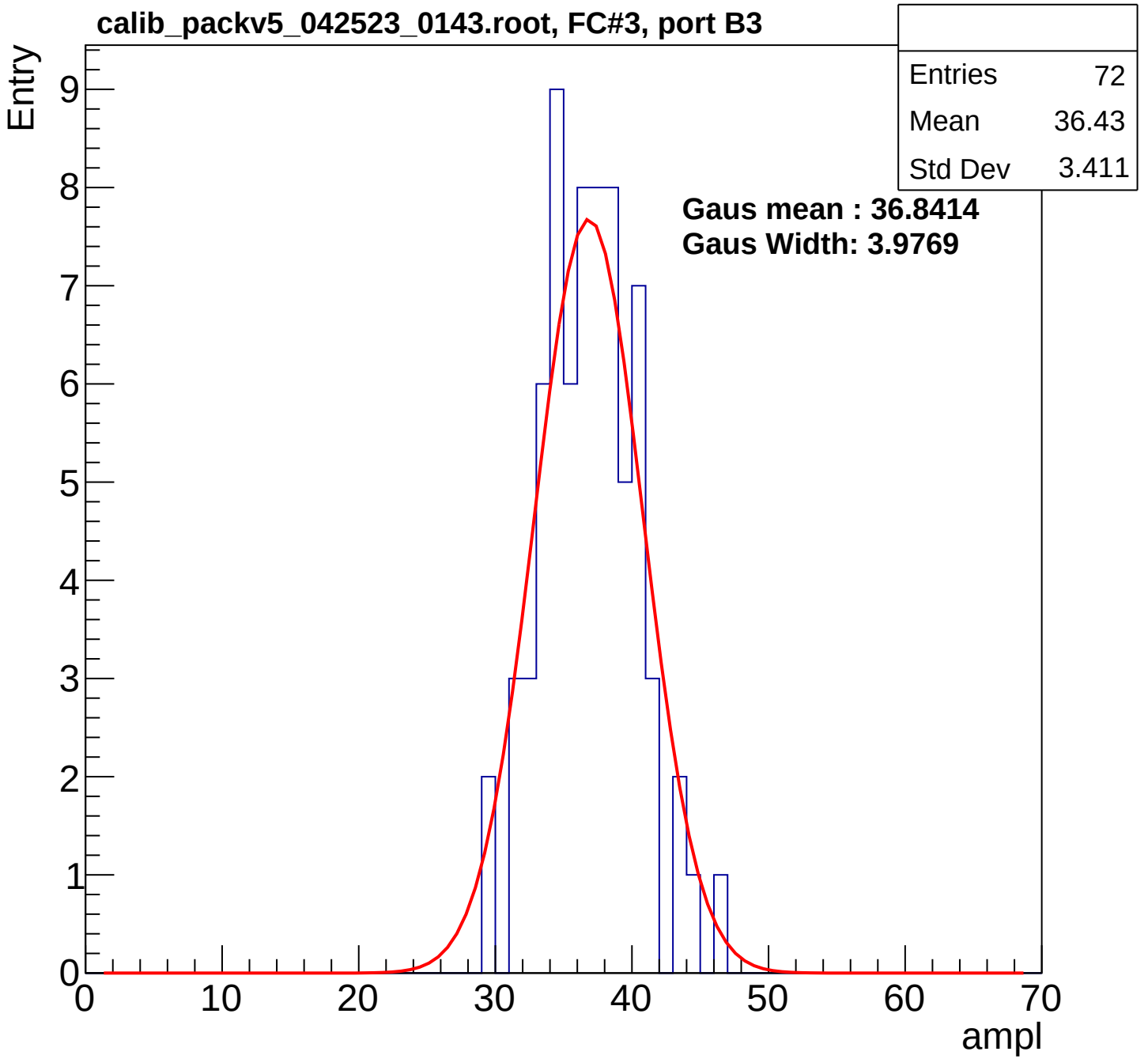
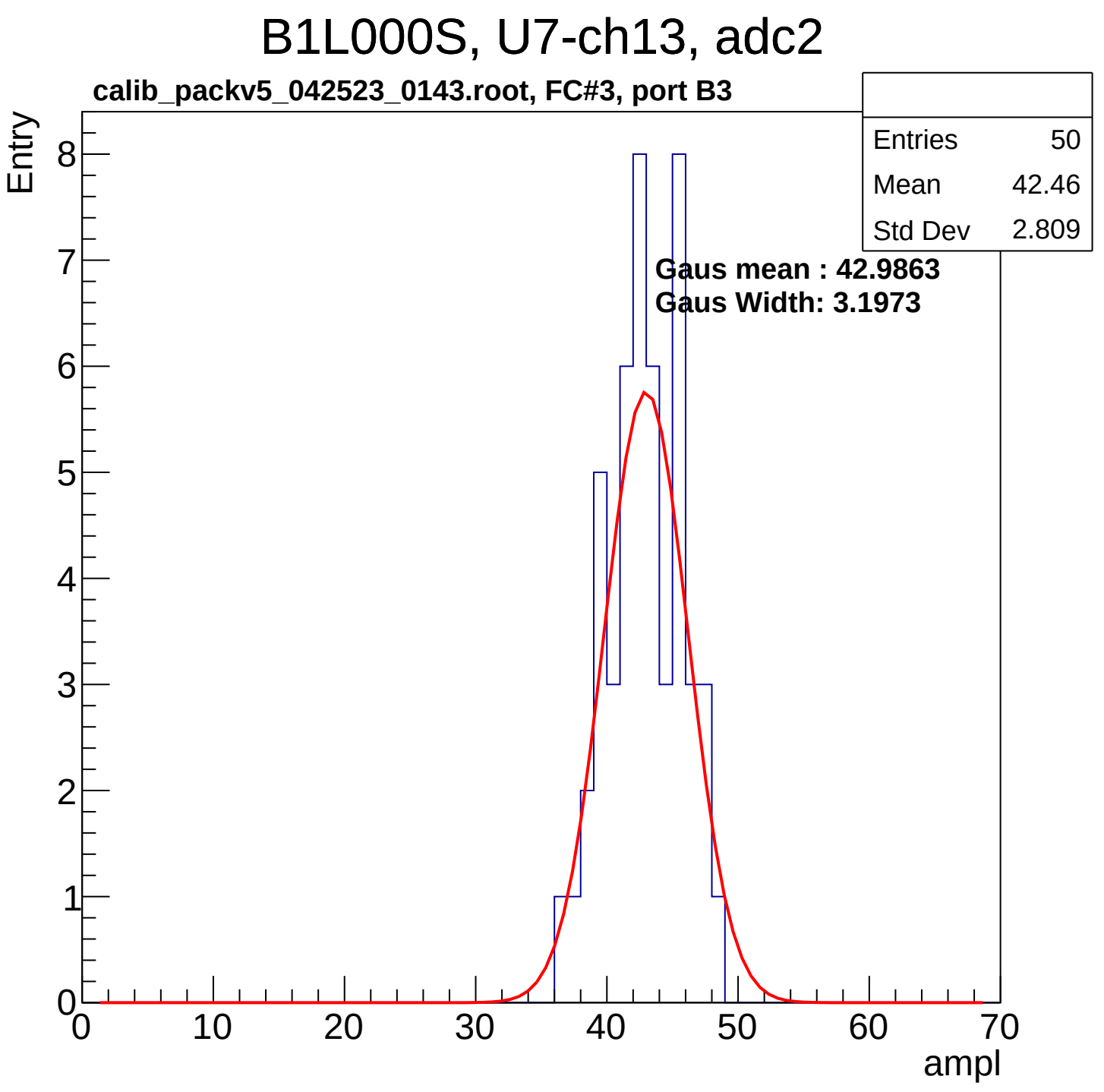
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.18
Std Dev 3.223
Gaus mean : 29.2815
Gaus Width: 3.2351
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 36.43
Std Dev 3.411
Gaus mean : 36.8414
Gaus Width: 3.9769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 42.46
Std Dev 2.809
Gaus mean : 42.9863
Gaus Width: 3.1973
Entry
ampl
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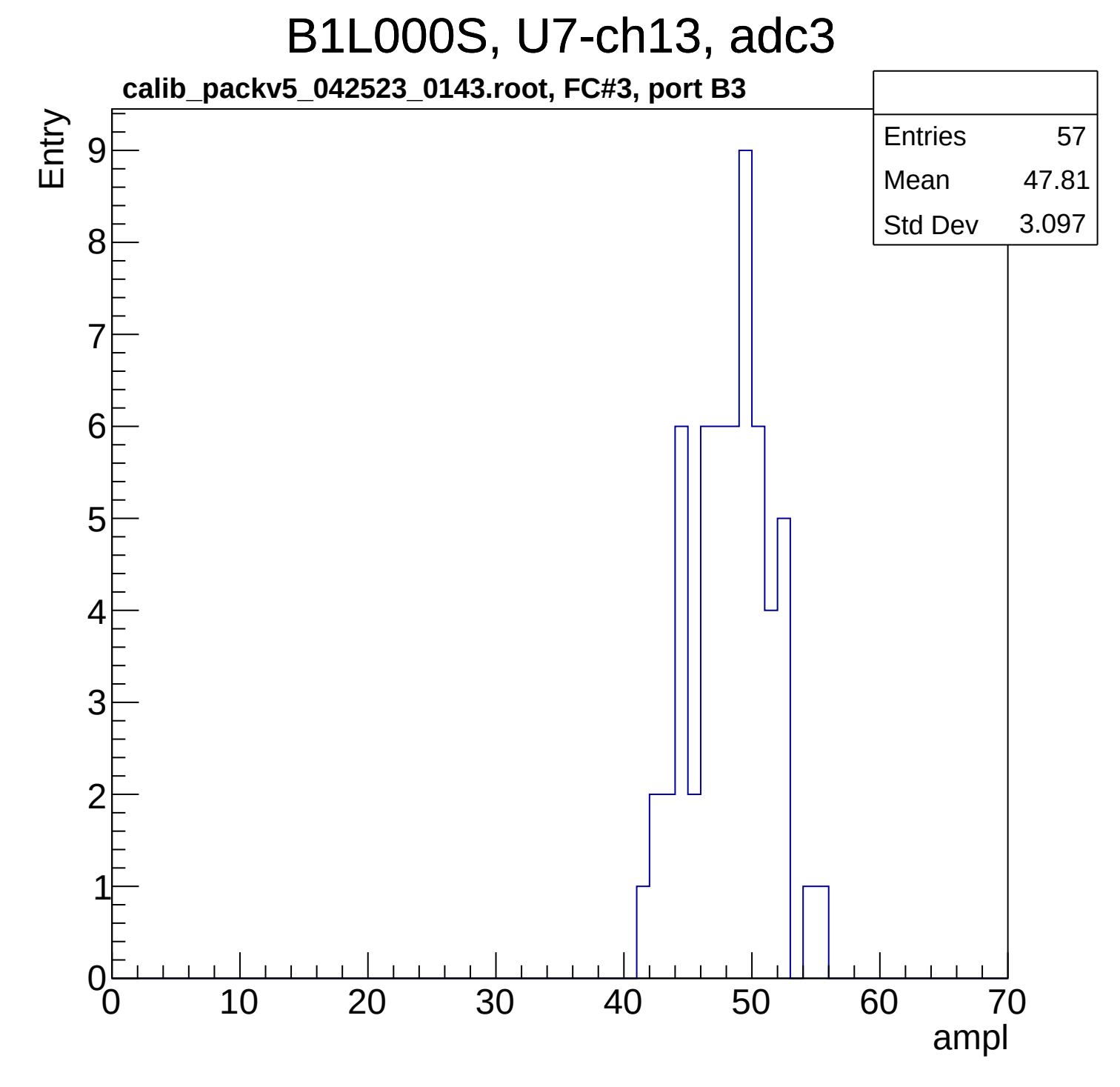
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.81
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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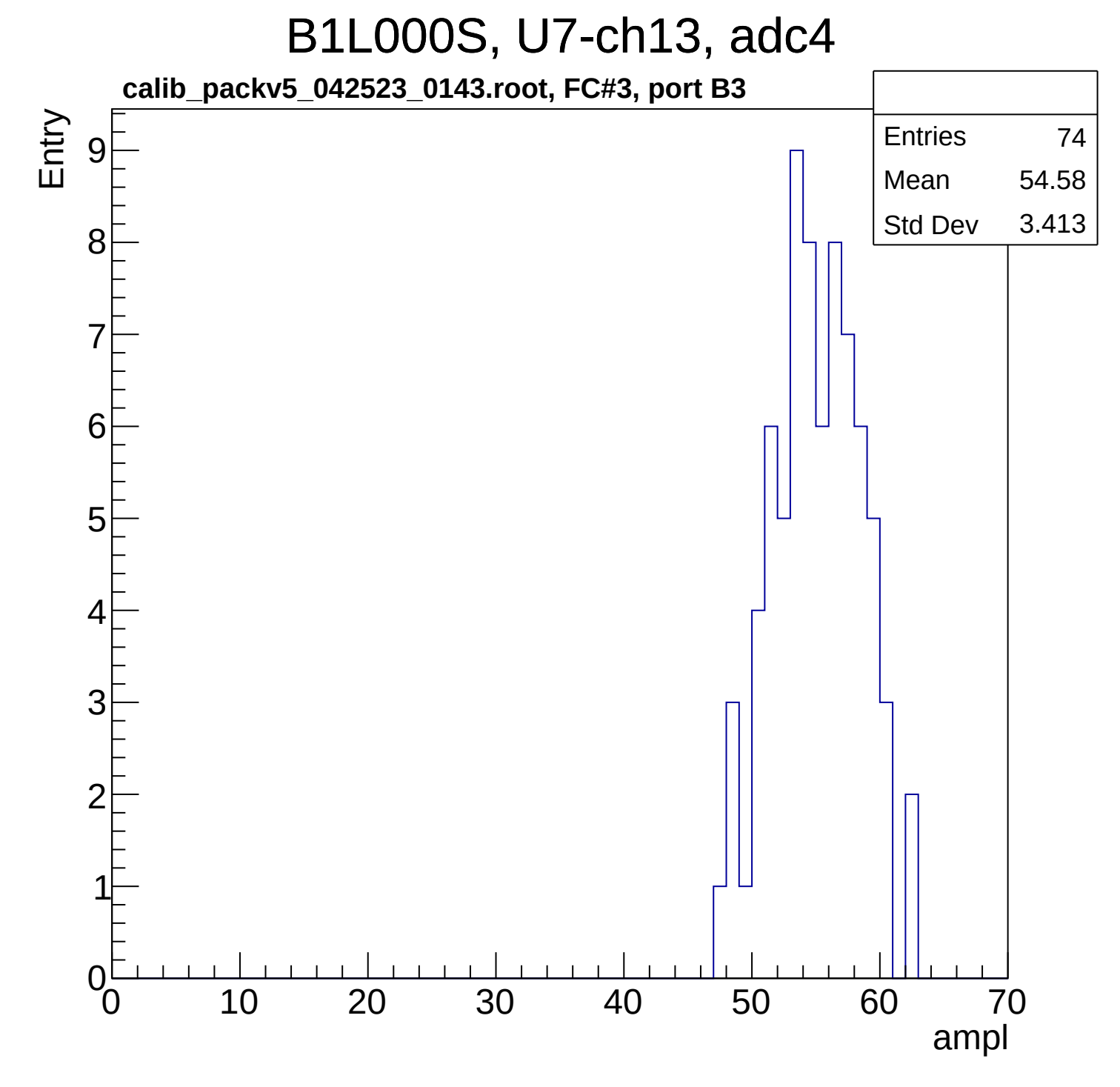
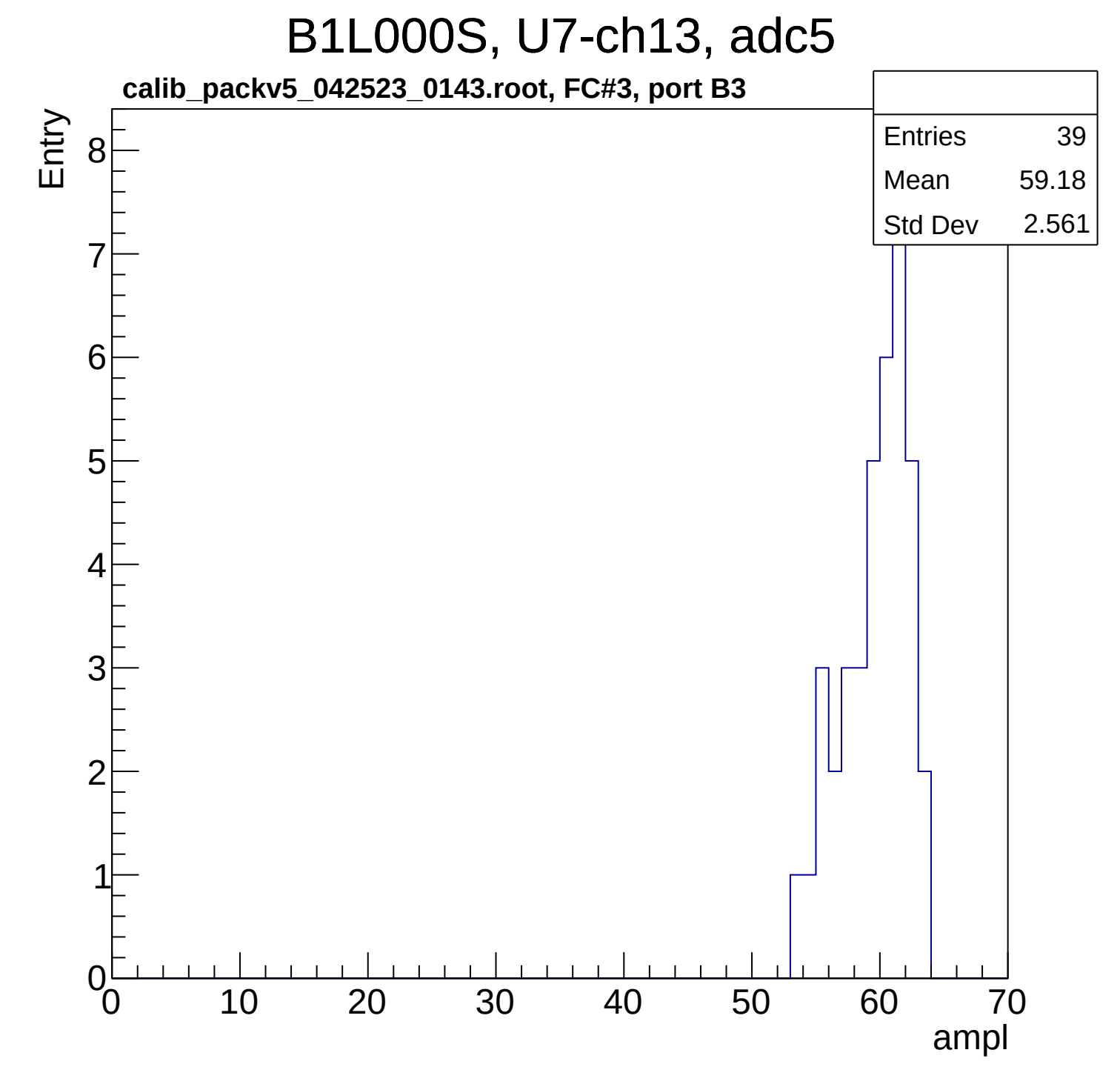

B1L000S, U7-ch13, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 54.58
Std Dev 3.413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 59.18
Std Dev 2.561
Entry
ampl
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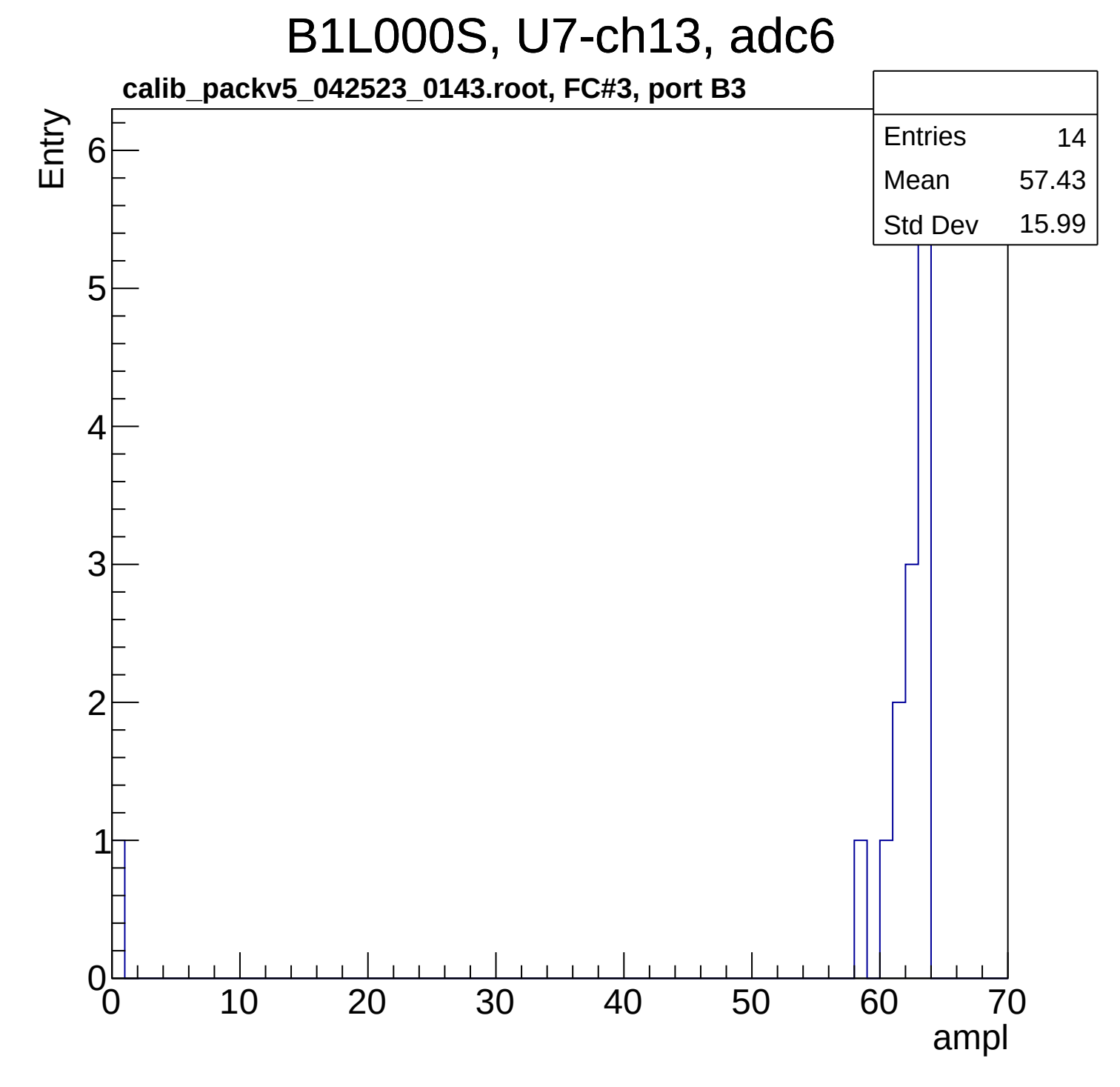
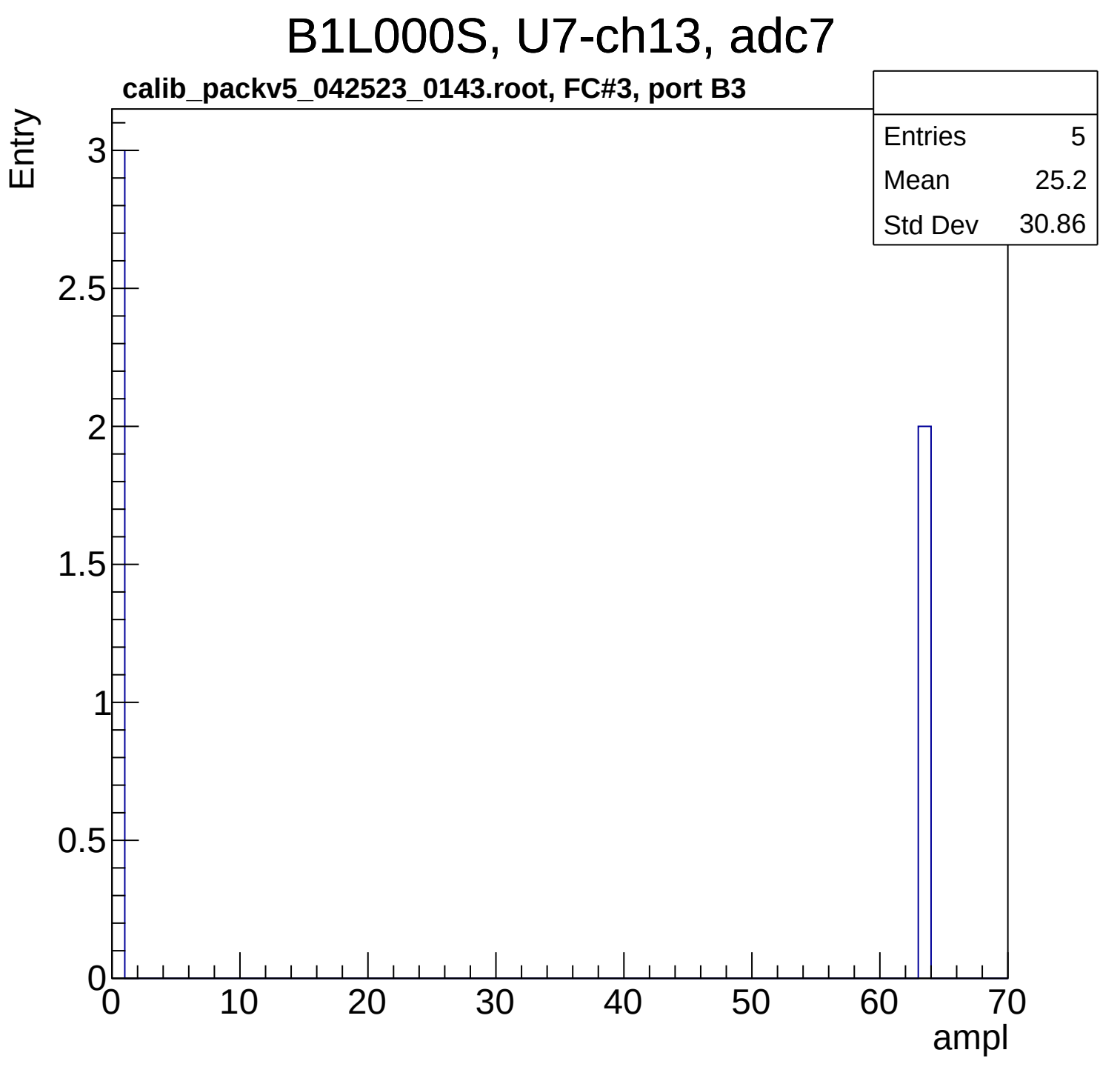
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch13, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.43
Std Dev 15.99
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch13, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
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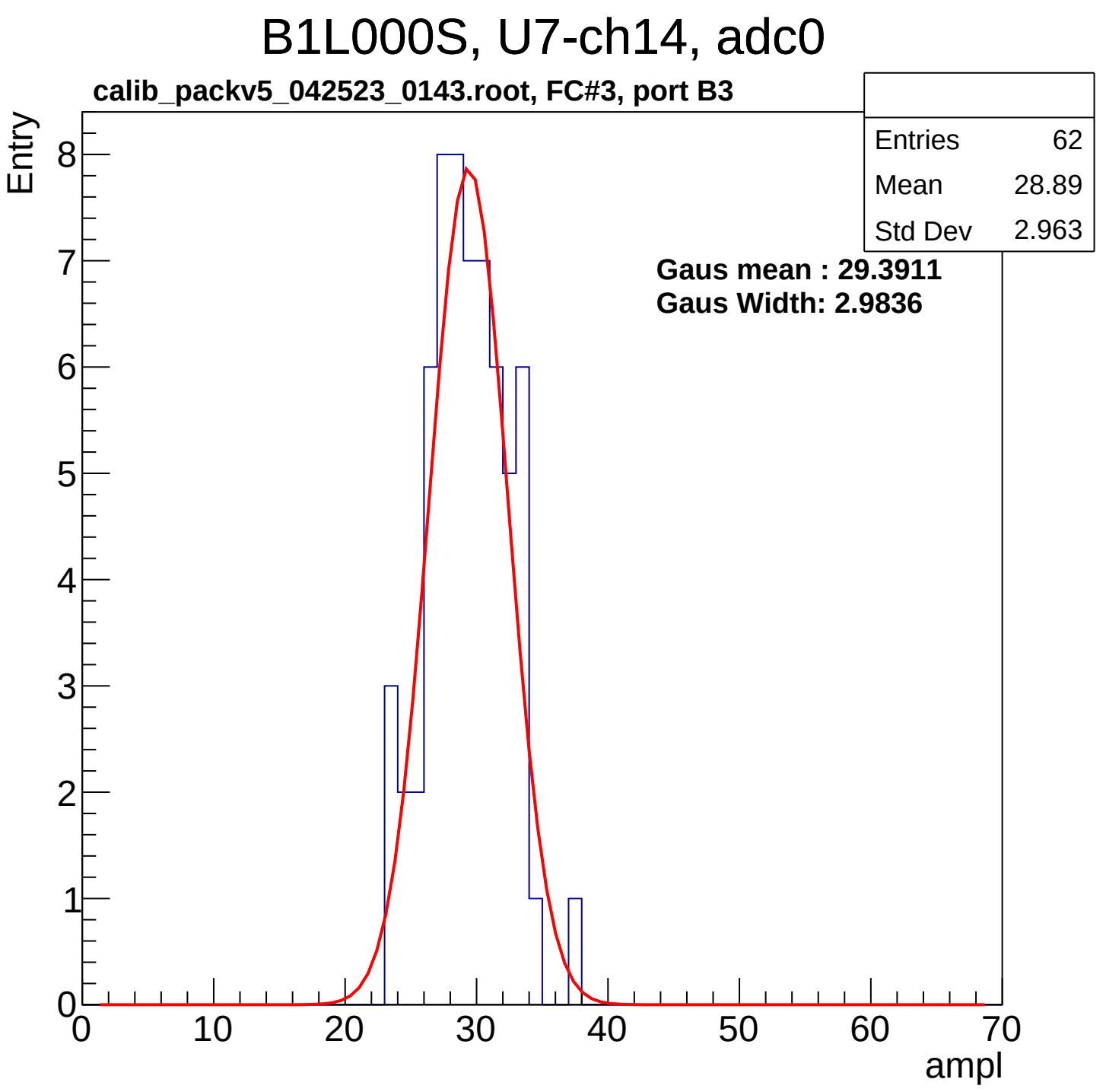
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch14, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.89
Std Dev 2.963
Gaus mean : 29.3911
Gaus Width: 2.9836
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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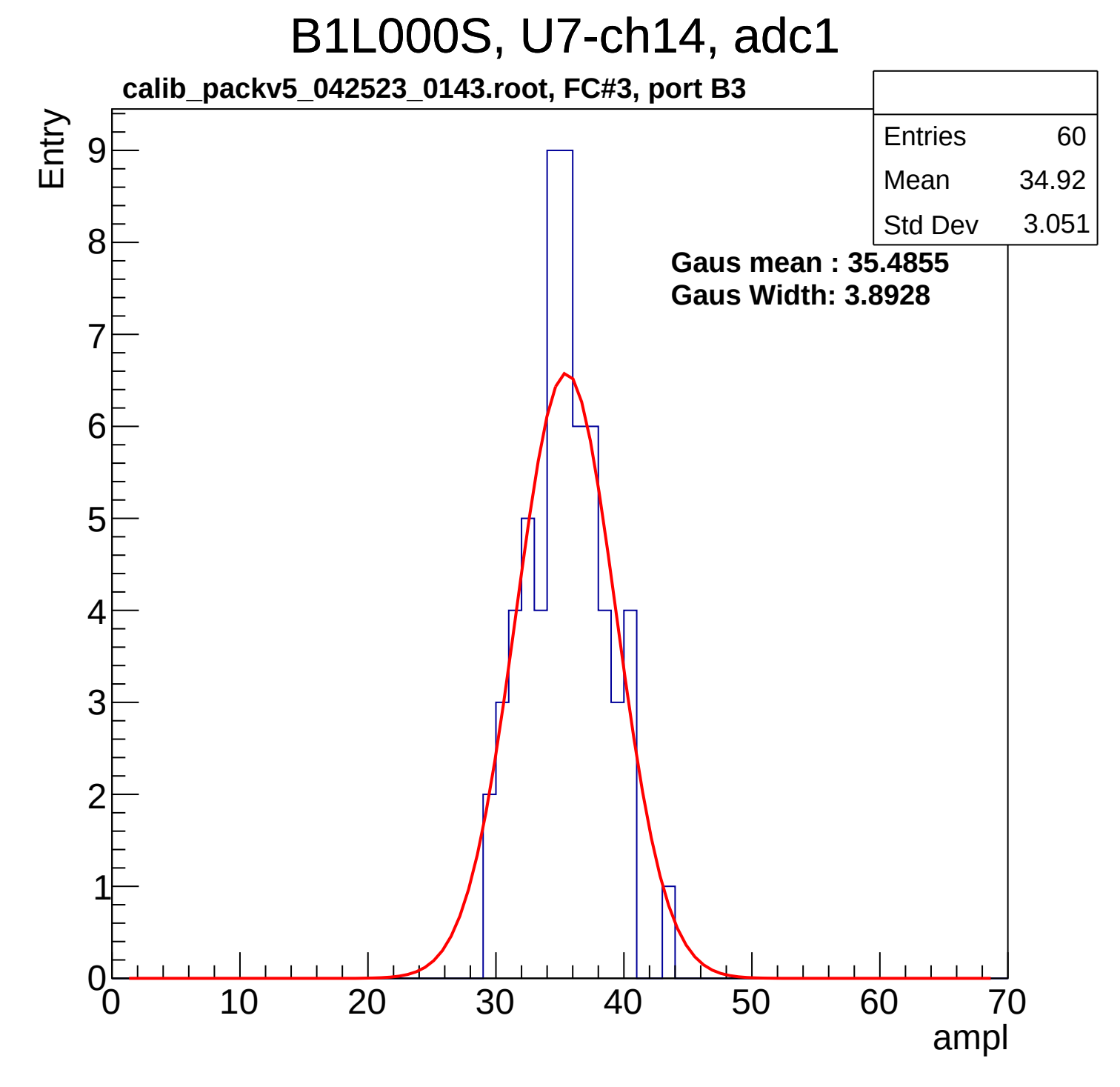
60
70


B1L000S, U7-ch14, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 34.92
Std Dev 3.051
Gaus mean : 35.4855
Gaus Width: 3.8928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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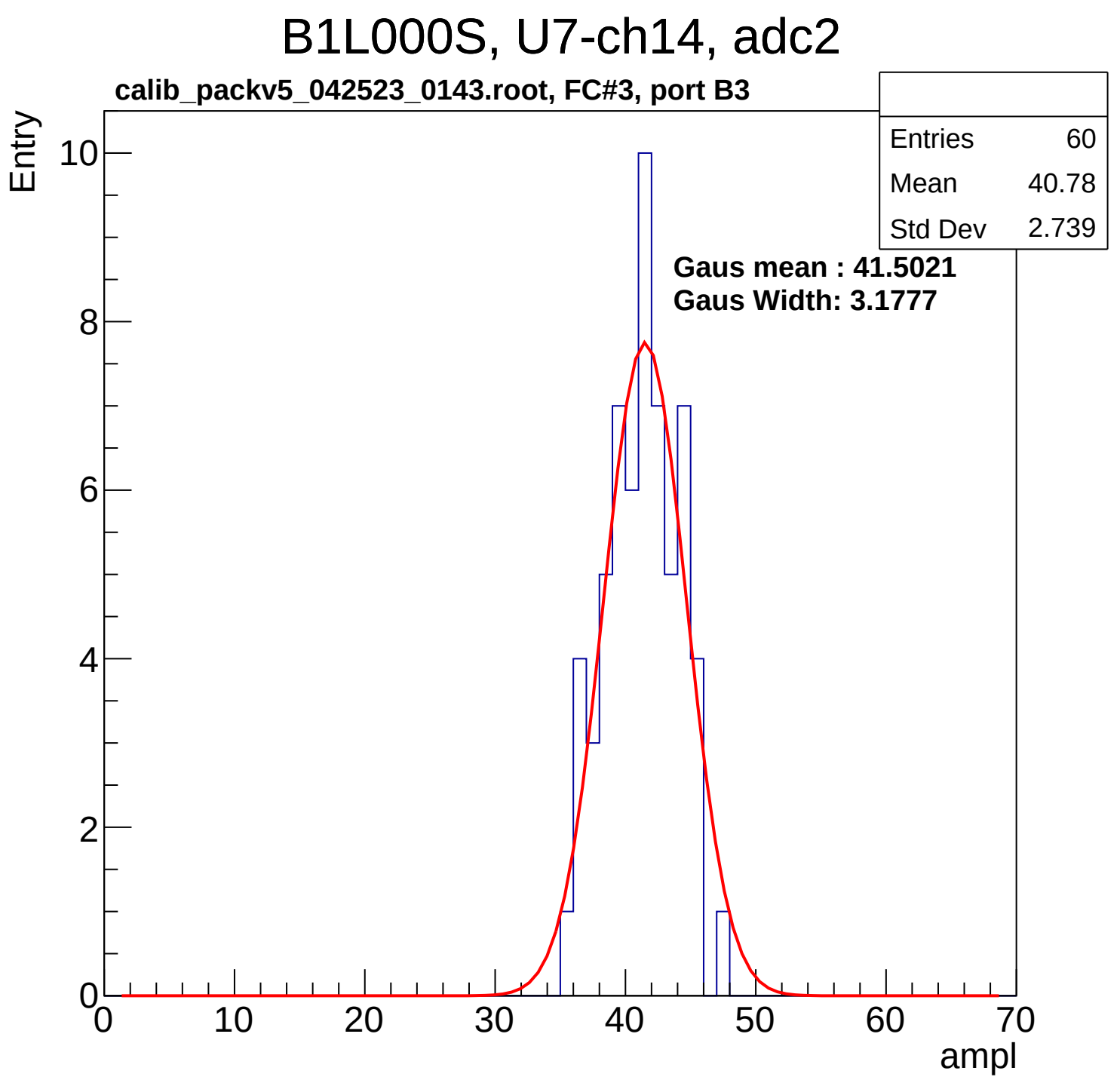
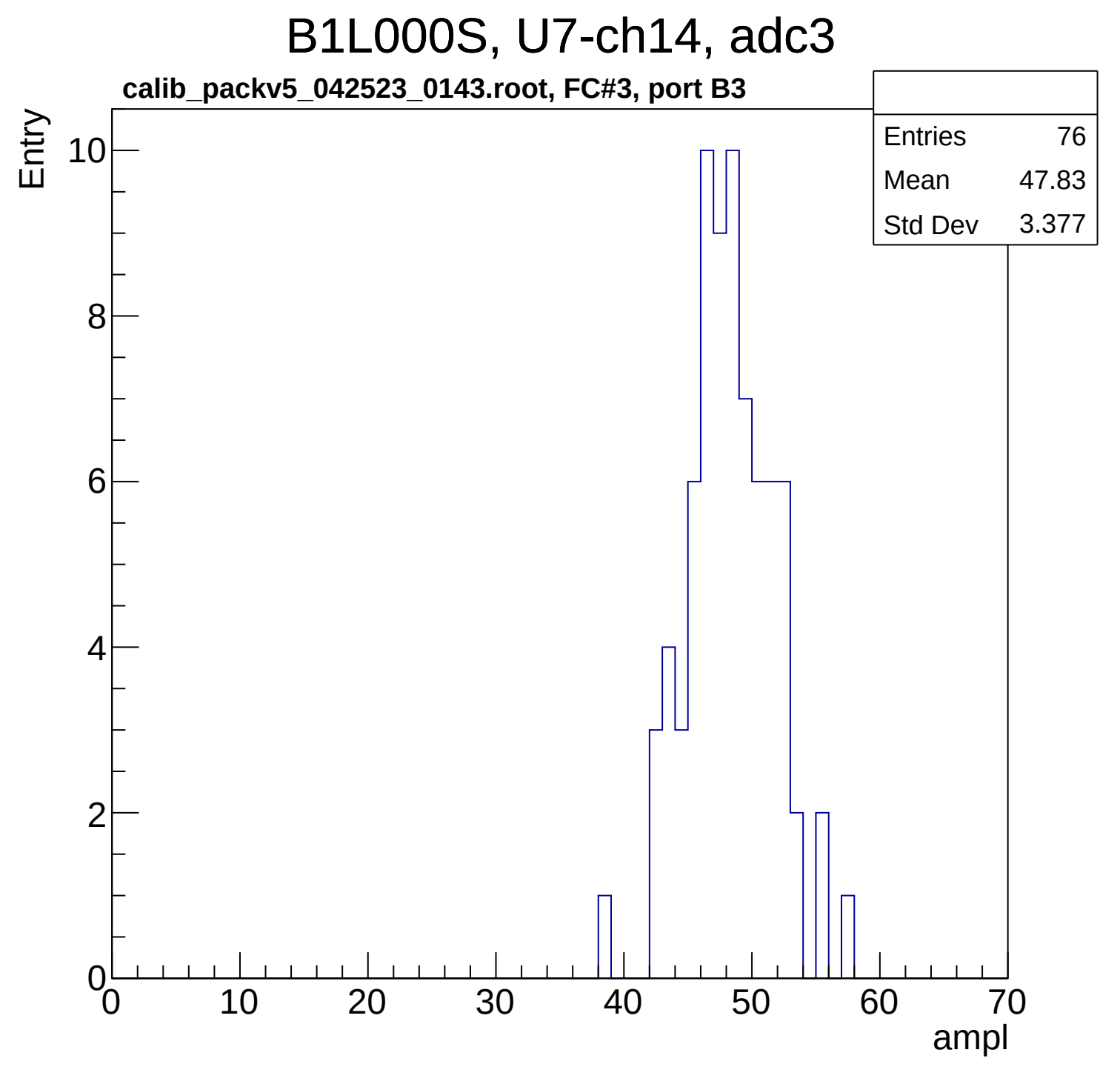

B1L000S, U7-ch14, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 40.78
Std Dev 2.739
Gaus mean : 41.5021
Gaus Width: 3.1777
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch14, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 47.83
Std Dev 3.377
Entry
ampl
0
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4
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8
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0
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20
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40
50
60
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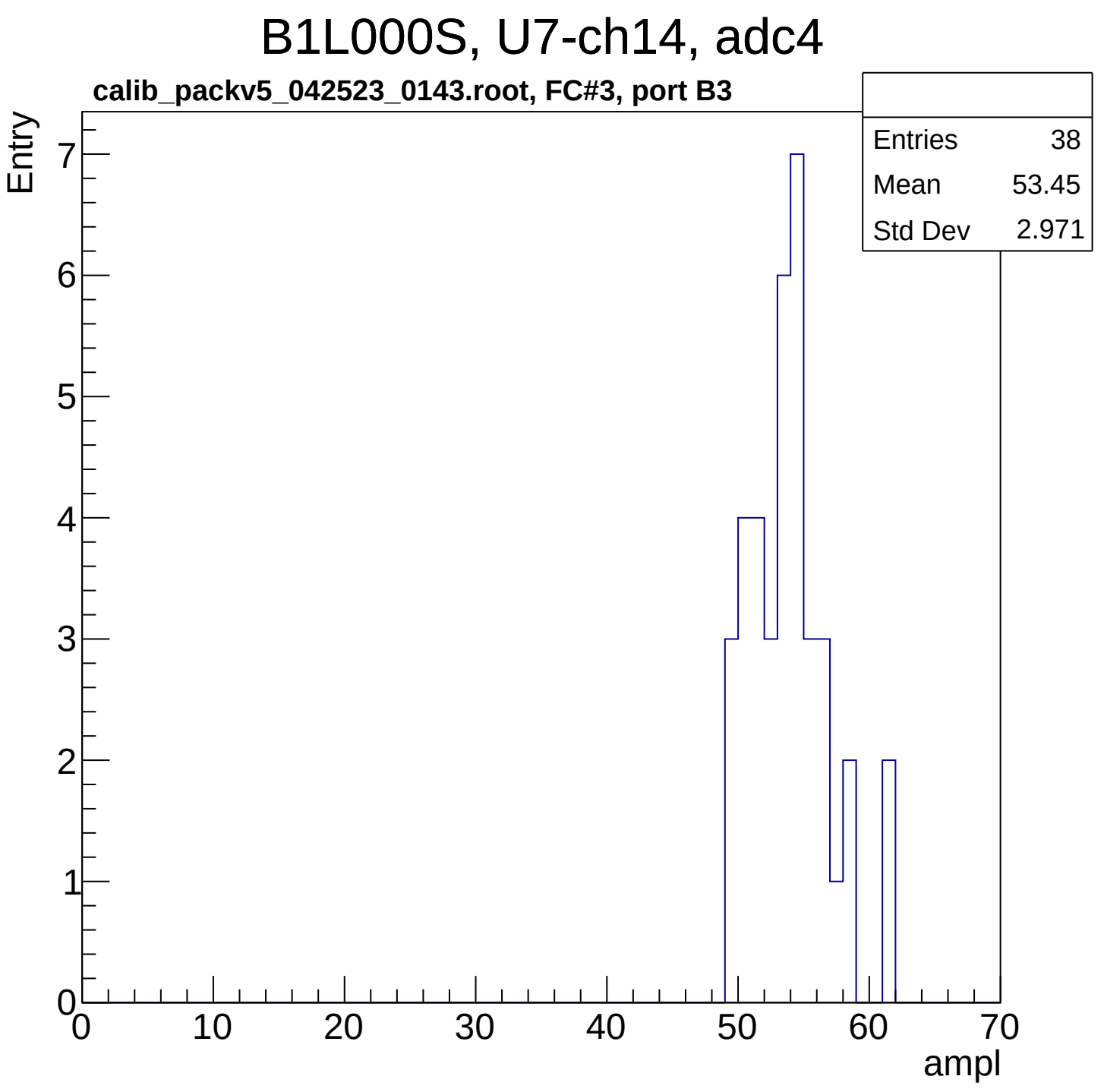
B1L000S, U7-ch14, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 38
Mean 53.45
Std Dev 2.971
Entry
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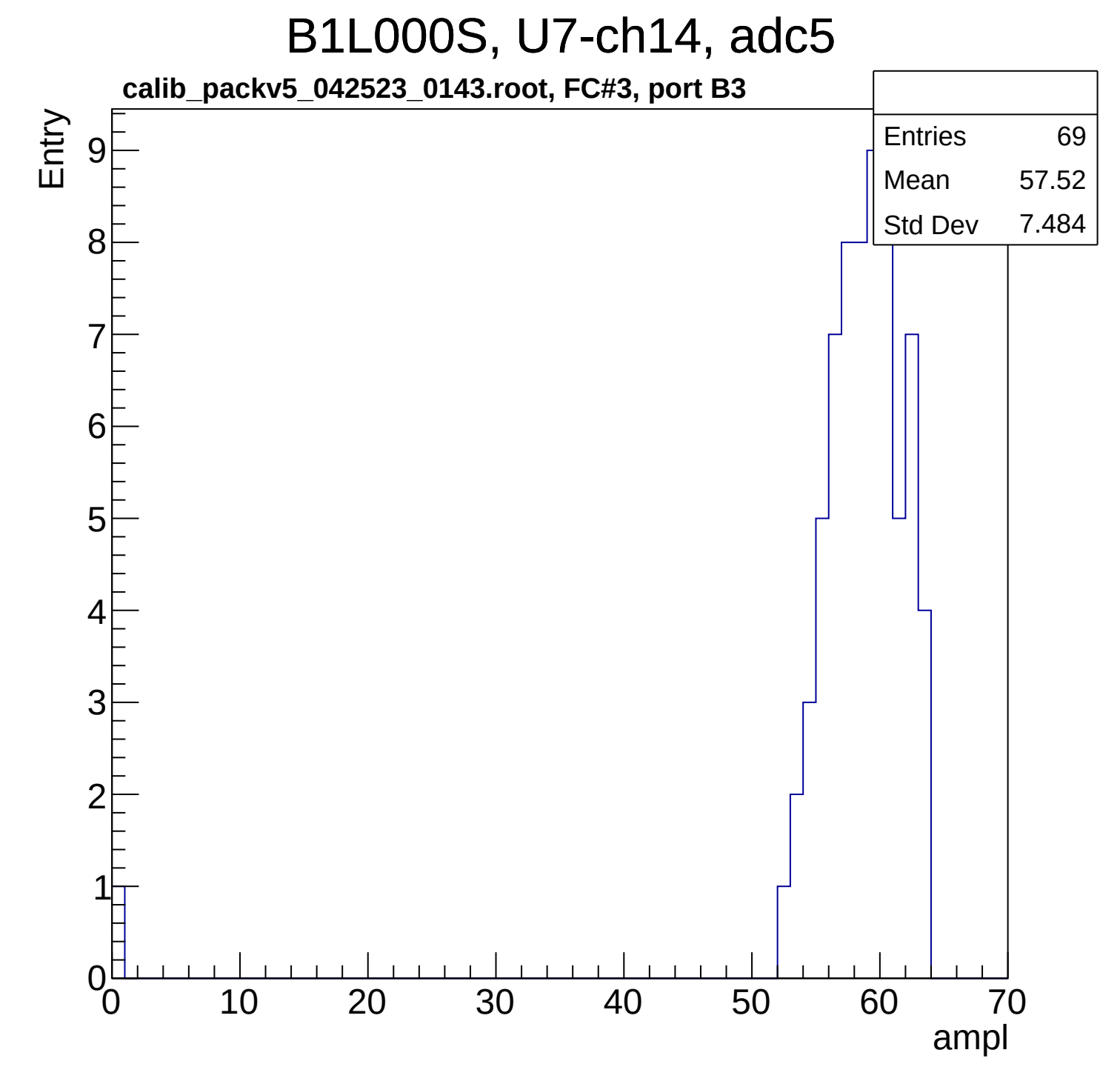
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch14, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 57.52
Std Dev 7.484
Entry
ampl
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3
4
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6
7
8
9
0
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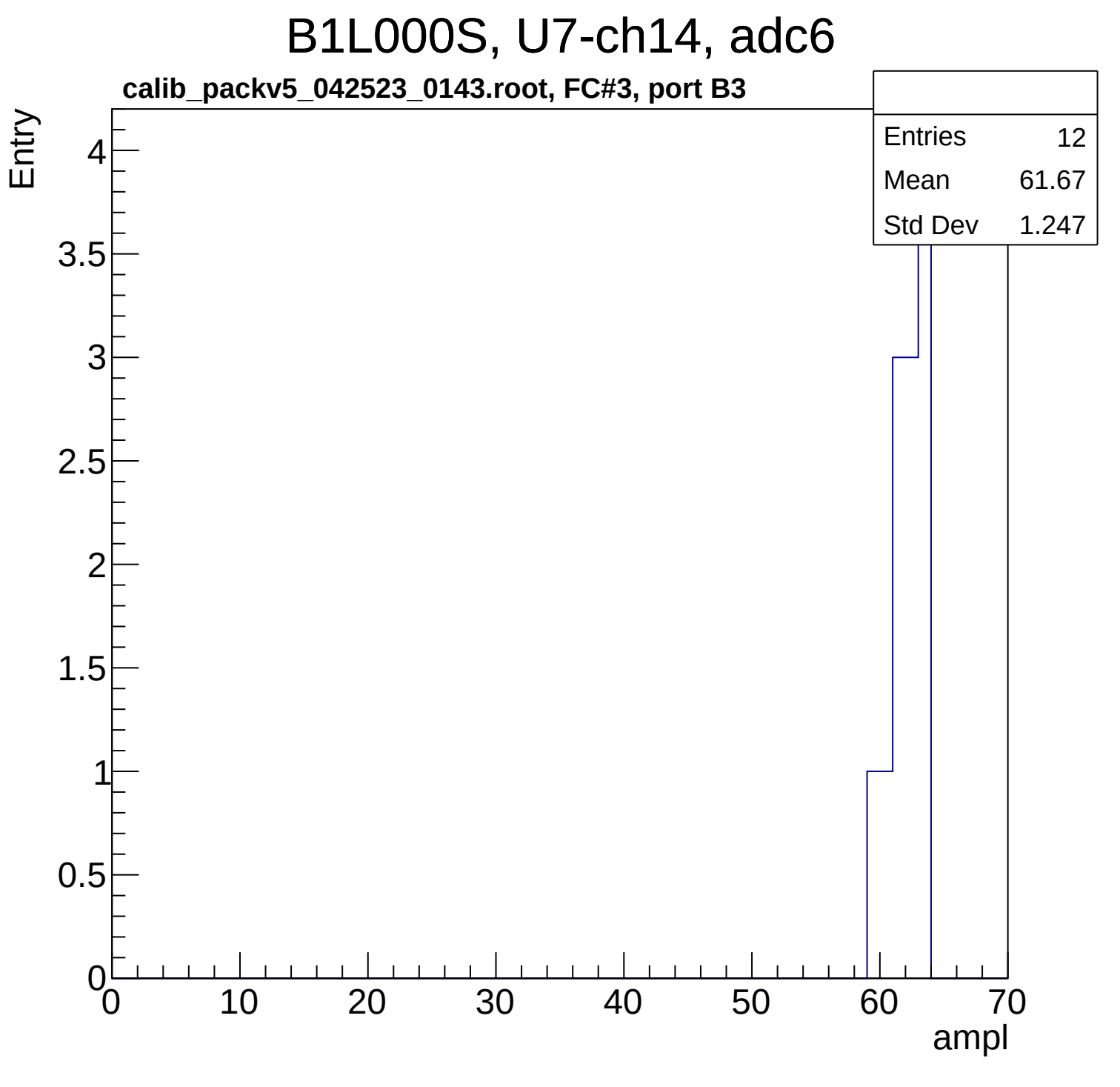
B1L000S, U7-ch14, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.67
Std Dev 1.247
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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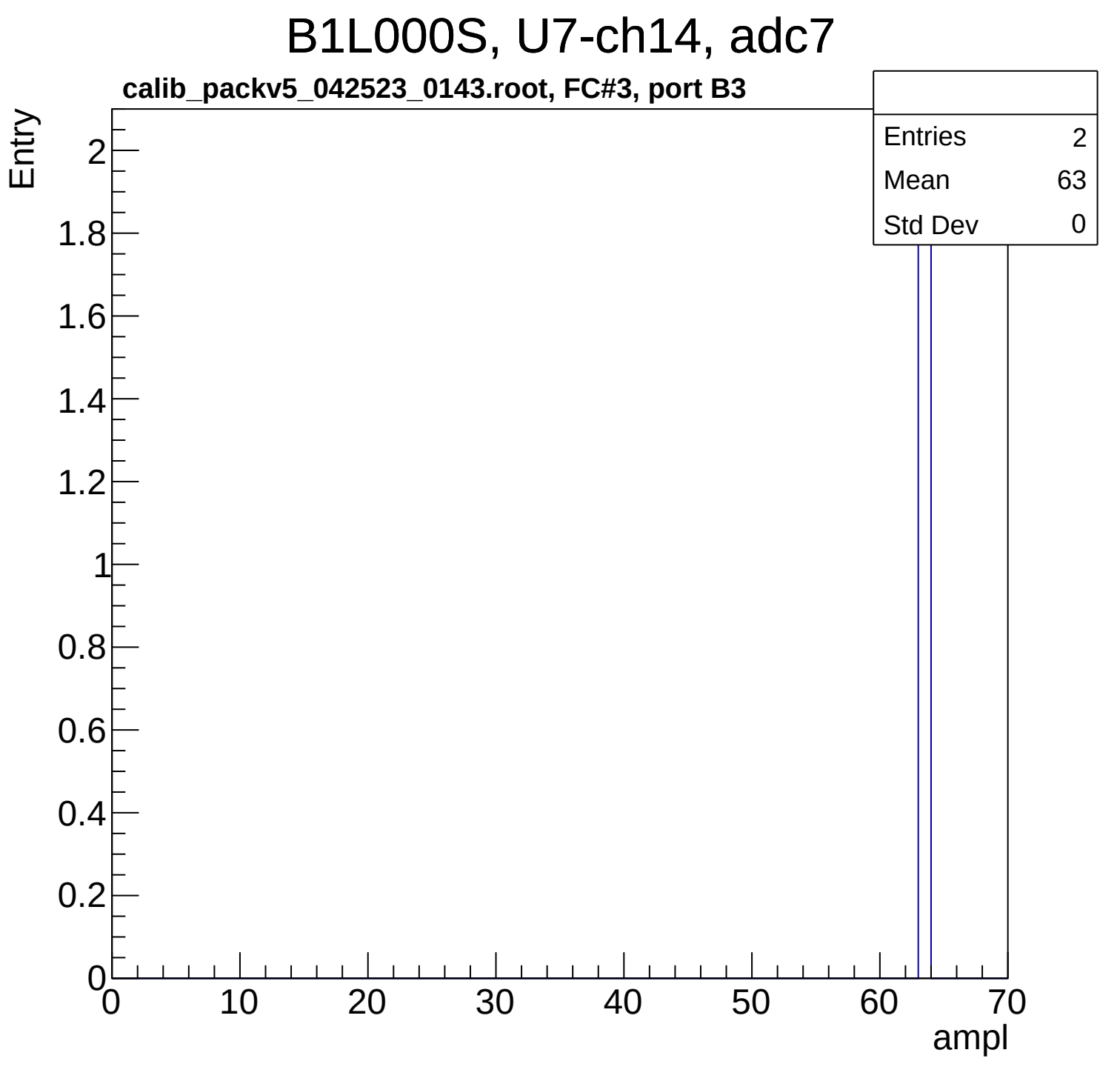
B1L000S, U7-ch14, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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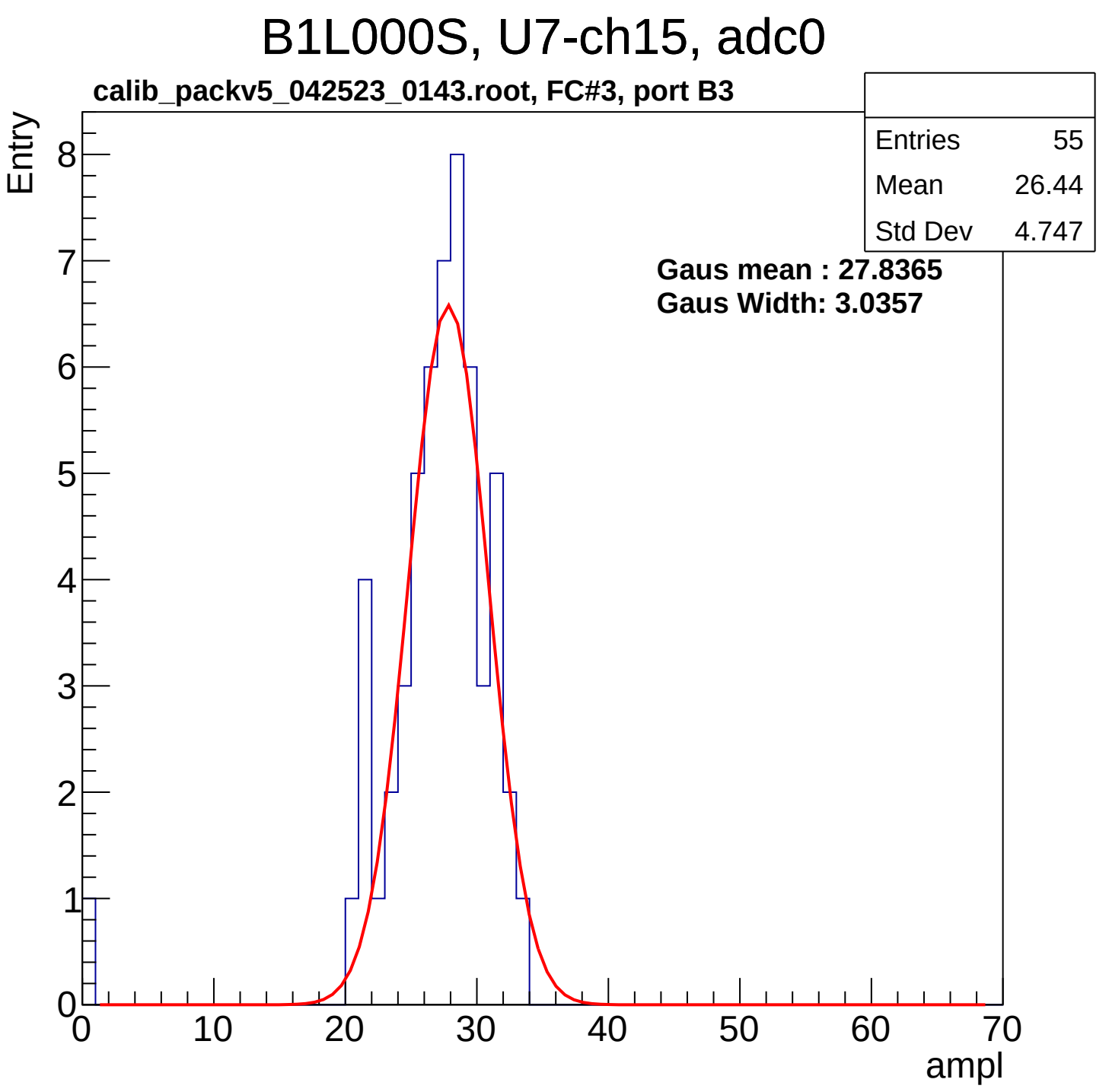
B1L000S, U7-ch15, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 26.44
Std Dev 4.747
Gaus mean : 27.8365
Gaus Width: 3.0357
Entry
ampl
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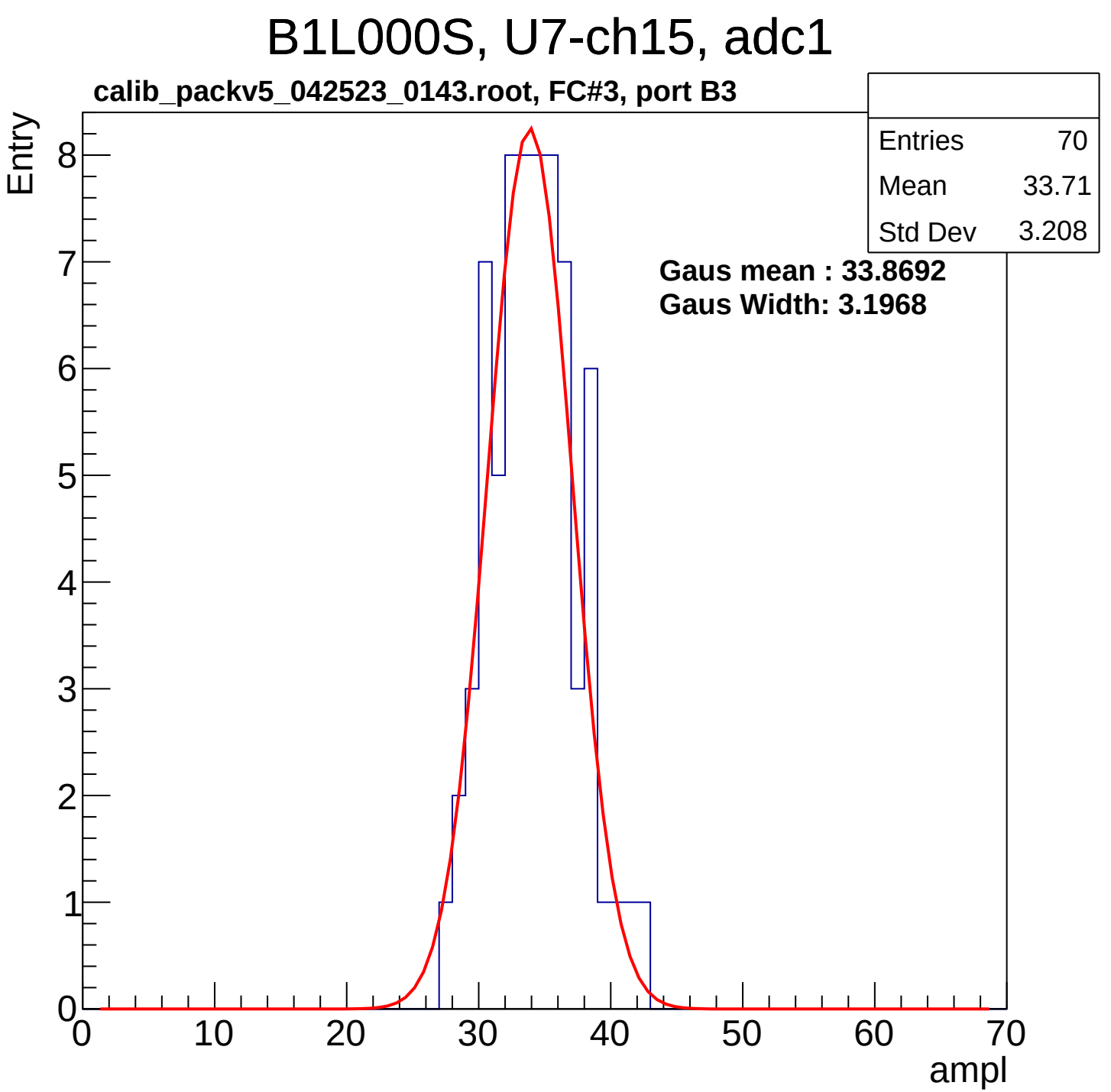
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch15, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 33.71
Std Dev 3.208
Gaus mean : 33.8692
Gaus Width: 3.1968
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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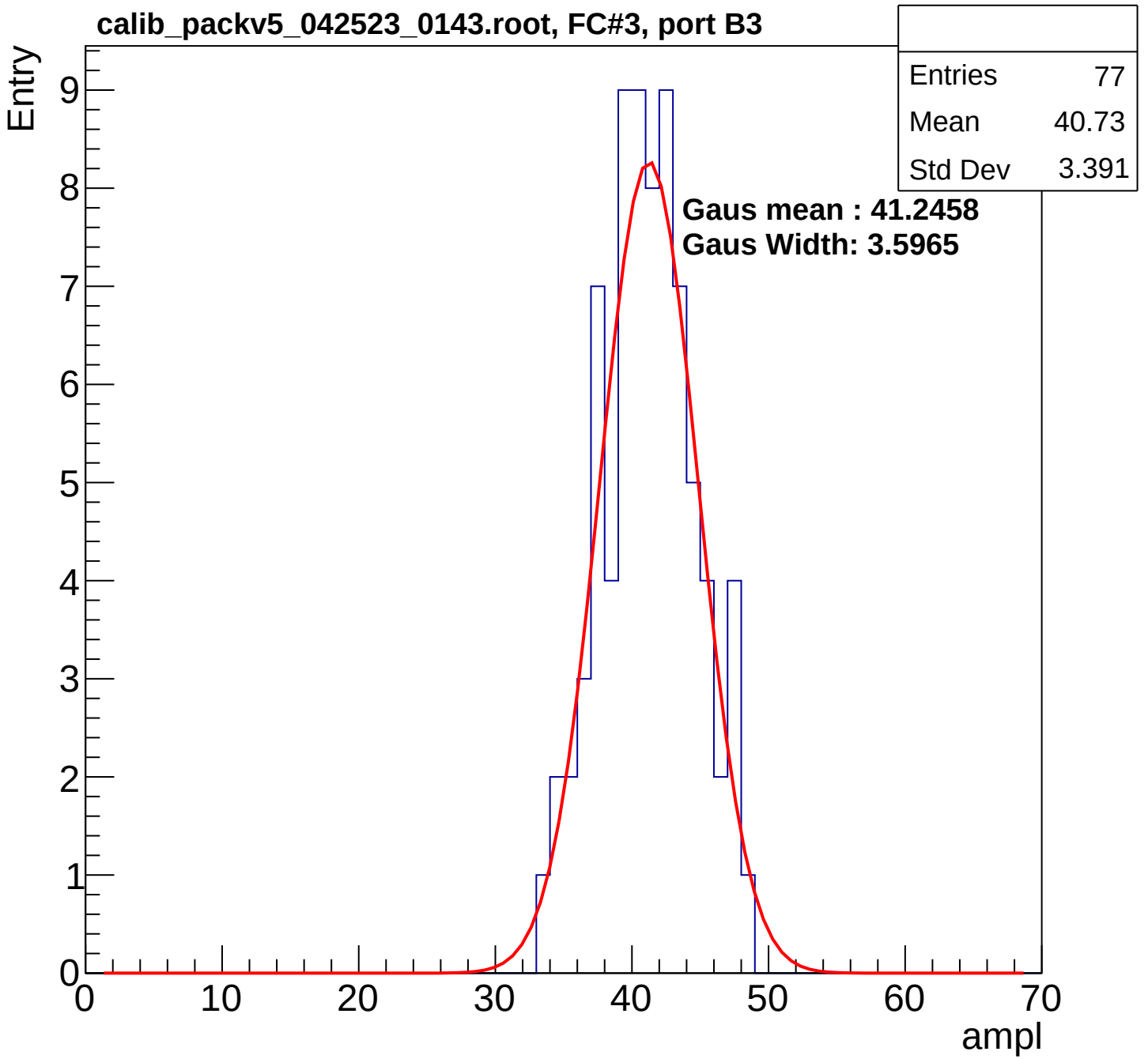
B1L000S, U7-ch15, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 40.73
Std Dev 3.391
Gaus mean : 41.2458
Gaus Width: 3.5965
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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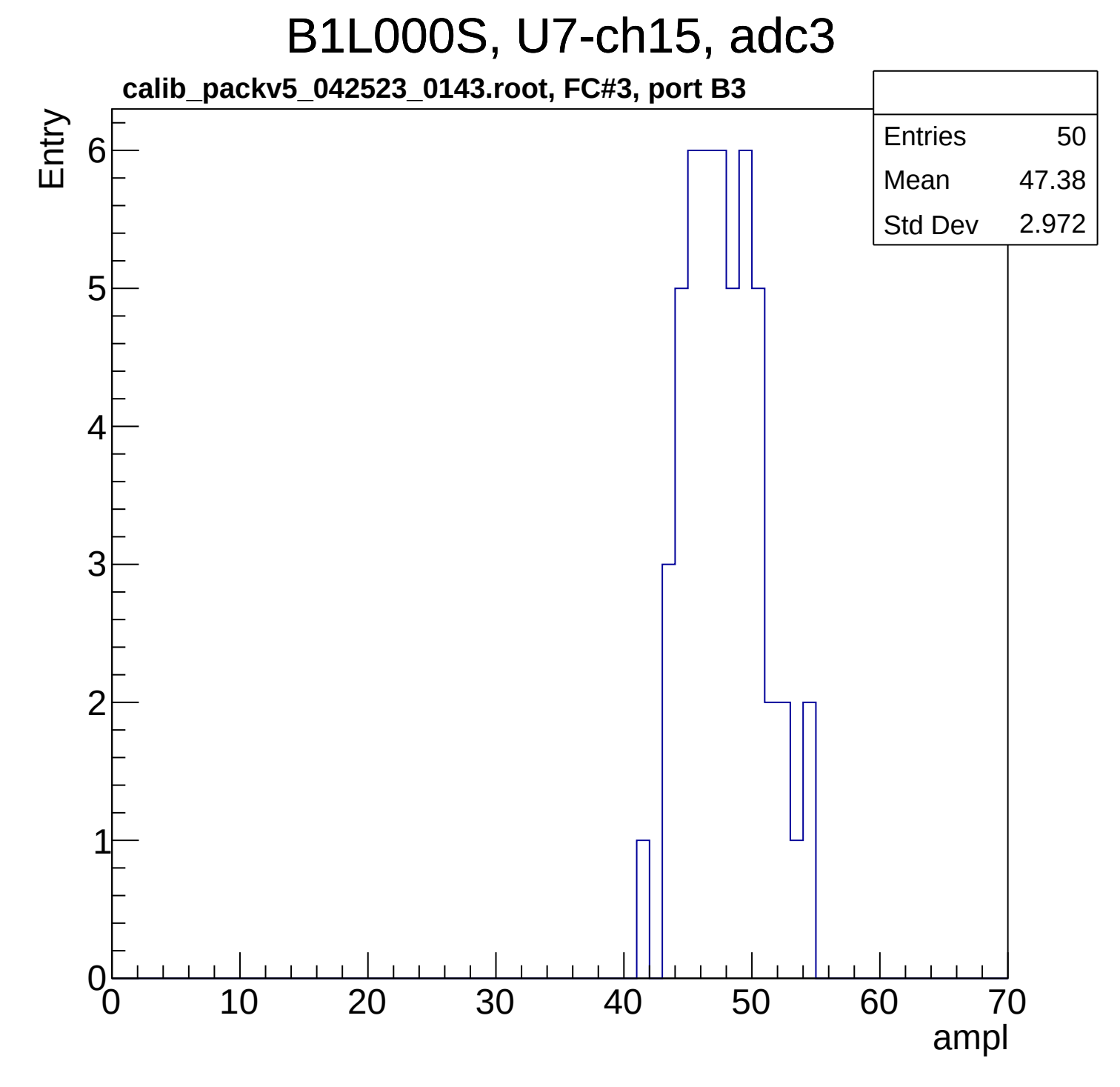
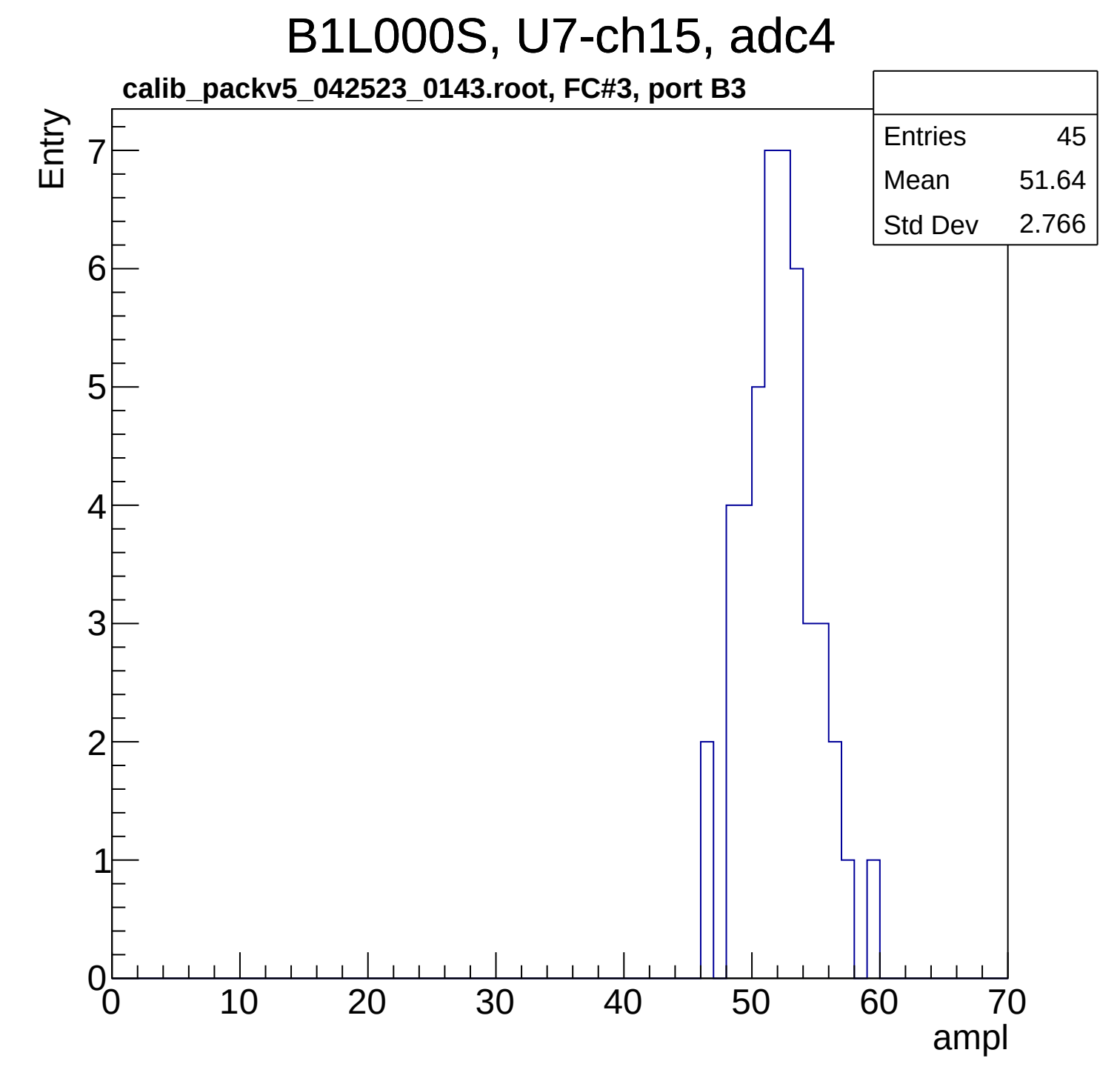
60
70


B1L000S, U7-ch15, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 47.38
Std Dev 2.972
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch15, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 51.64
Std Dev 2.766
Entry
ampl
0
1
2
3
4
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6
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0
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60
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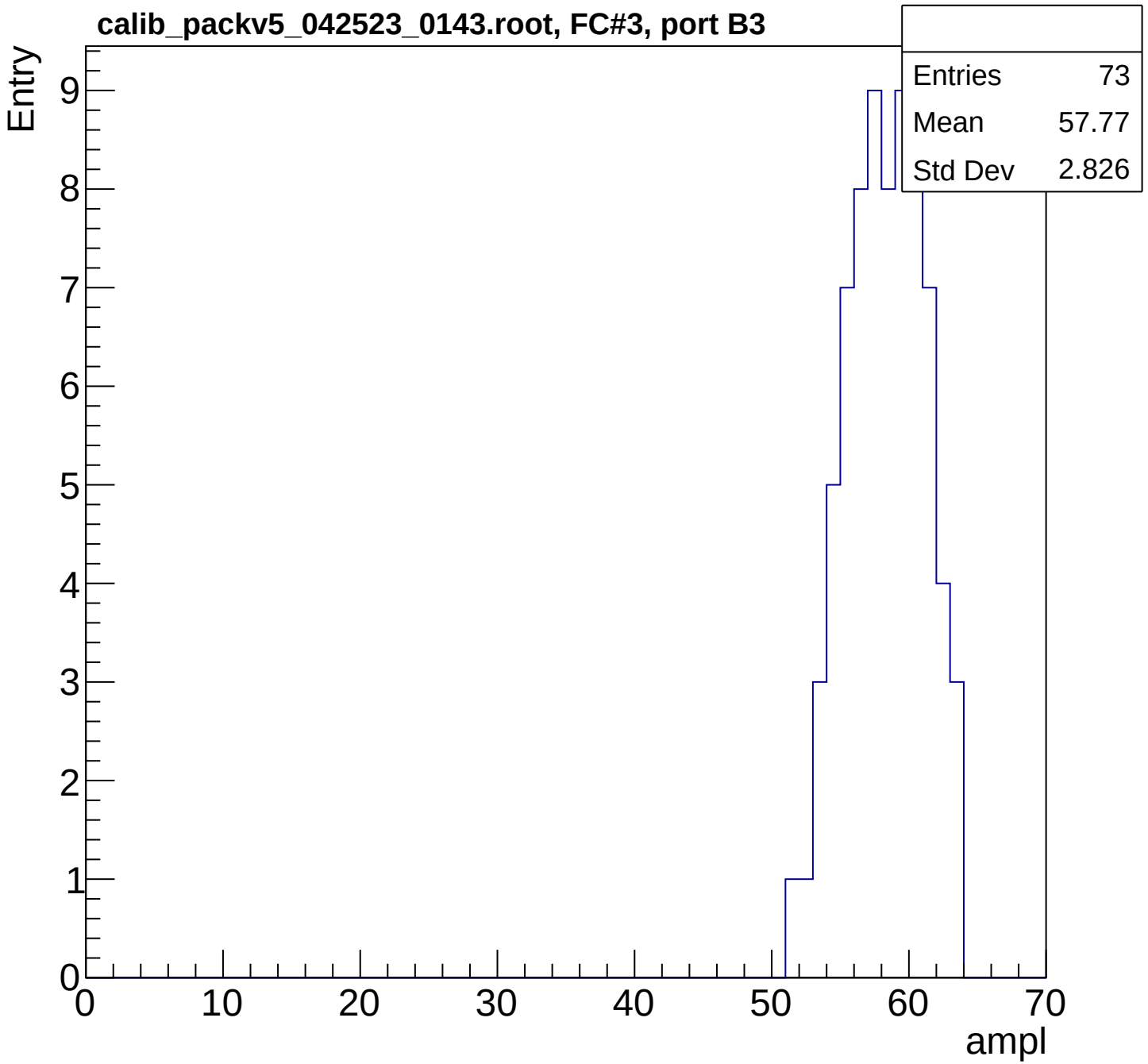
B1L000S, U7-ch15, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 57.77
Std Dev 2.826
Entry
ampl
0
1
2
3
4
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6
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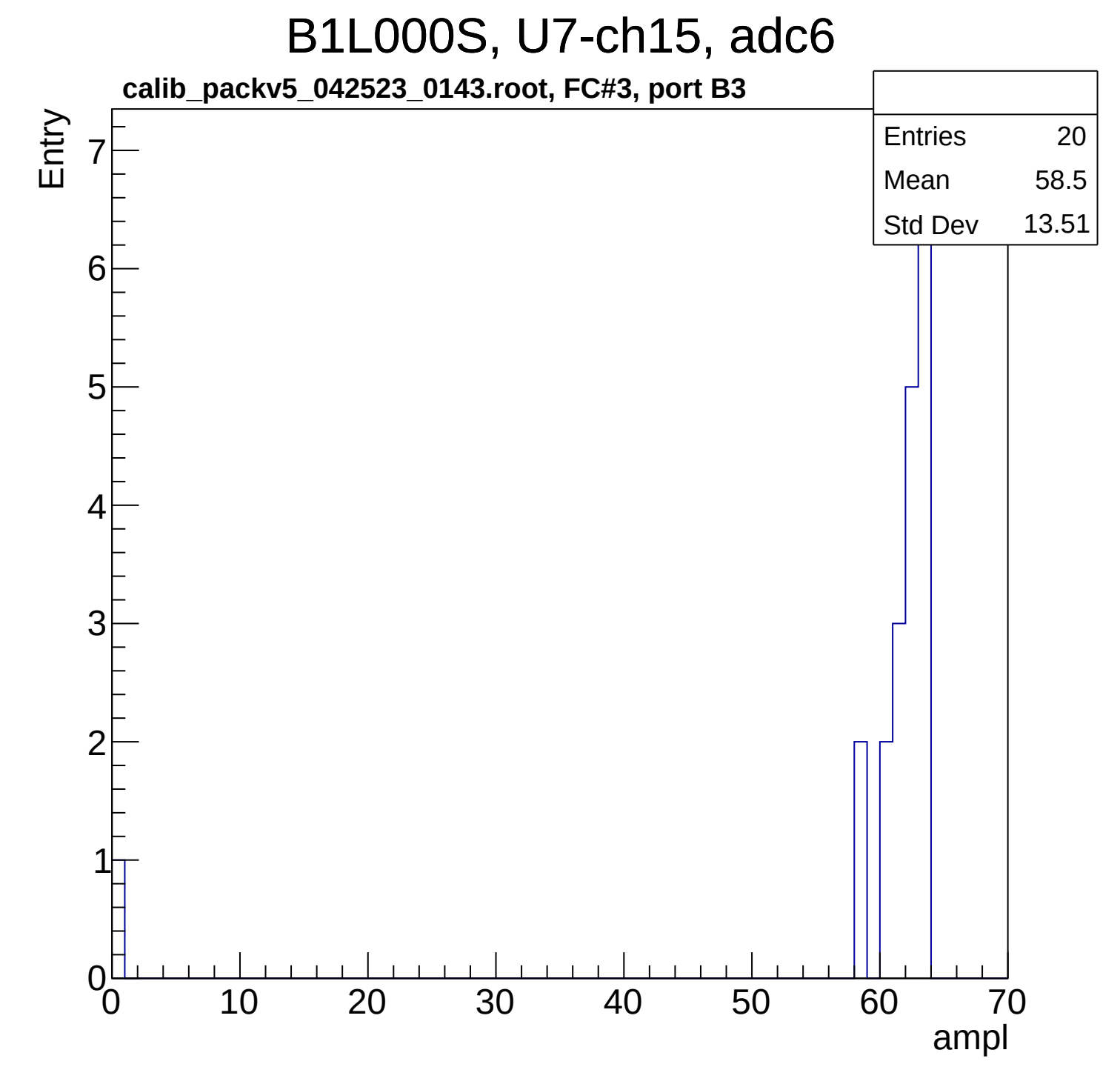
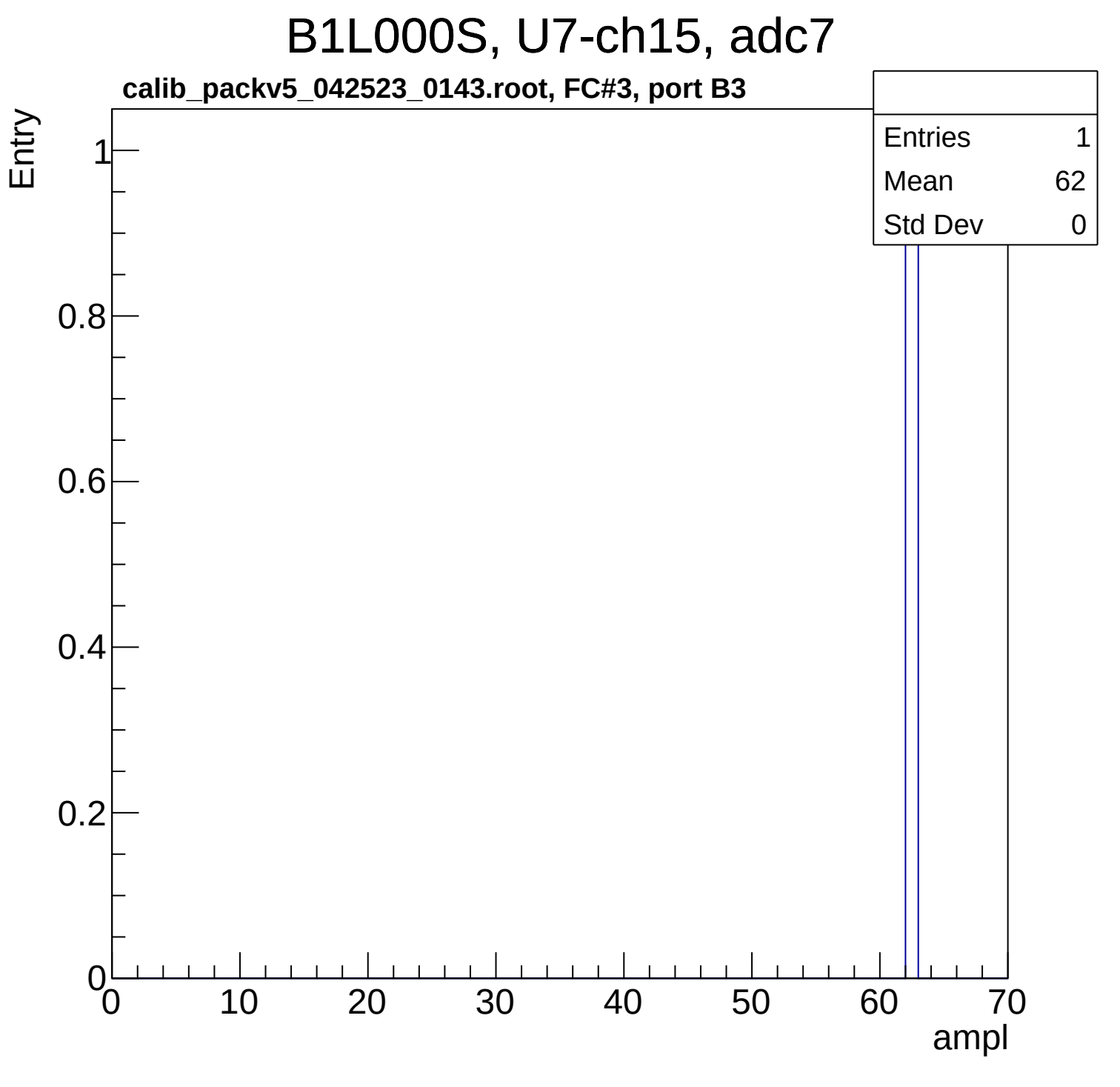
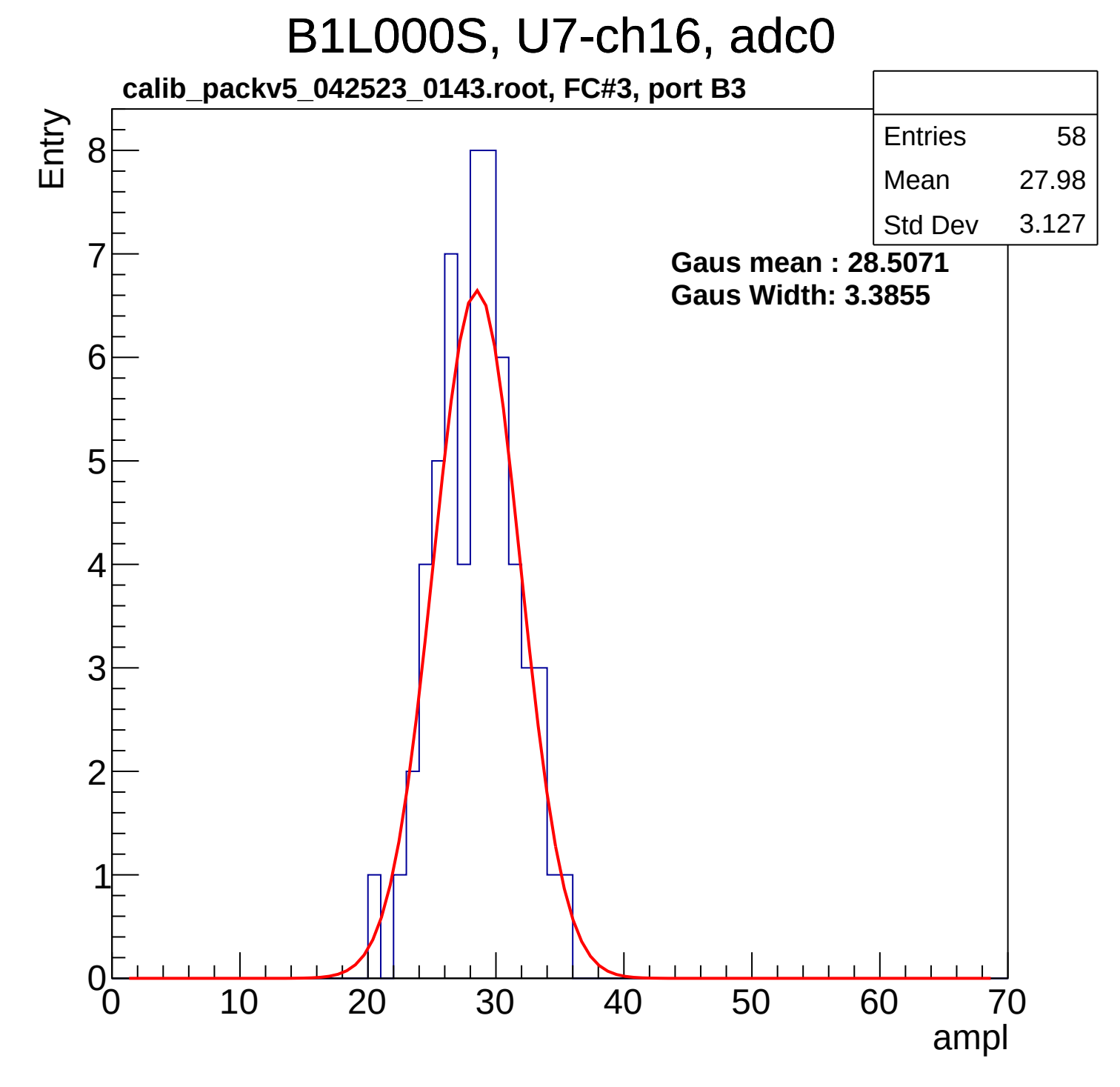

B1L000S, U7-ch15, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.5
Std Dev 13.51
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch15, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch16, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 27.98
Std Dev 3.127
Gaus mean : 28.5071
Gaus Width: 3.3855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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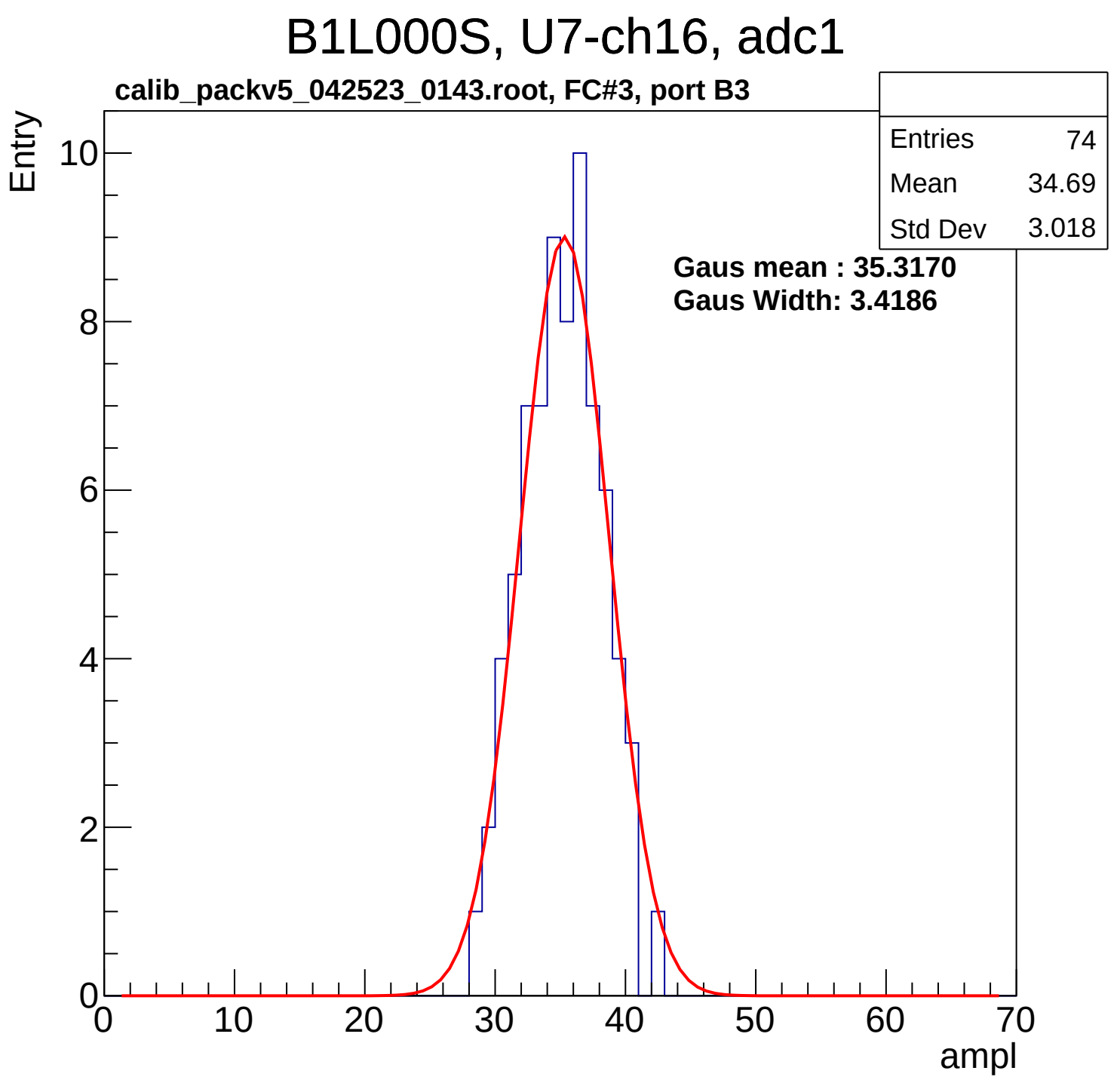
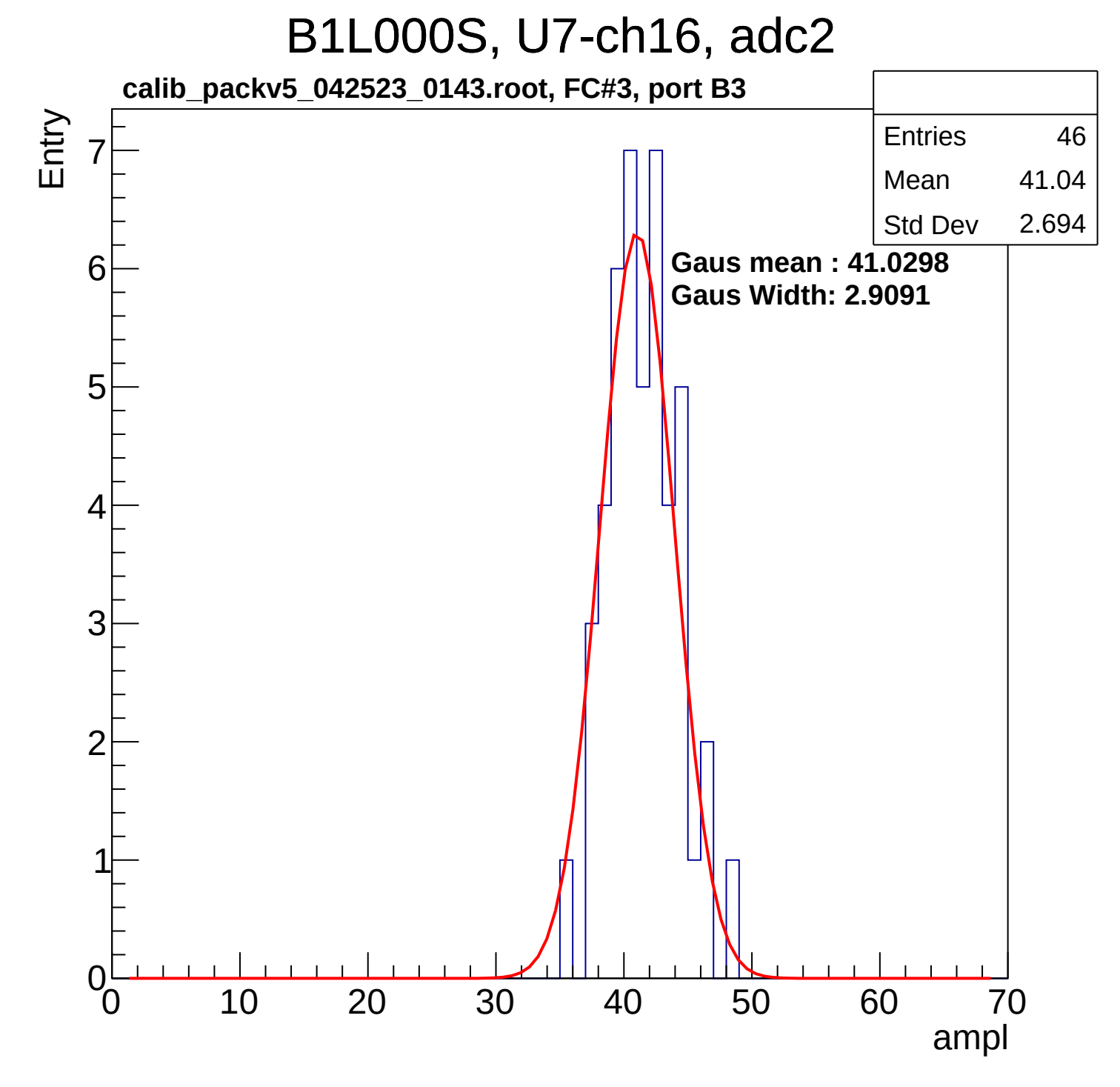

B1L000S, U7-ch16, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 34.69
Std Dev 3.018
Gaus mean : 35.3170
Gaus Width: 3.4186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch16, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 41.04
Std Dev 2.694
Gaus mean : 41.0298
Gaus Width: 2.9091
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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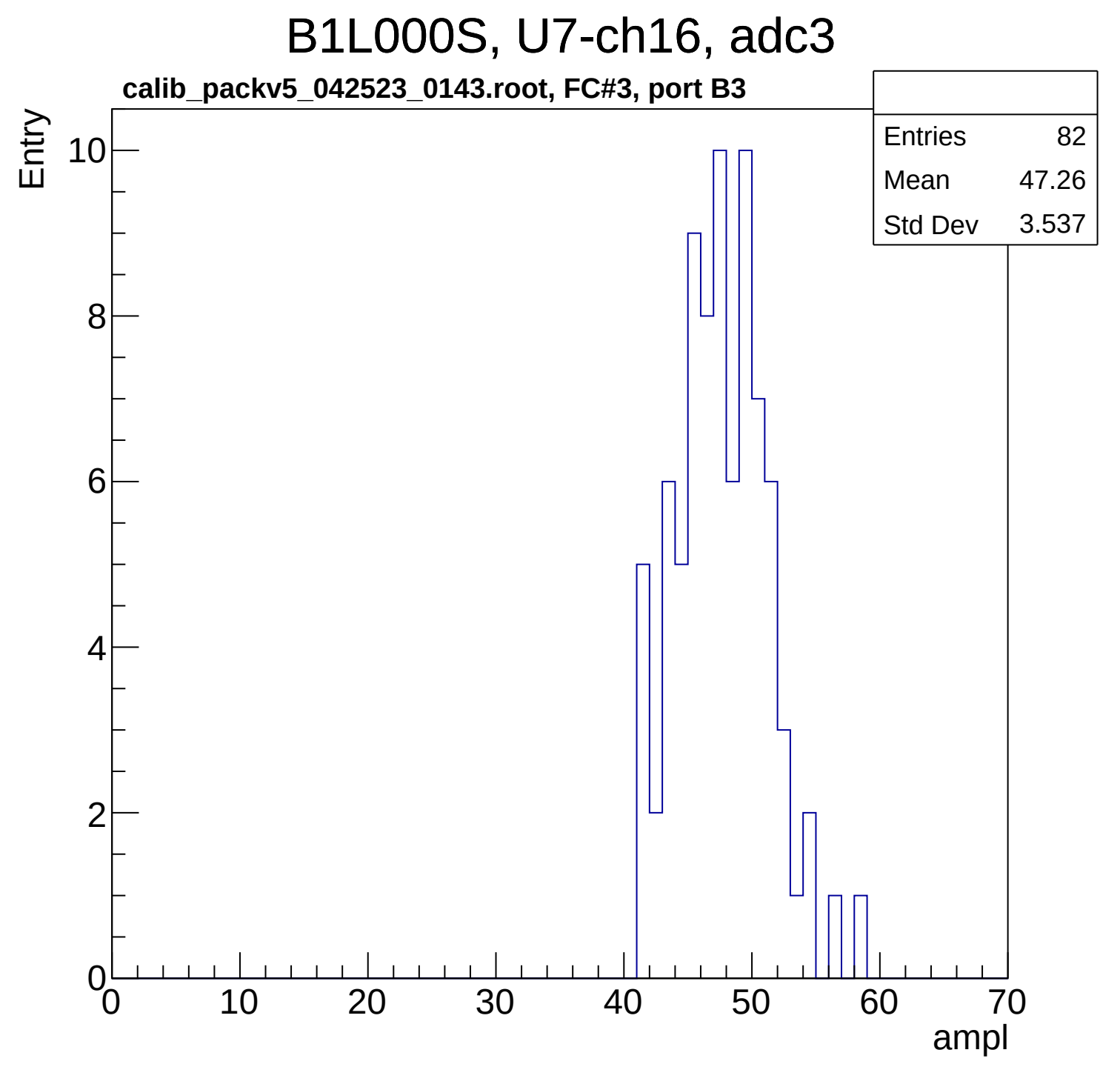
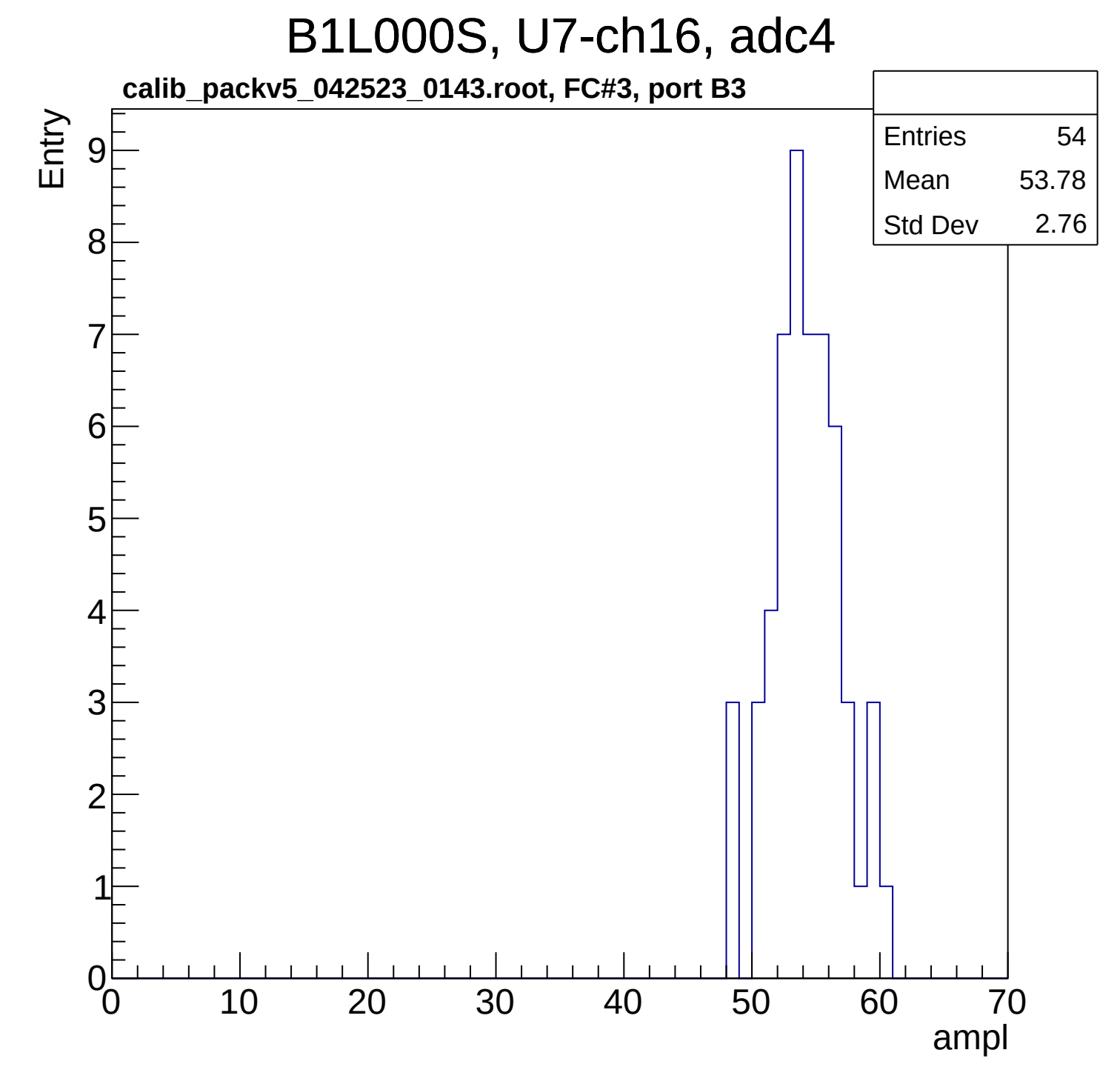
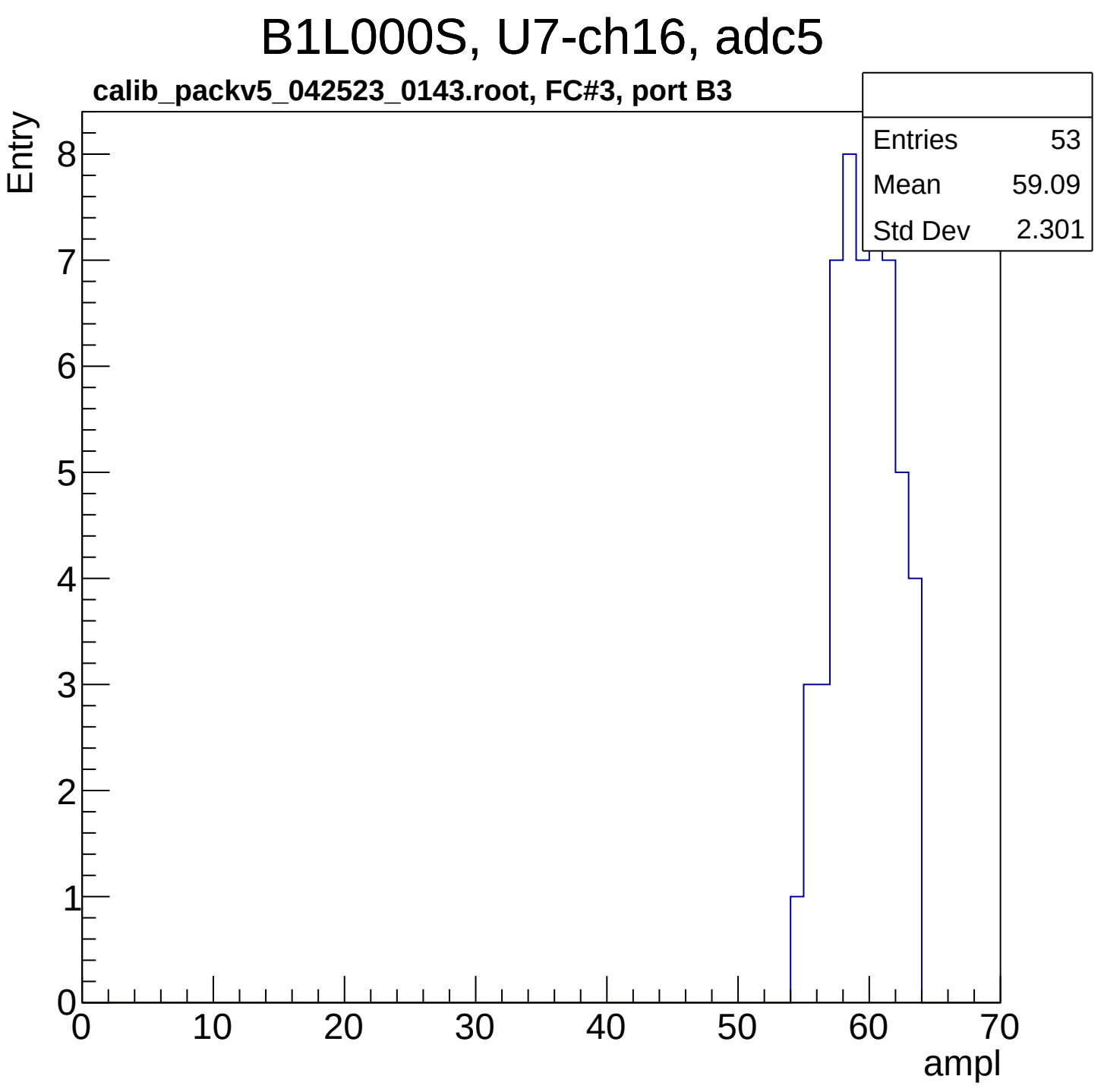

B1L000S, U7-ch16, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 47.26
Std Dev 3.537
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch16, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 53.78
Std Dev 2.76
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch16, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 59.09
Std Dev 2.301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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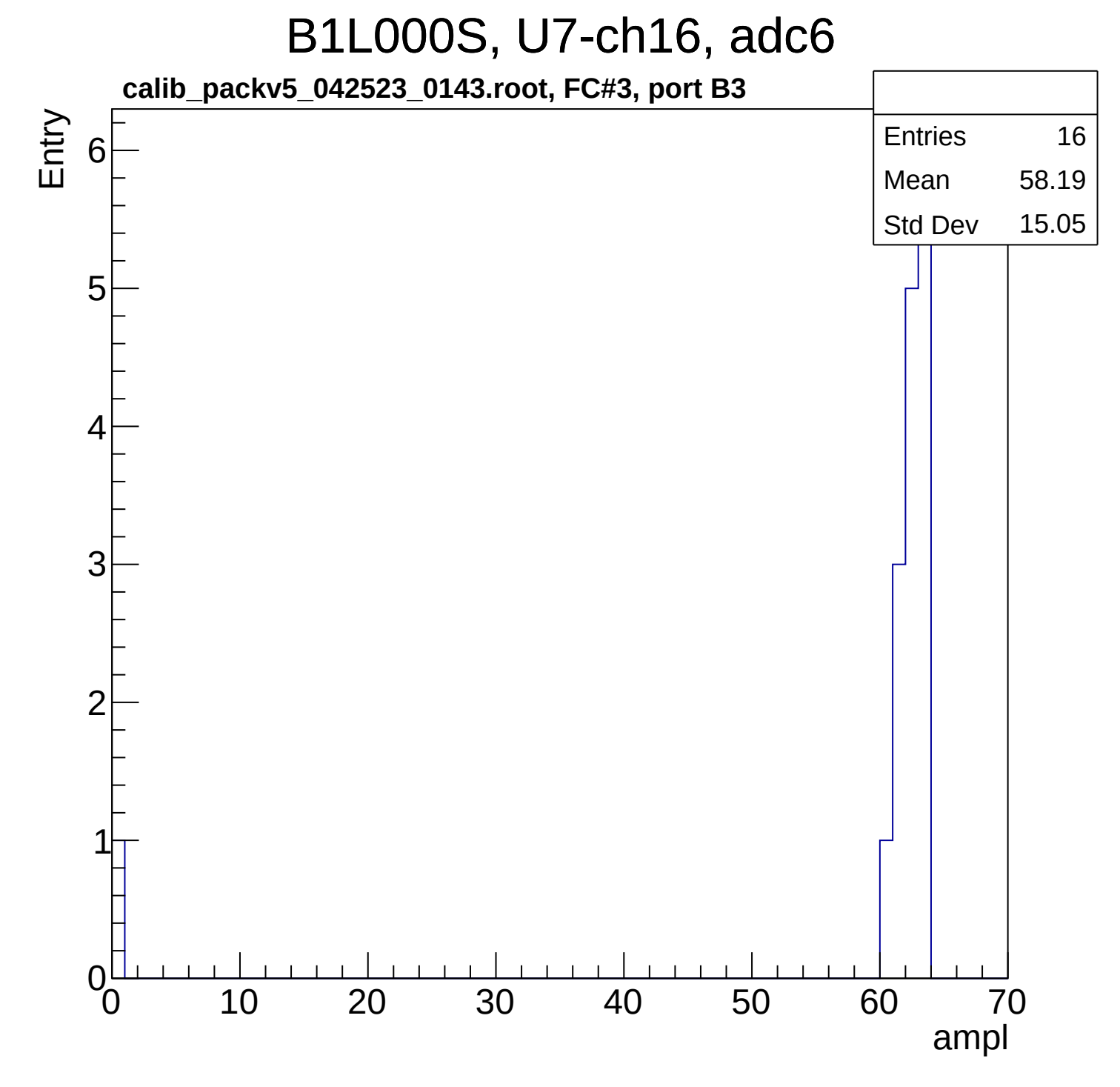
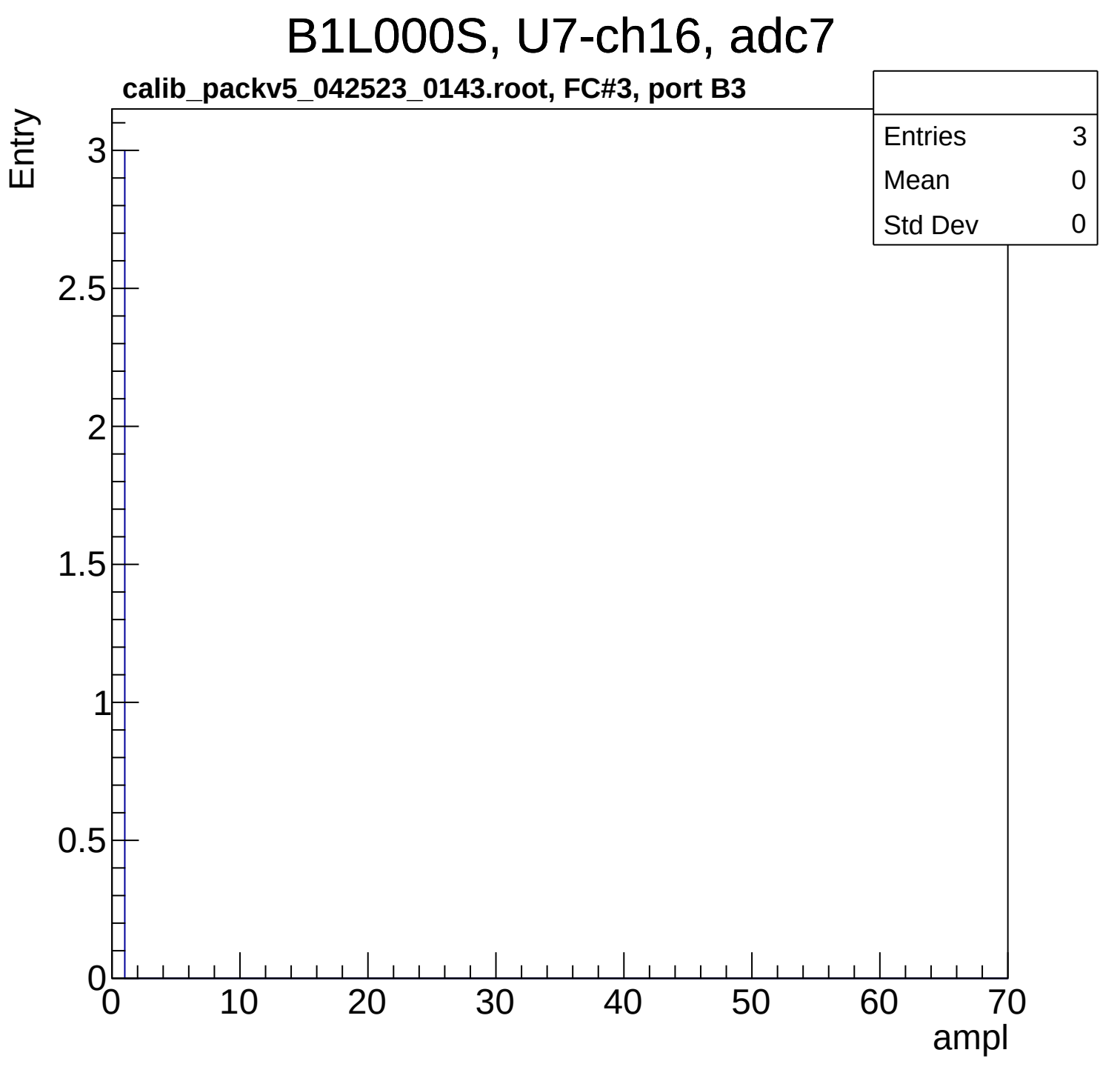
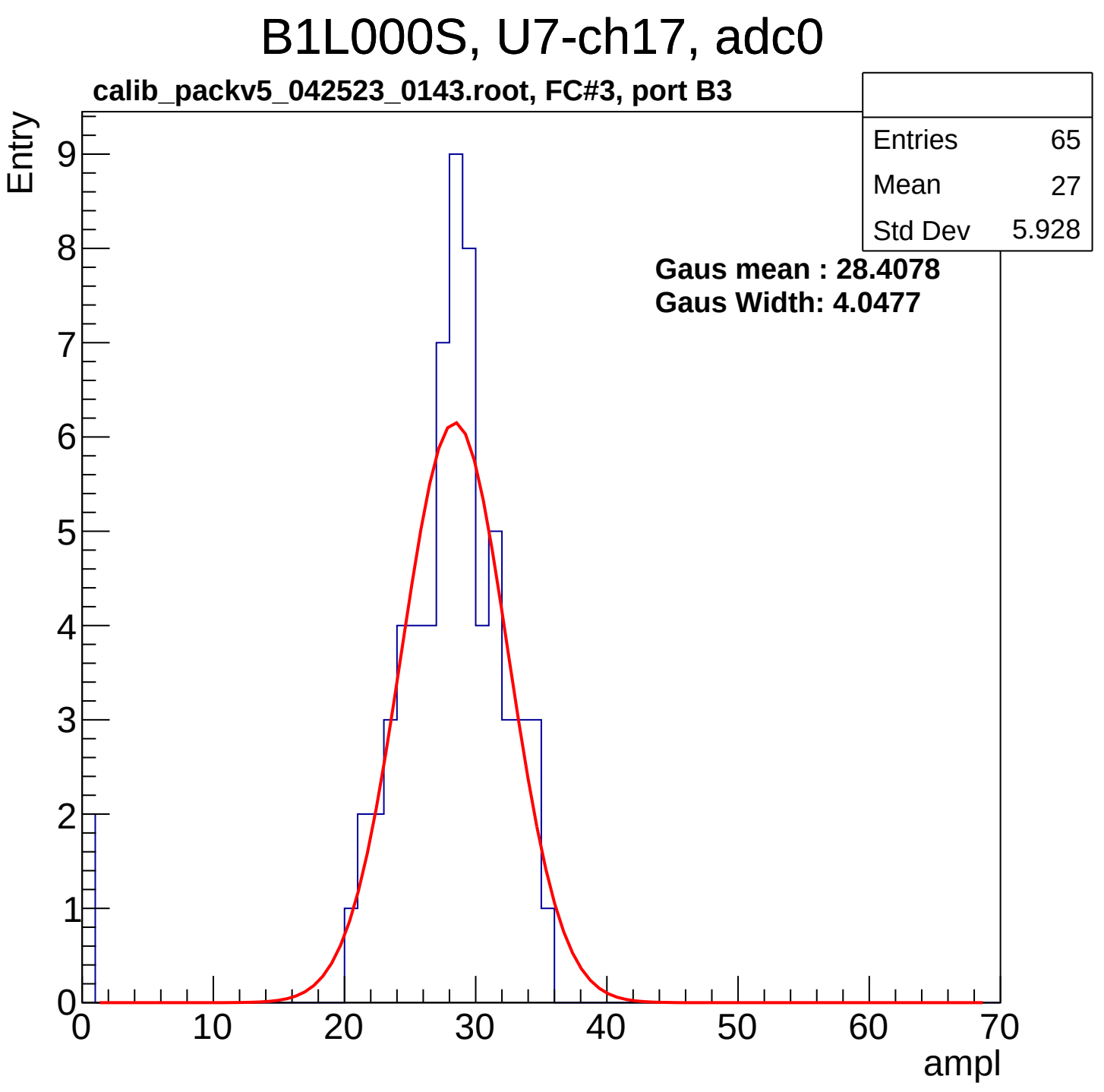
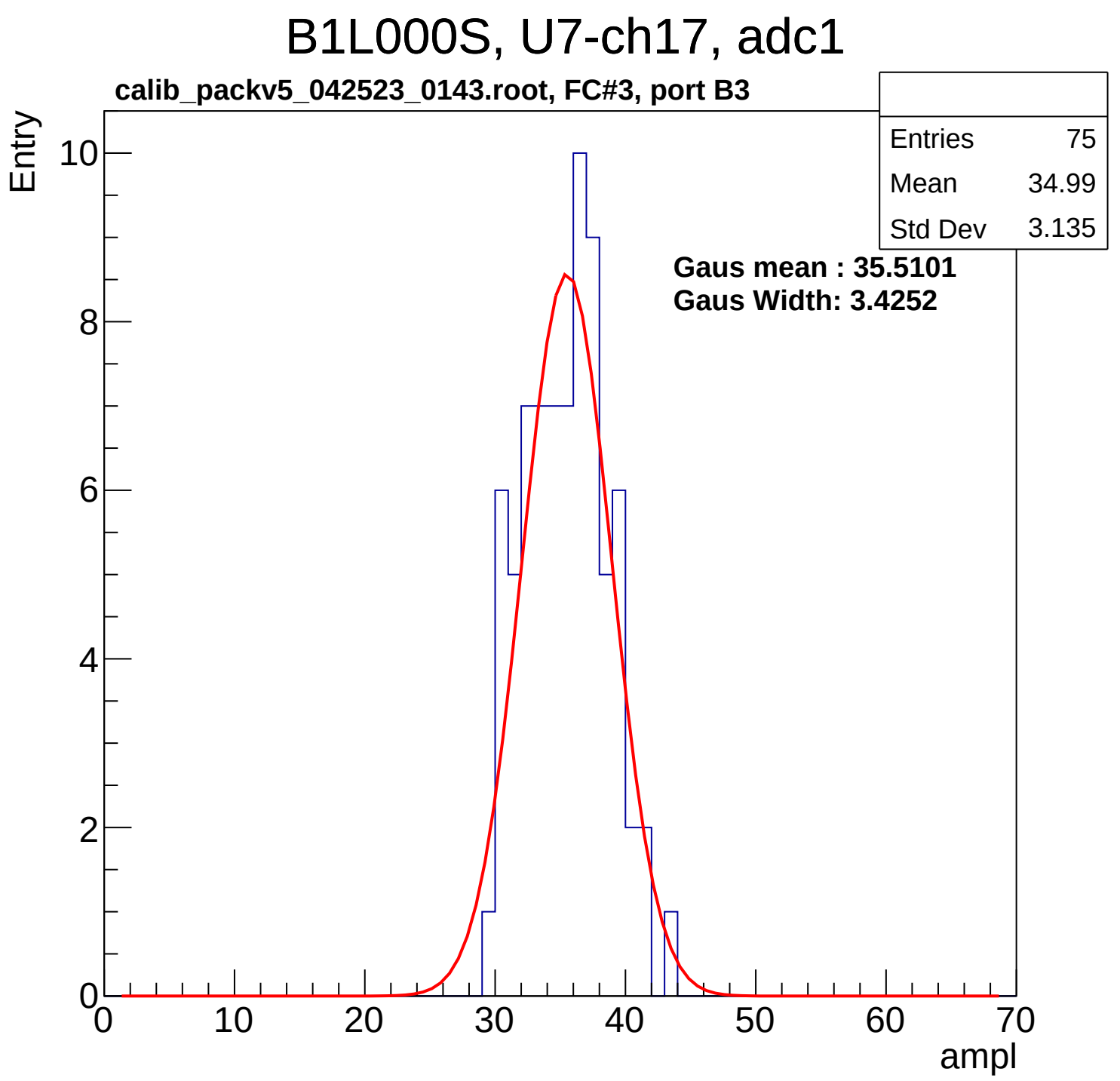
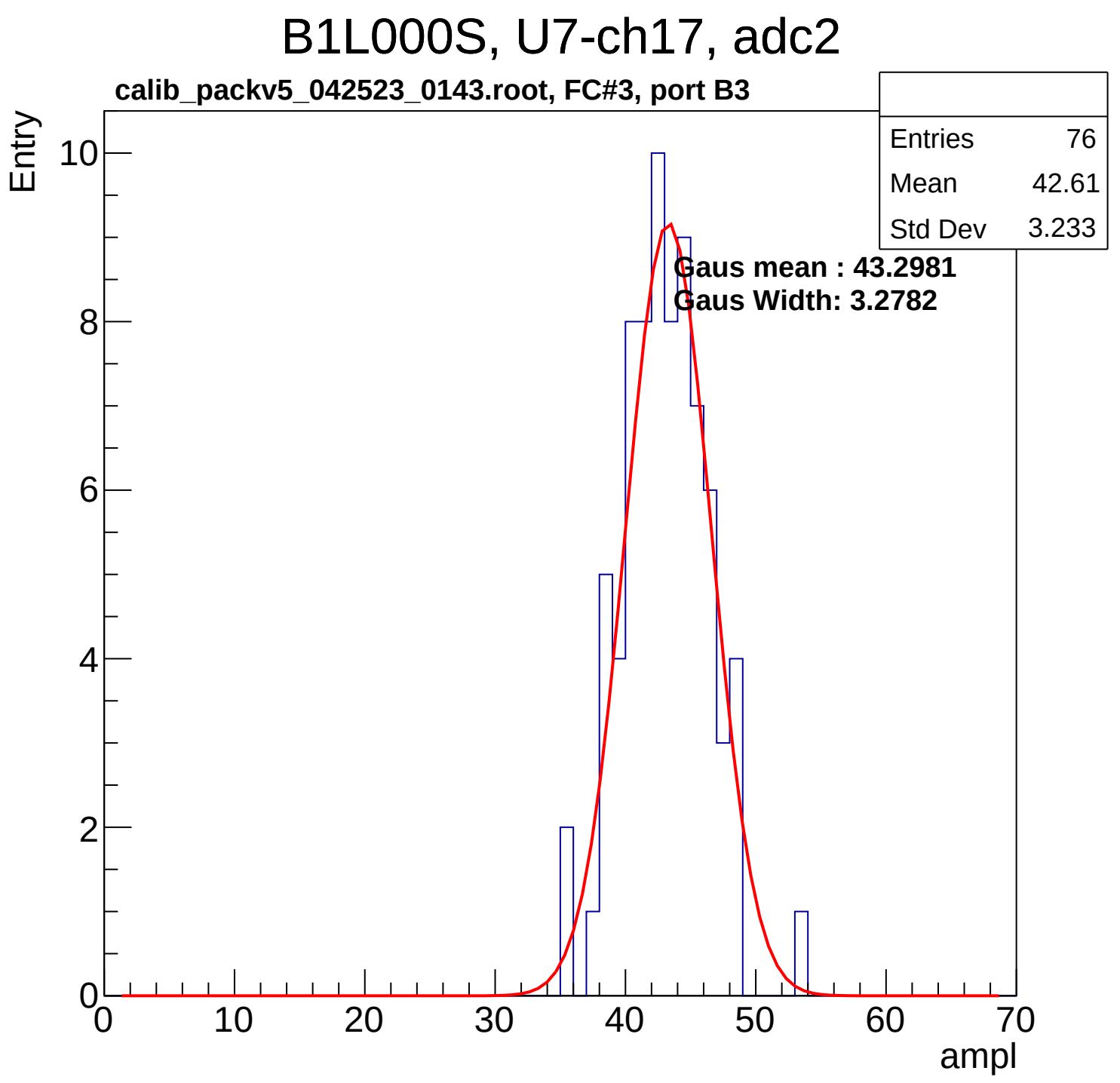
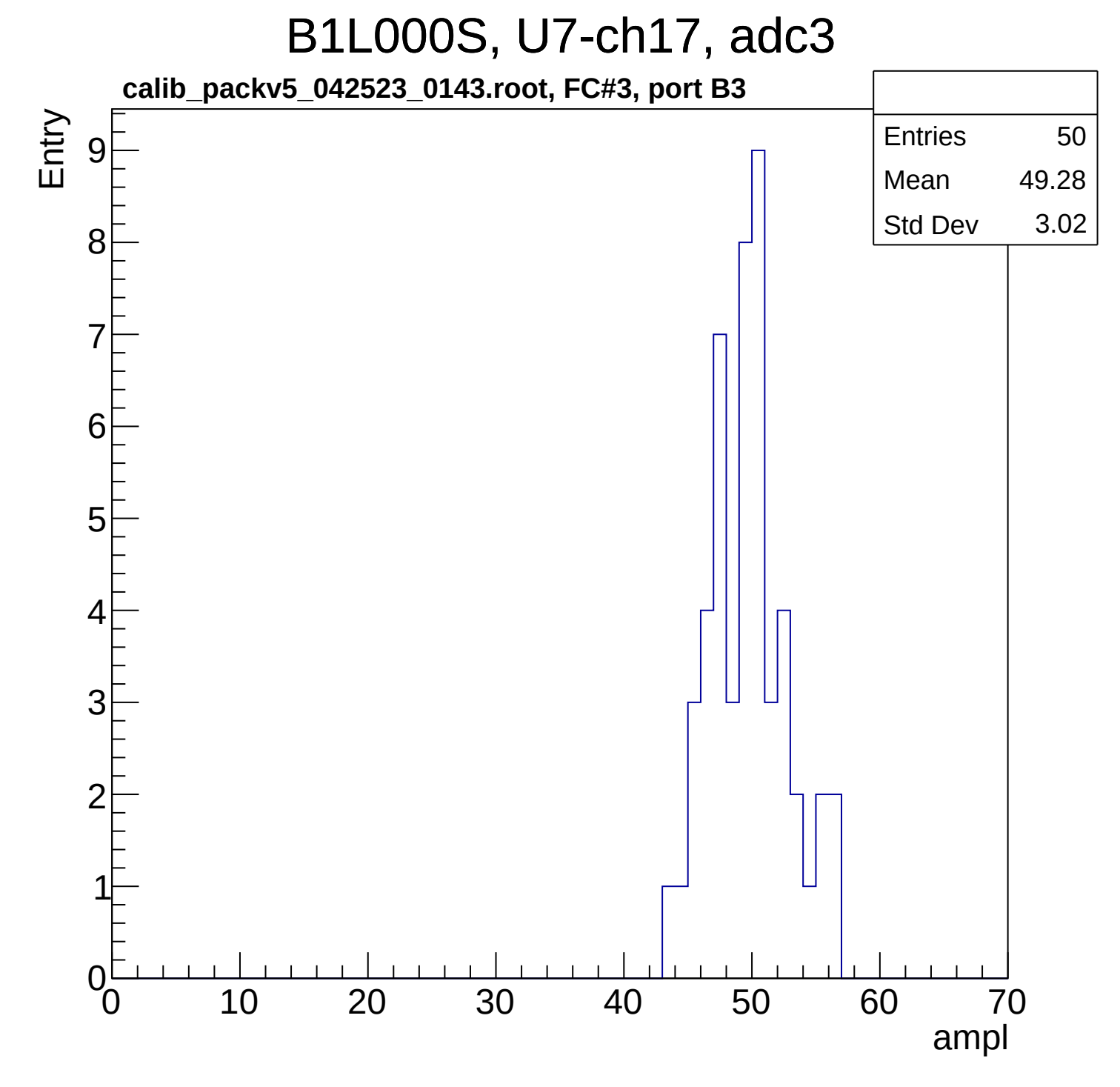

B1L000S, U7-ch16, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 58.19
Std Dev 15.05
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch16, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch17, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 27
Std Dev 5.928
Gaus mean : 28.4078
Gaus Width: 4.0477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch17, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 34.99
Std Dev 3.135
Gaus mean : 35.5101
Gaus Width: 3.4252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch17, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 42.61
Std Dev 3.233
Gaus mean : 43.2981
Gaus Width: 3.2782
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch17, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 49.28
Std Dev 3.02
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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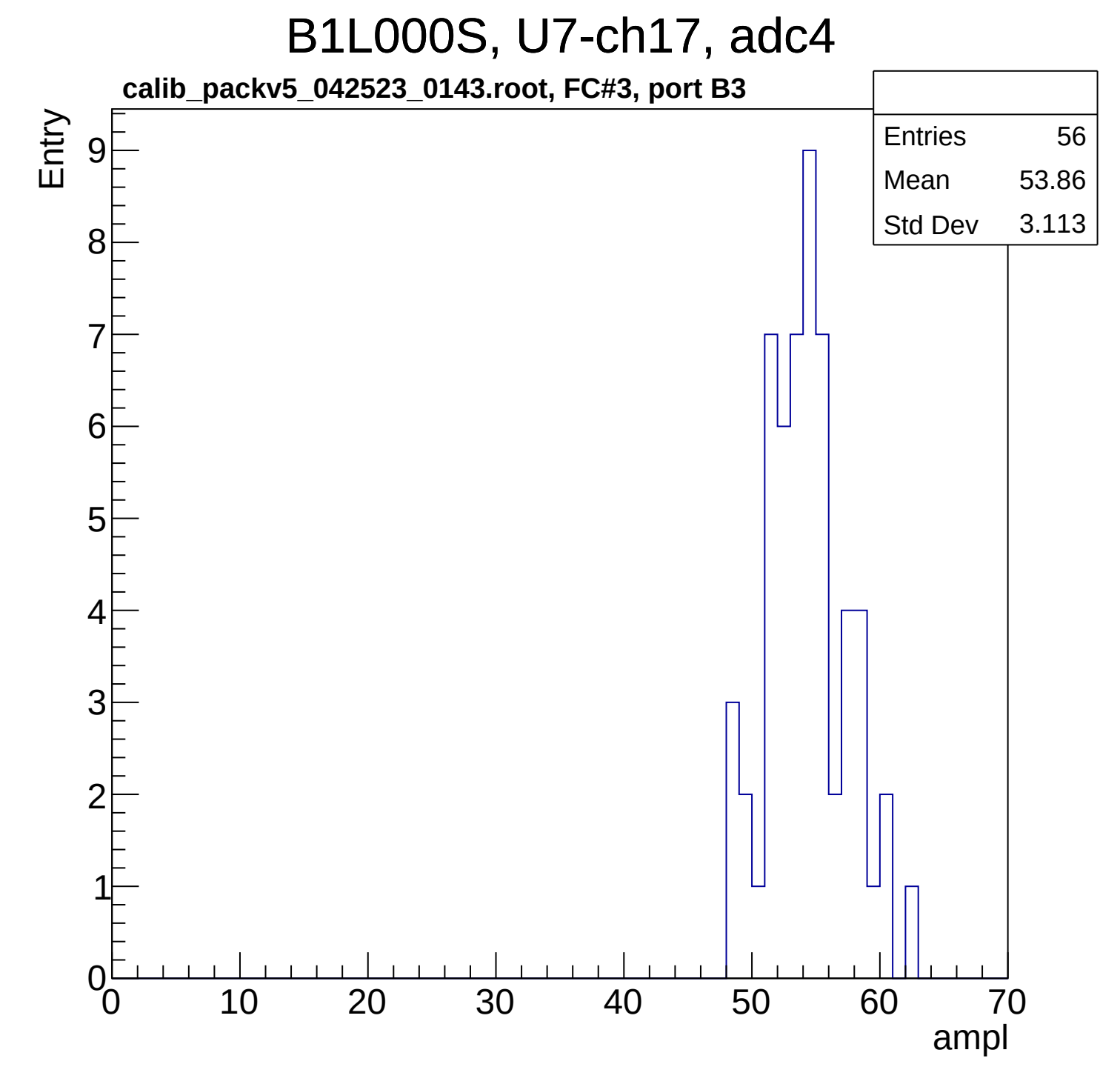
B1L000S, U7-ch17, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.86
Std Dev 3.113
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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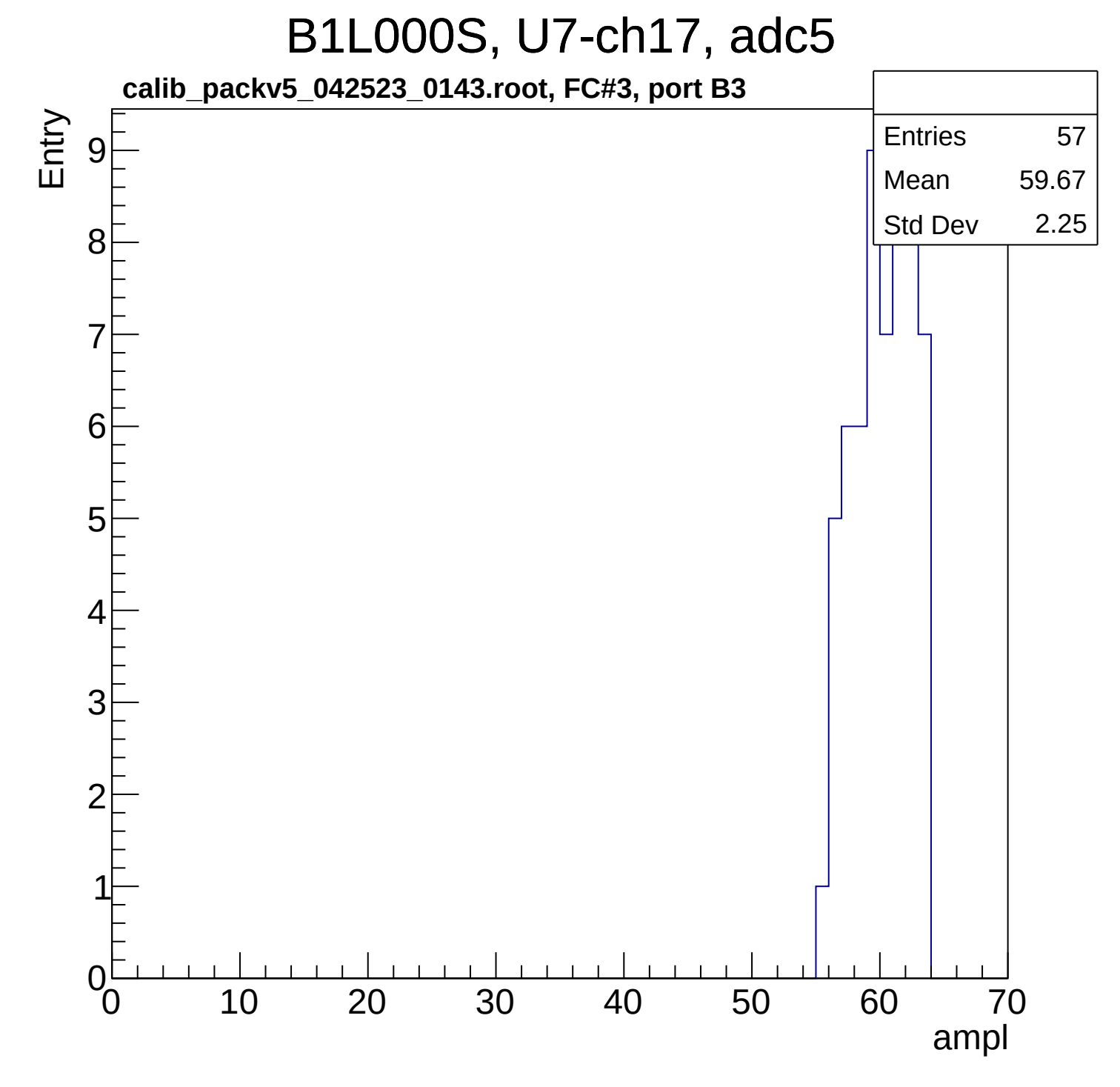
B1L000S, U7-ch17, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 59.67
Std Dev 2.25
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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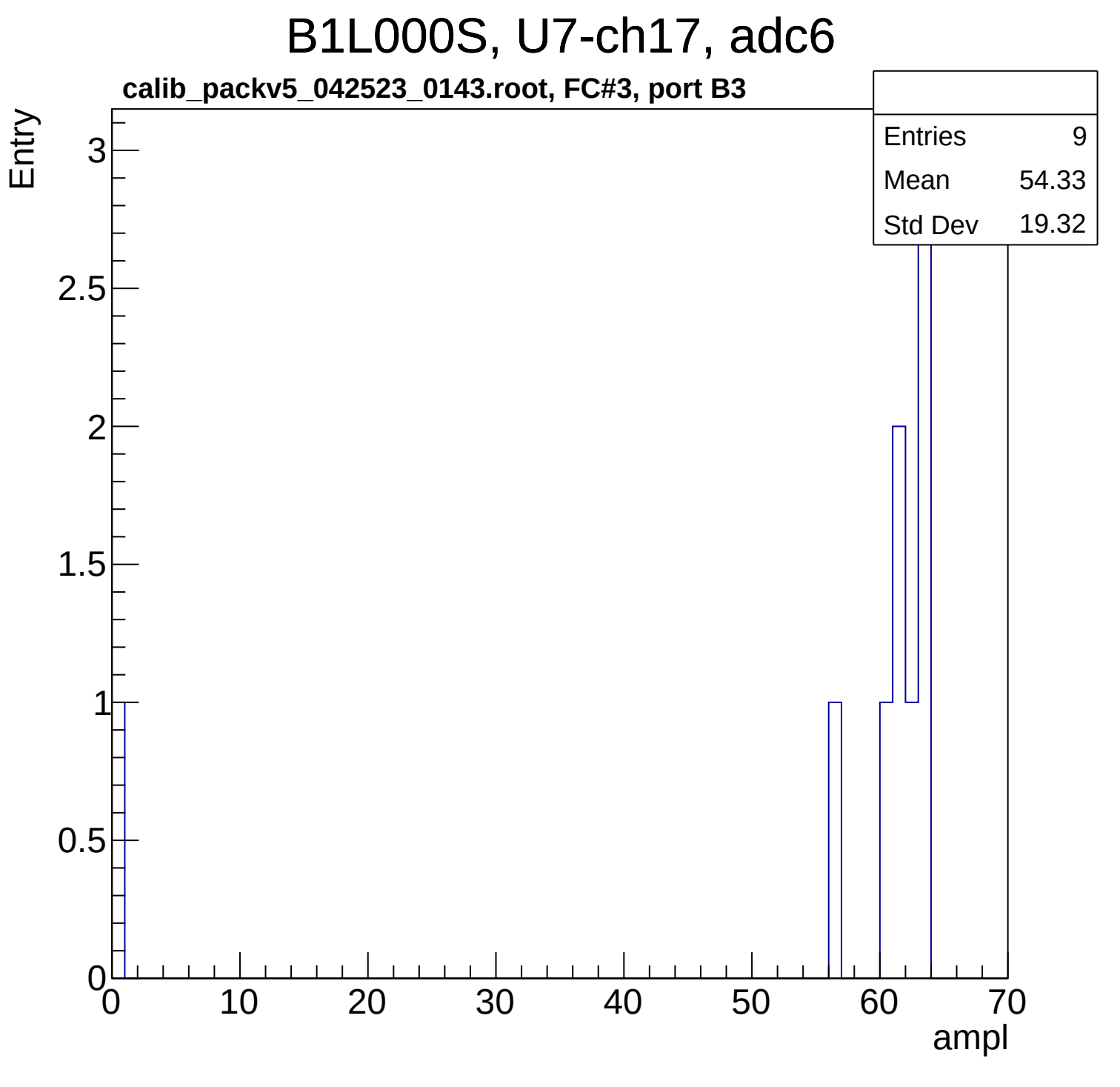
B1L000S, U7-ch17, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 54.33
Std Dev 19.32
Entry
ampl
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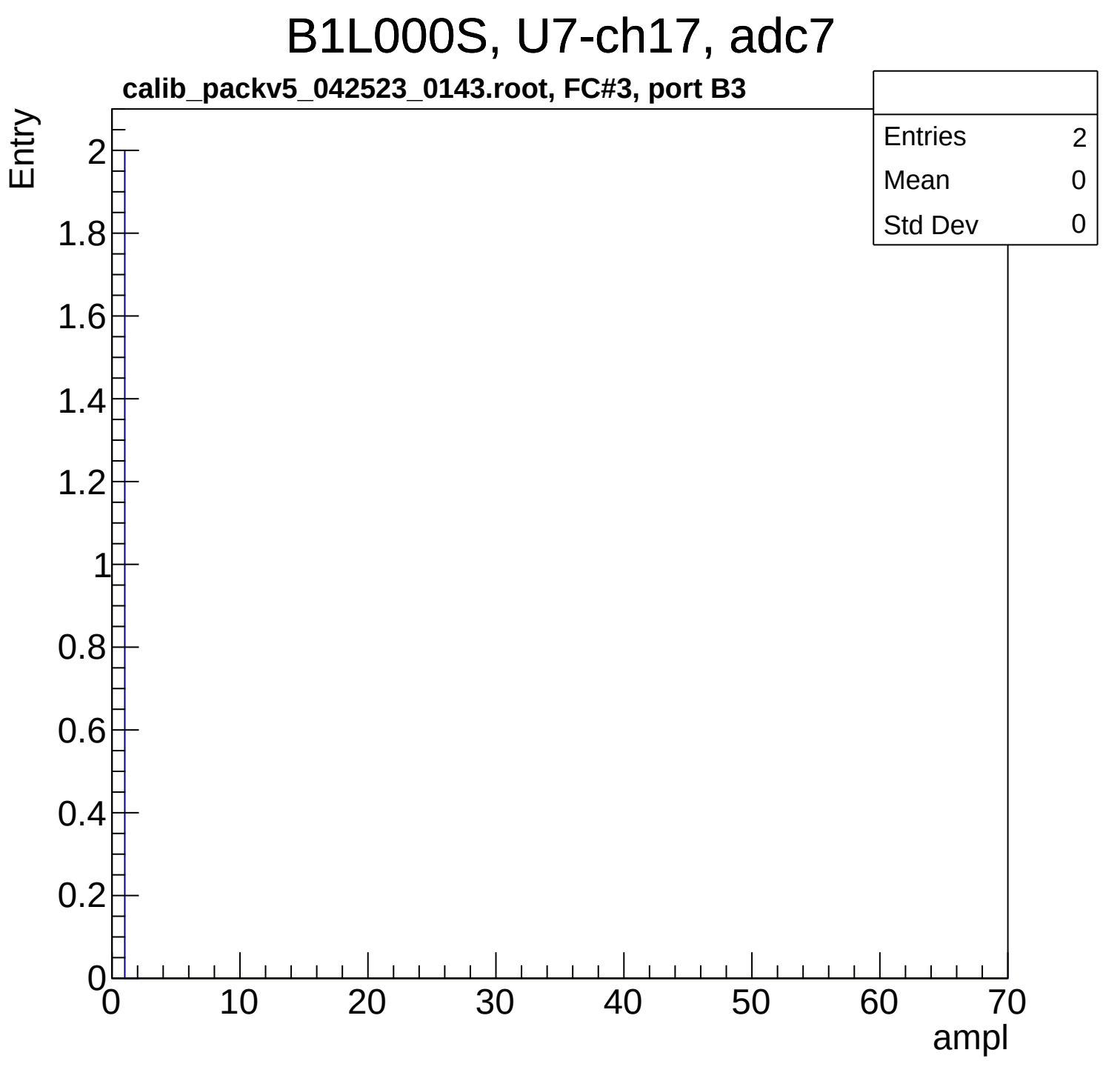
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch17, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.8
0.6
0.4
0.2
0
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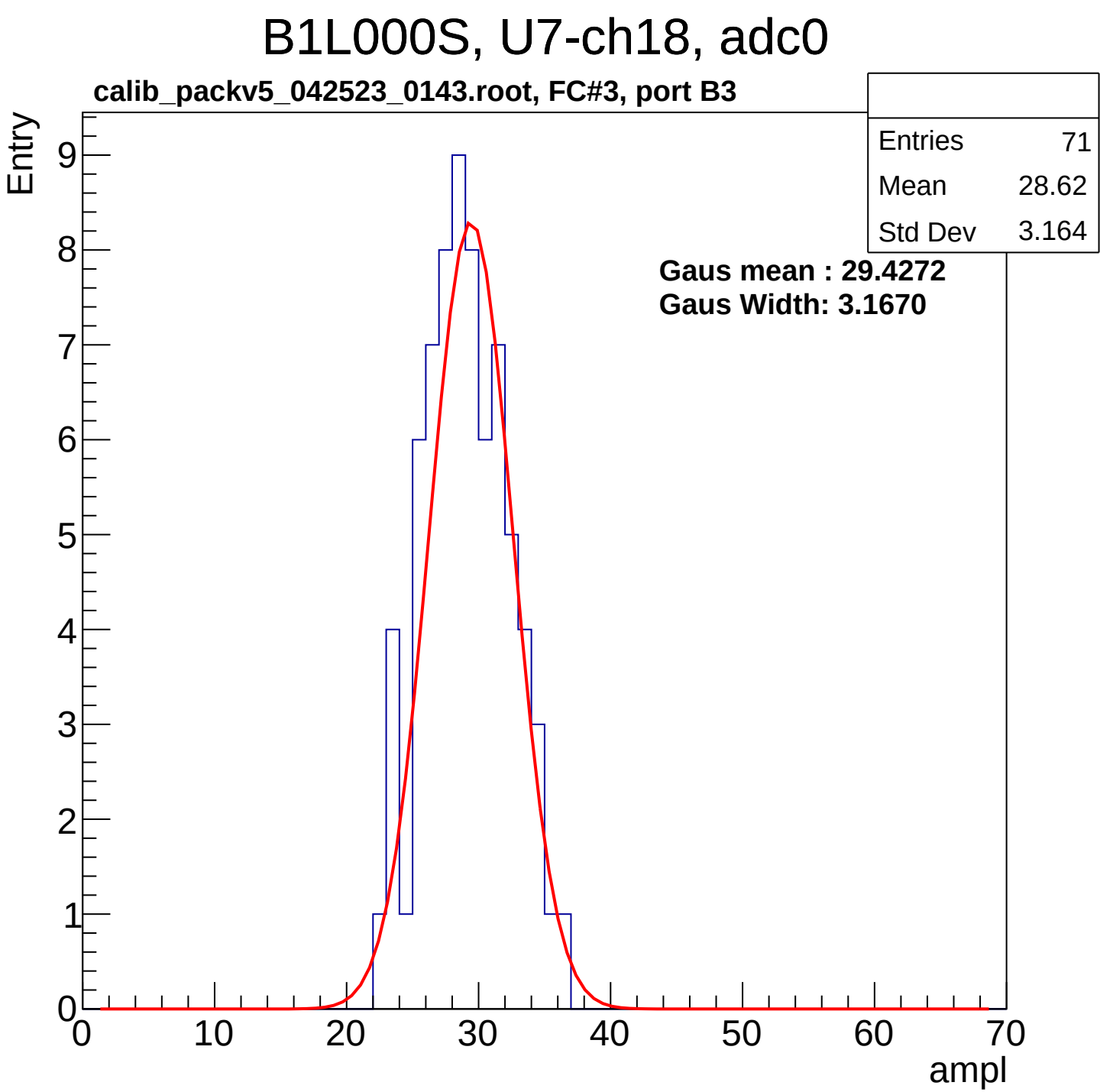
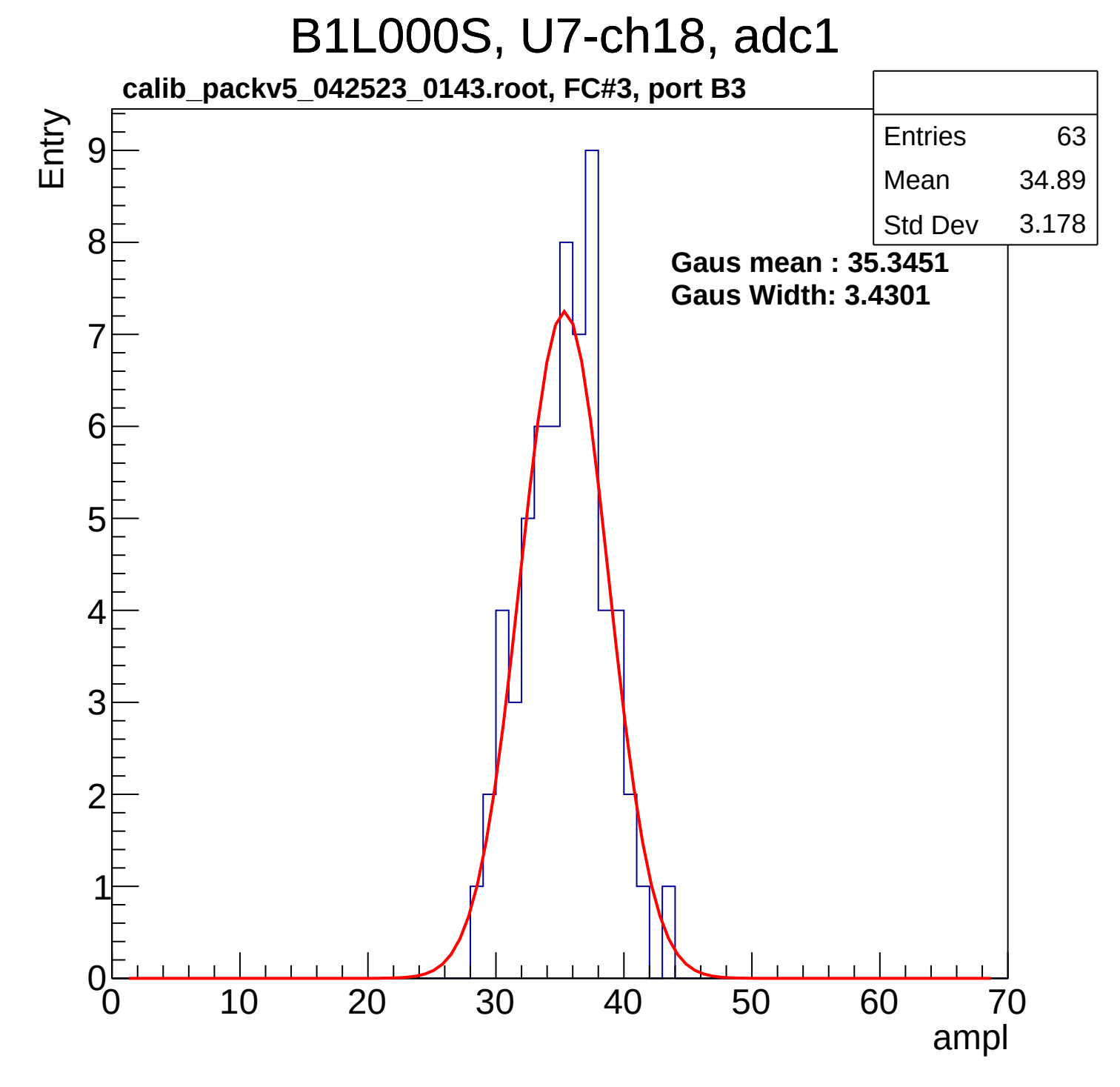
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch18, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 28.62
Std Dev 3.164
Gaus mean : 29.4272
Gaus Width: 3.1670
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch18, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.89
Std Dev 3.178
Gaus mean : 35.3451
Gaus Width: 3.4301
Entry
ampl
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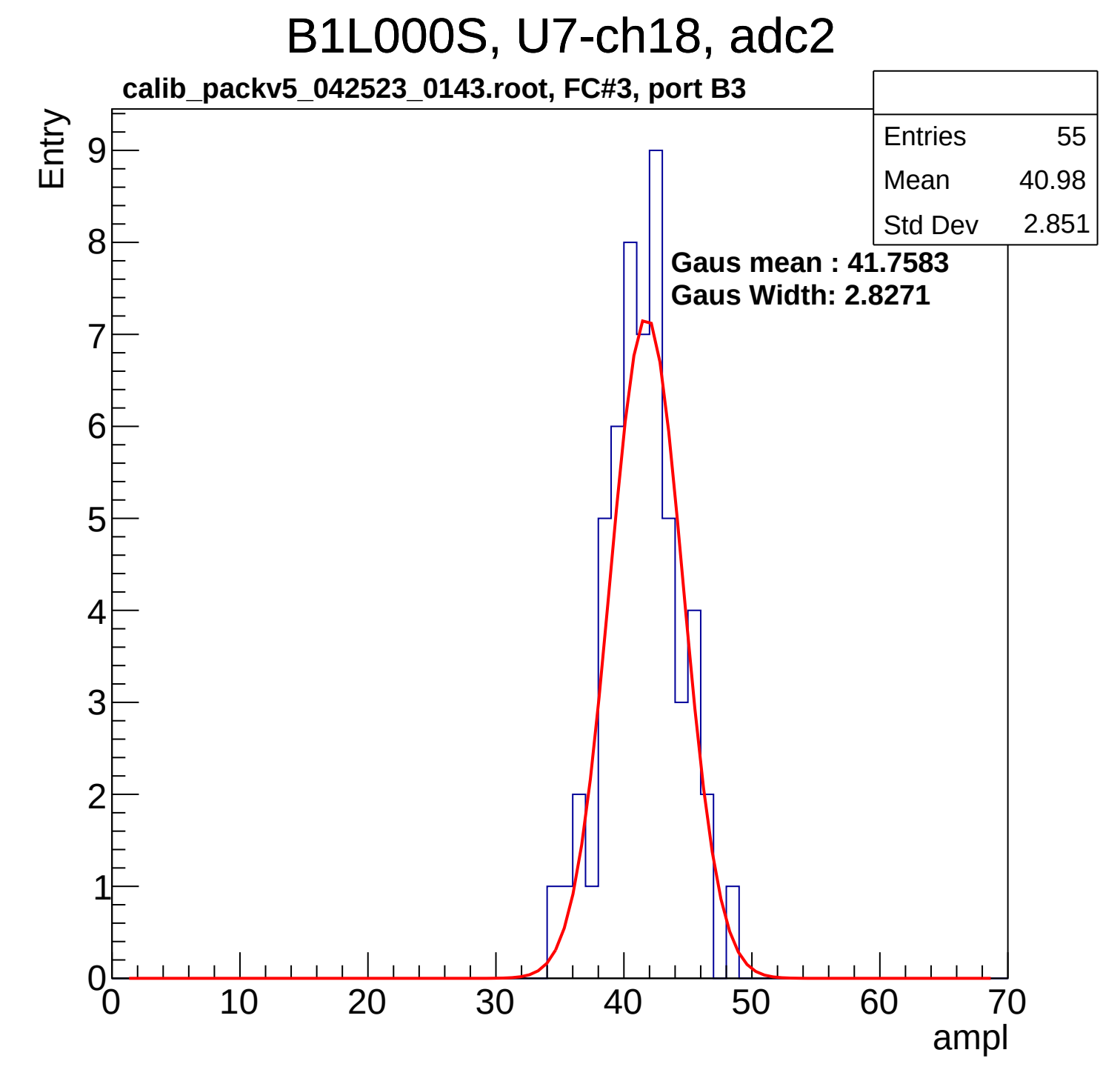
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch18, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 40.98
Std Dev 2.851
Gaus mean : 41.7583
Gaus Width: 2.8271
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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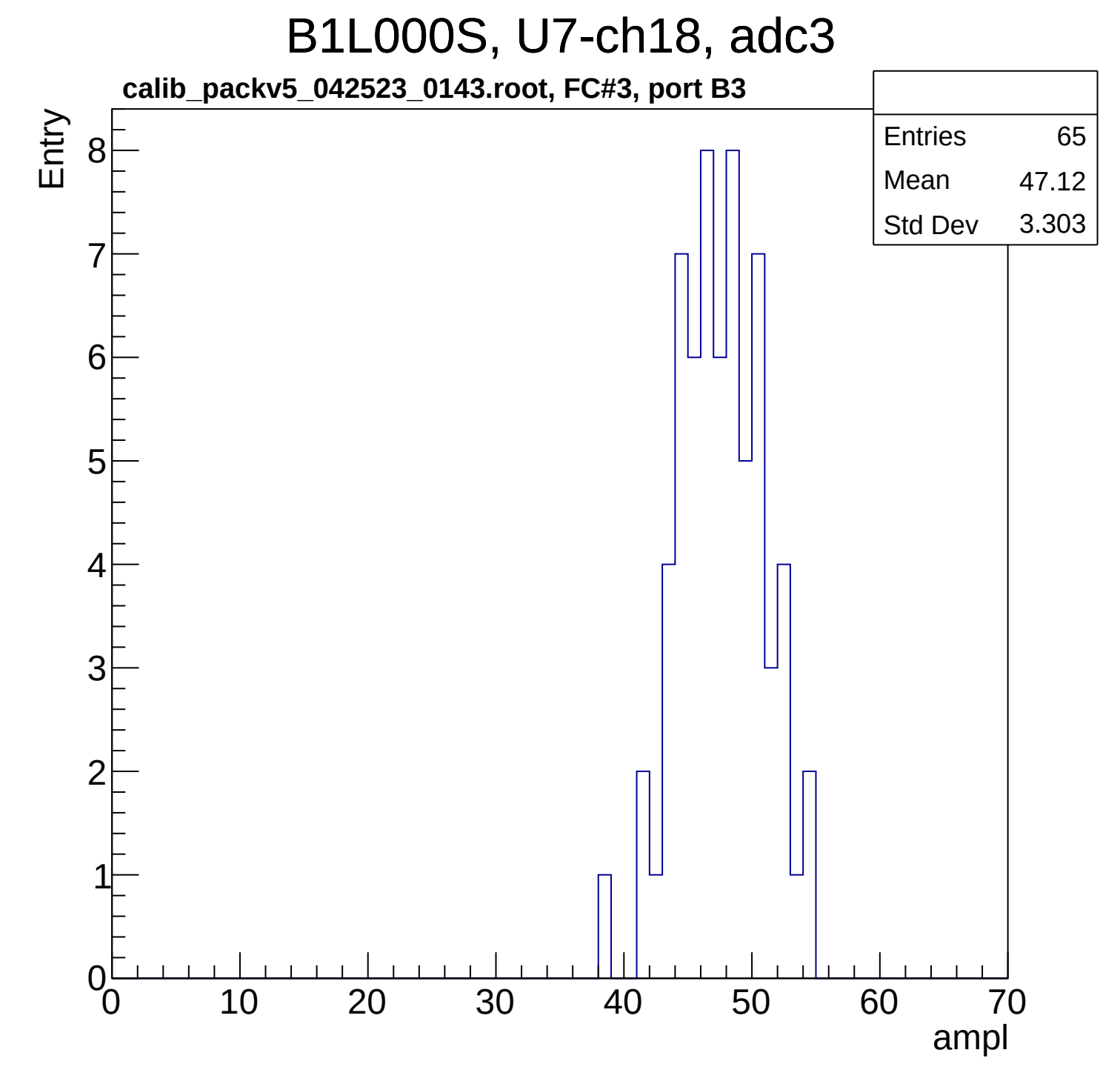
B1L000S, U7-ch18, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 47.12
Std Dev 3.303
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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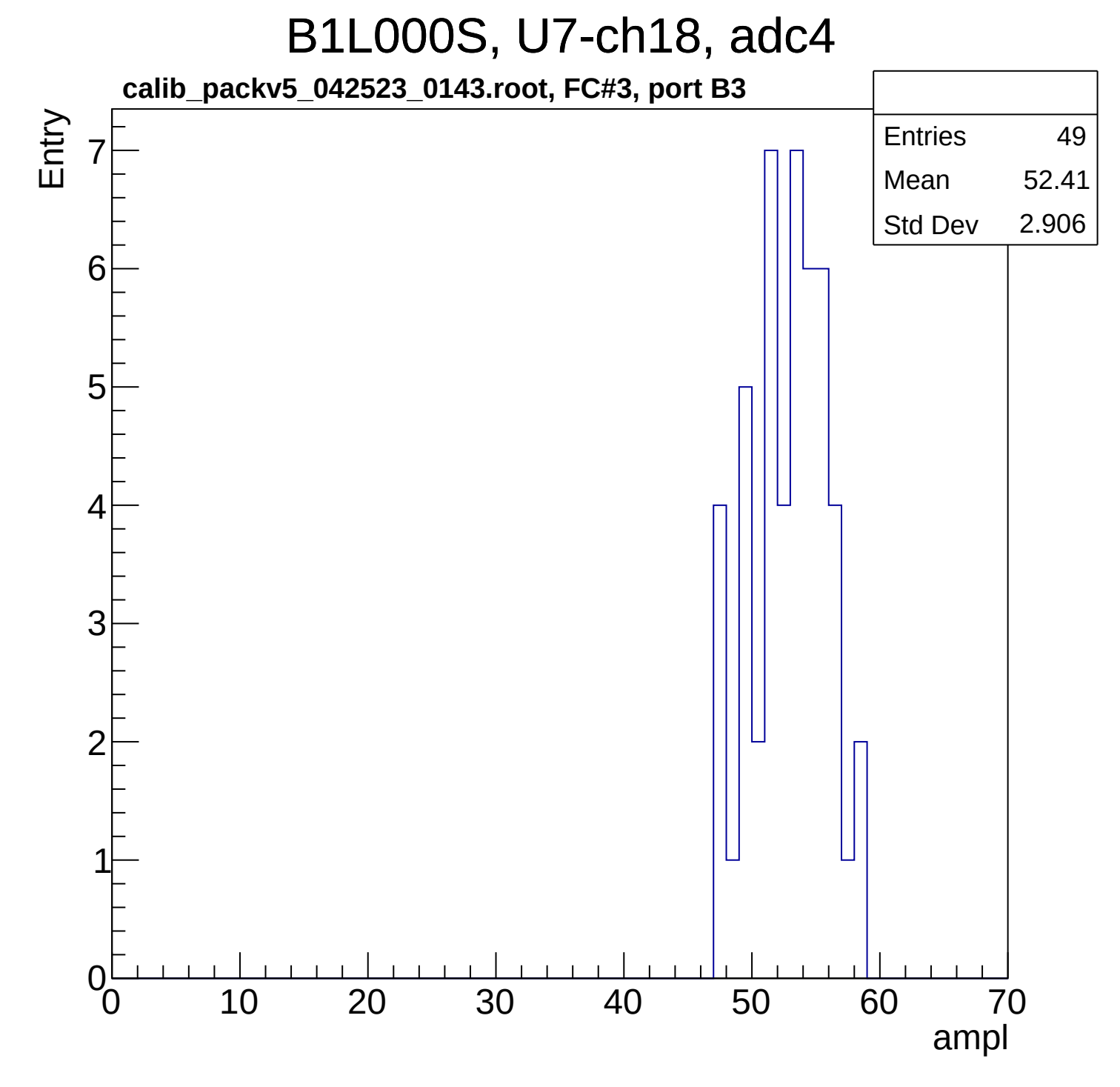
B1L000S, U7-ch18, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 52.41
Std Dev 2.906
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
50
60
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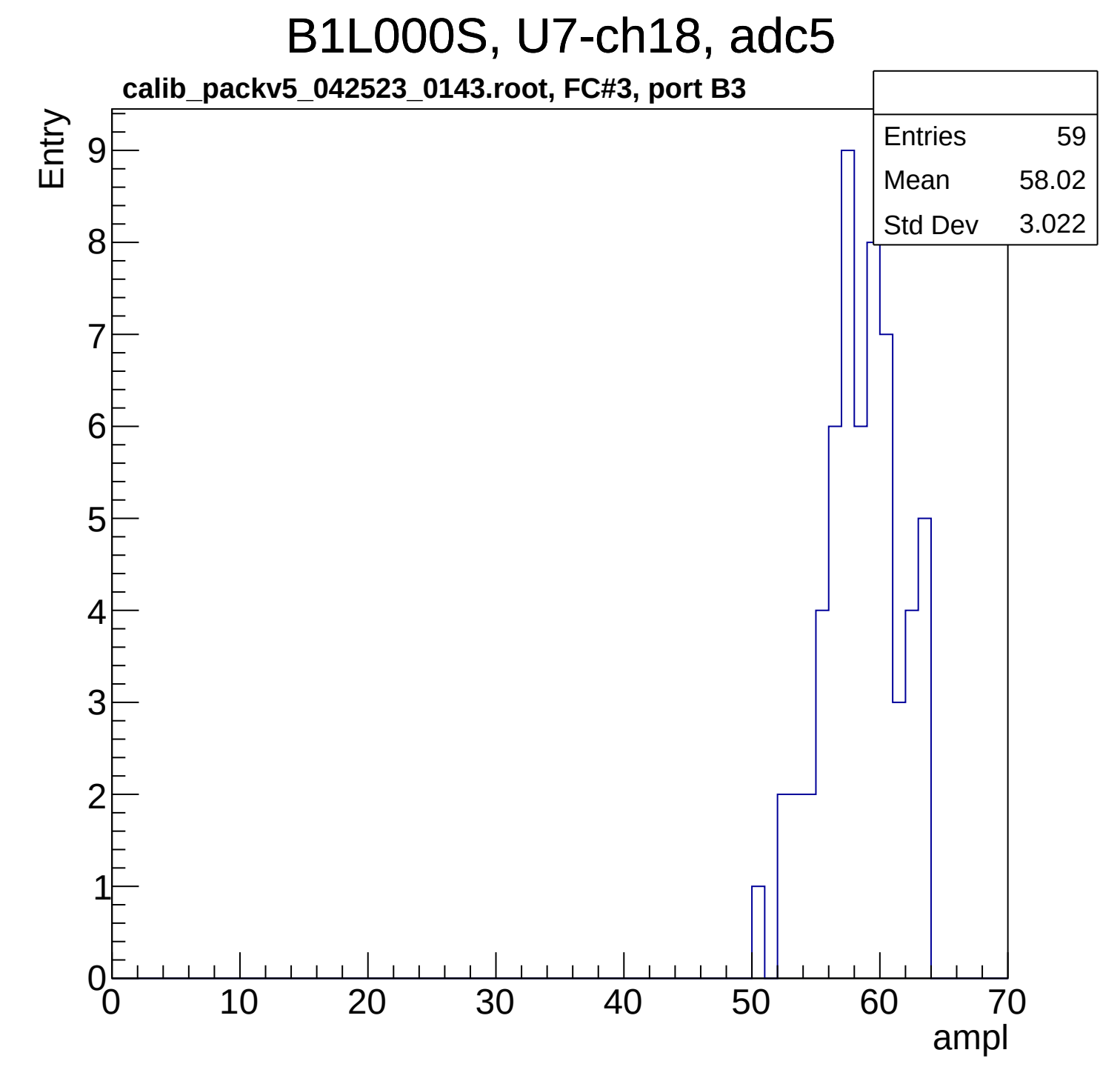
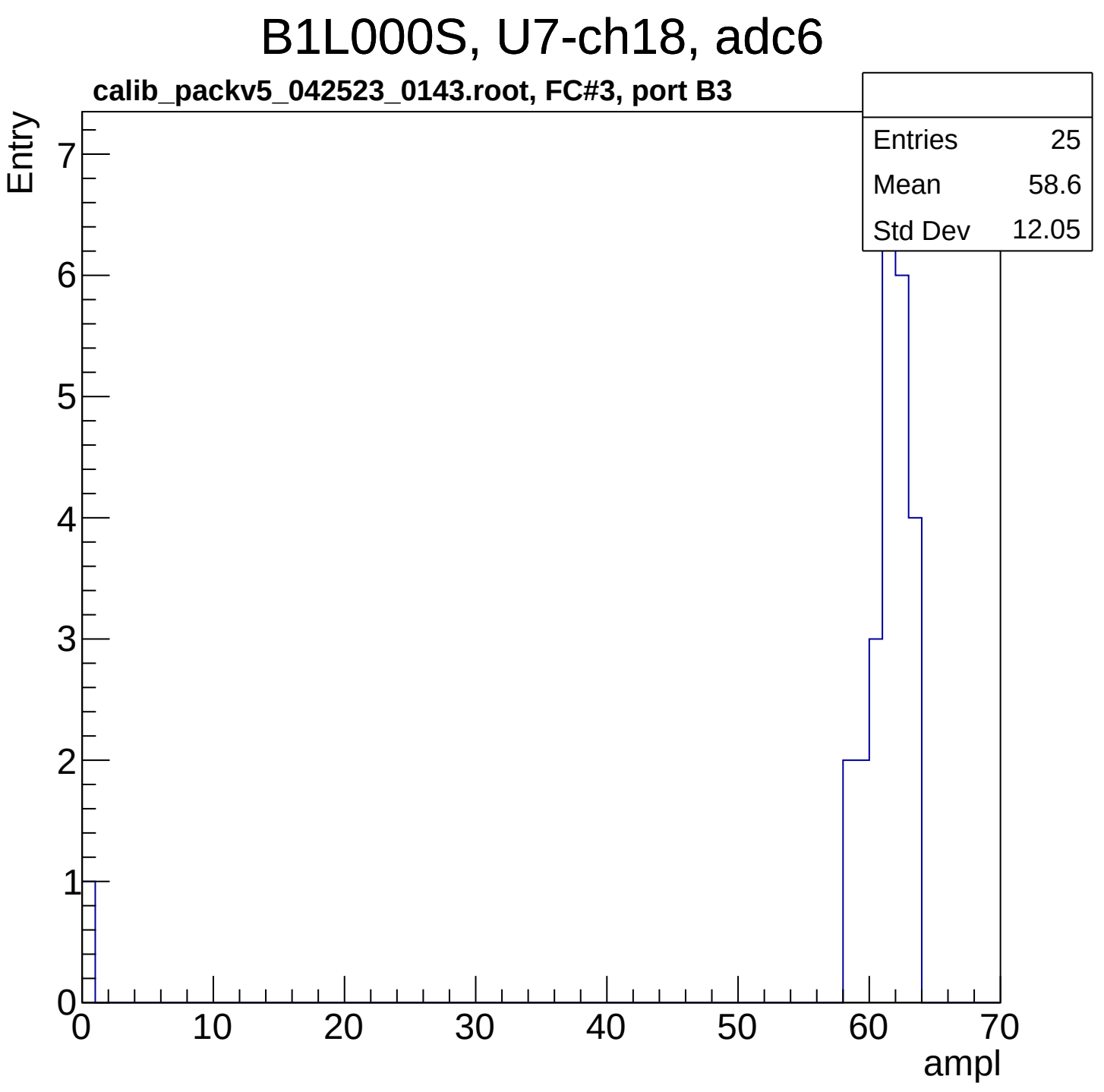
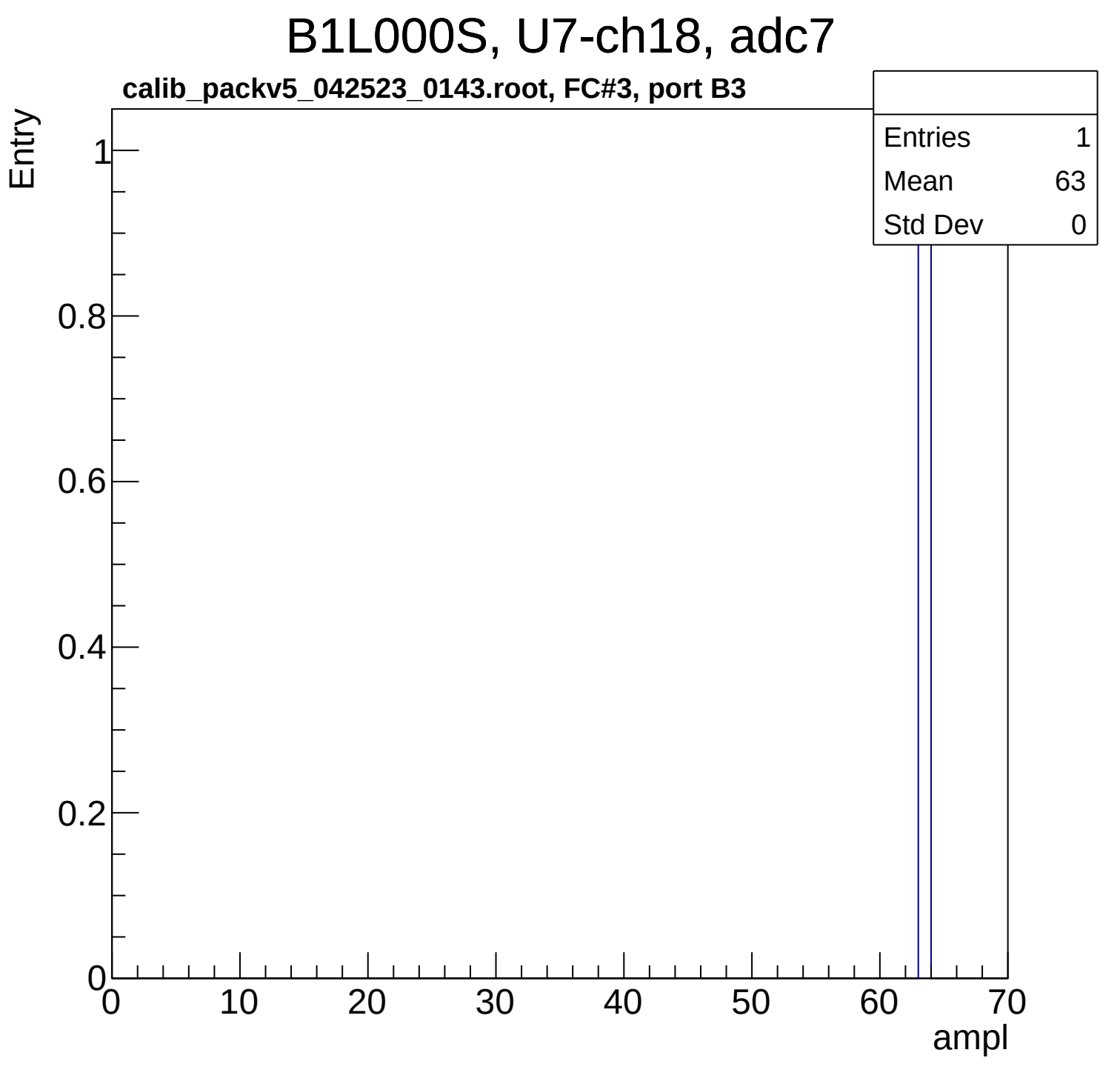
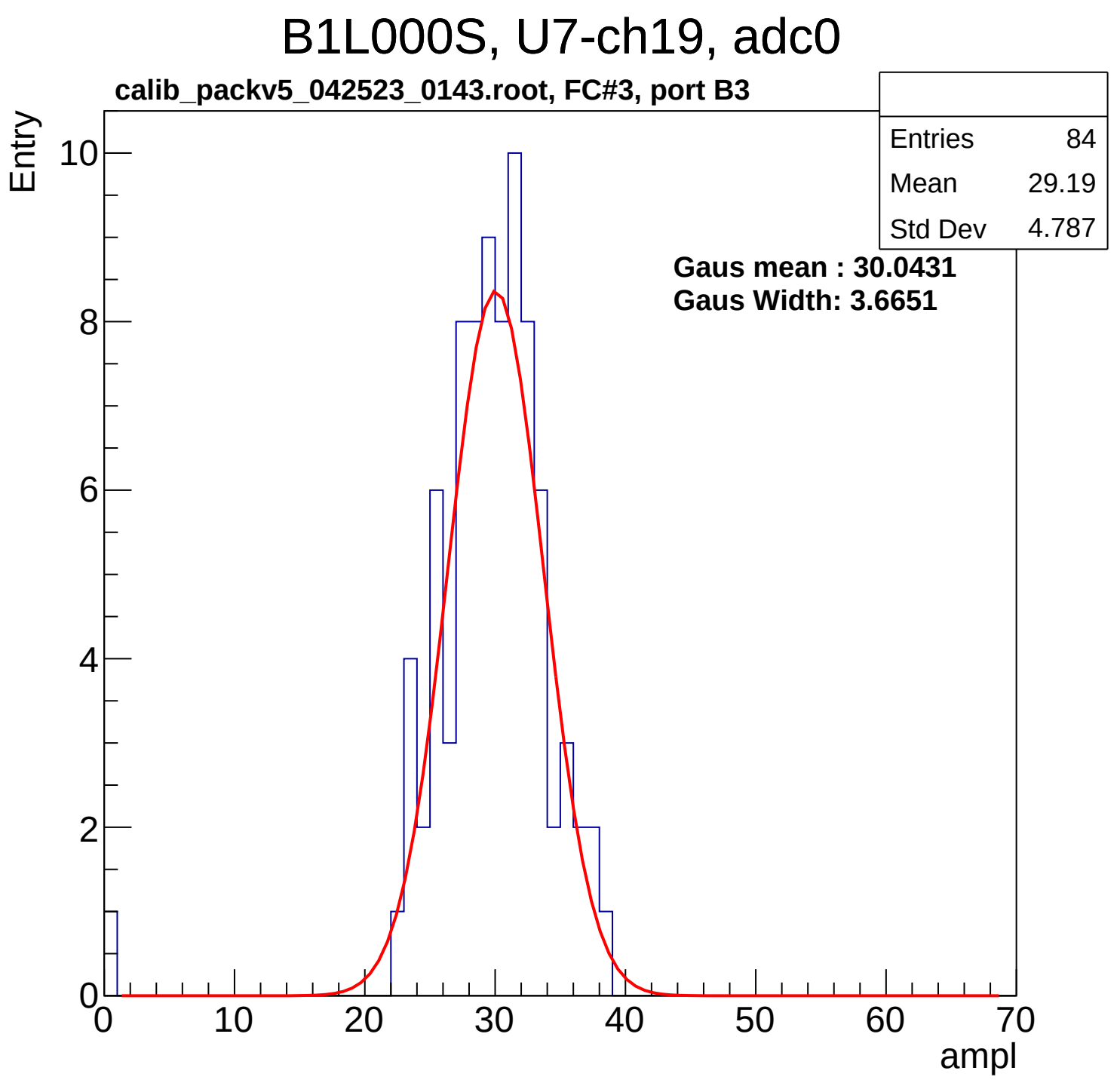

B1L000S, U7-ch18, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.02
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch18, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.6
Std Dev 12.05
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch18, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch19, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 29.19
Std Dev 4.787
Gaus mean : 30.0431
Gaus Width: 3.6651
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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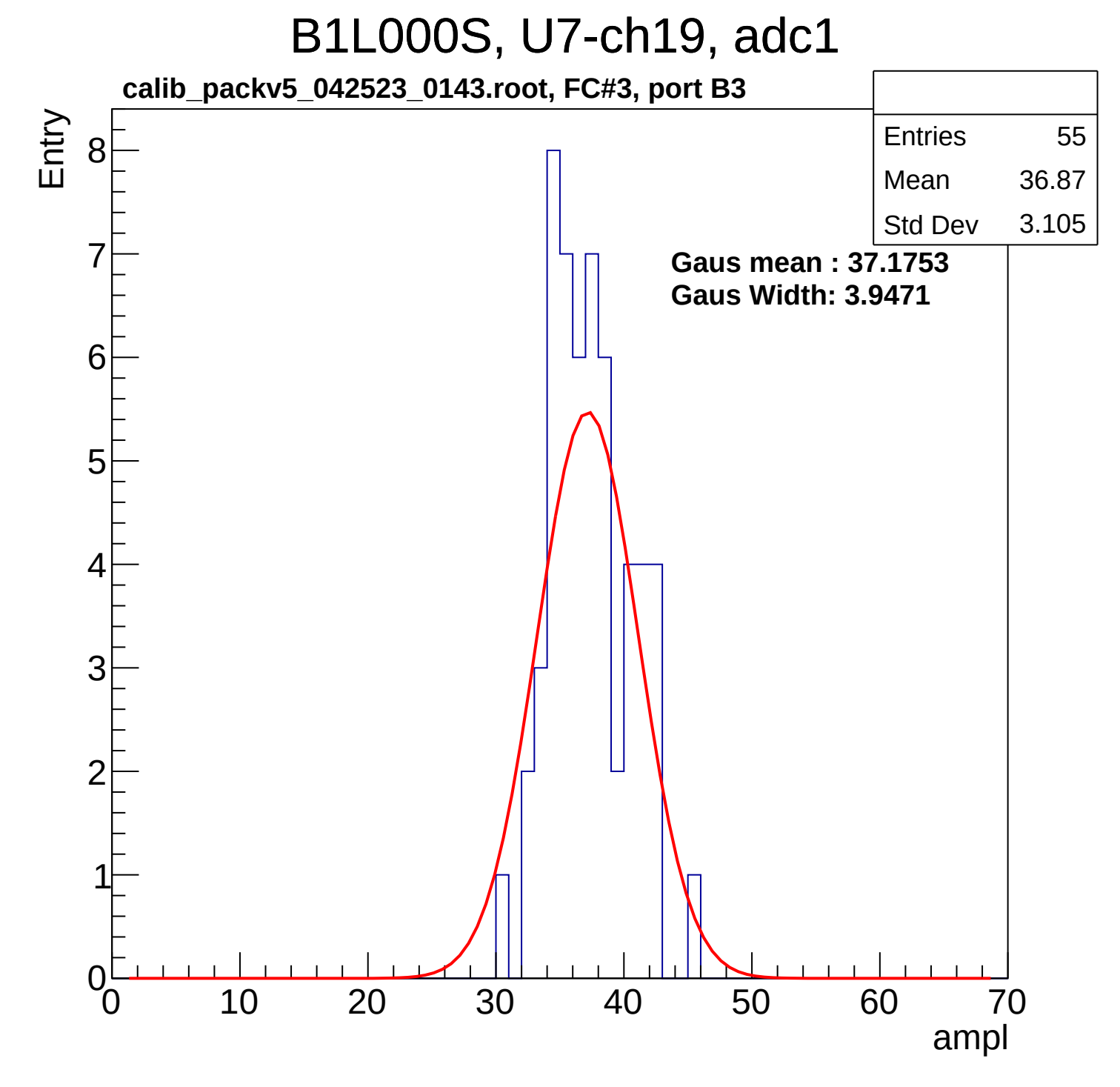
B1L000S, U7-ch19, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 36.87
Std Dev 3.105
Gaus mean : 37.1753
Gaus Width: 3.9471
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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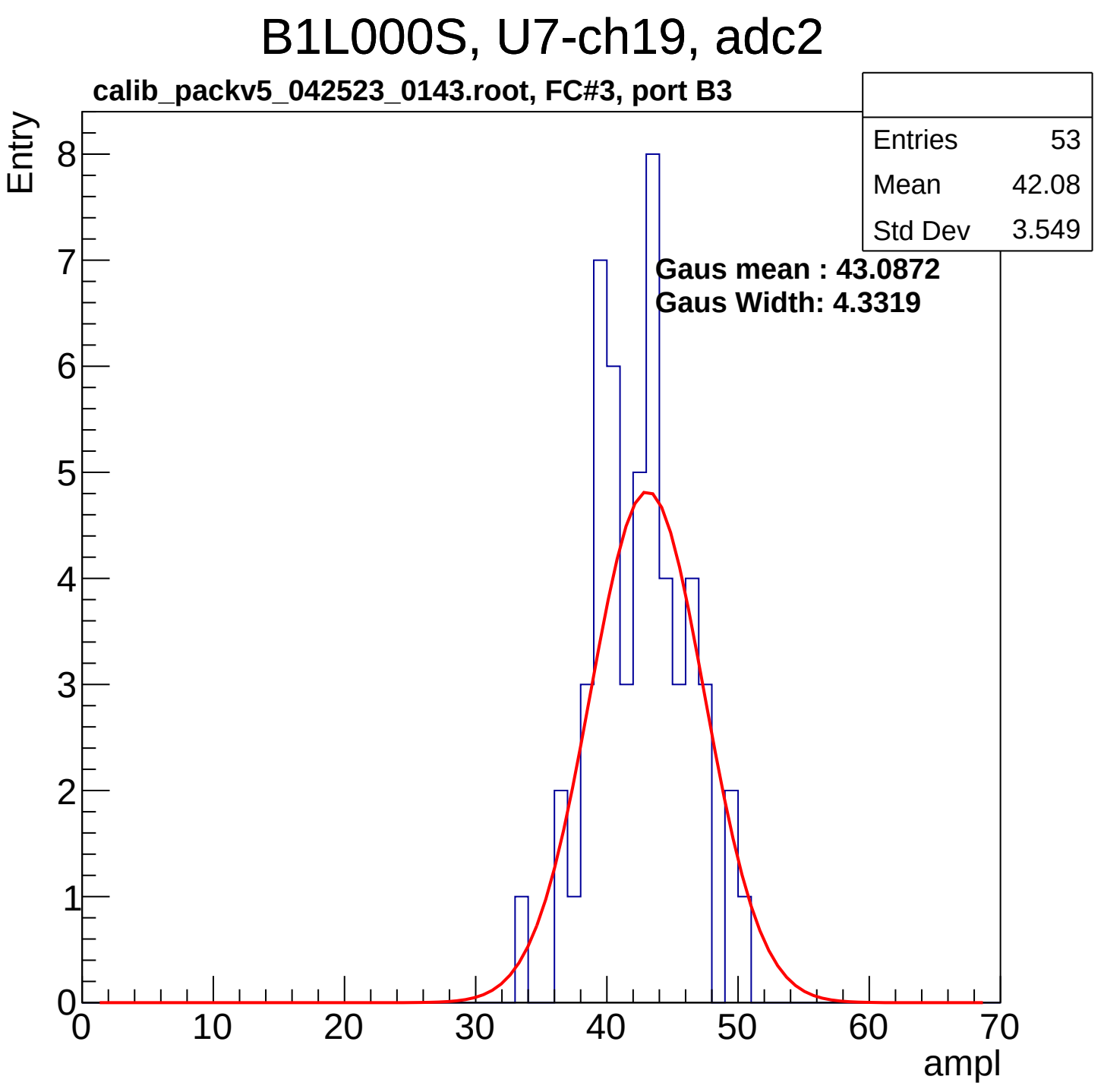
60
70


B1L000S, U7-ch19, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 42.08
Std Dev 3.549
Gaus mean : 43.0872
Gaus Width: 4.3319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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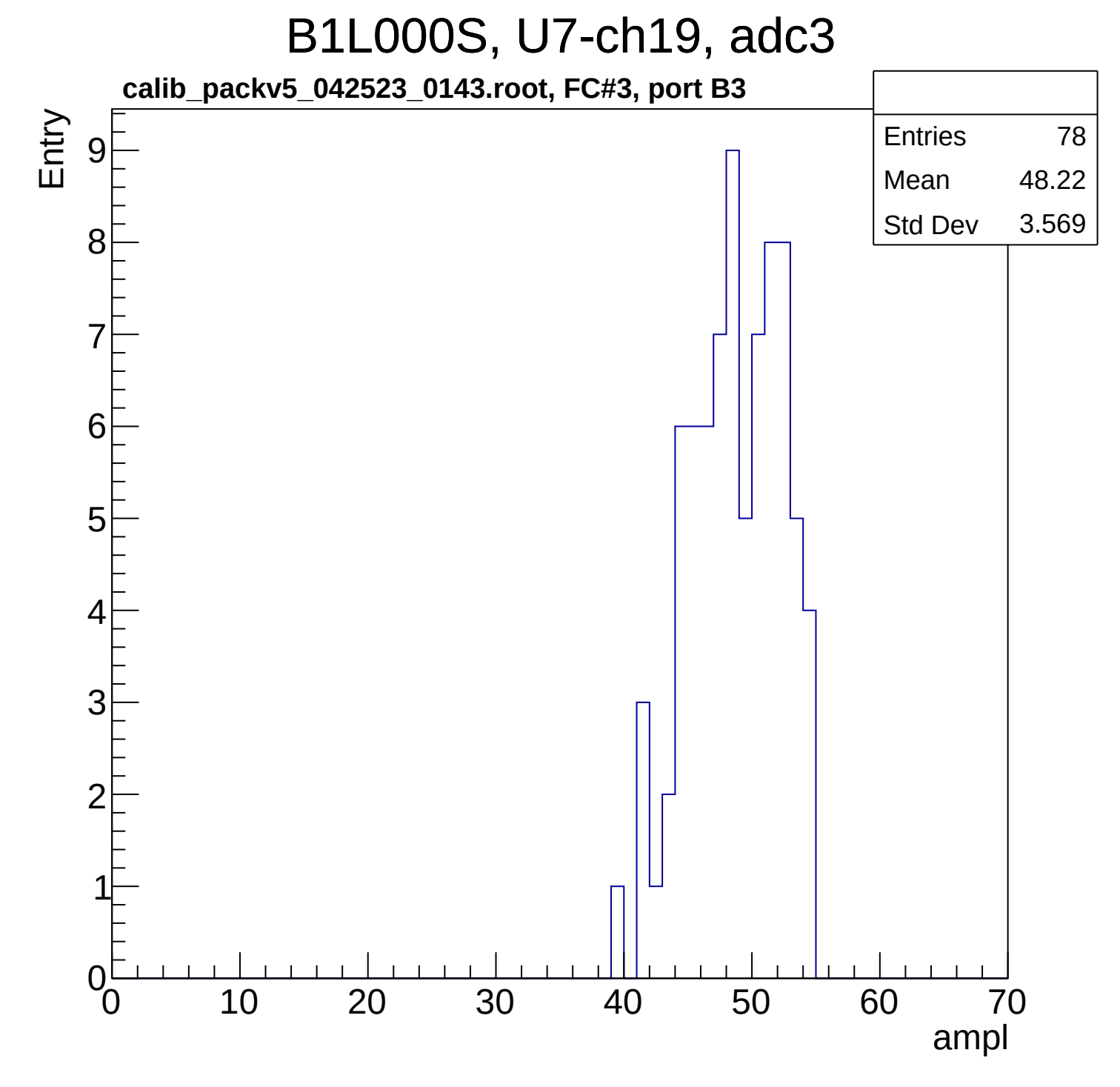
B1L000S, U7-ch19, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 48.22
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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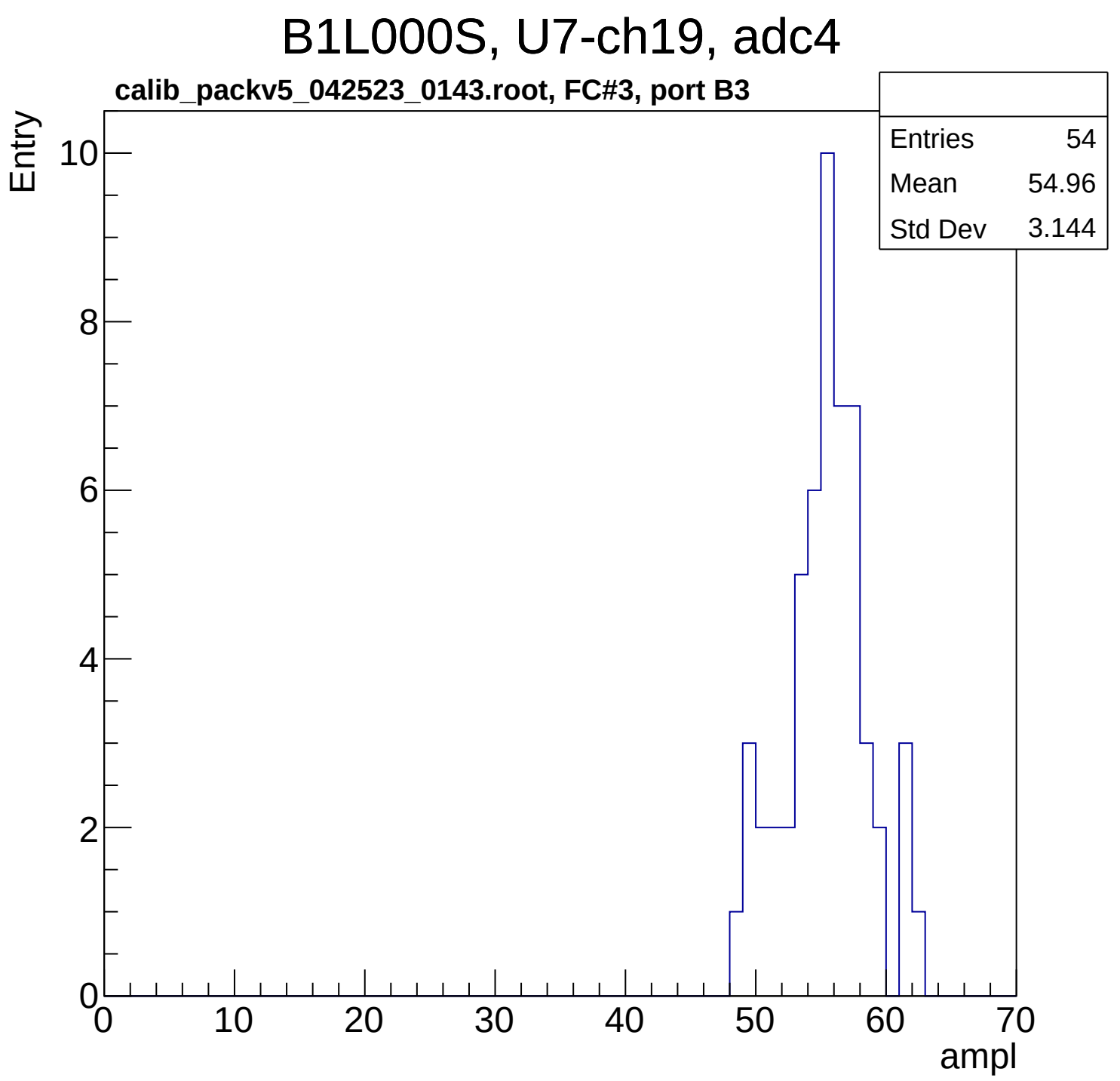
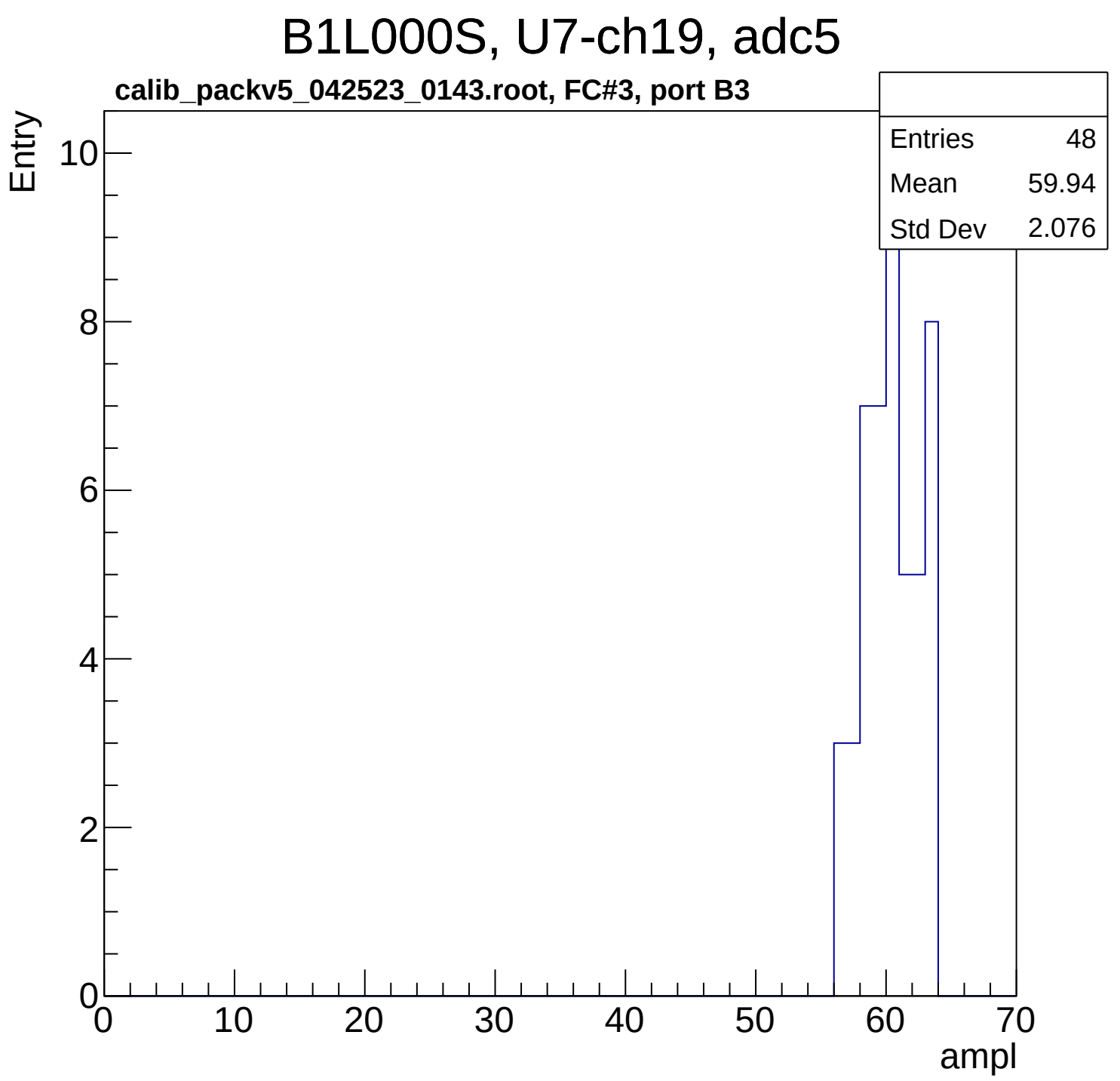
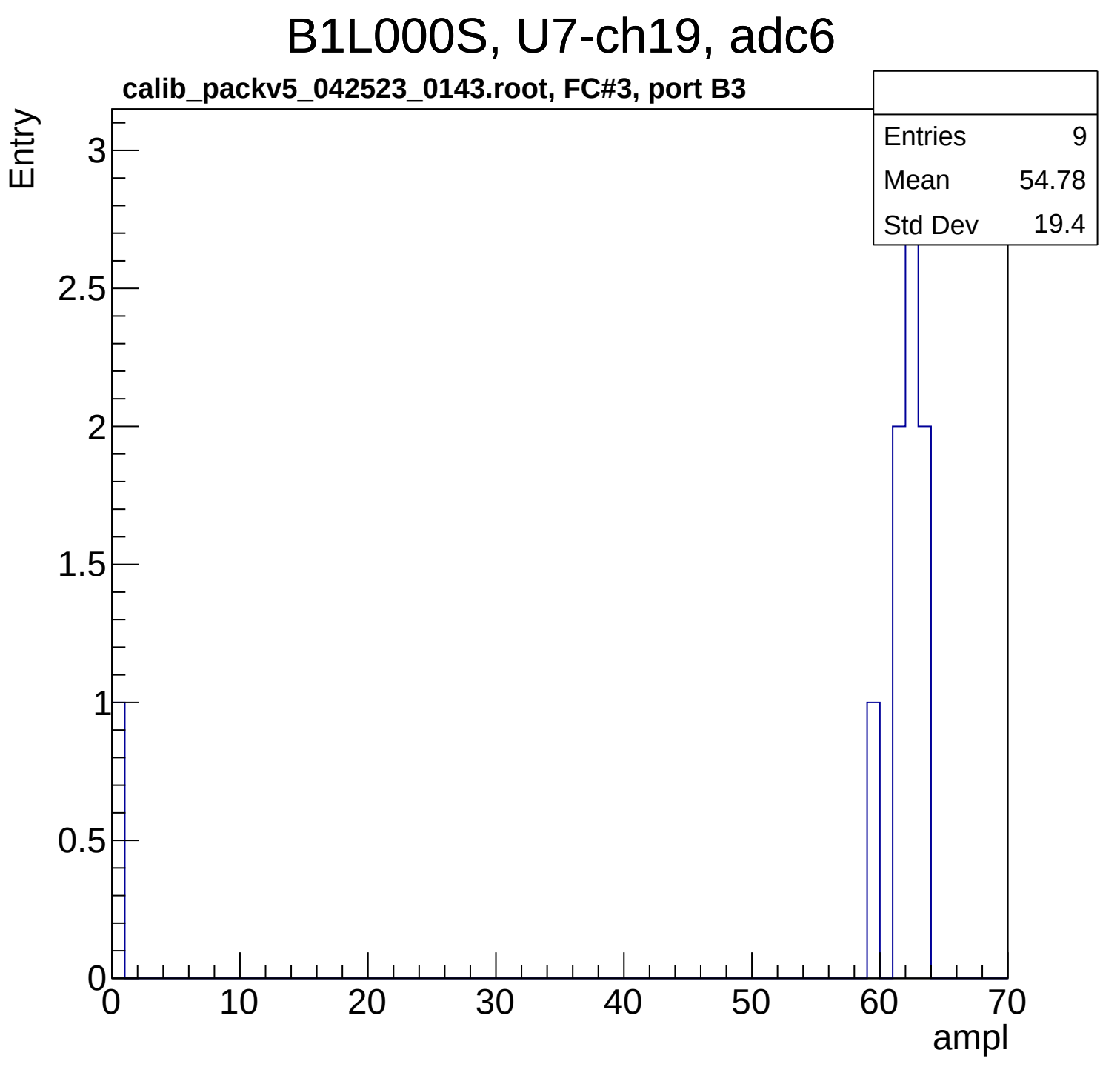
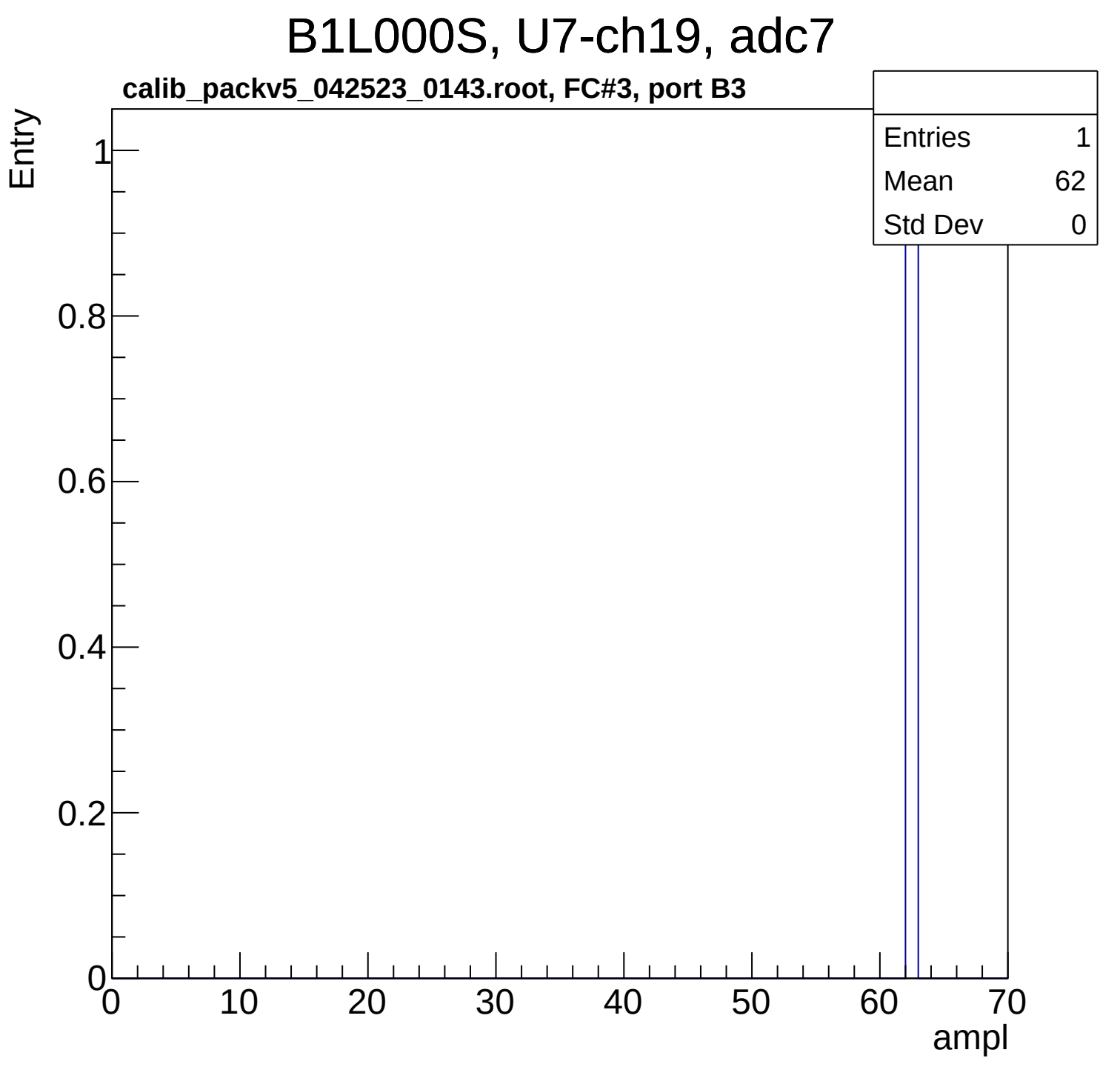
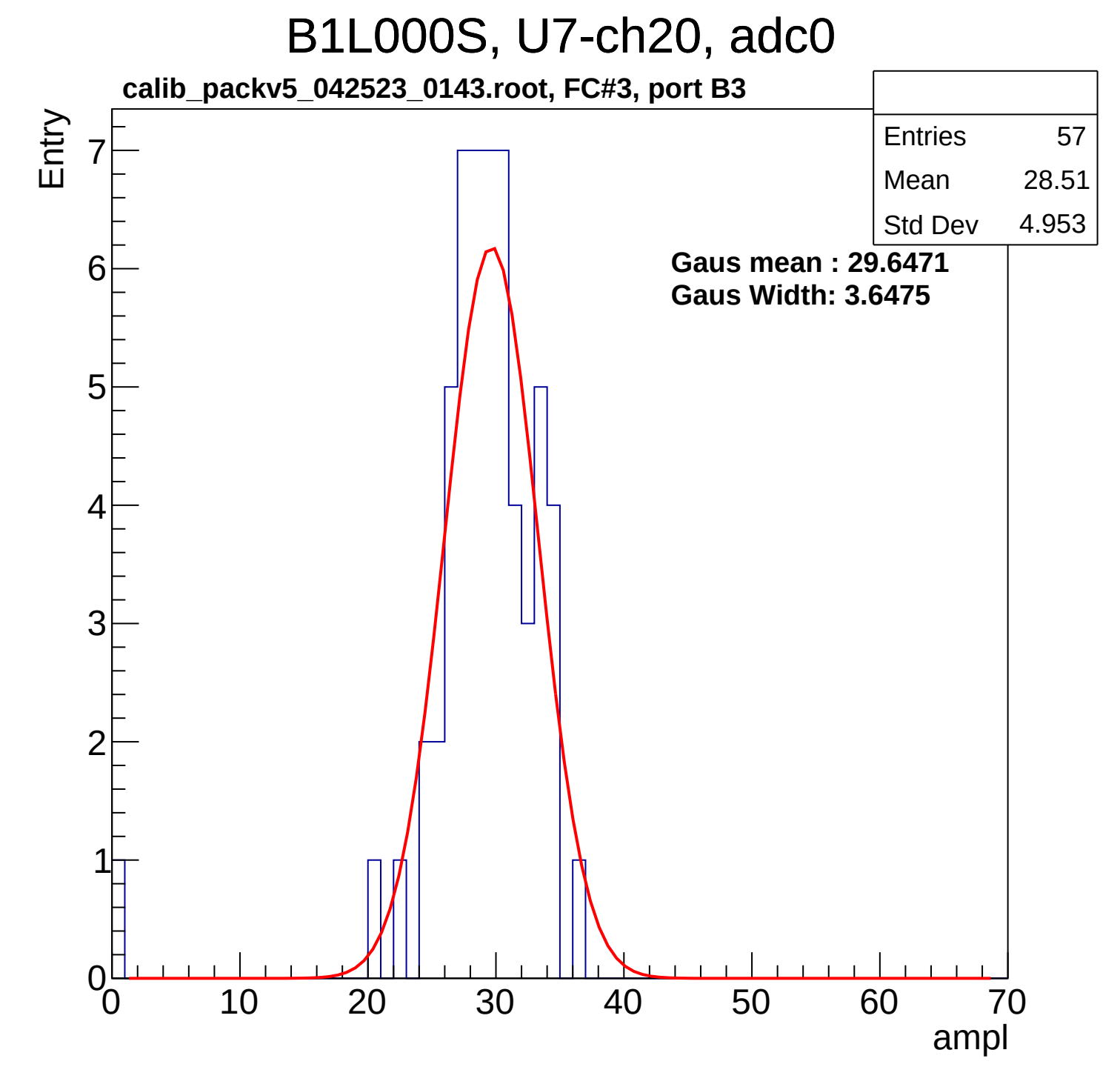
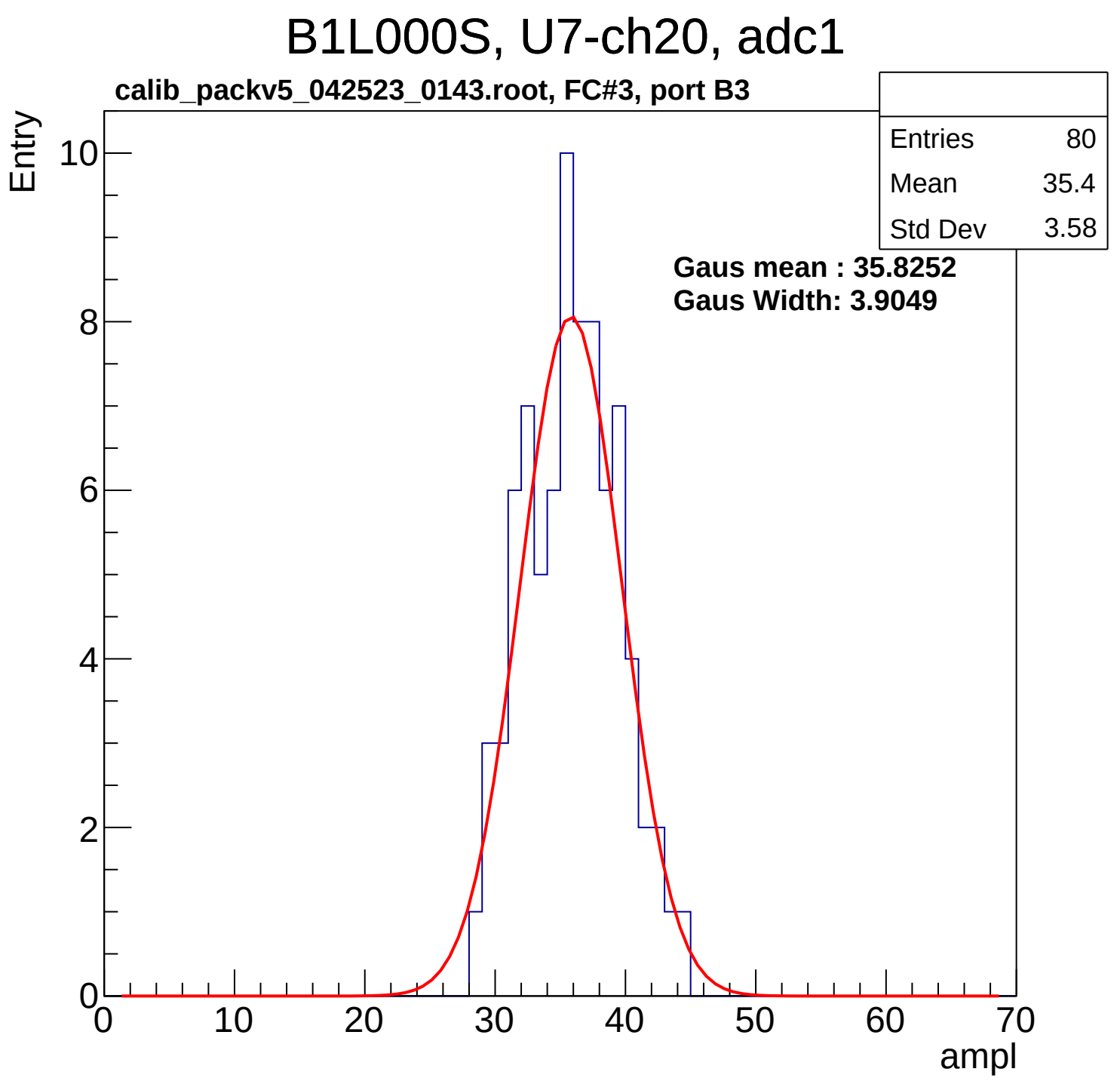

B1L000S, U7-ch19, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.96
Std Dev 3.144
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

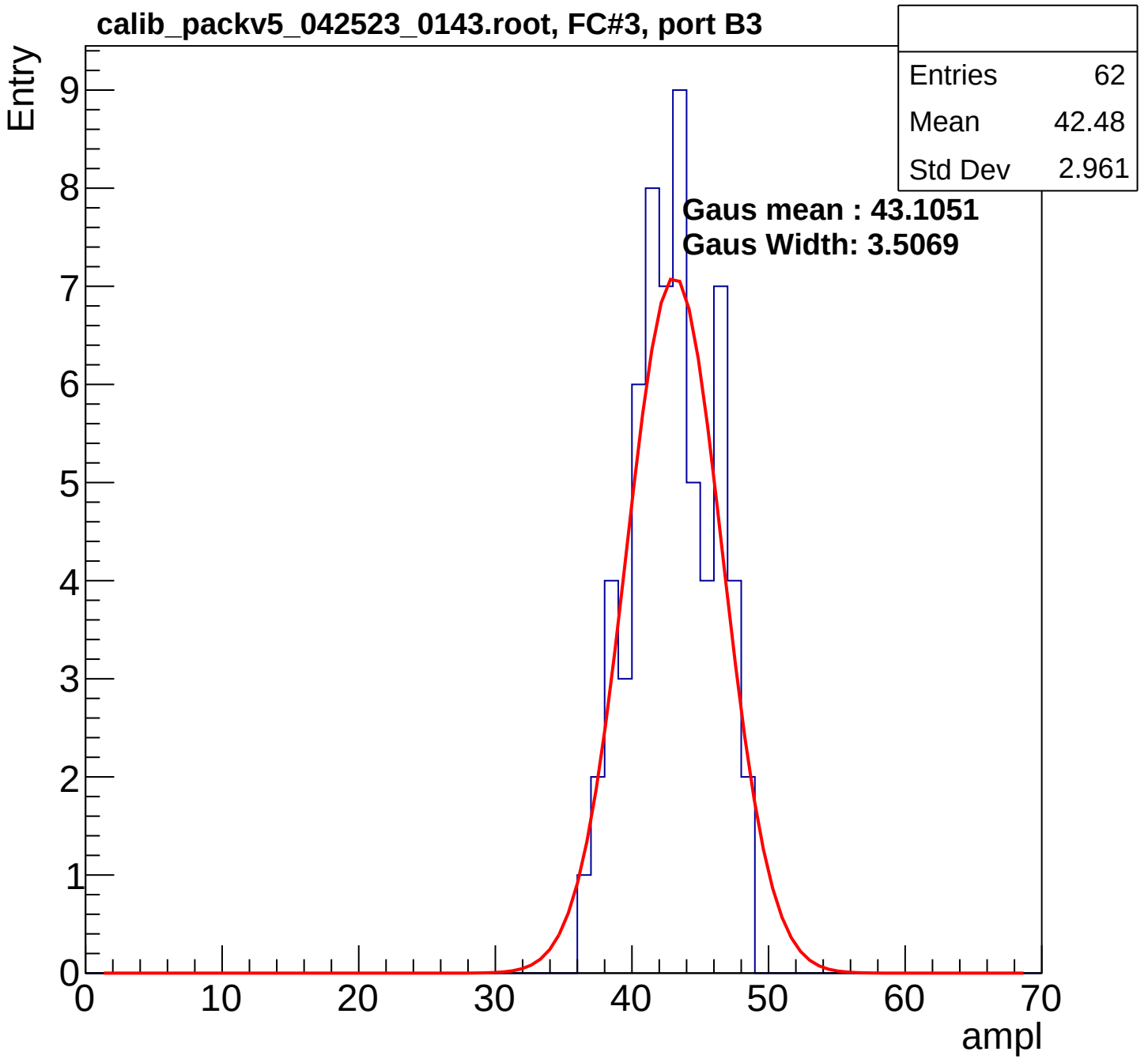

B1L000S, U7-ch19, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 59.94
Std Dev 2.076
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch19, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 54.78
Std Dev 19.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch19, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch20, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.51
Std Dev 4.953
Gaus mean : 29.6471
Gaus Width: 3.6475
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch20, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 35.4
Std Dev 3.58
Gaus mean : 35.8252
Gaus Width: 3.9049
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch20, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 42.48
Std Dev 2.961
Gaus mean : 43.1051
Gaus Width: 3.5069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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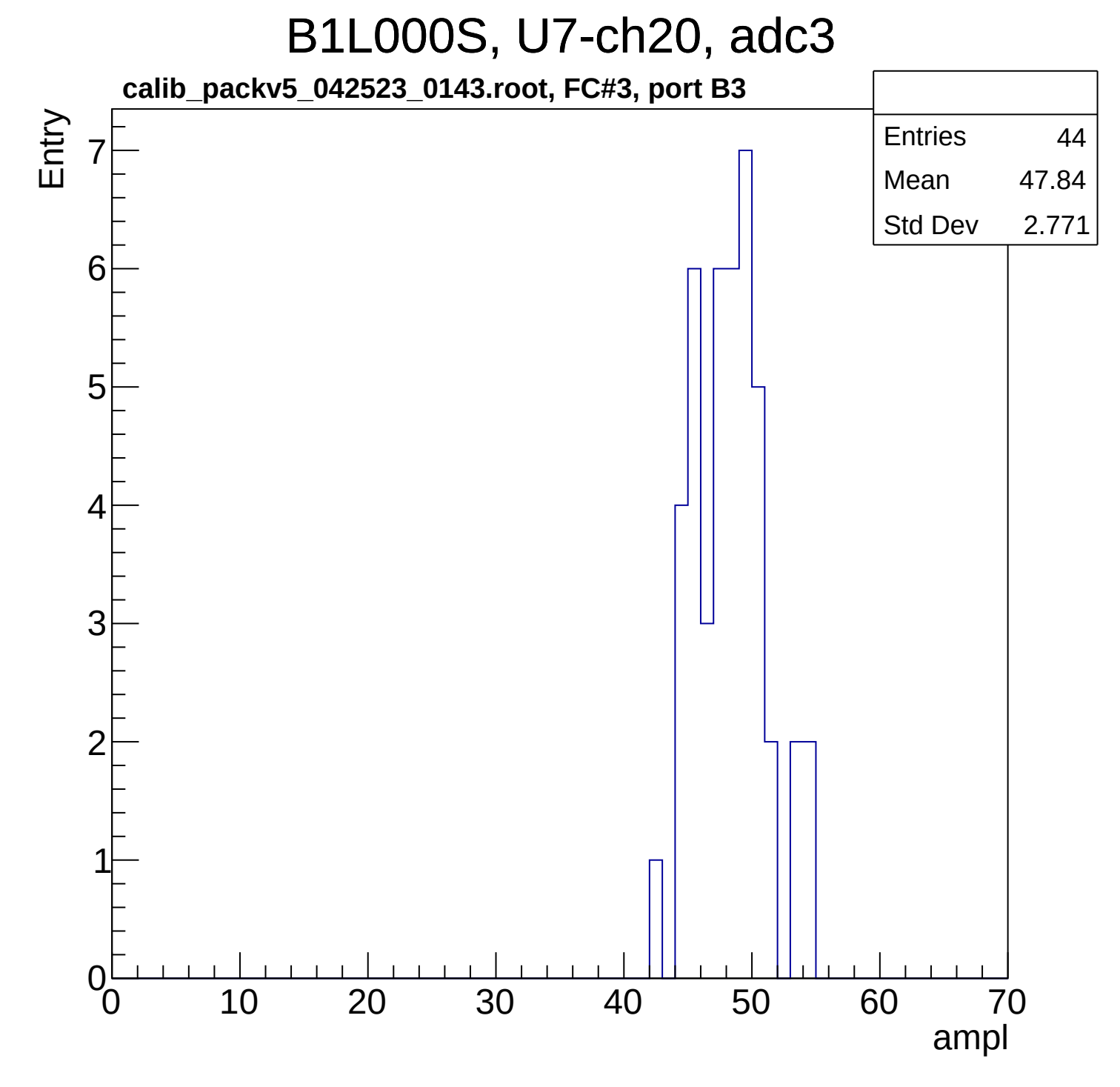
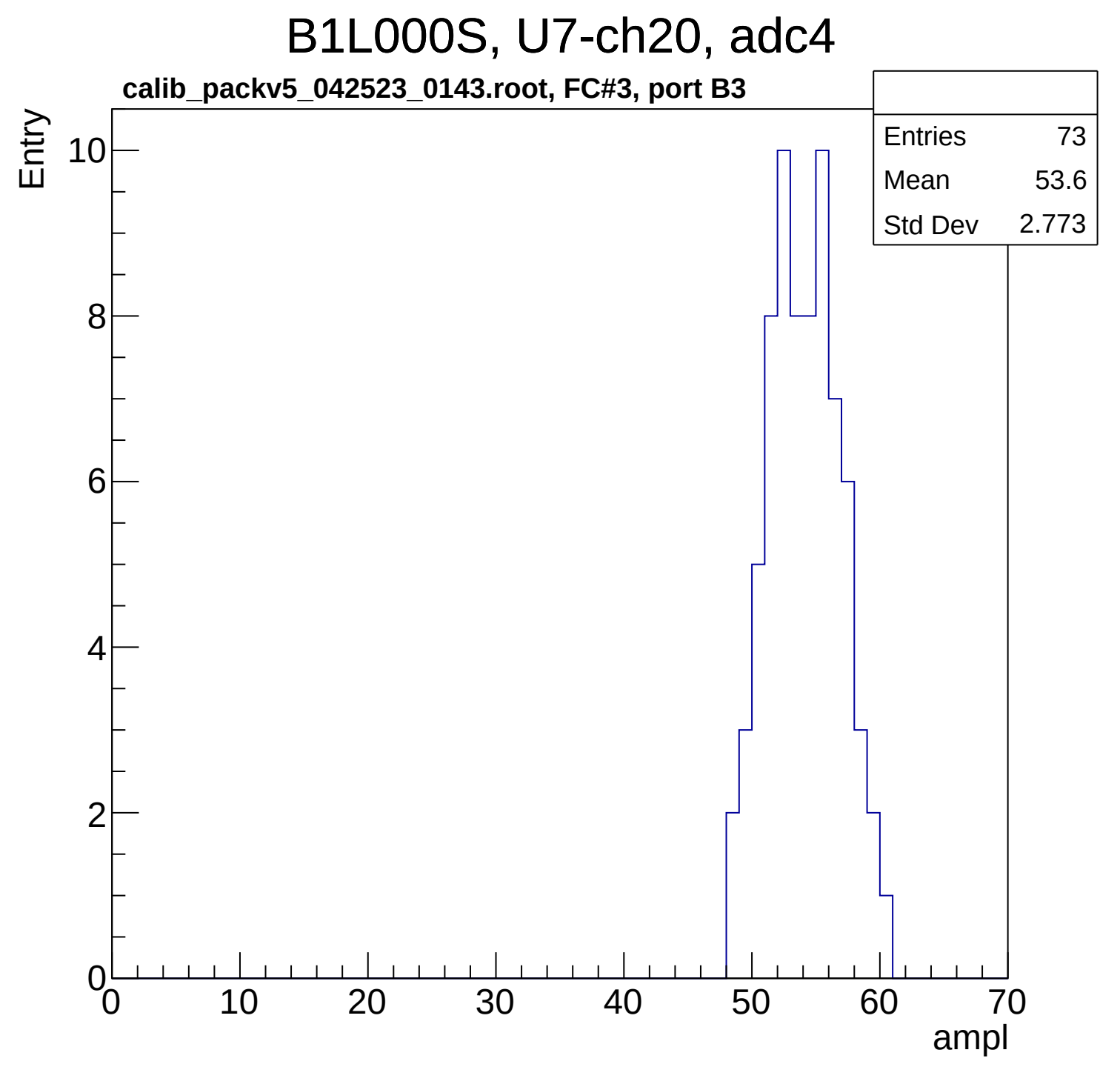
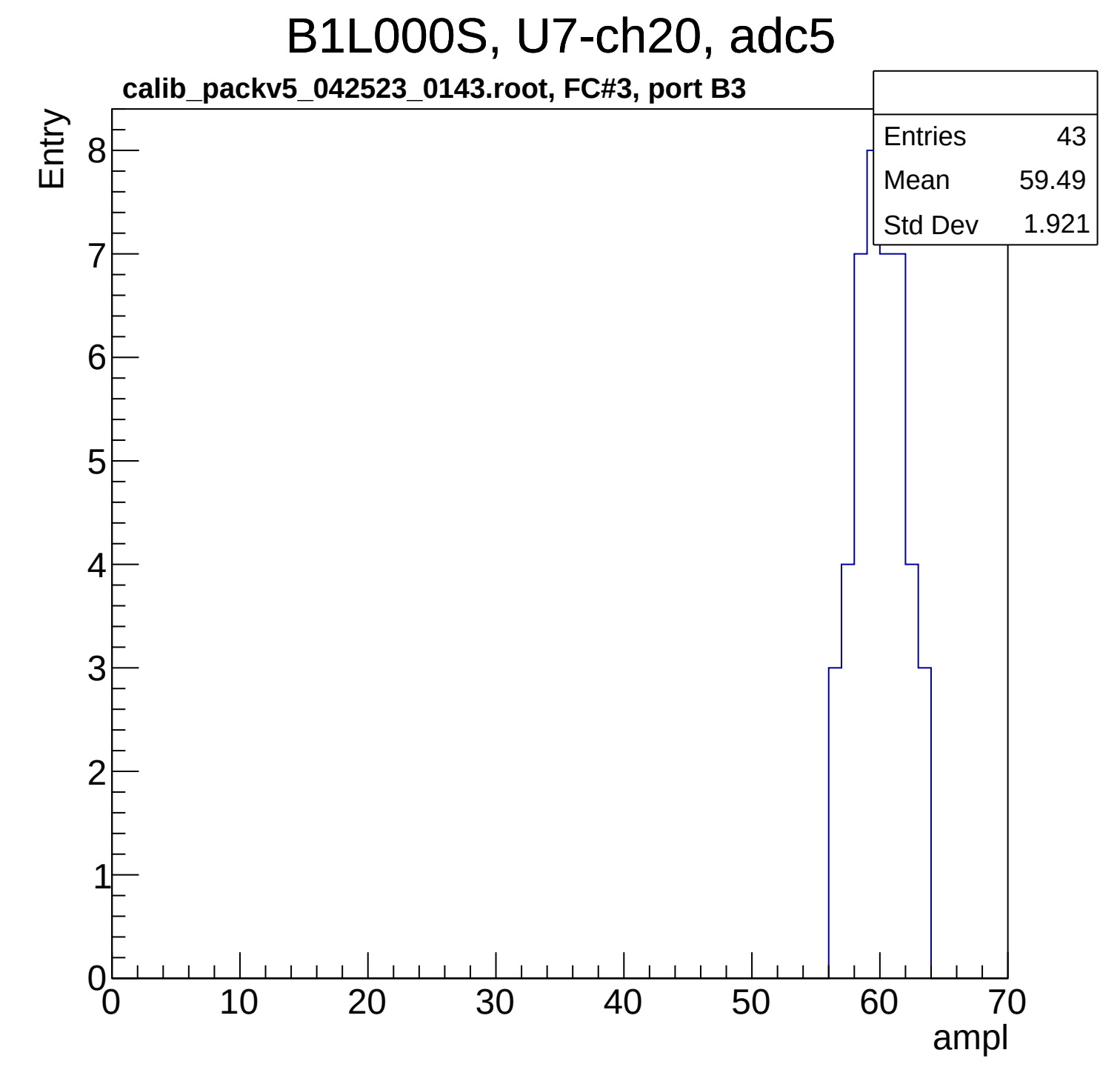
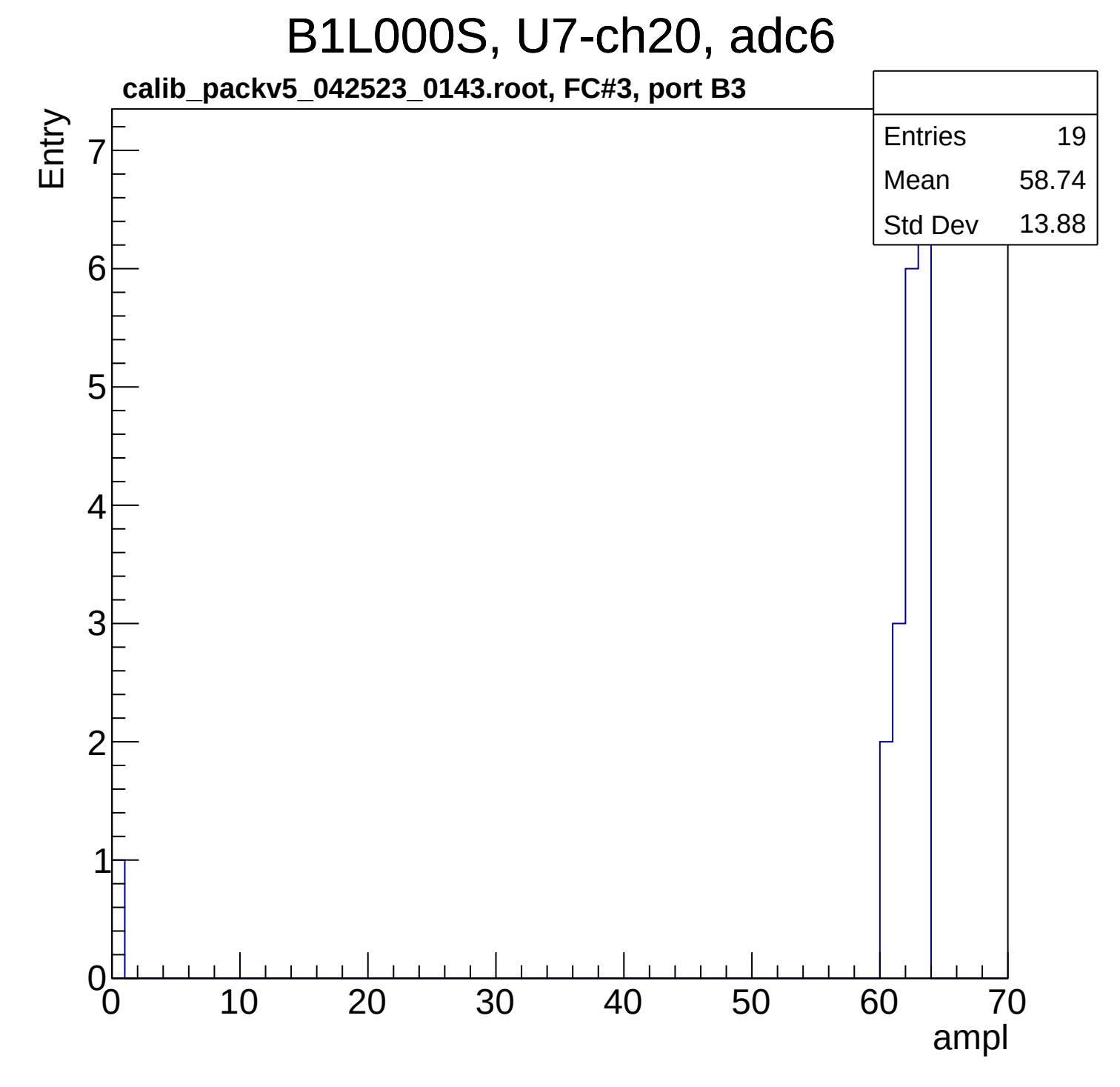

B1L000S, U7-ch20, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 47.84
Std Dev 2.771
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch20, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 53.6
Std Dev 2.773
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch20, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 59.49
Std Dev 1.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch20, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.74
Std Dev 13.88
Entry
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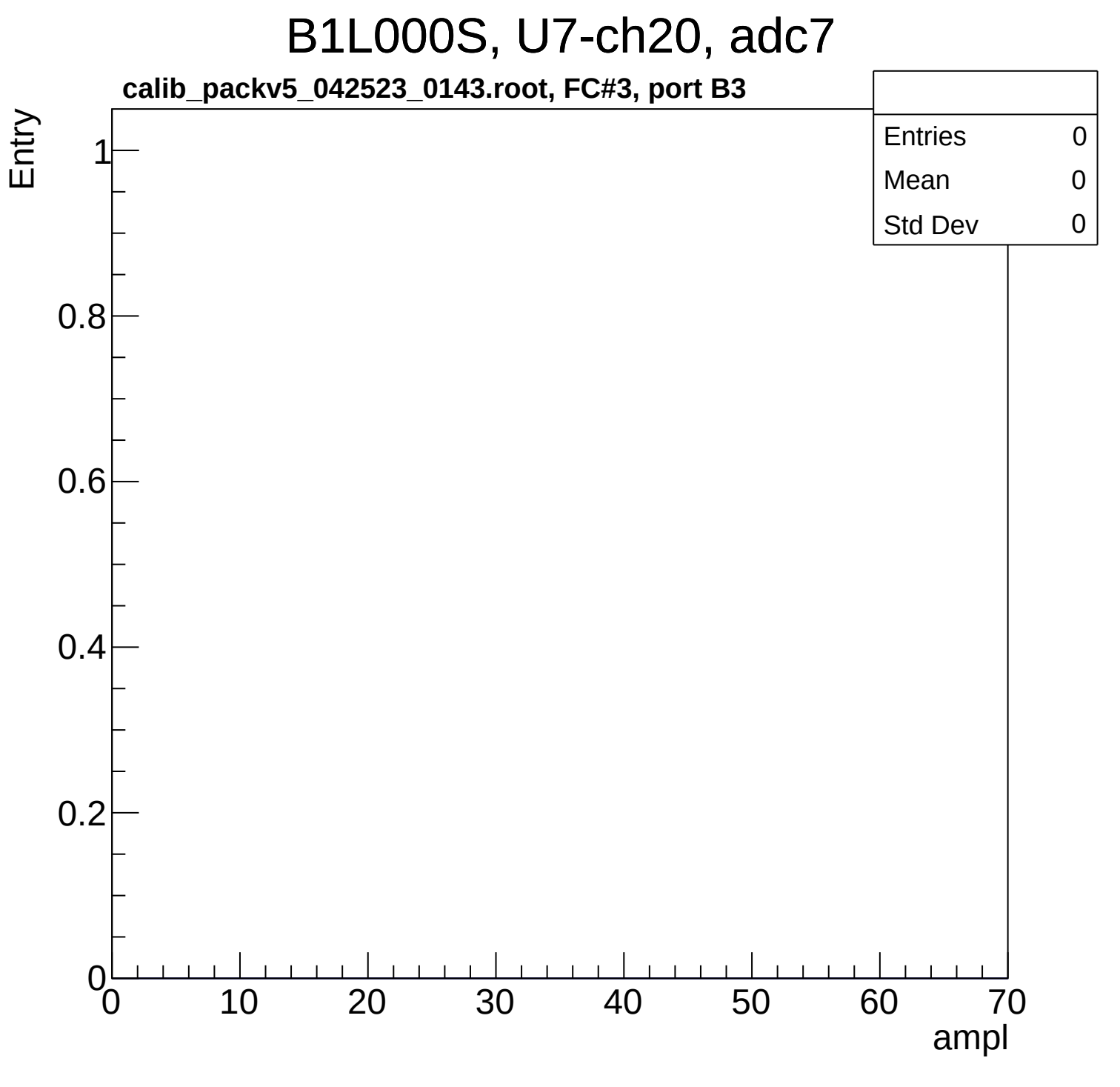
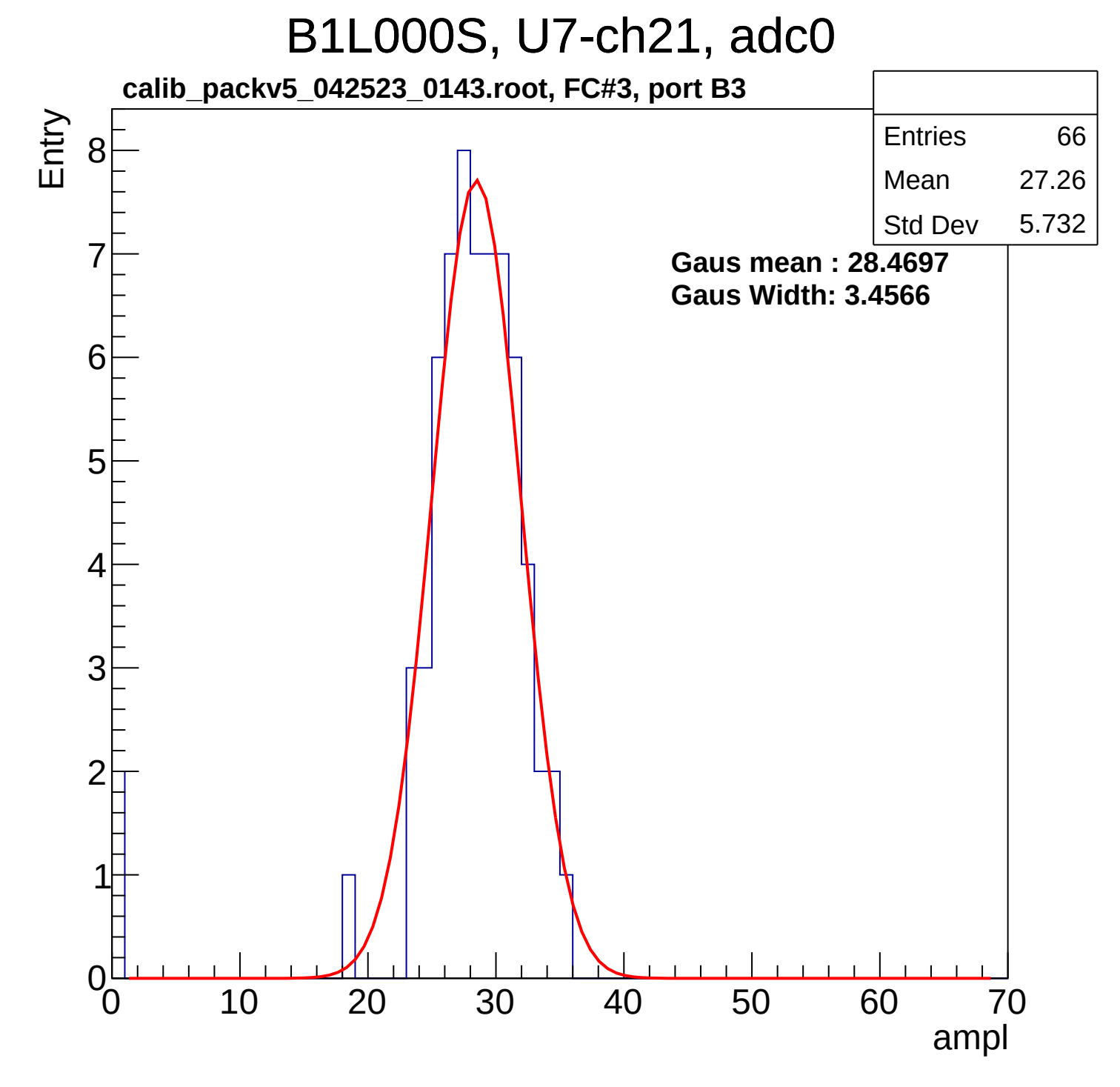
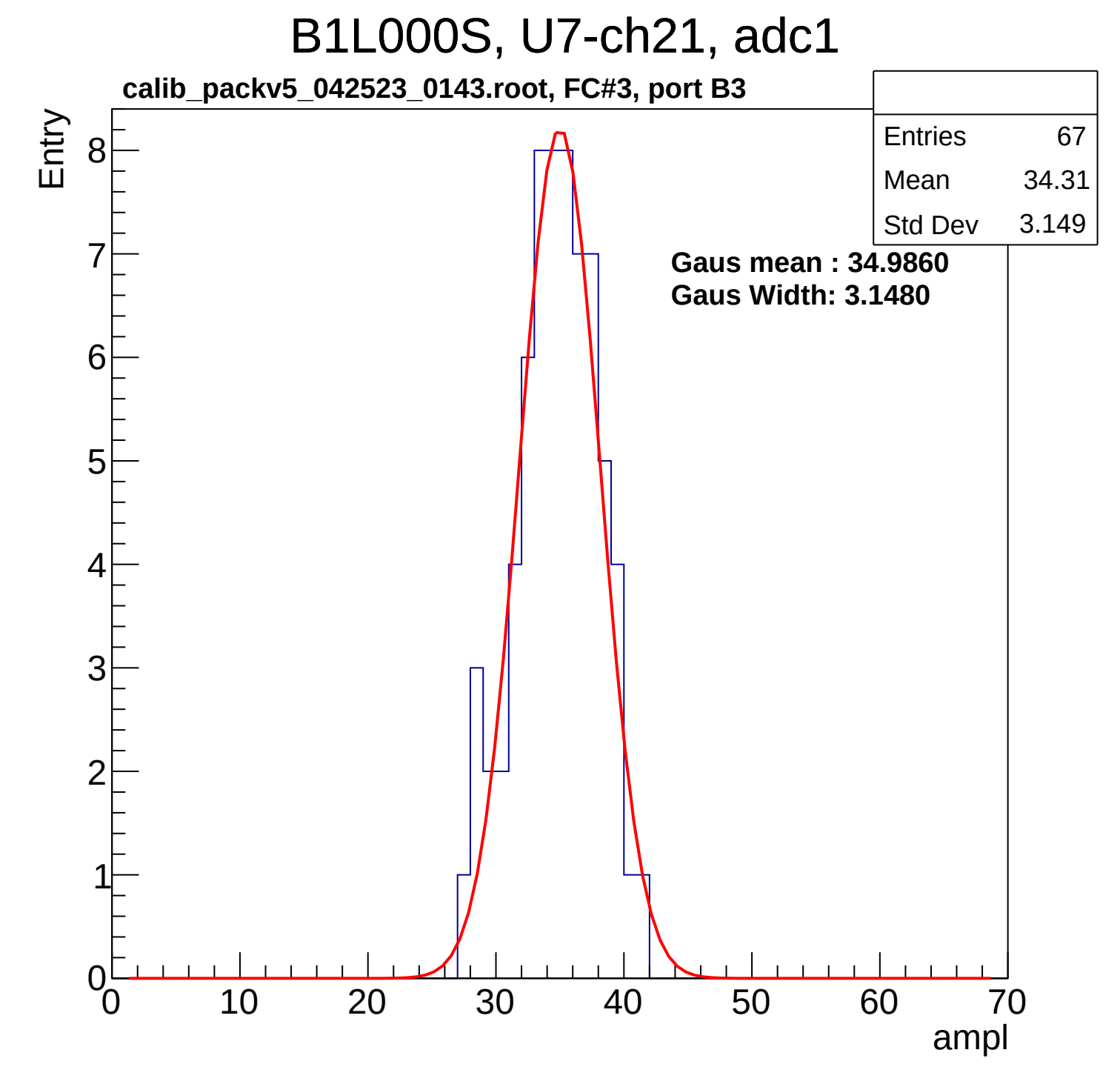
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch20, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch21, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 27.26
Std Dev 5.732
Gaus mean : 28.4697
Gaus Width: 3.4566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch21, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.31
Std Dev 3.149
Gaus mean : 34.9860
Gaus Width: 3.1480
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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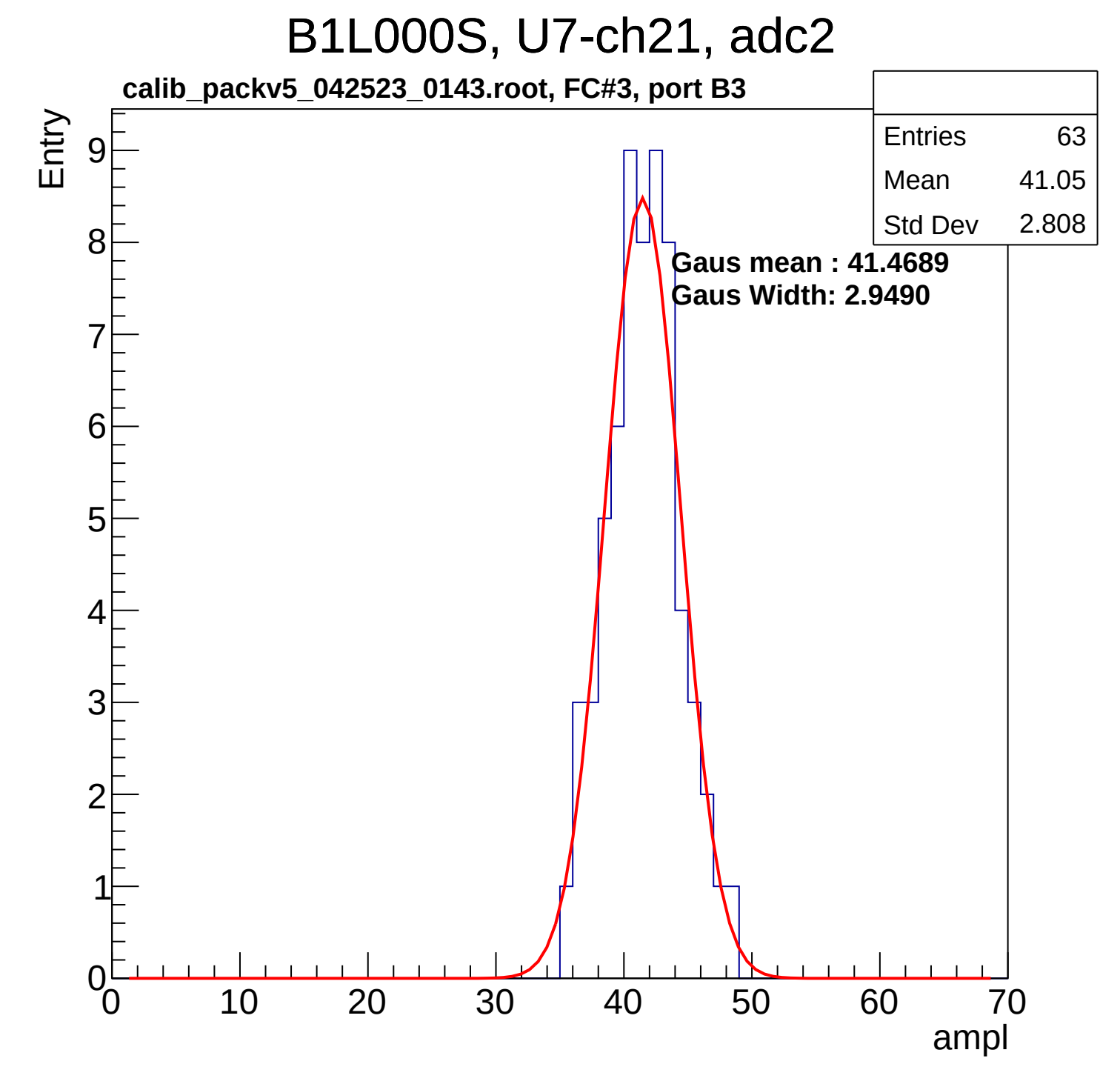
B1L000S, U7-ch21, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 41.05
Std Dev 2.808
Gaus mean : 41.4689
Gaus Width: 2.9490
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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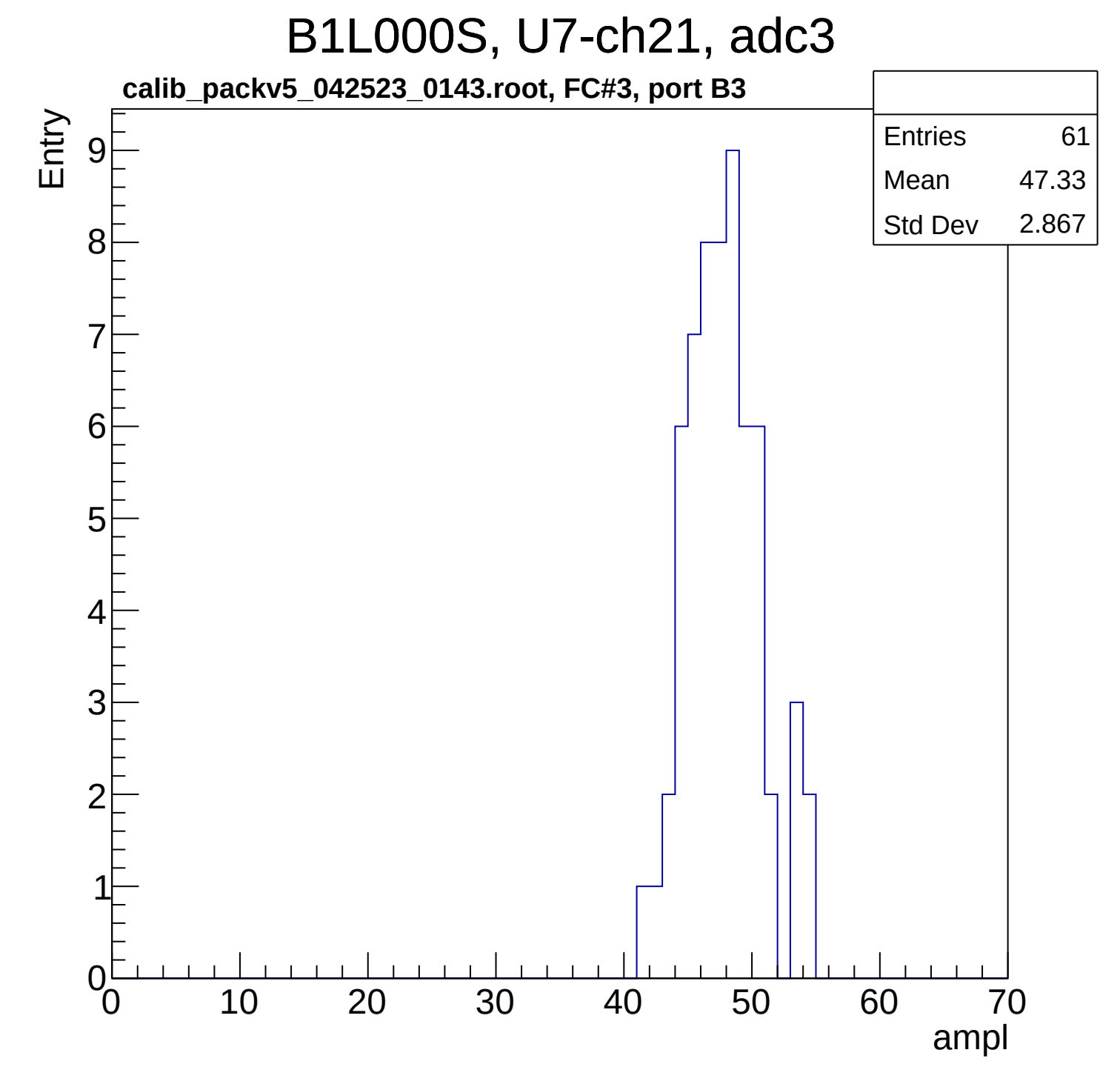
B1L000S, U7-ch21, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 47.33
Std Dev 2.867
Entry
ampl
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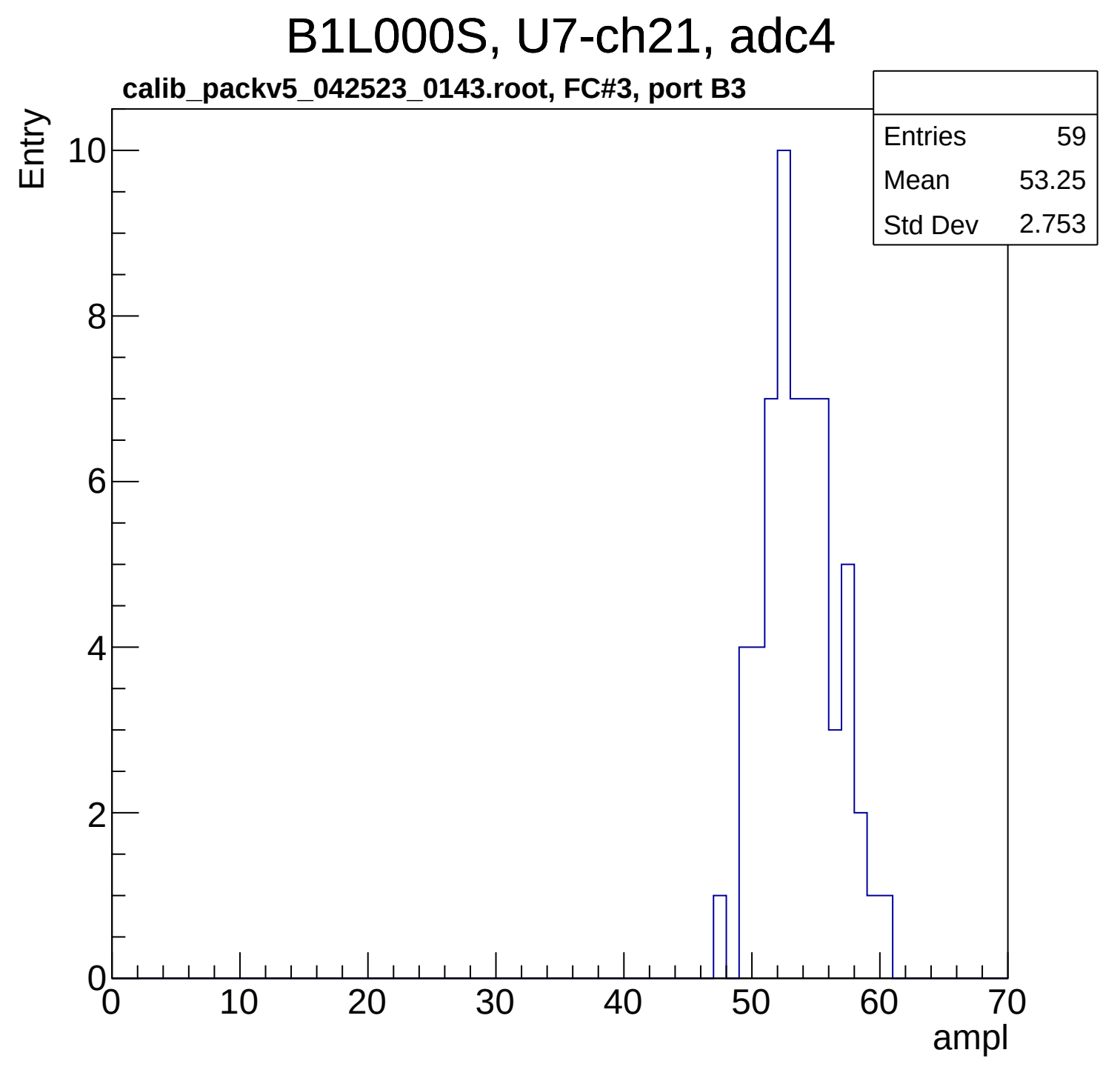
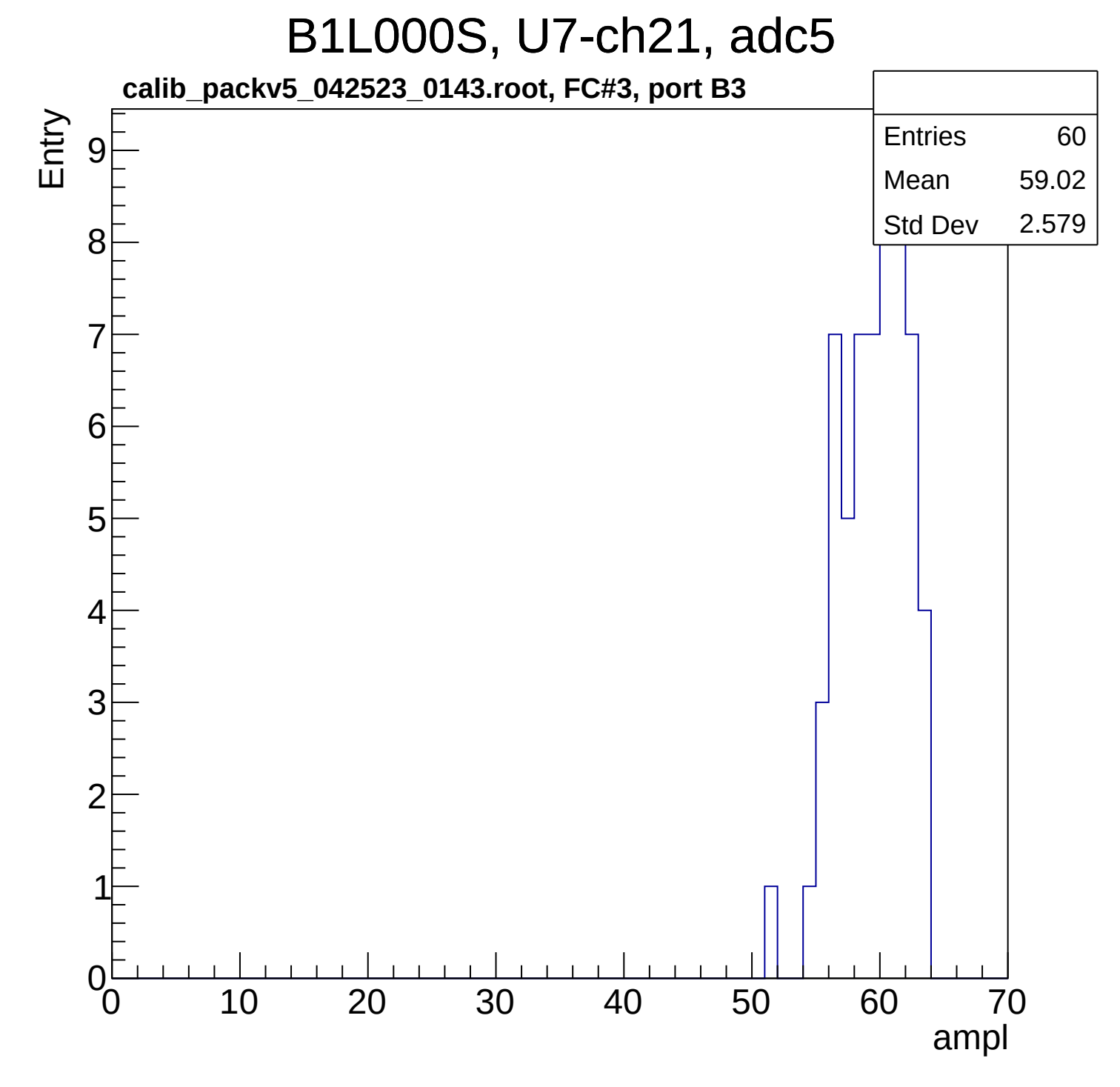
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch21, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.25
Std Dev 2.753
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch21, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 59.02
Std Dev 2.579
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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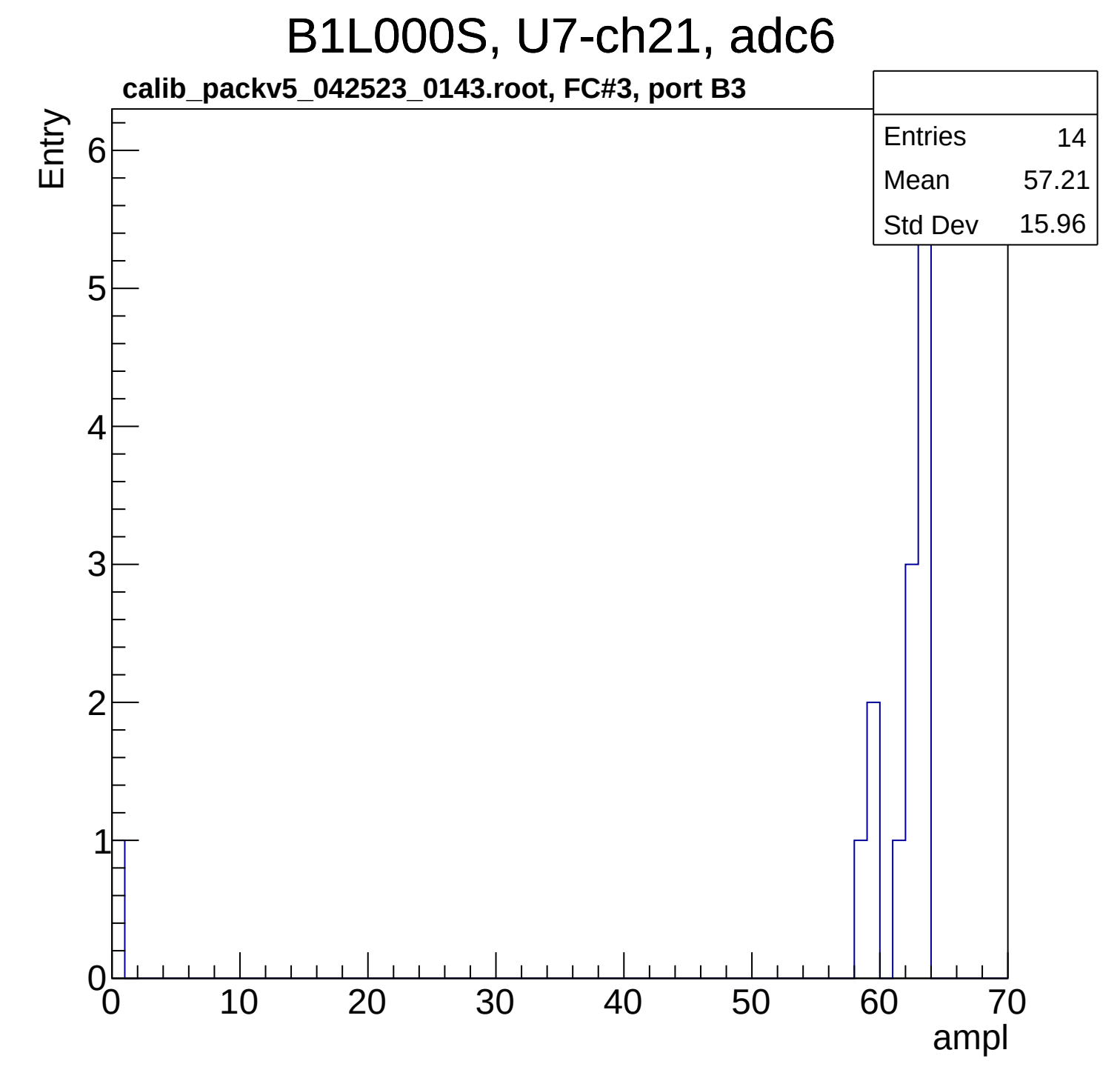
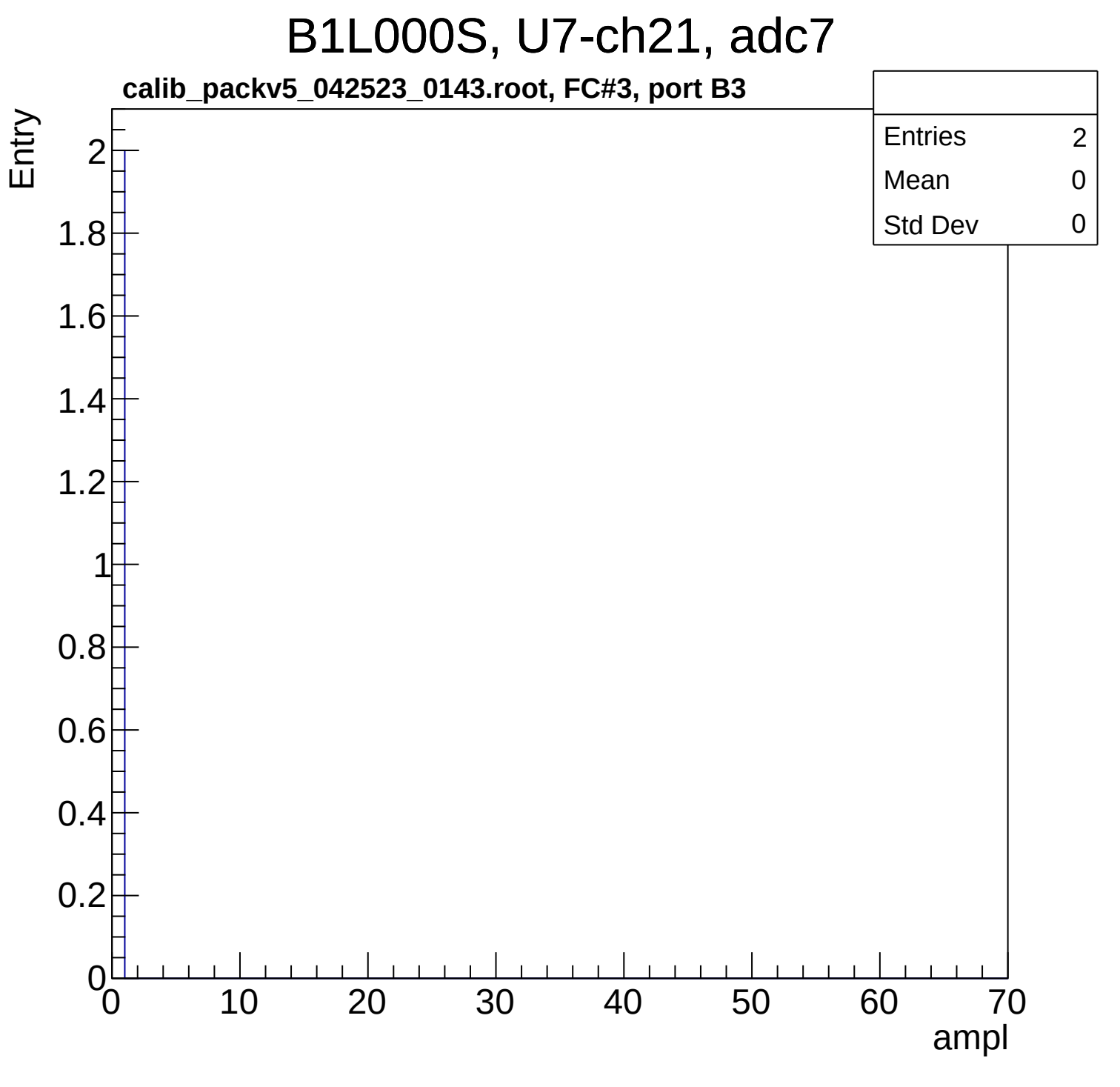
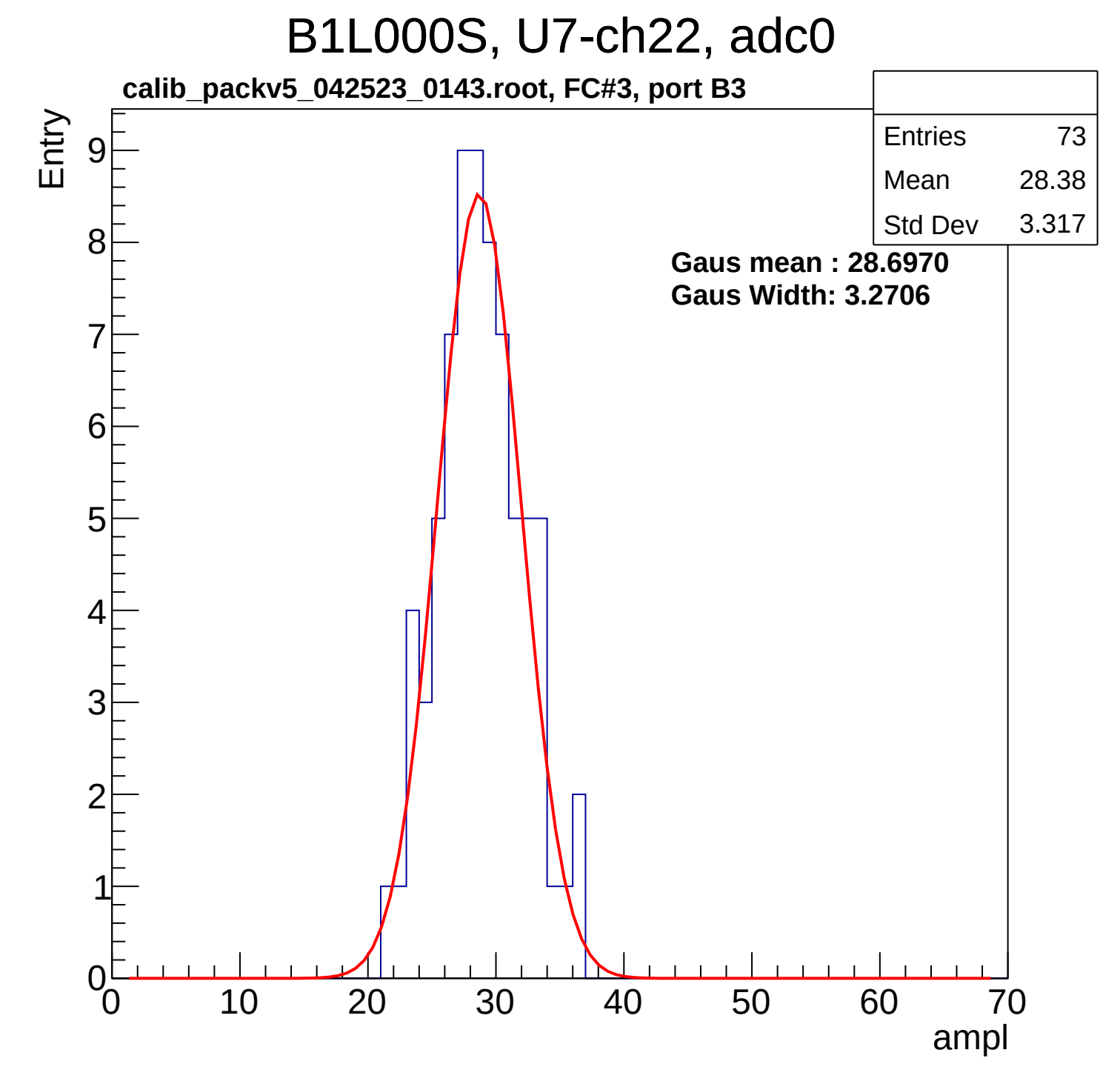
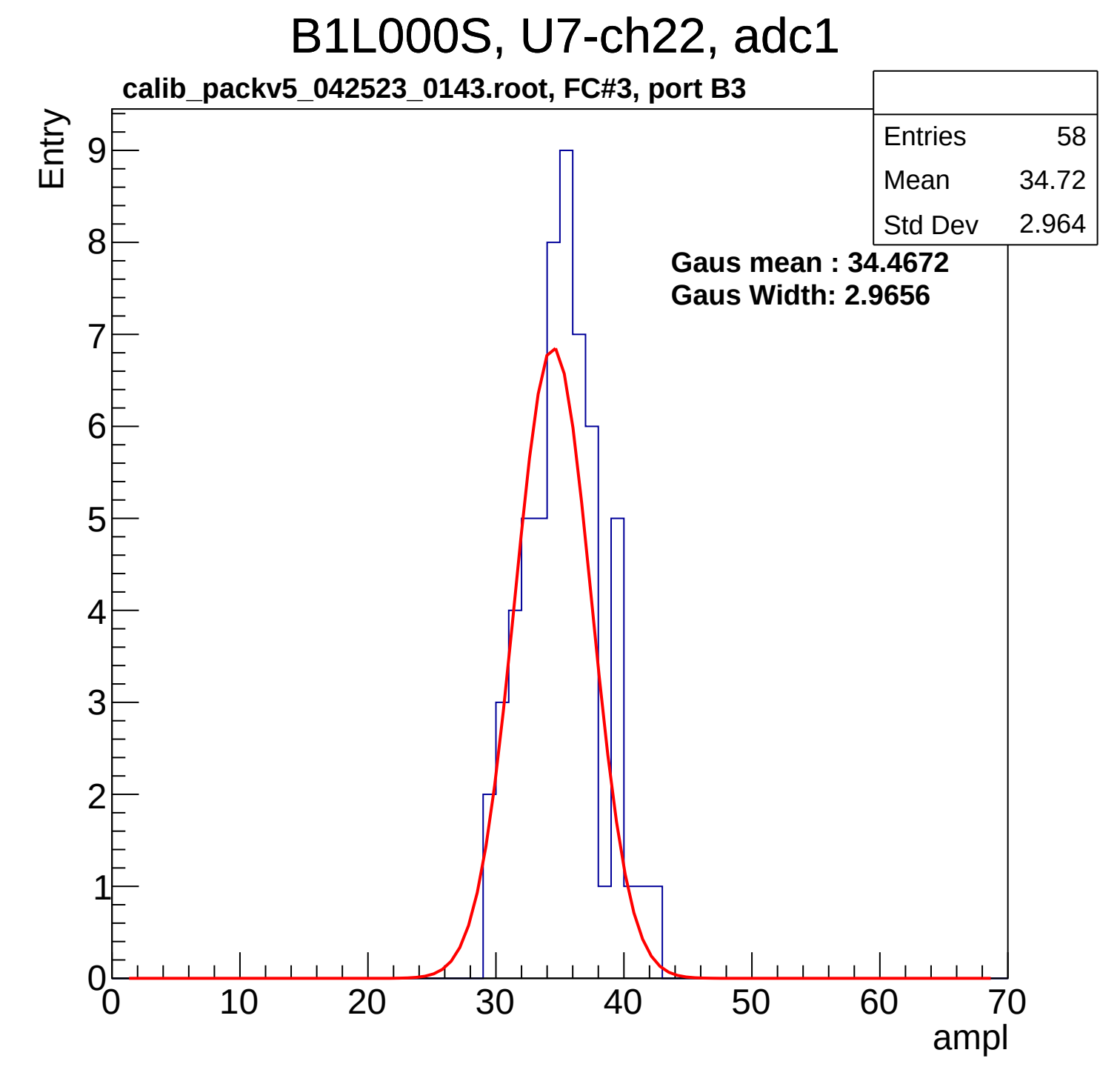
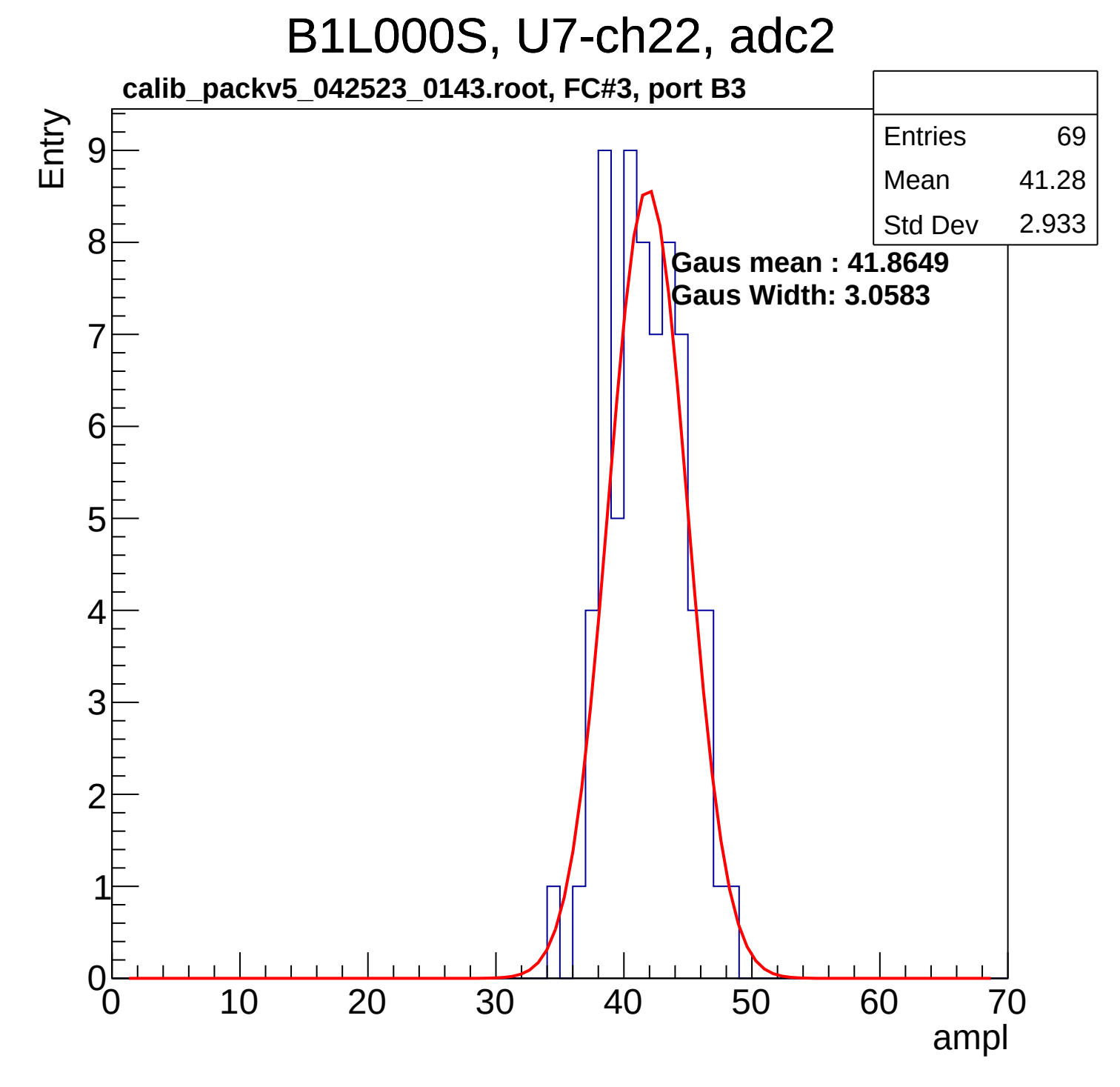
60
70


B1L000S, U7-ch21, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.21
Std Dev 15.96
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch21, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch22, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 28.38
Std Dev 3.317
Gaus mean : 28.6970
Gaus Width: 3.2706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch22, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.72
Std Dev 2.964
Gaus mean : 34.4672
Gaus Width: 2.9656
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch22, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.28
Std Dev 2.933
Gaus mean : 41.8649
Gaus Width: 3.0583
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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60
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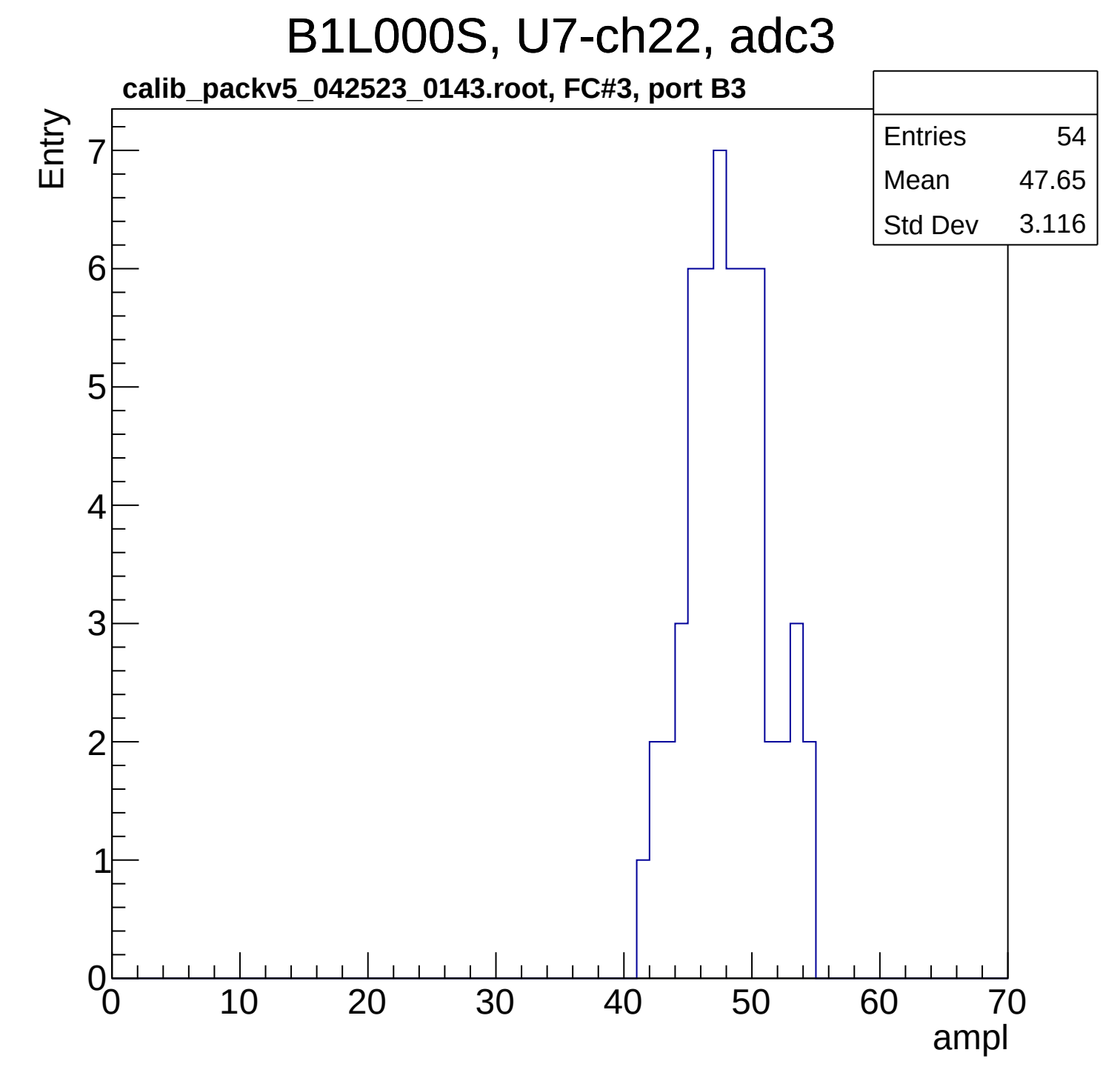
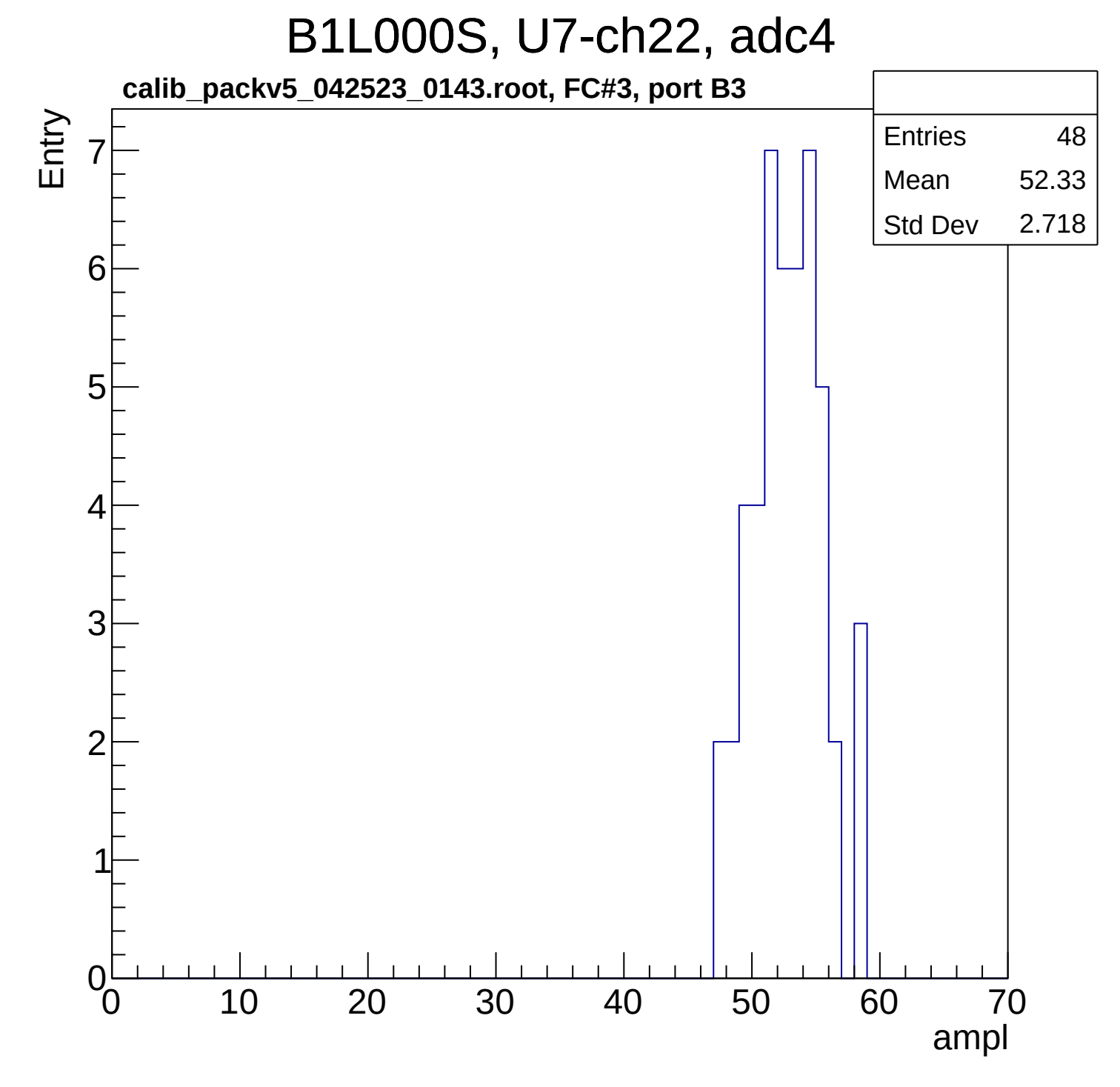

B1L000S, U7-ch22, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 47.65
Std Dev 3.116
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch22, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 52.33
Std Dev 2.718
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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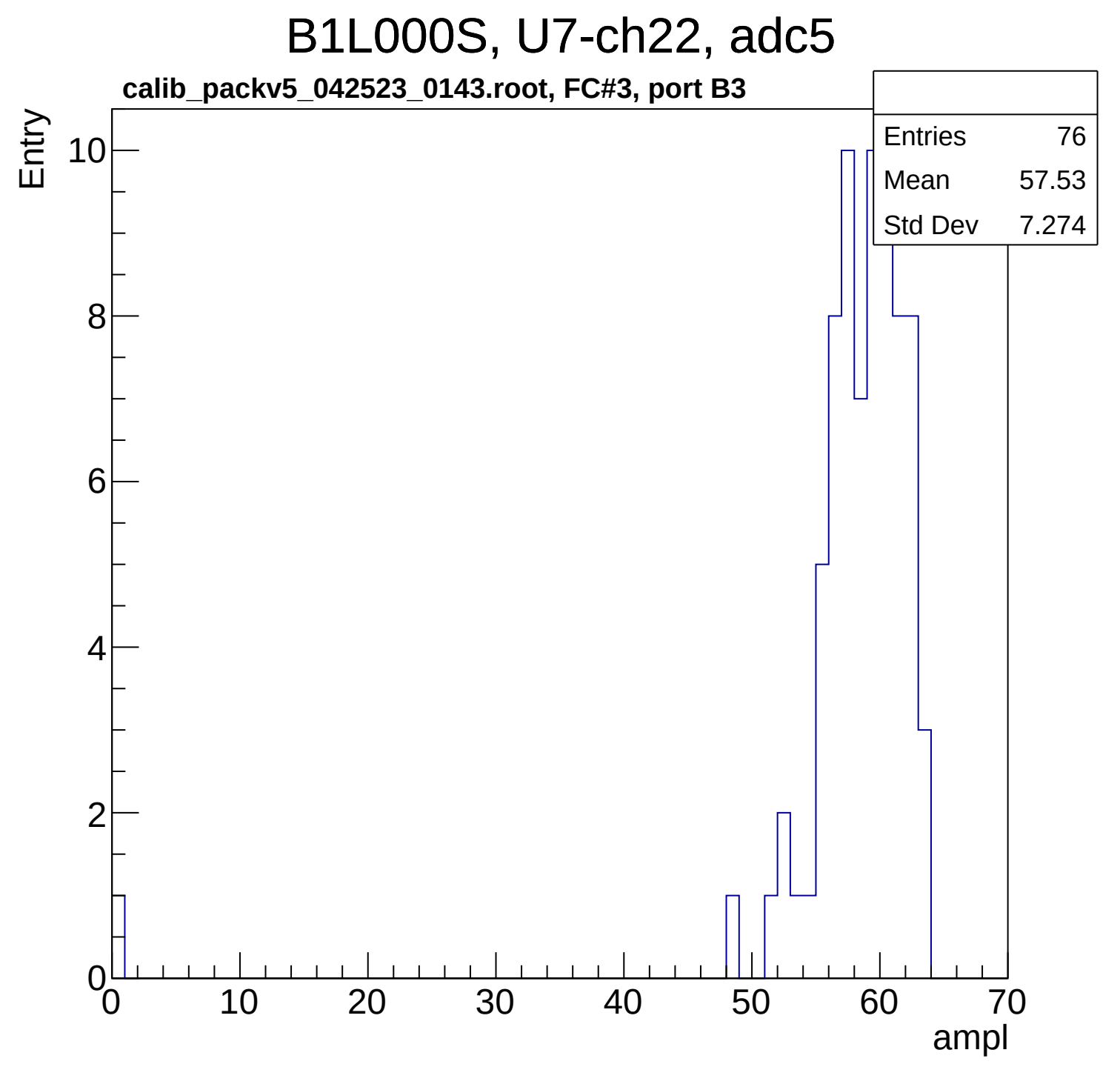
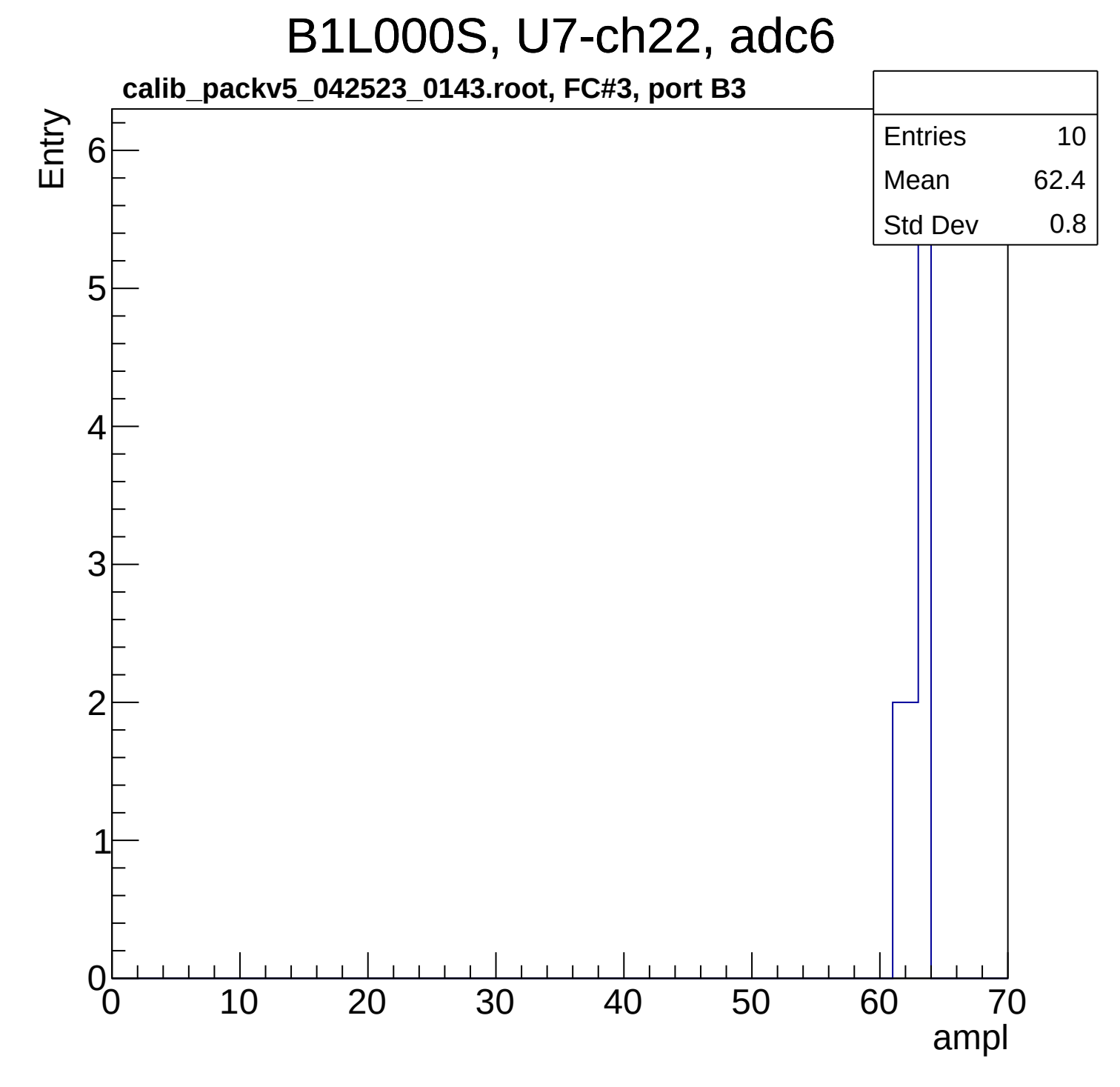
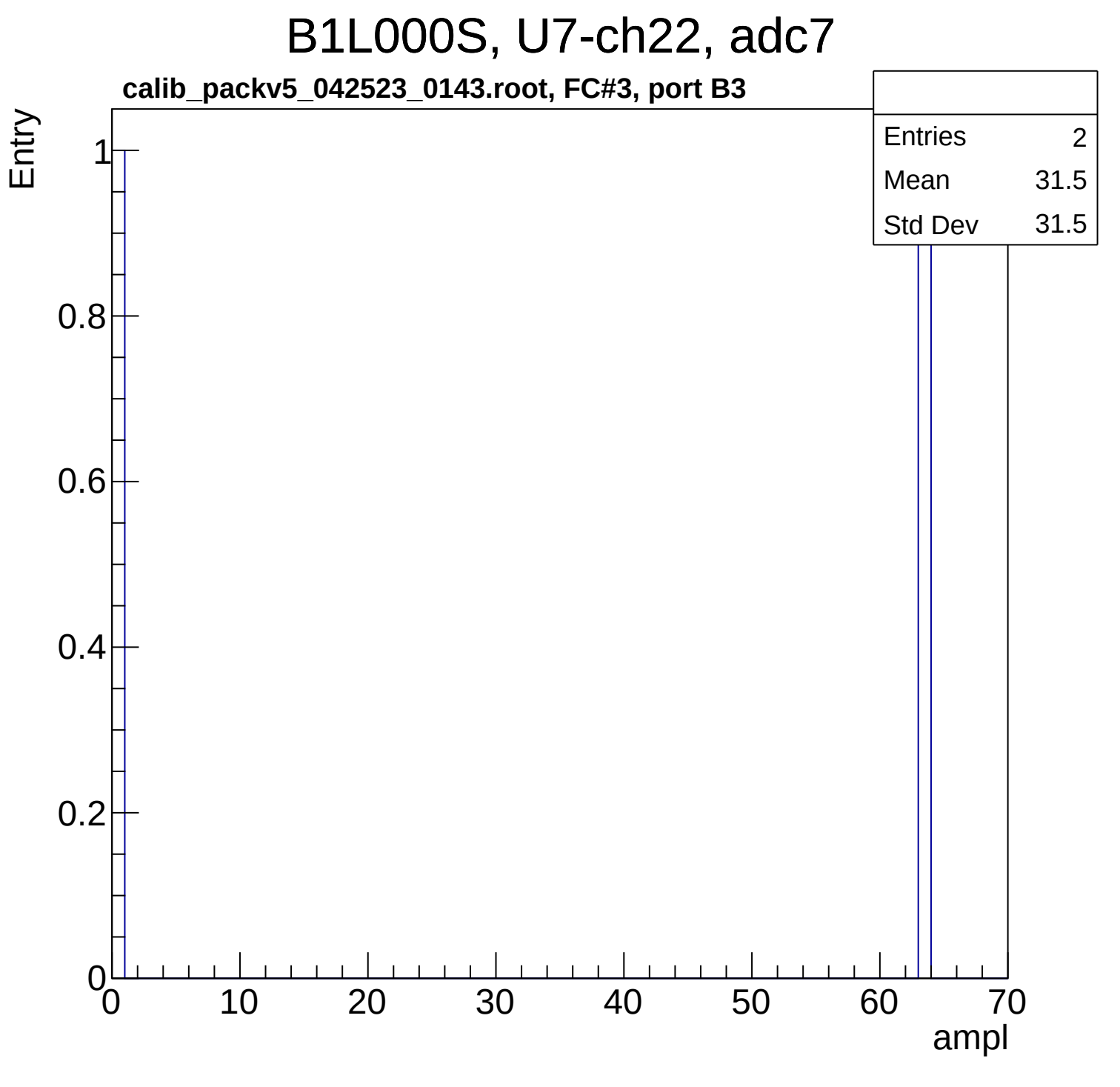
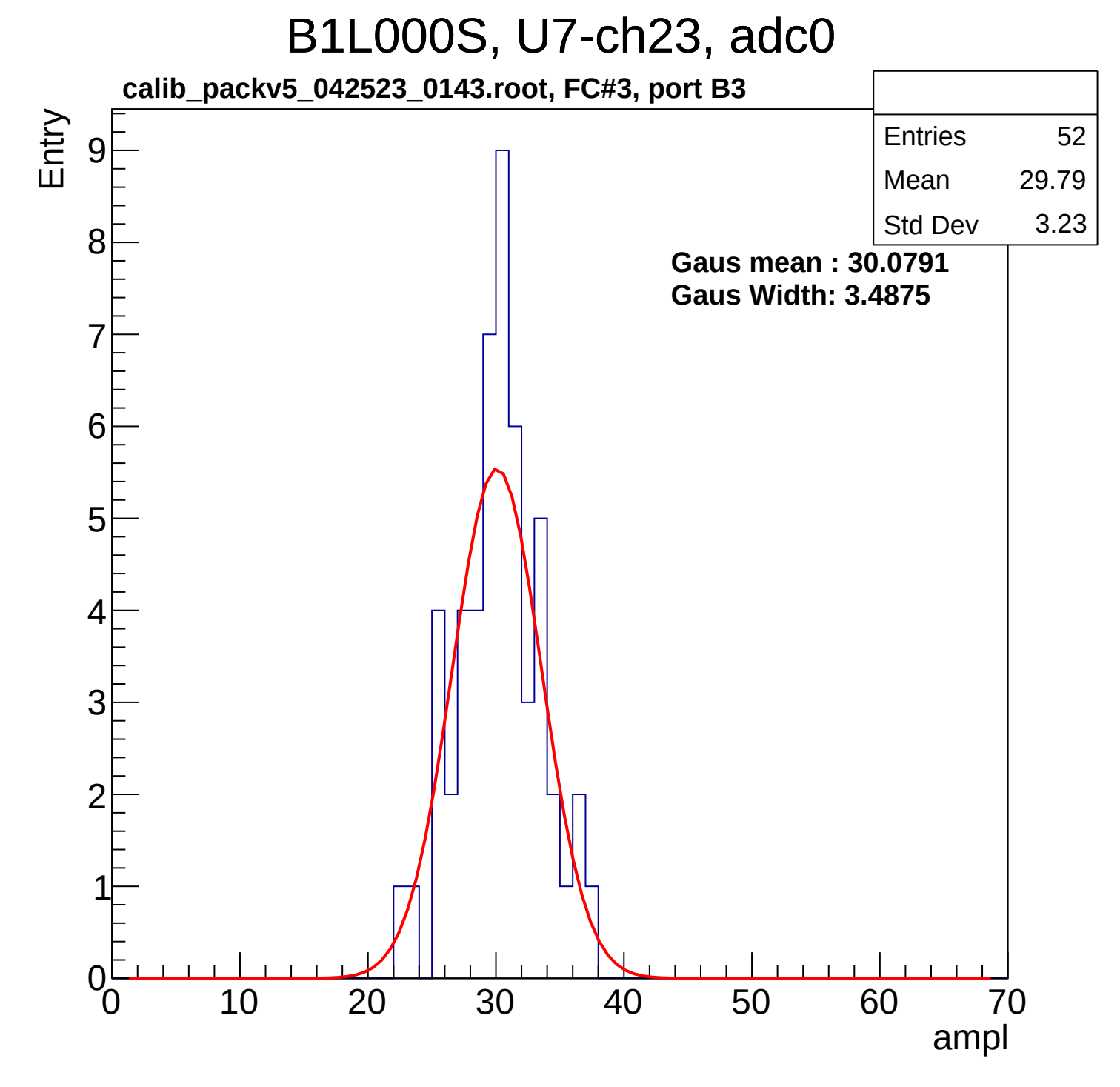
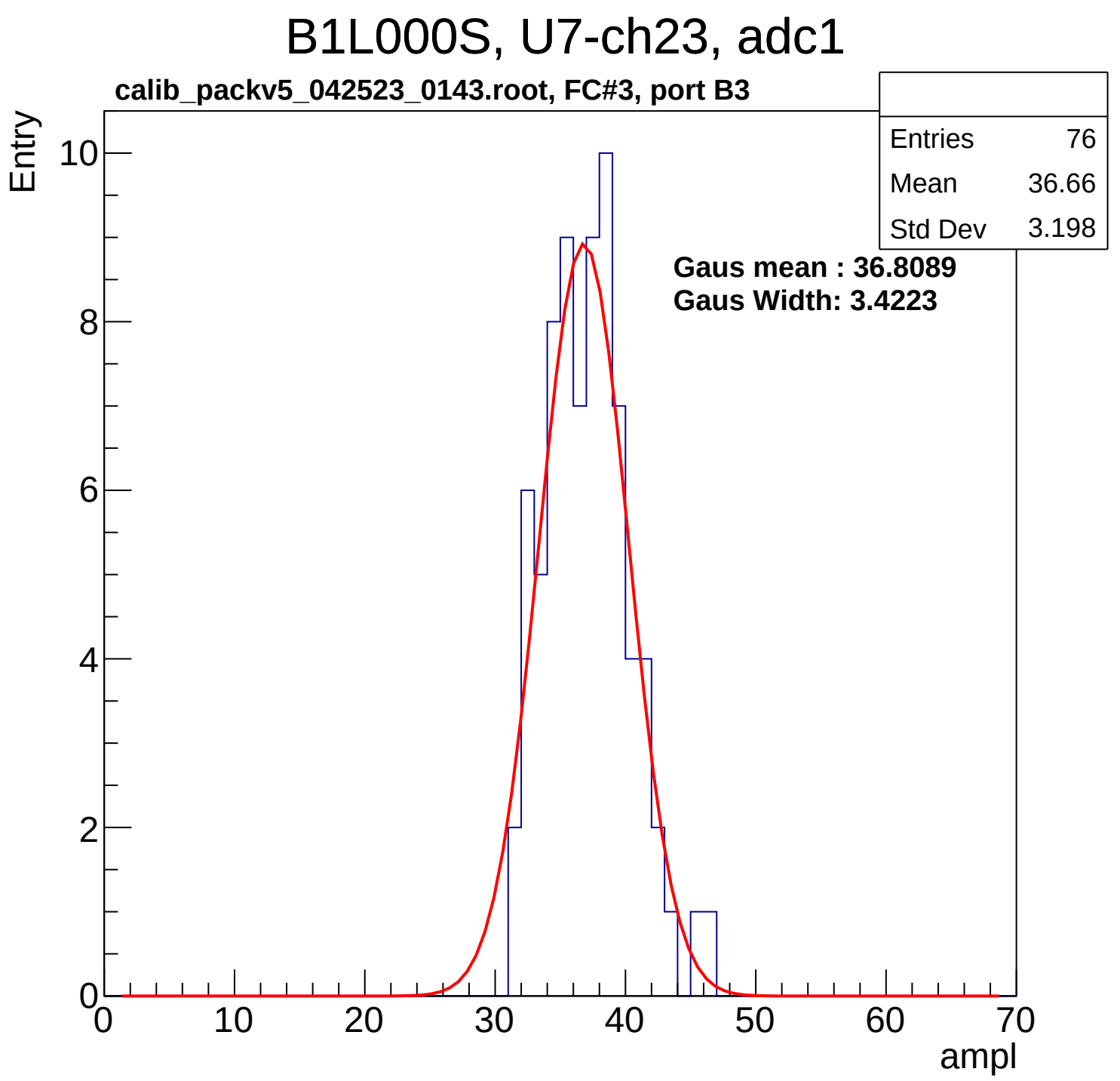
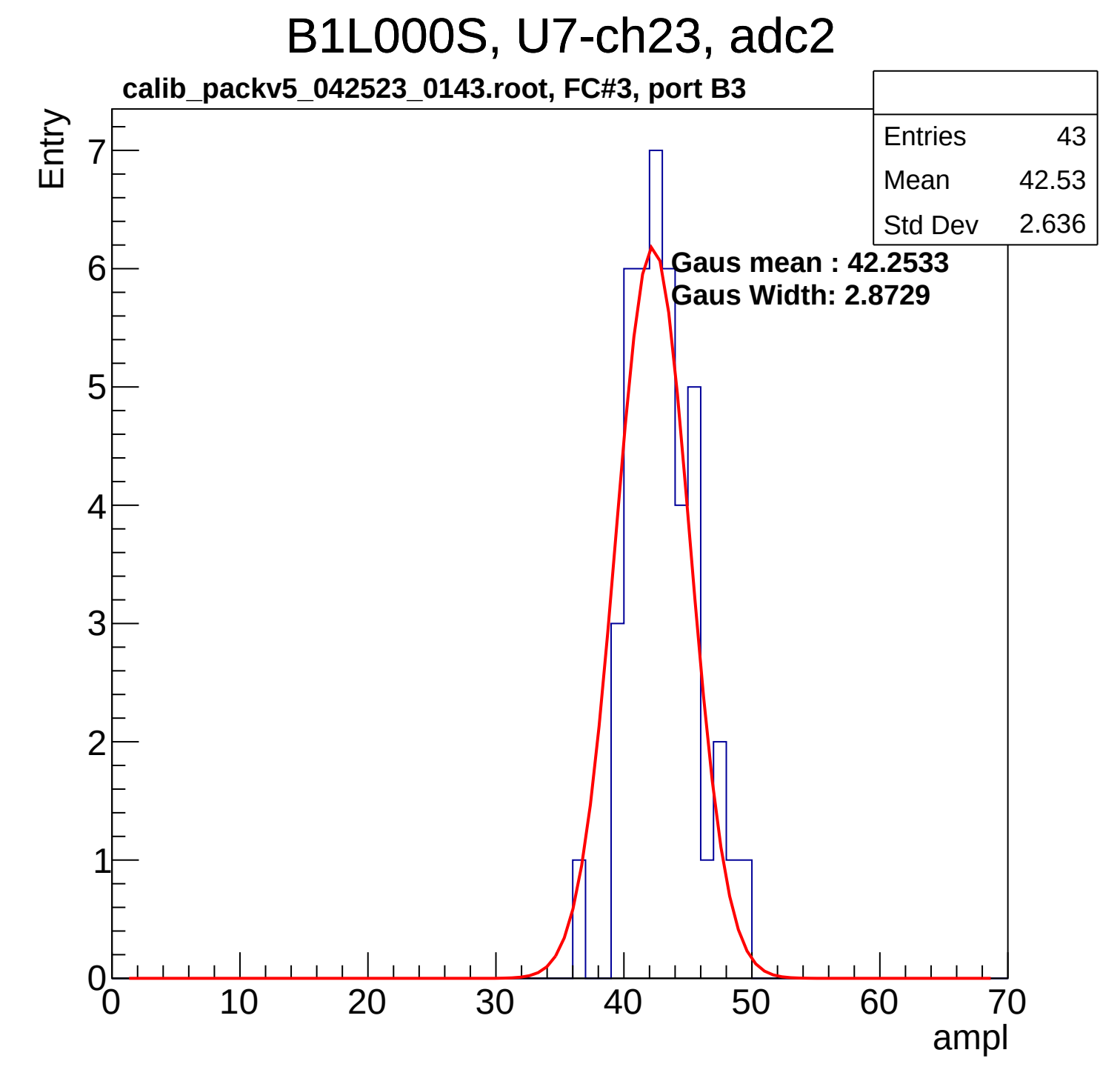

B1L000S, U7-ch22, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 57.53
Std Dev 7.274
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch22, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.4
Std Dev 0.8
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch22, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch23, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 29.79
Std Dev 3.23
Gaus mean : 30.0791
Gaus Width: 3.4875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch23, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 36.66
Std Dev 3.198
Gaus mean : 36.8089
Gaus Width: 3.4223
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch23, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 42.53
Std Dev 2.636
Gaus mean : 42.2533
Gaus Width: 2.8729
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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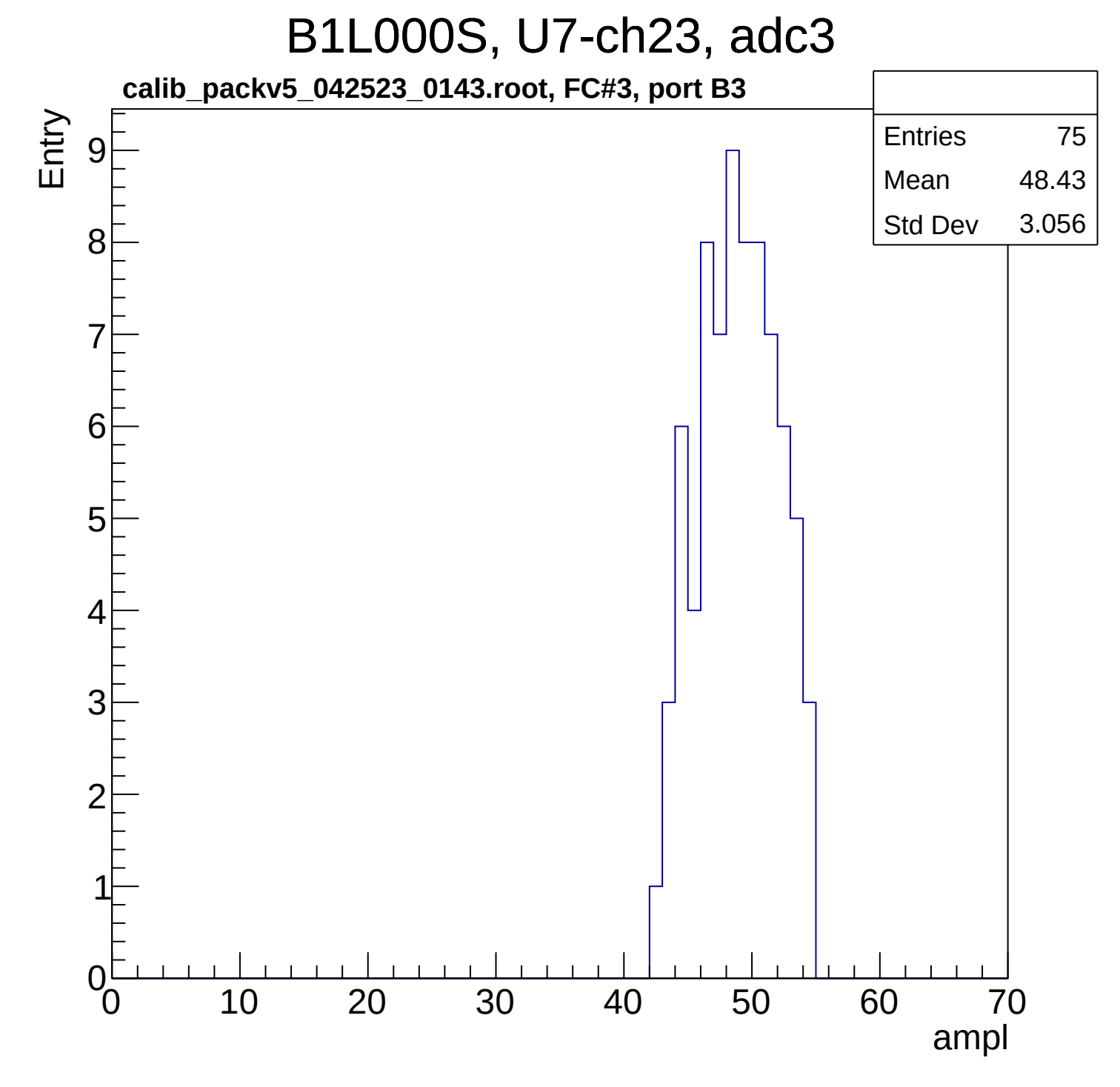
40
50
60
70


B1L000S, U7-ch23, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 48.43
Std Dev 3.056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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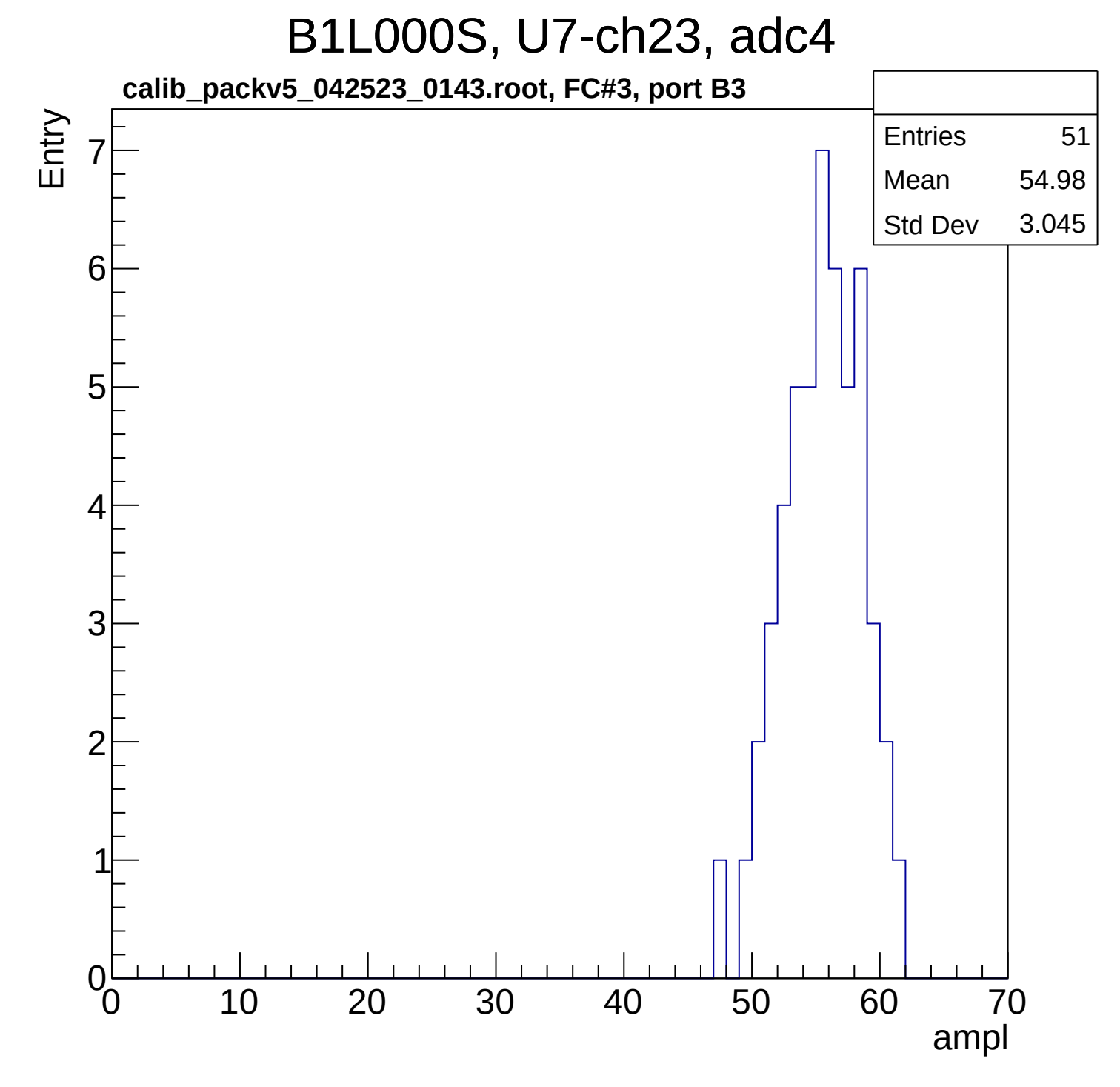
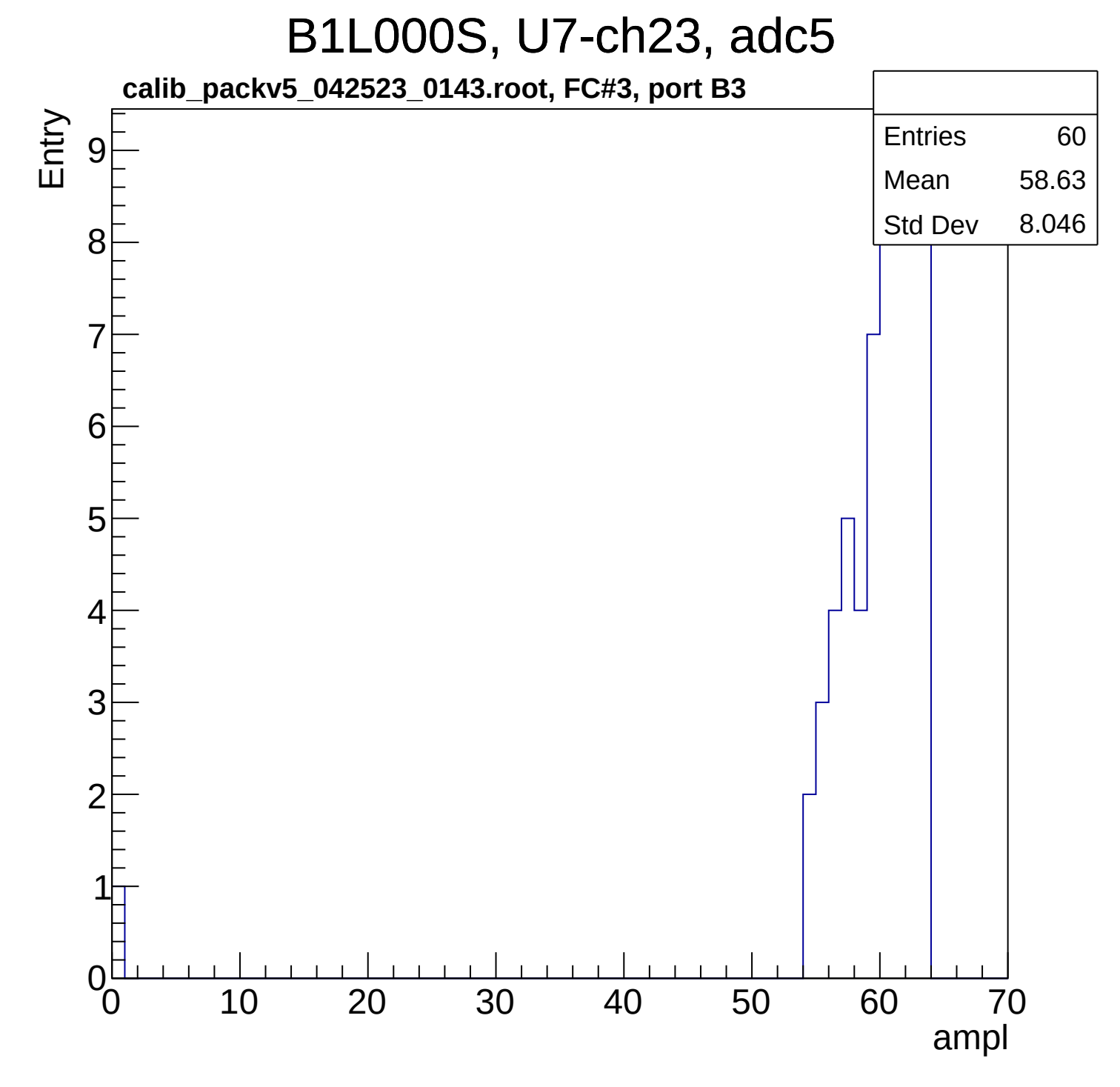
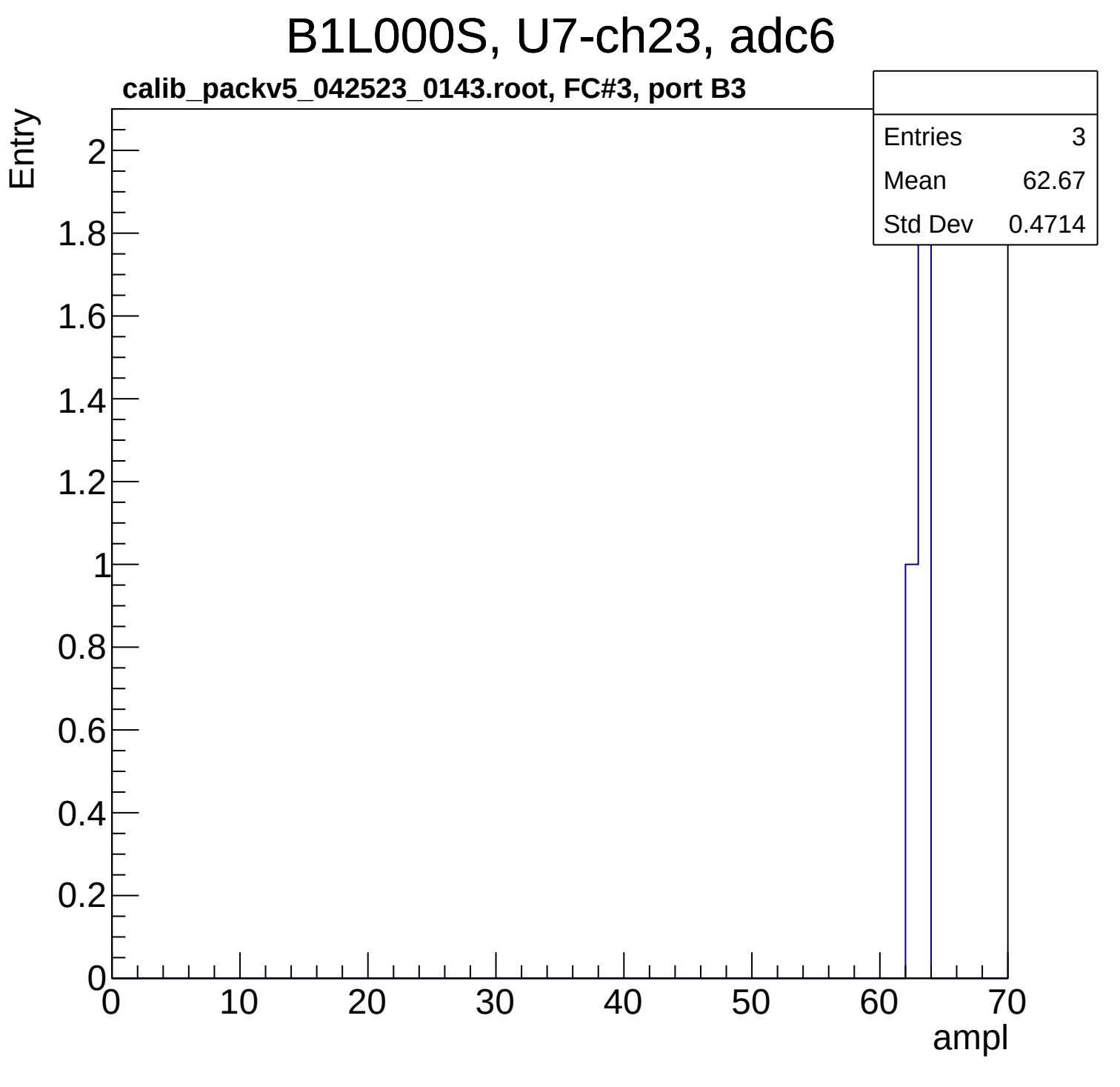
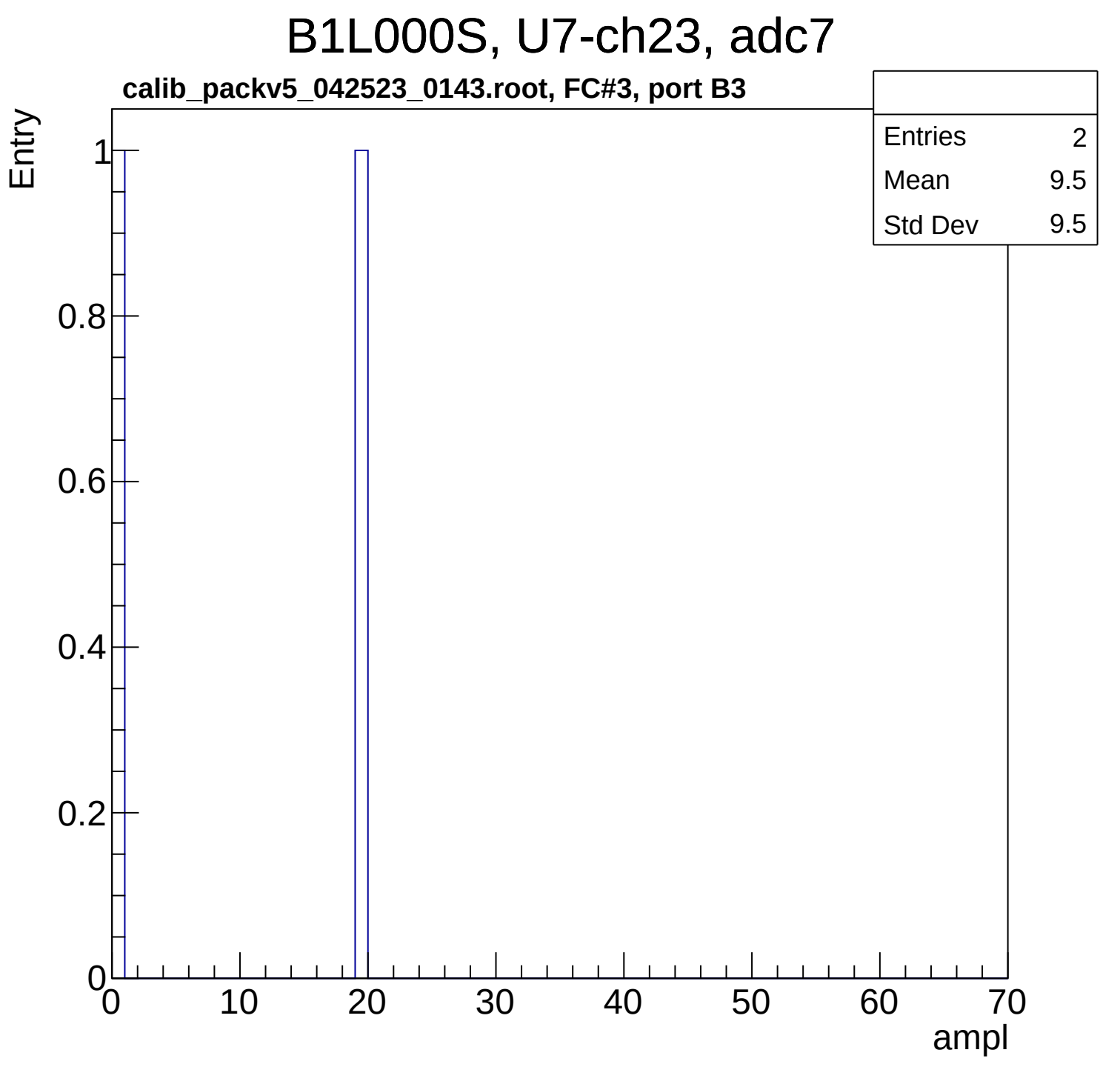
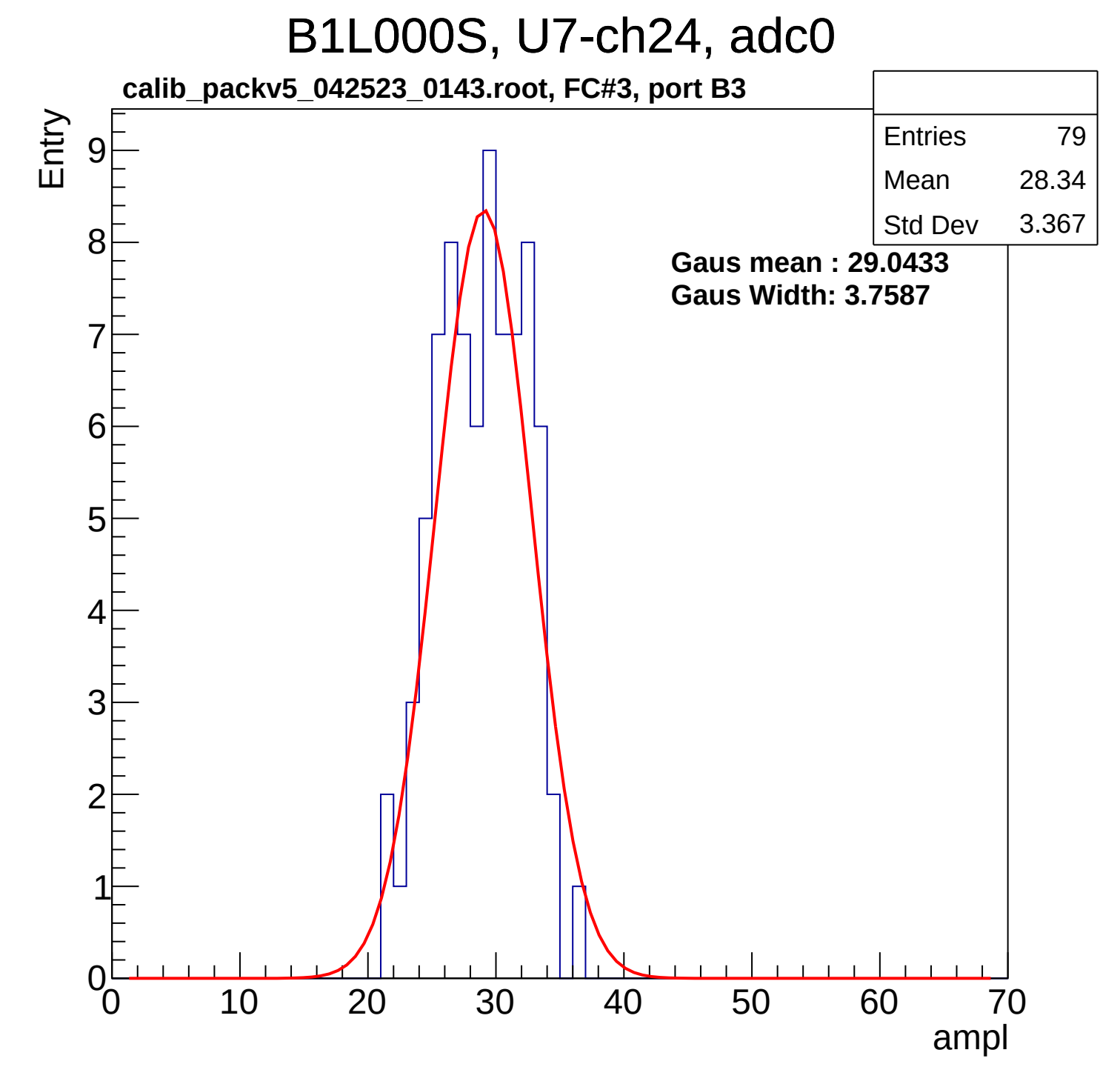
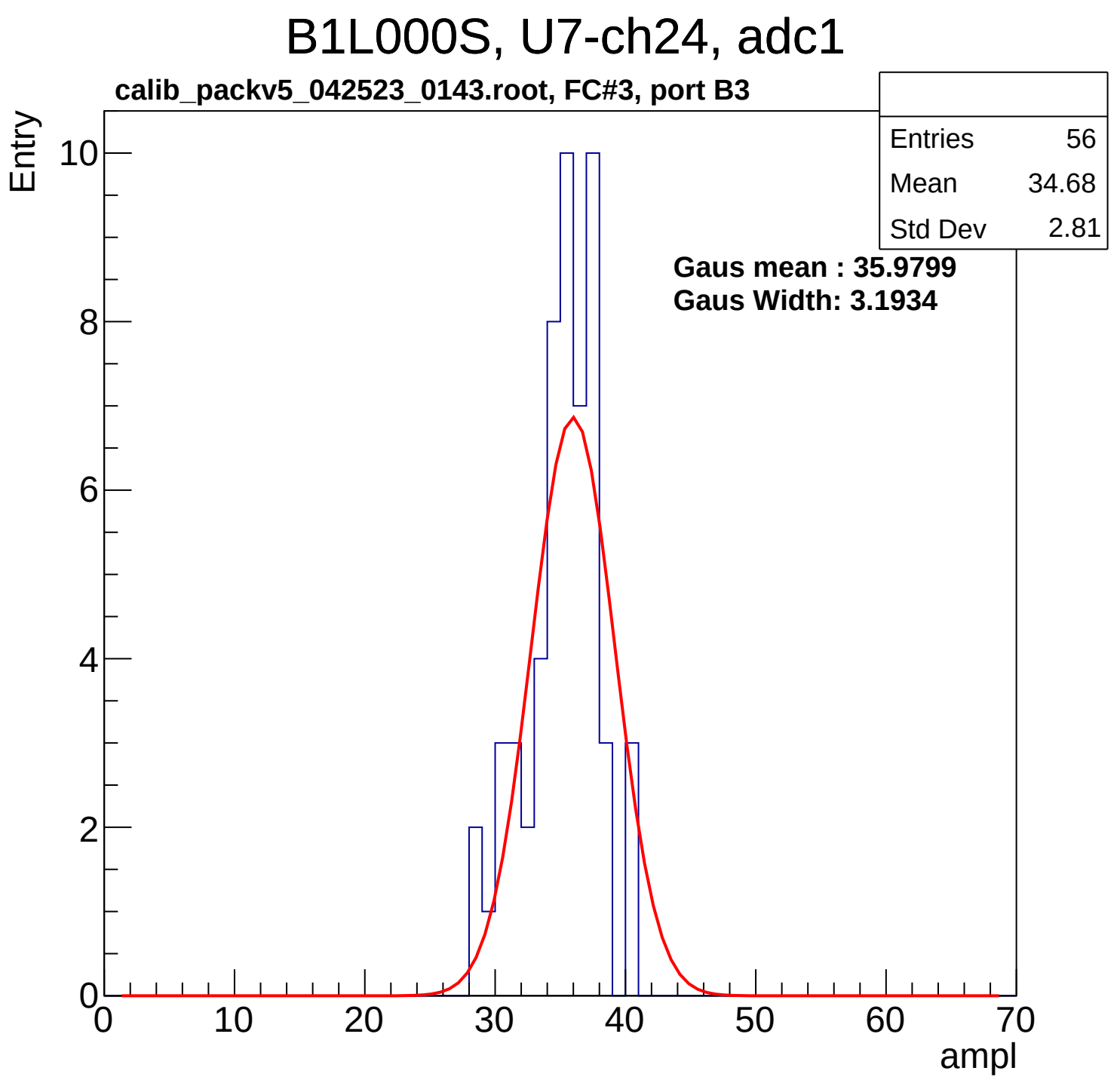

B1L000S, U7-ch23, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.98
Std Dev 3.045
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

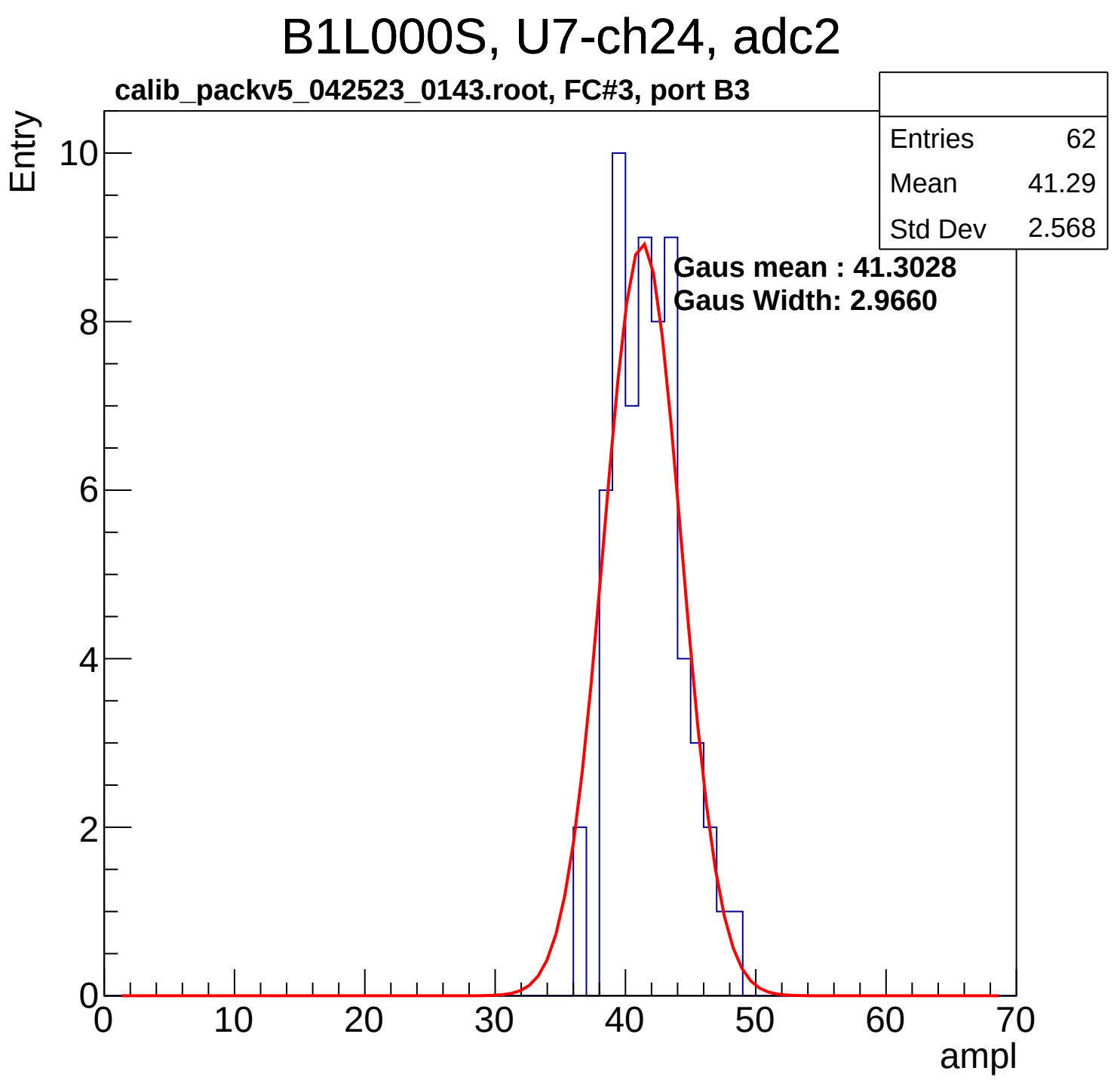

B1L000S, U7-ch23, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.63
Std Dev 8.046
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

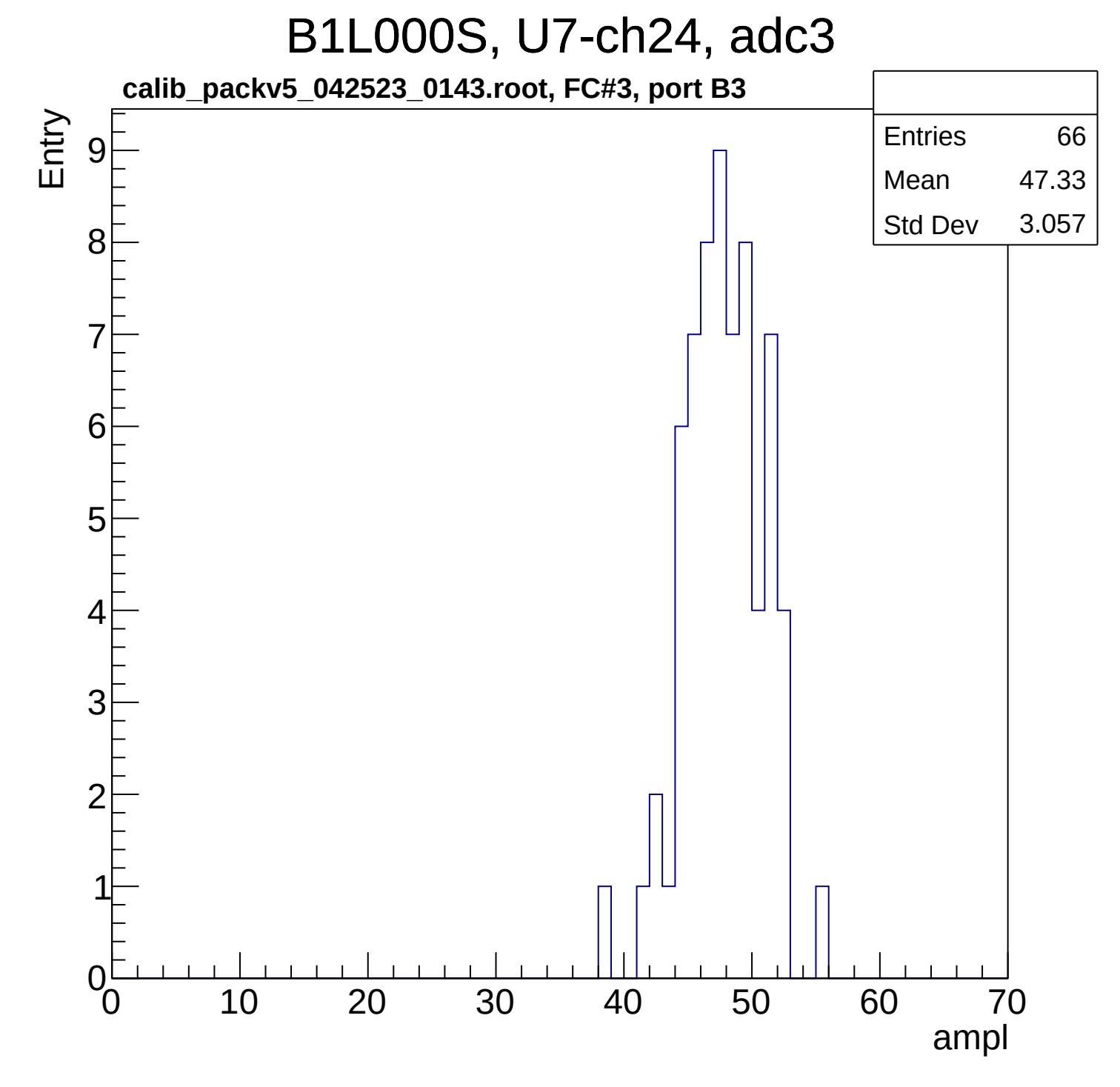

B1L000S, U7-ch23, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

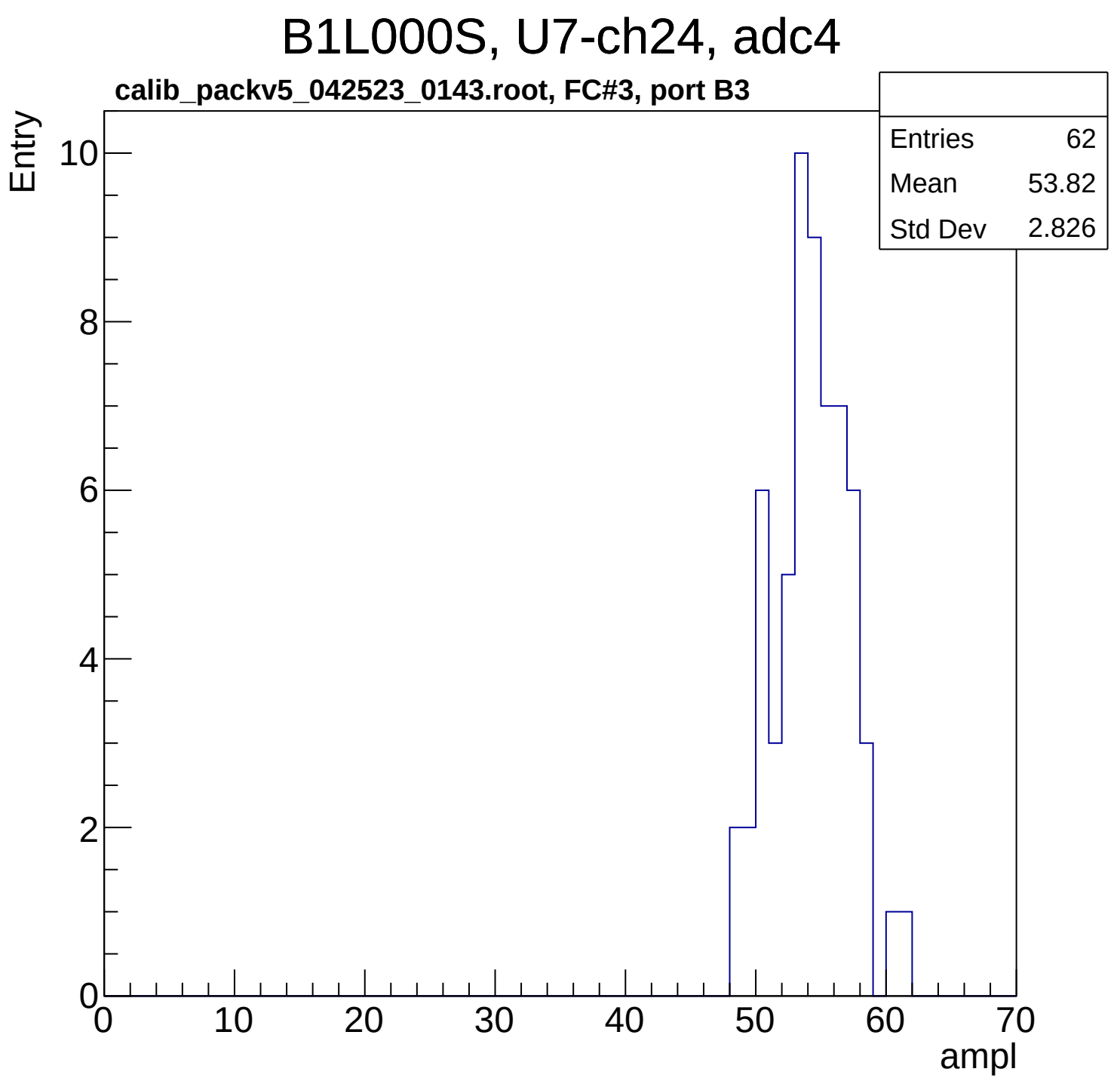

B1L000S, U7-ch23, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

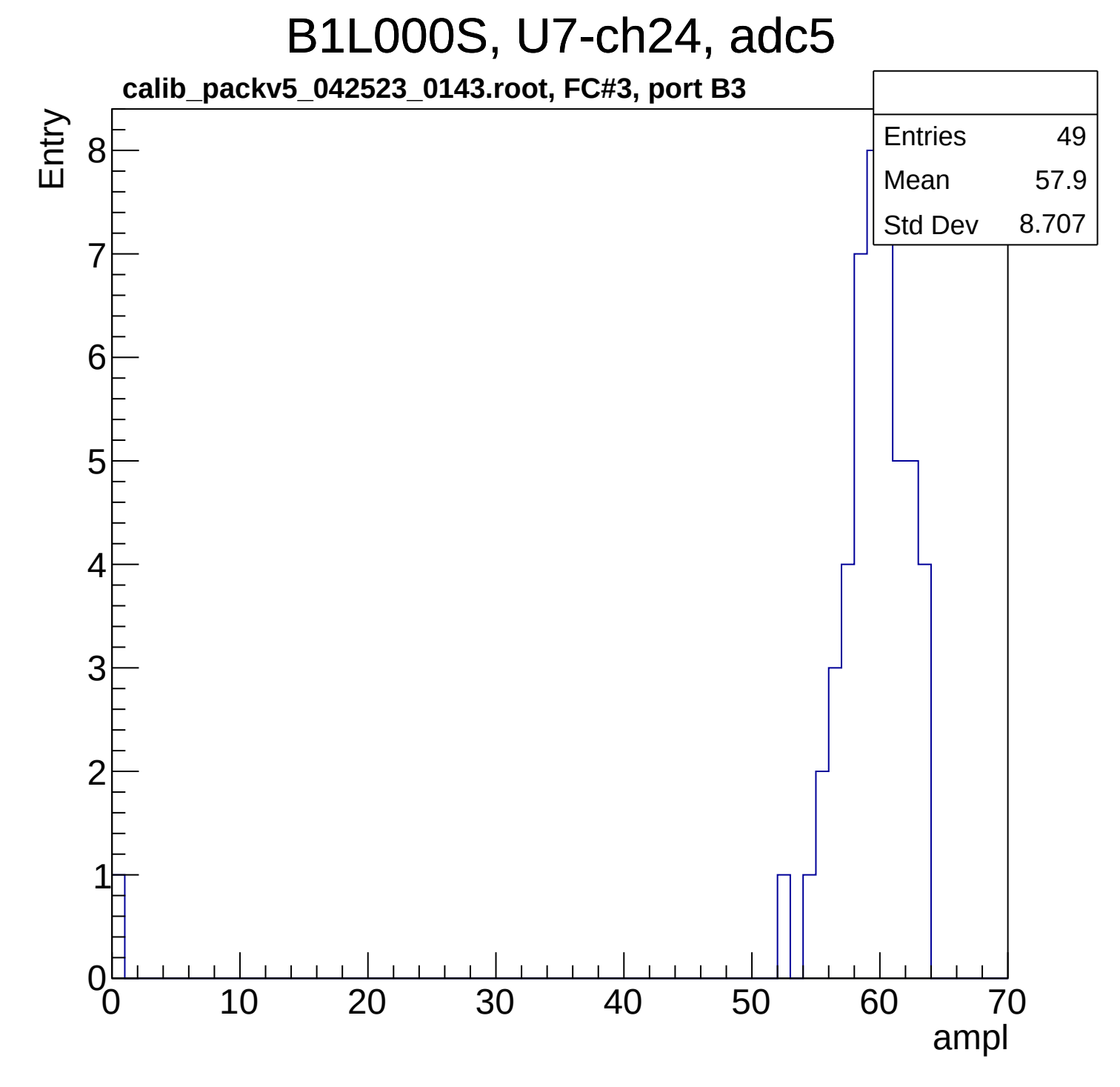

B1L000S, U7-ch24, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 28.34
Std Dev 3.367
Gaus mean : 29.0433
Gaus Width: 3.7587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch24, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 34.68
Std Dev 2.81
Gaus mean : 35.9799
Gaus Width: 3.1934
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch24, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.29
Std Dev 2.568
Gaus mean : 41.3028
Gaus Width: 2.9660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch24, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.33
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch24, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 53.82
Std Dev 2.826
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch24, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 57.9
Std Dev 8.707
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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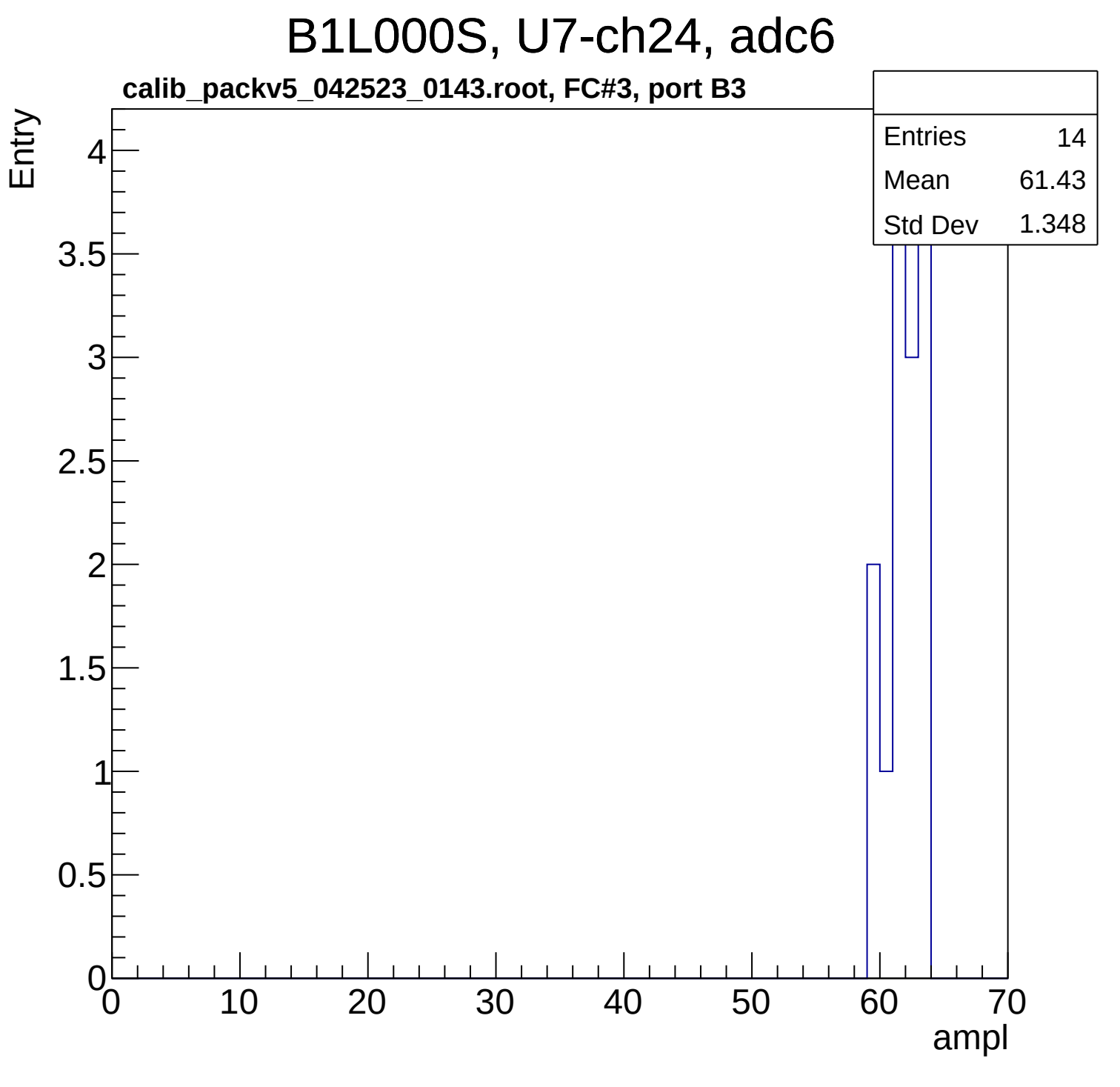
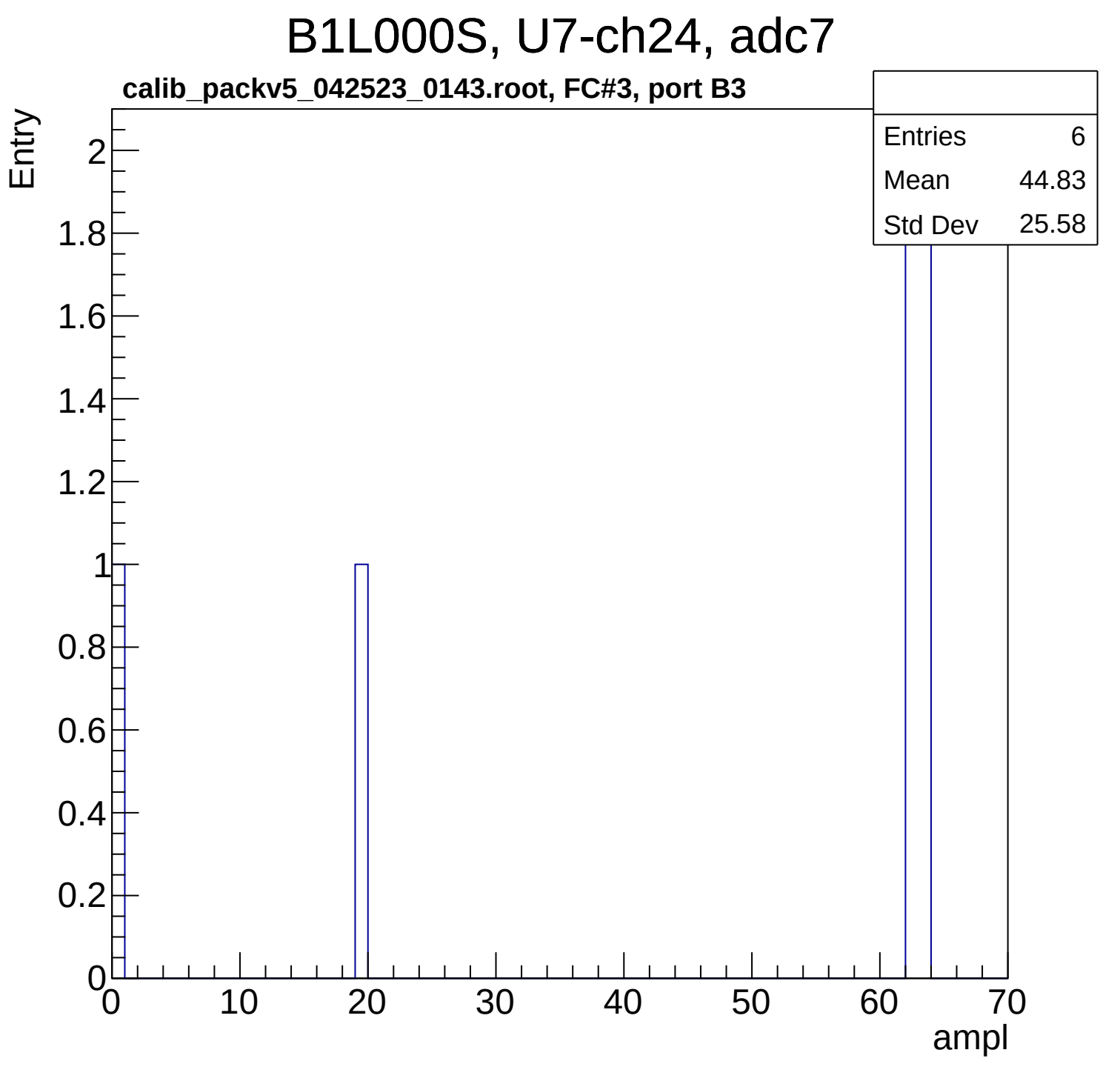
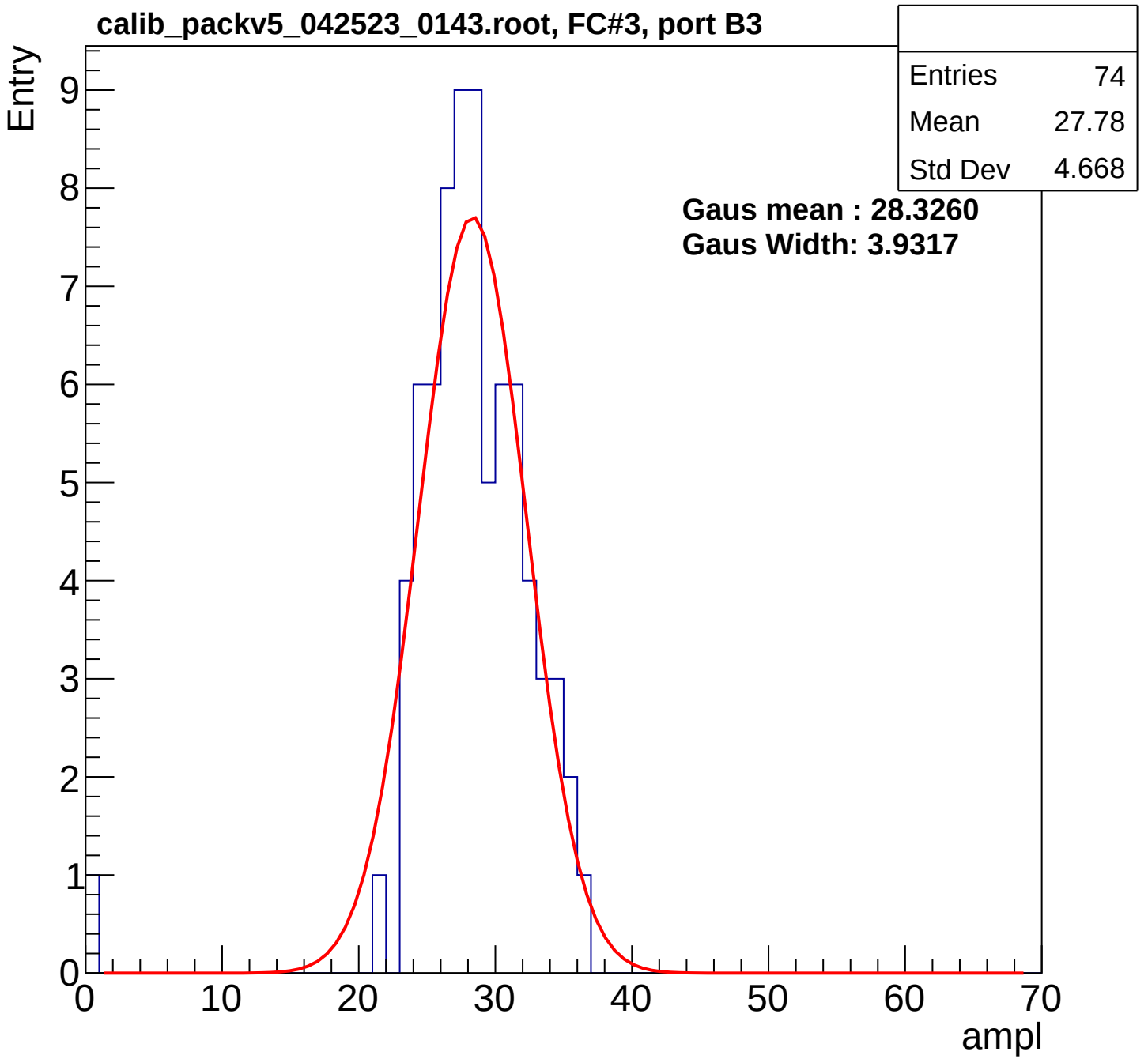
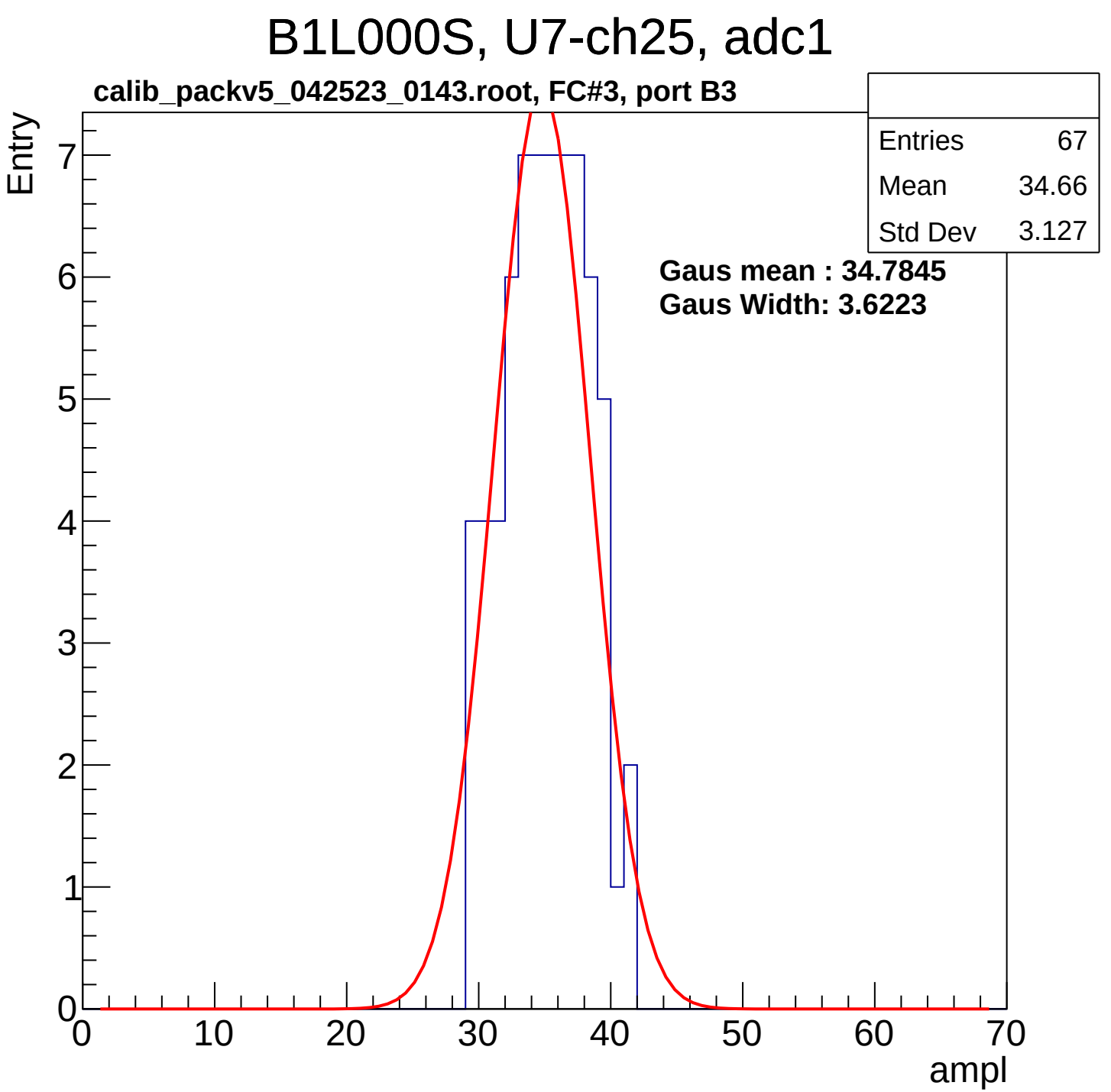
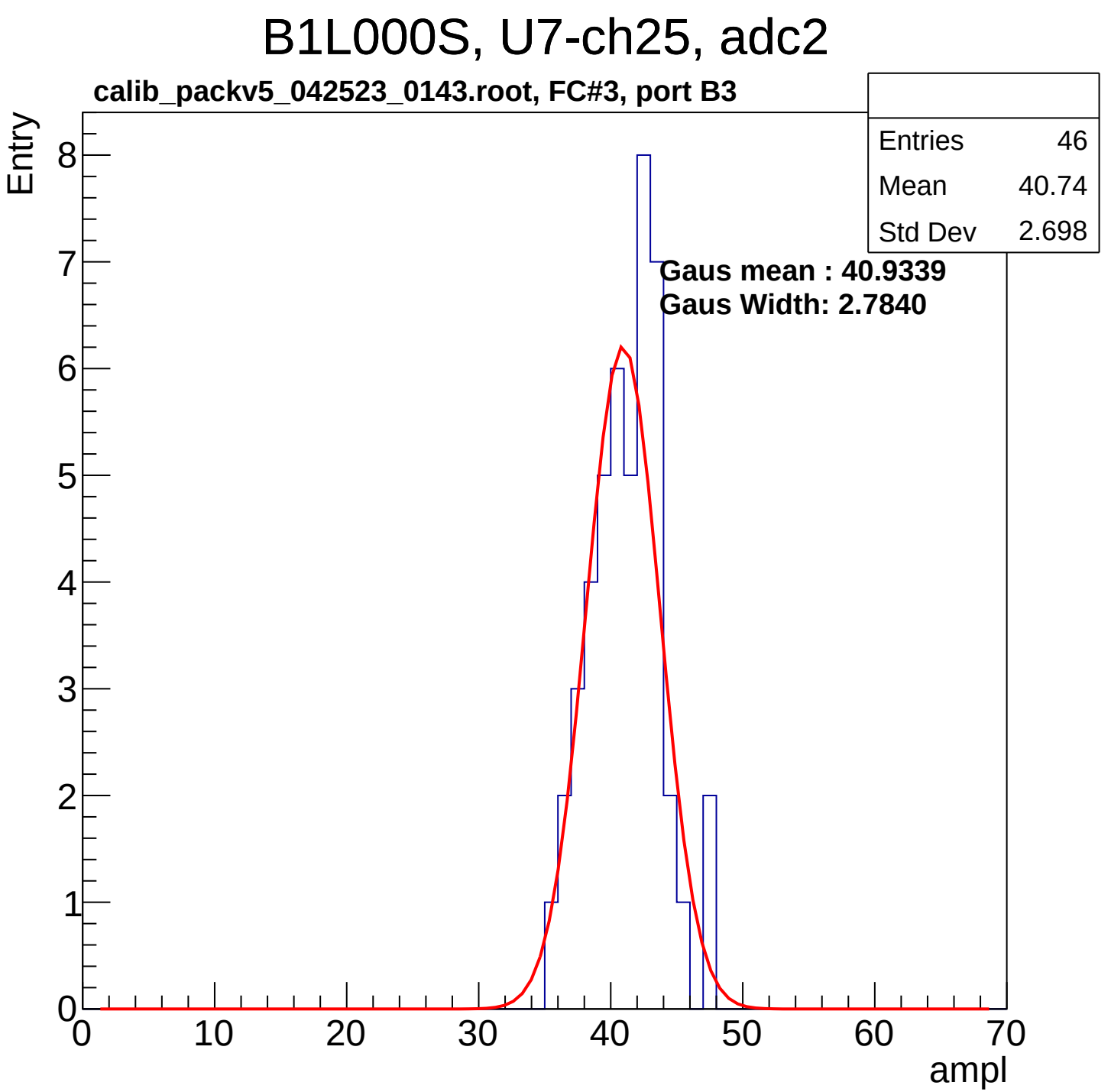
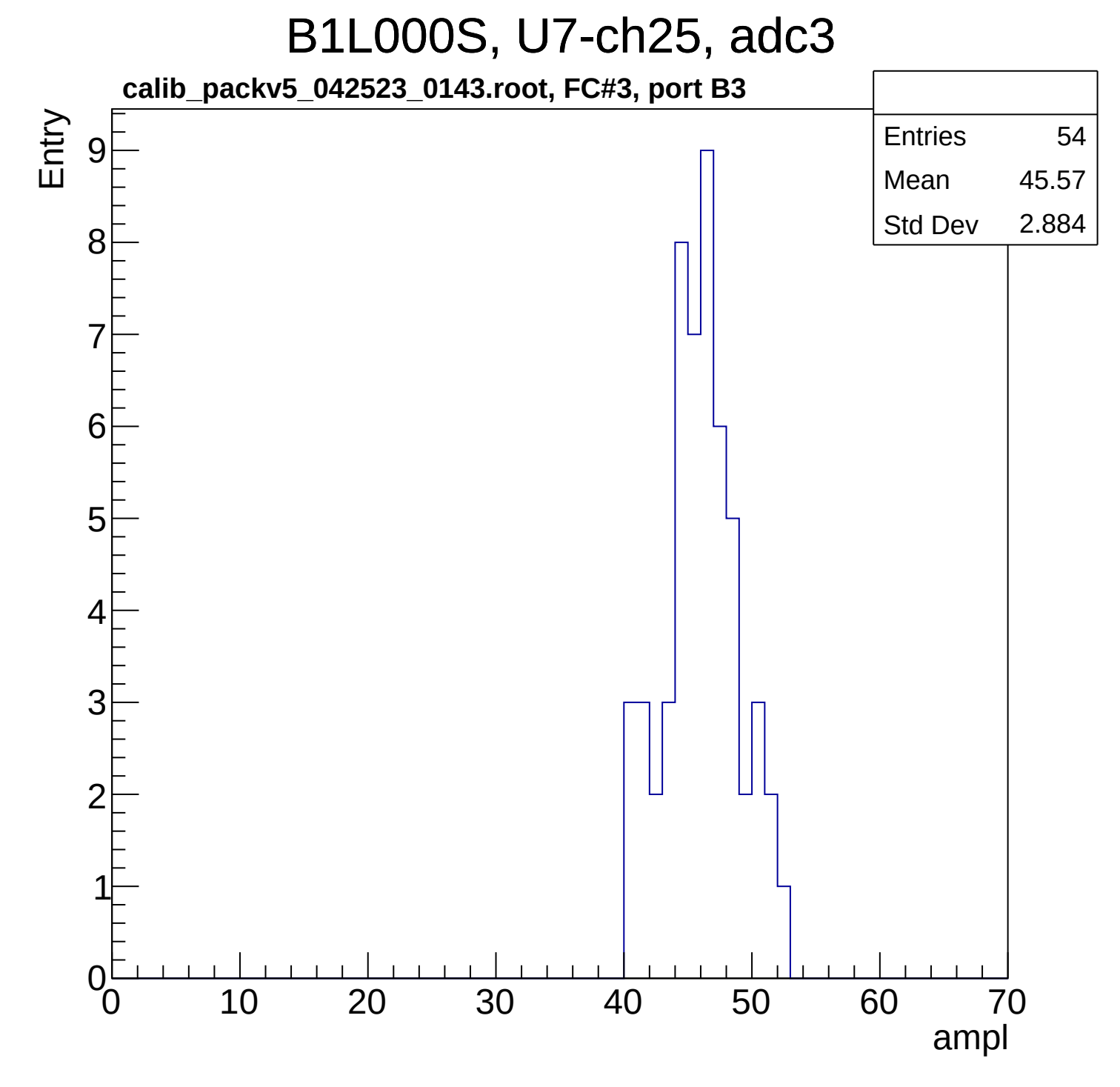

B1L000S, U7-ch24, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 61.43
Std Dev 1.348
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

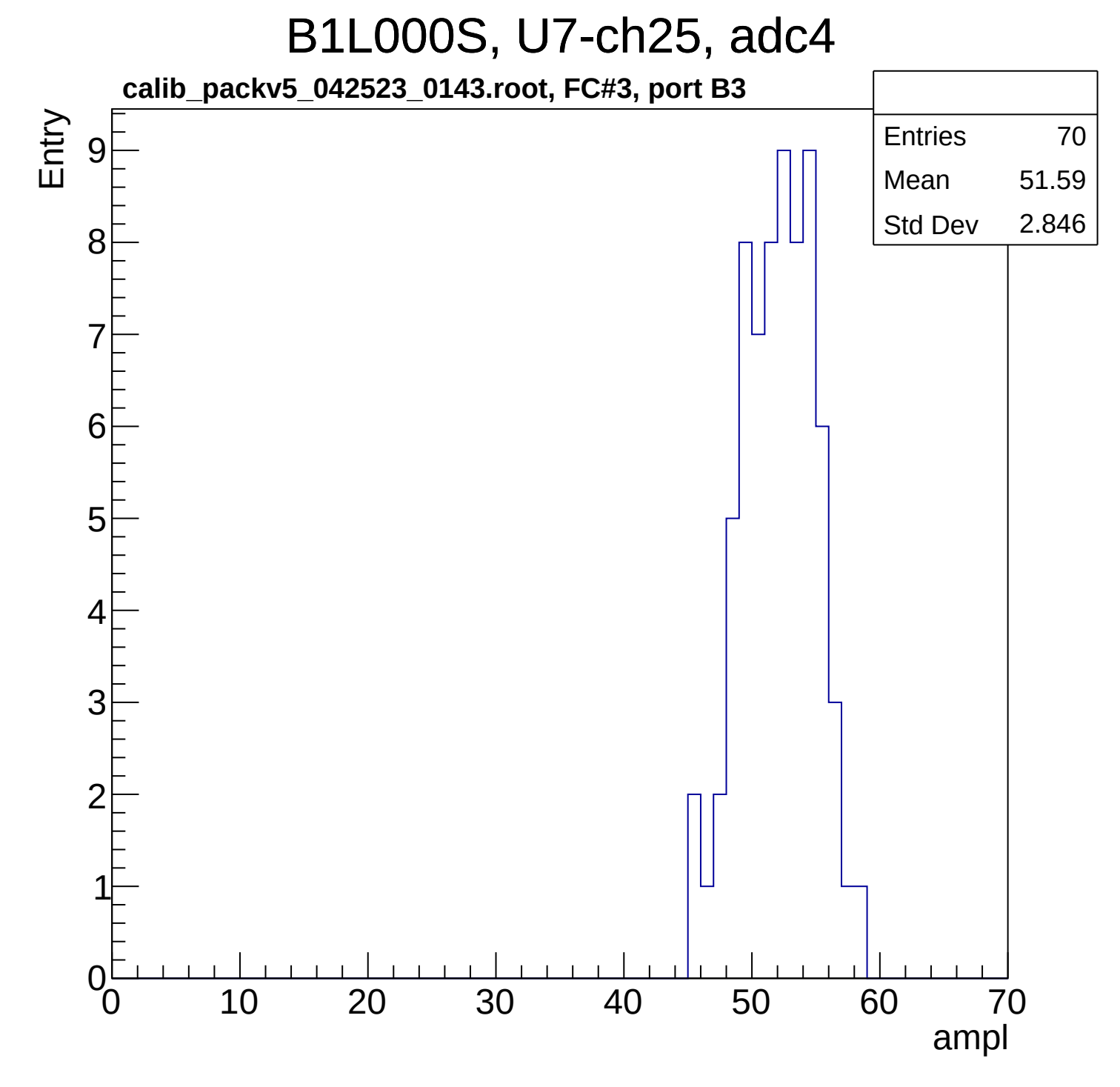

B1L000S, U7-ch24, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 44.83
Std Dev 25.58
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch25, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 27.78
Std Dev 4.668
Gaus mean : 28.3260
Gaus Width: 3.9317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch25, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.66
Std Dev 3.127
Gaus mean : 34.7845
Gaus Width: 3.6223
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch25, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 40.74
Std Dev 2.698
Gaus mean : 40.9339
Gaus Width: 2.7840
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch25, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 45.57
Std Dev 2.884
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch25, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 51.59
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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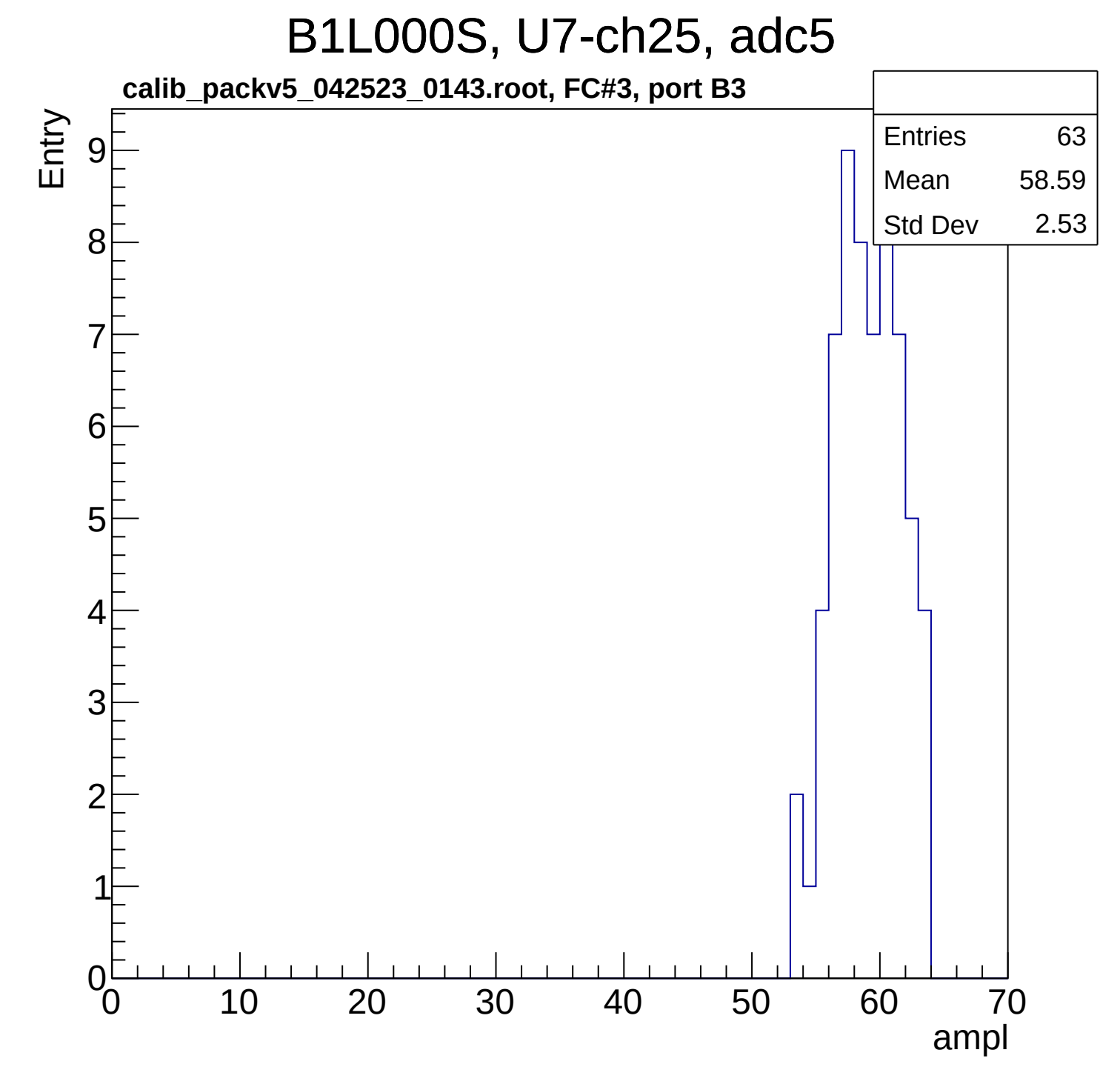
B1L000S, U7-ch25, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 58.59
Std Dev 2.53
Entry
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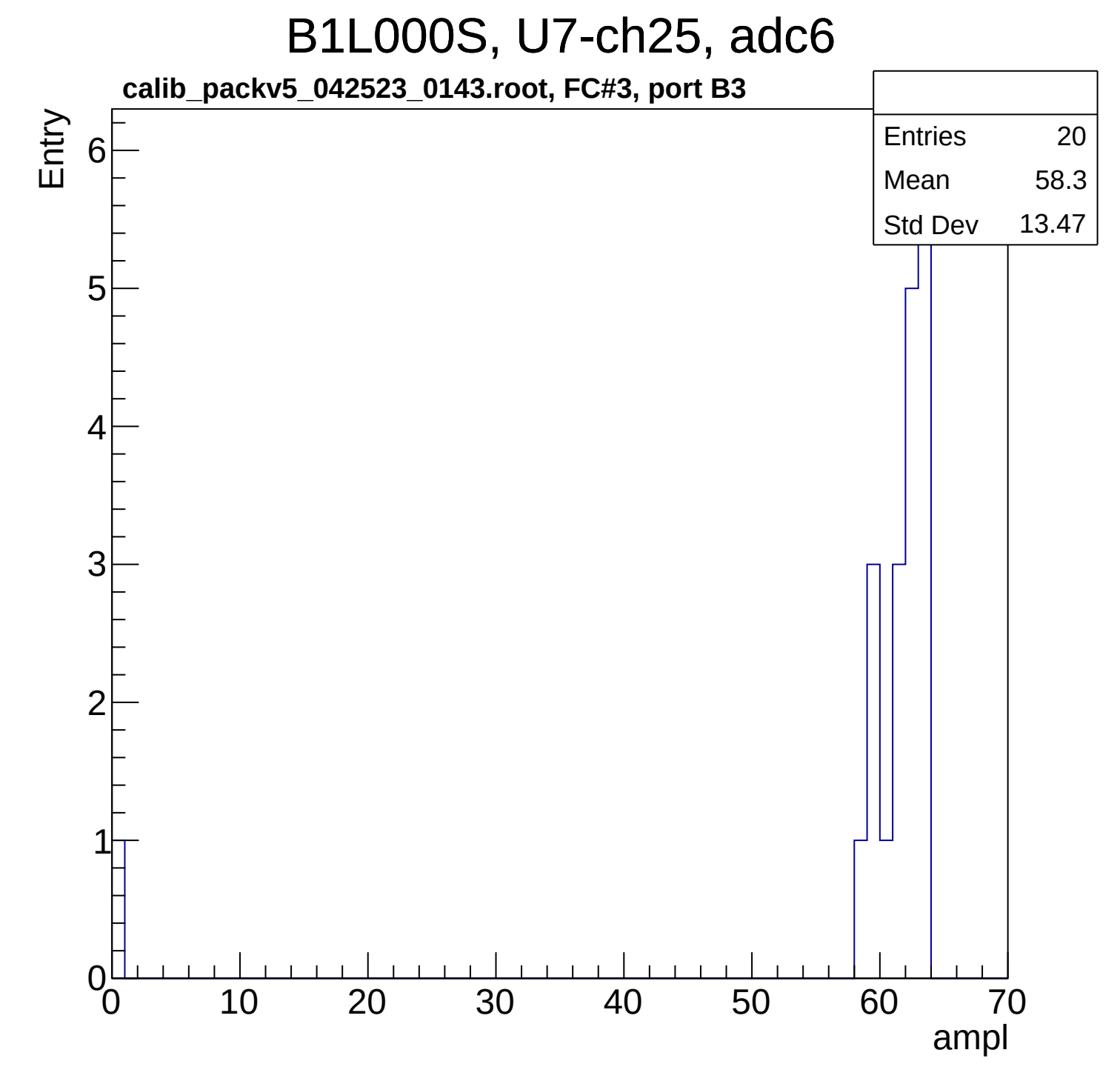
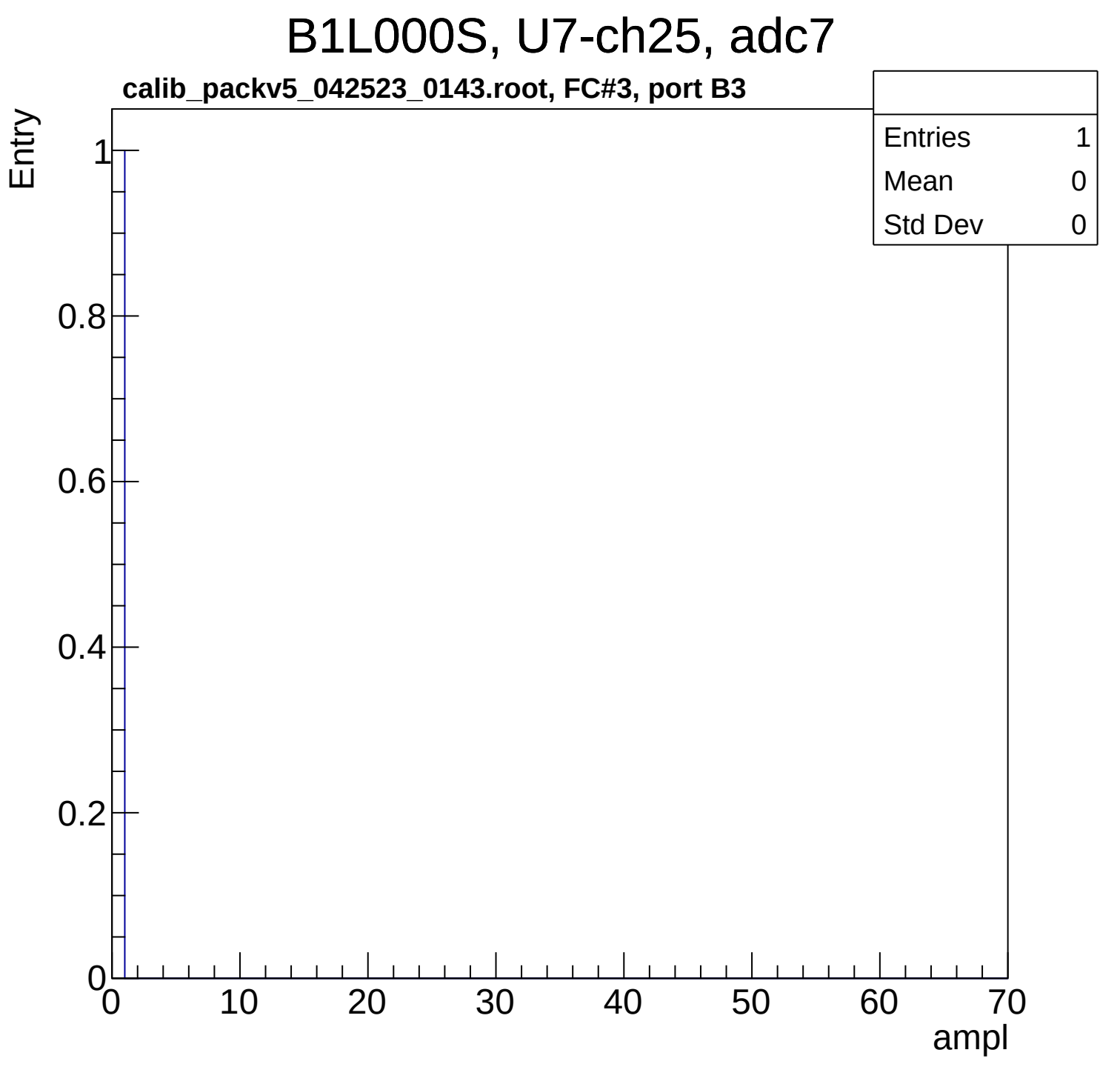
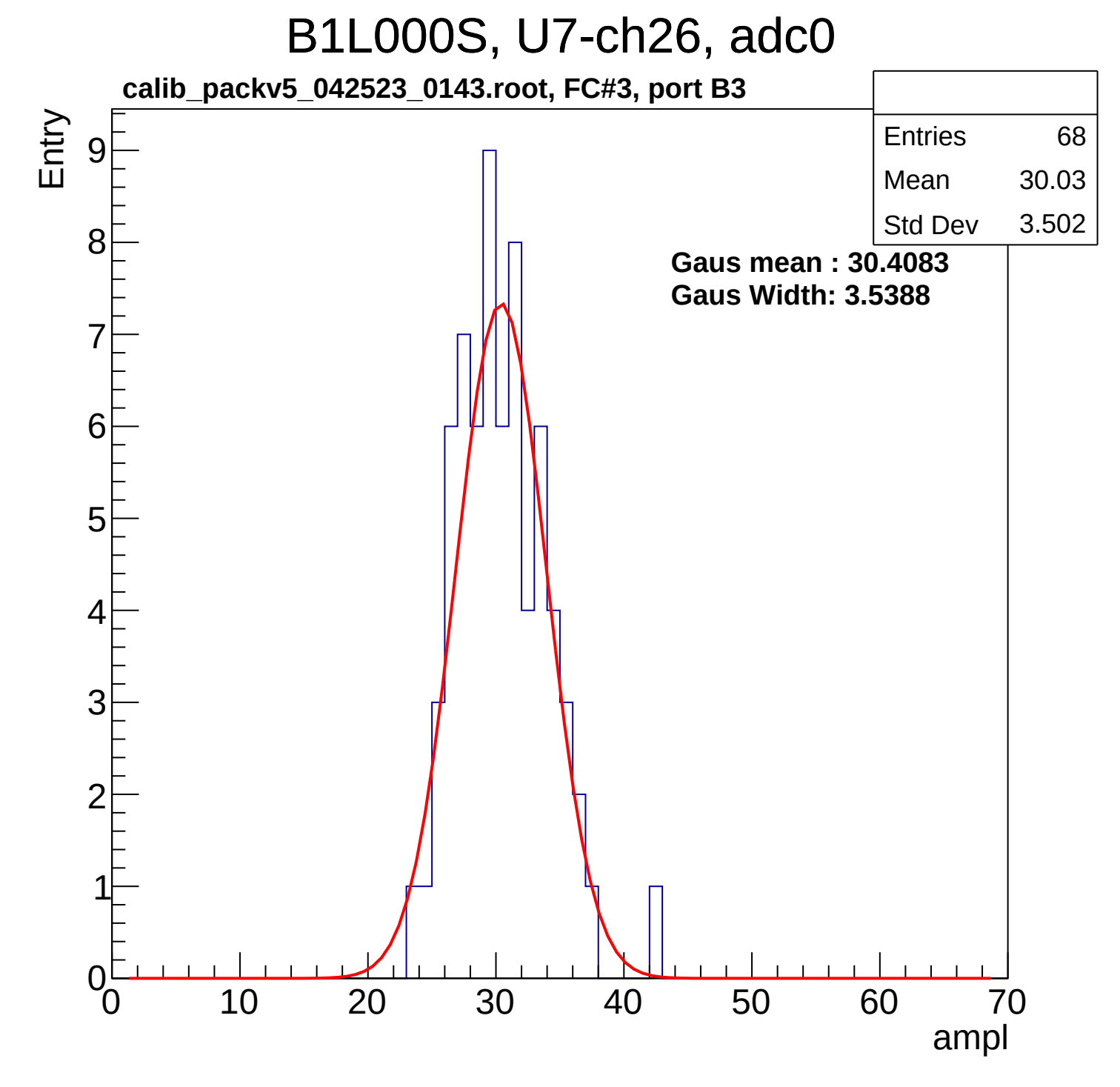
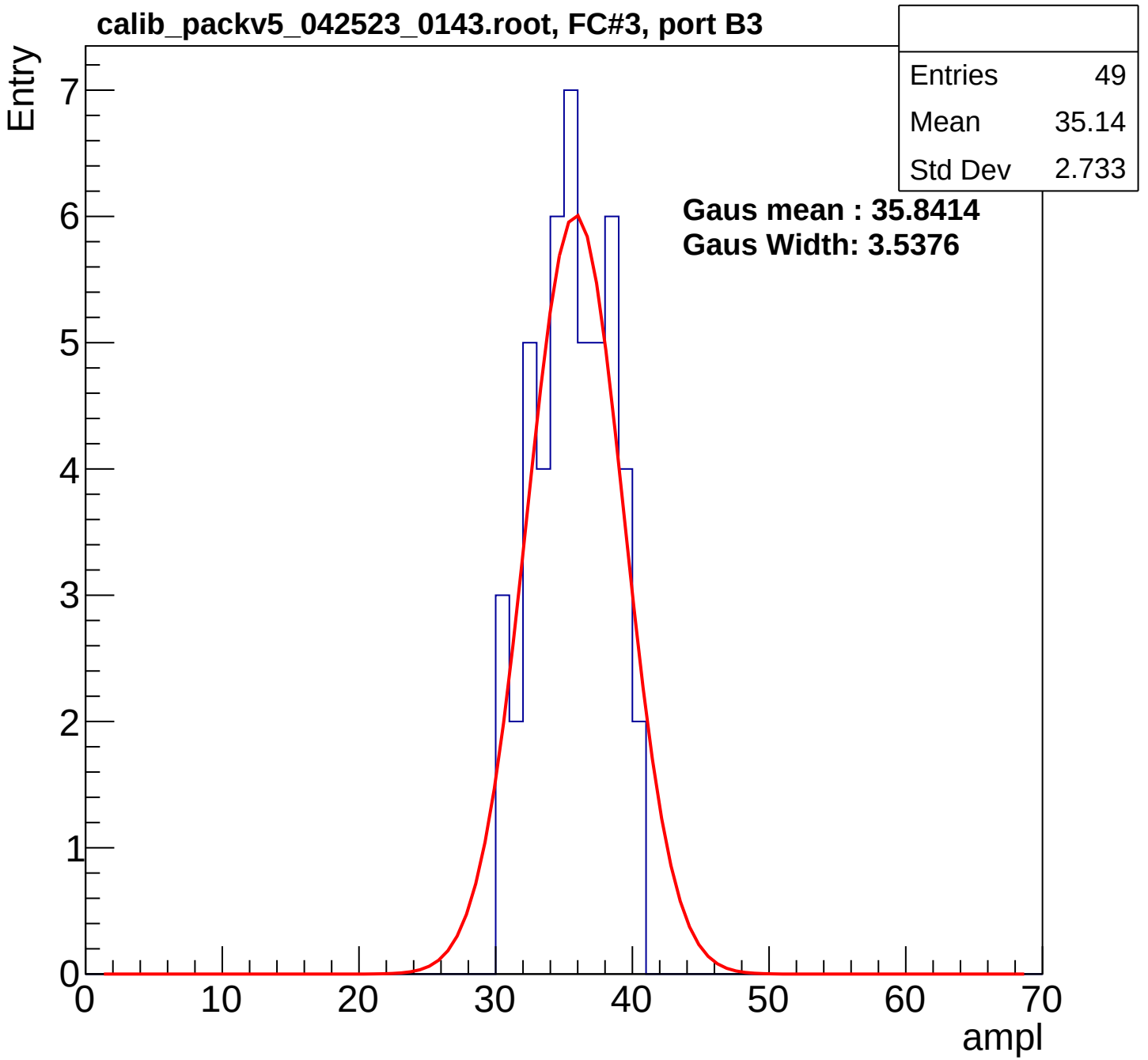
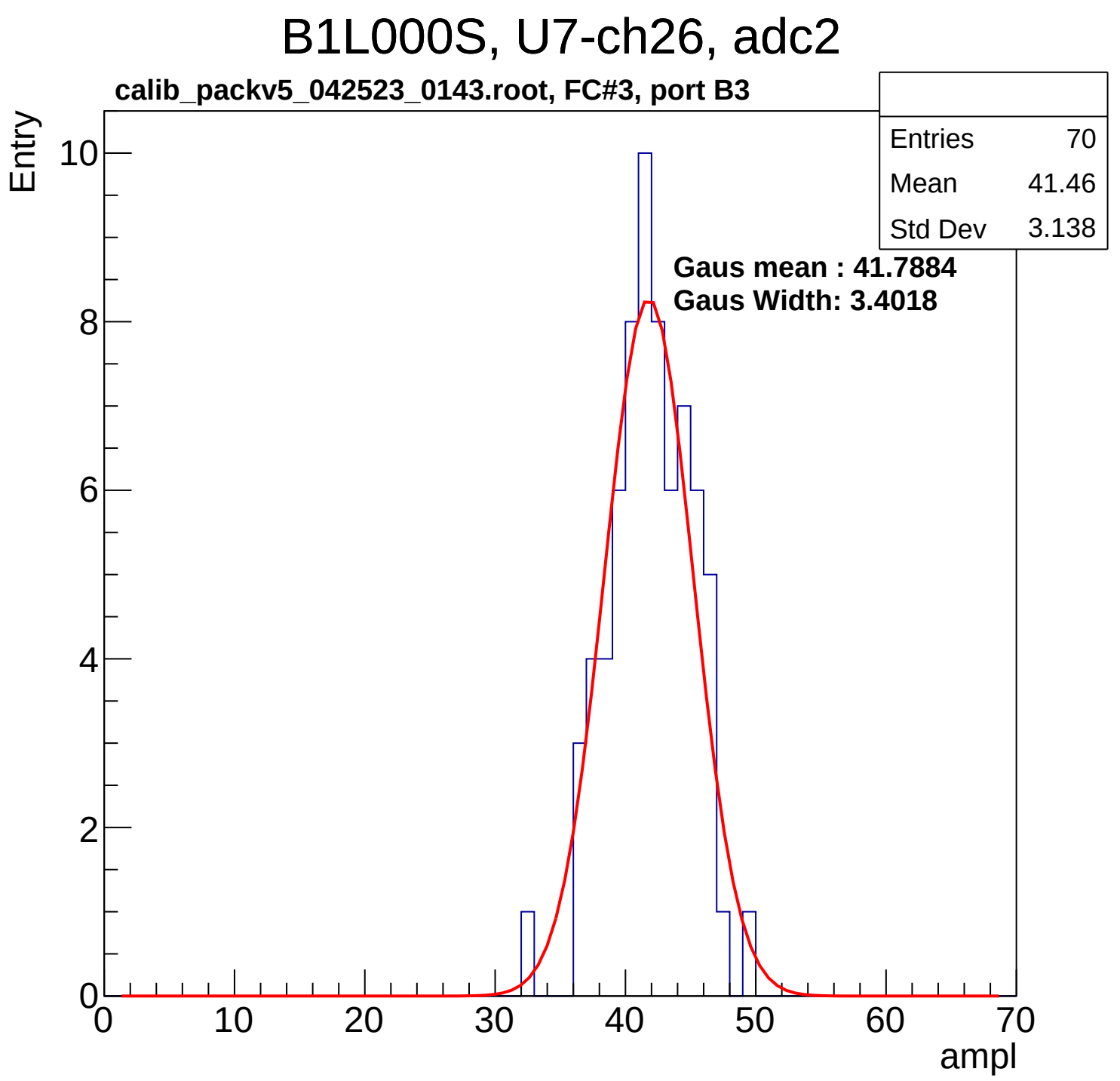
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

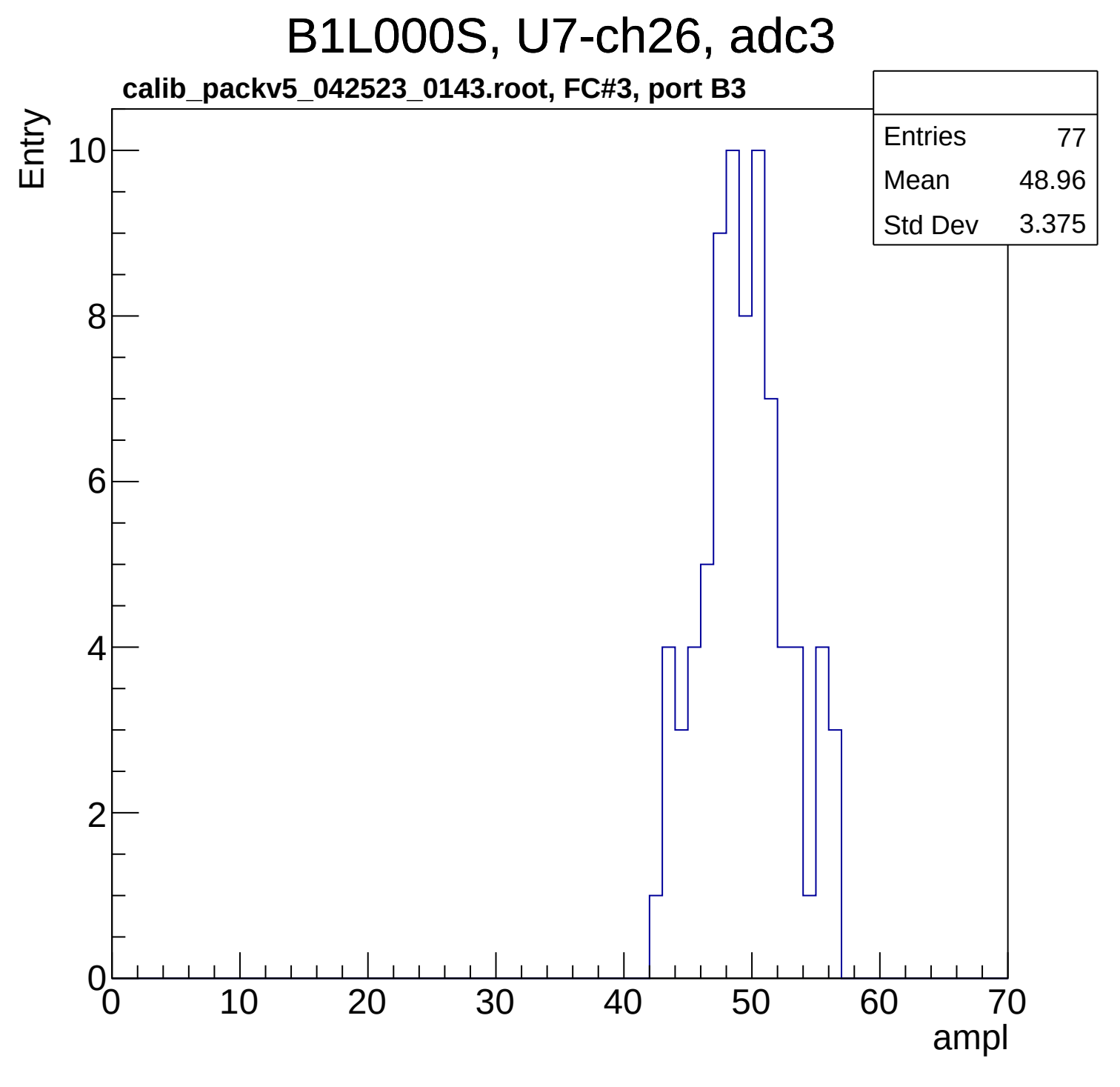

B1L000S, U7-ch25, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58.3
Std Dev 13.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

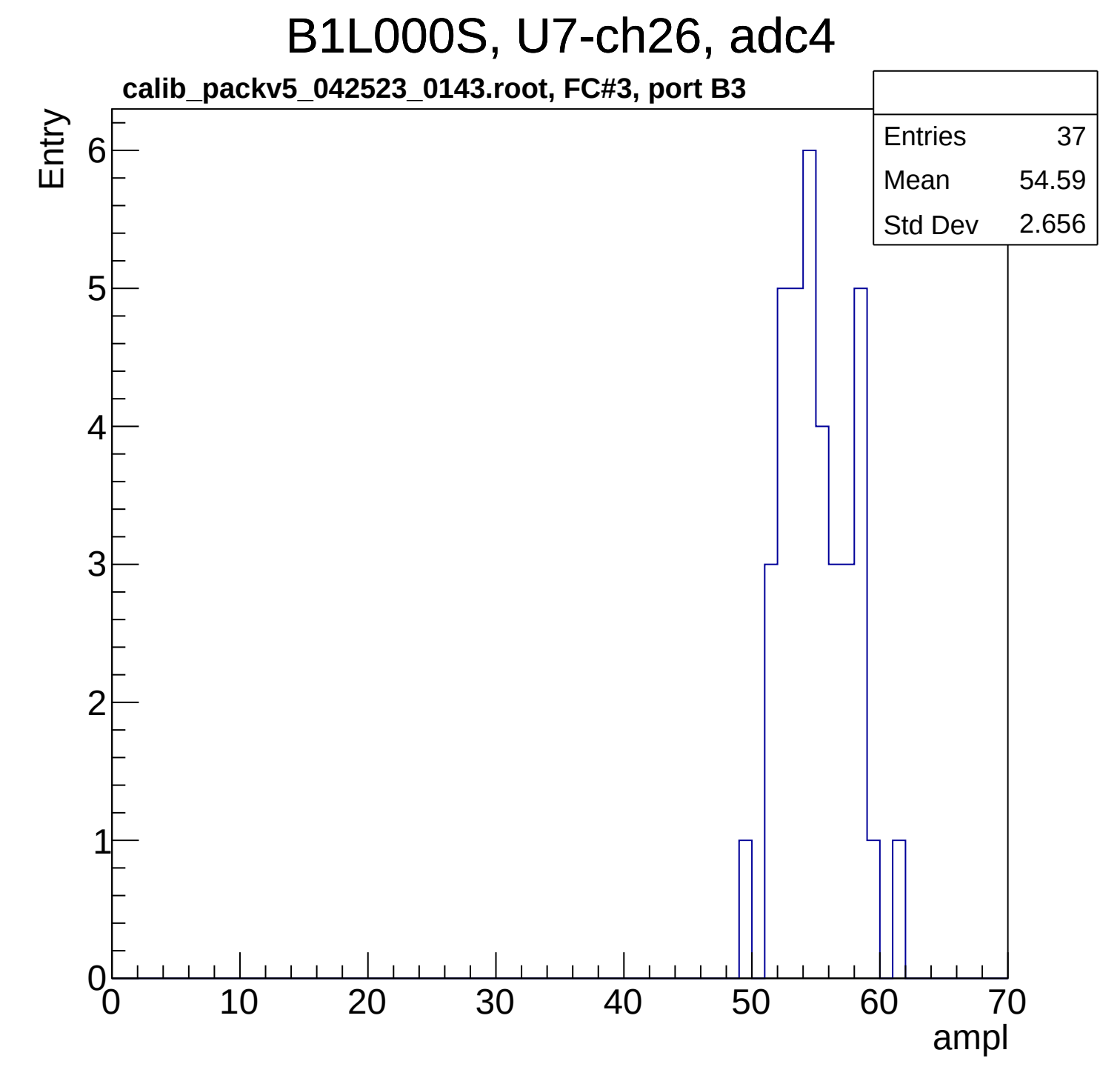

B1L000S, U7-ch25, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

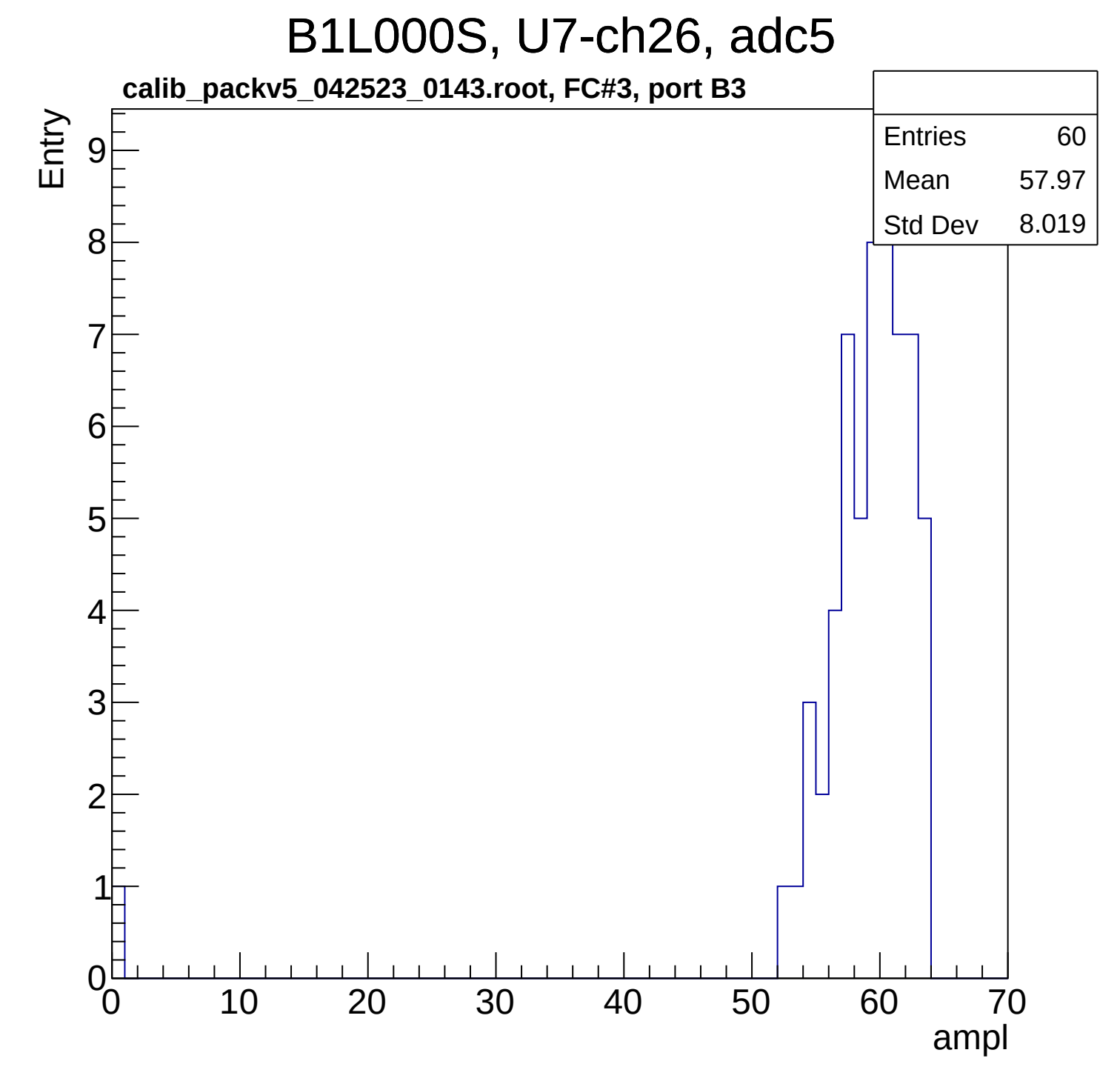

B1L000S, U7-ch26, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 30.03
Std Dev 3.502
Gaus mean : 30.4083
Gaus Width: 3.5388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

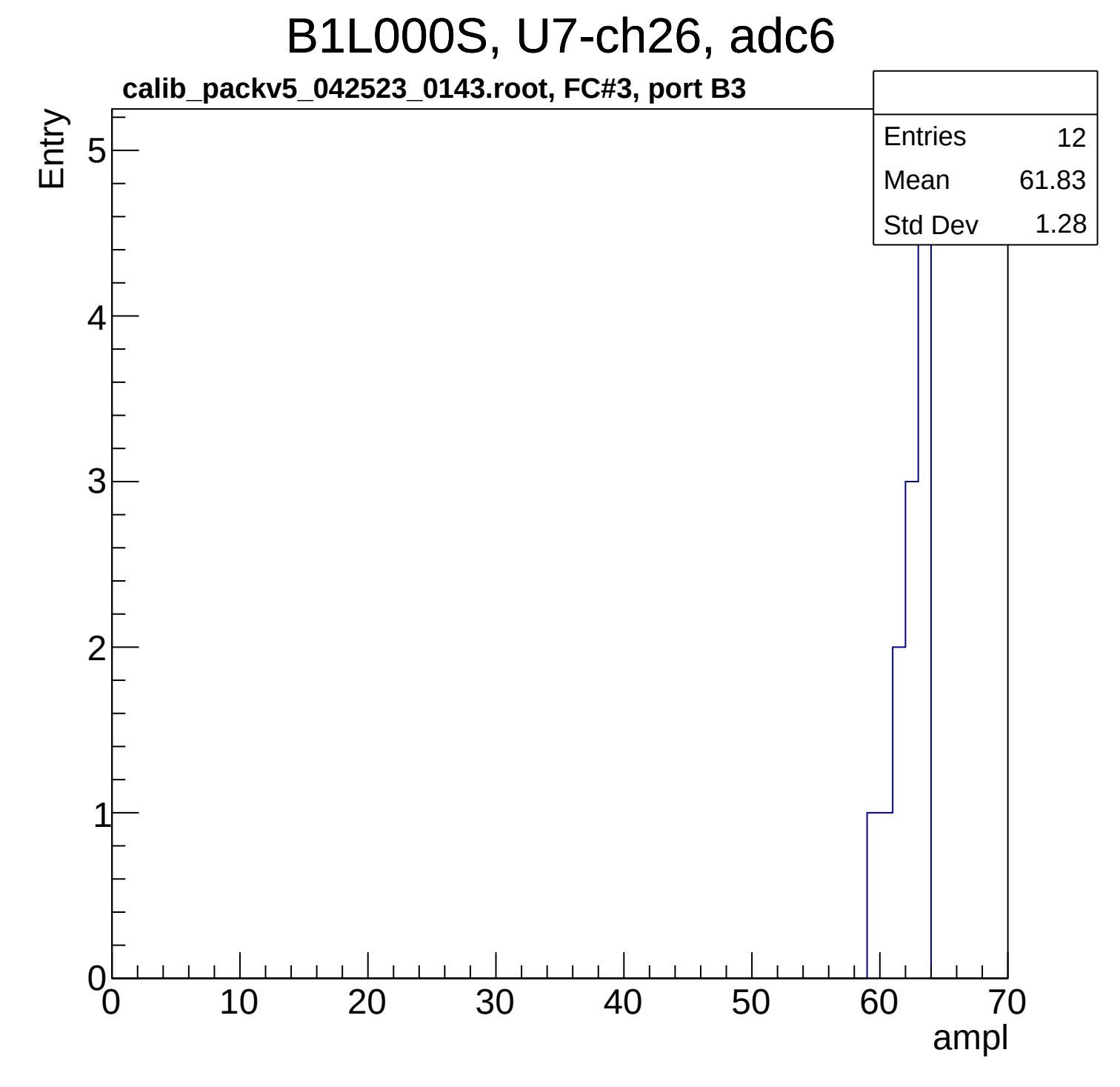

B1L000S, U7-ch26, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 35.14
Std Dev 2.733
Gaus mean : 35.8414
Gaus Width: 3.5376
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

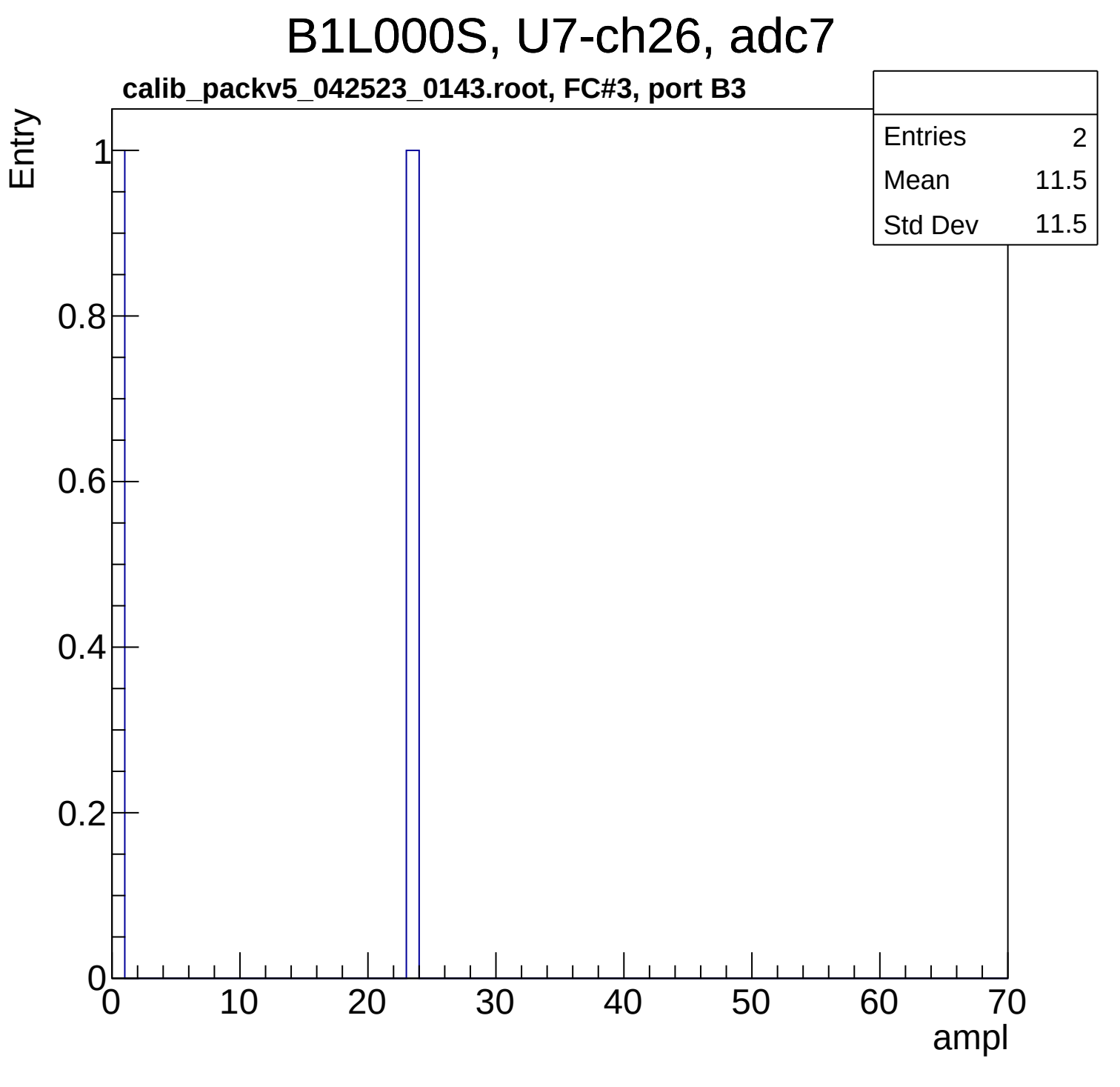

B1L000S, U7-ch26, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.46
Std Dev 3.138
Gaus mean : 41.7884
Gaus Width: 3.4018
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

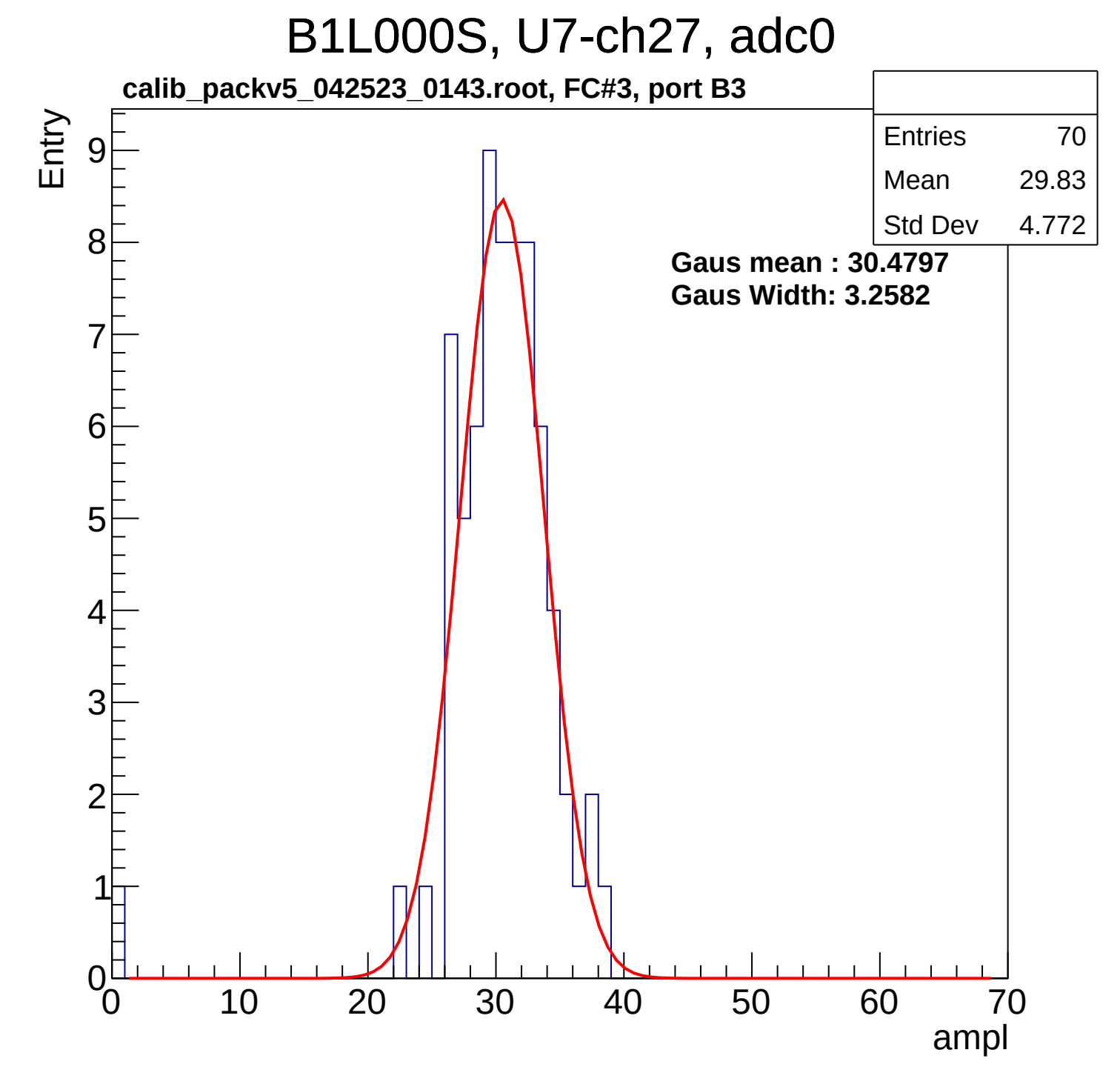

B1L000S, U7-ch26, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 48.96
Std Dev 3.375
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

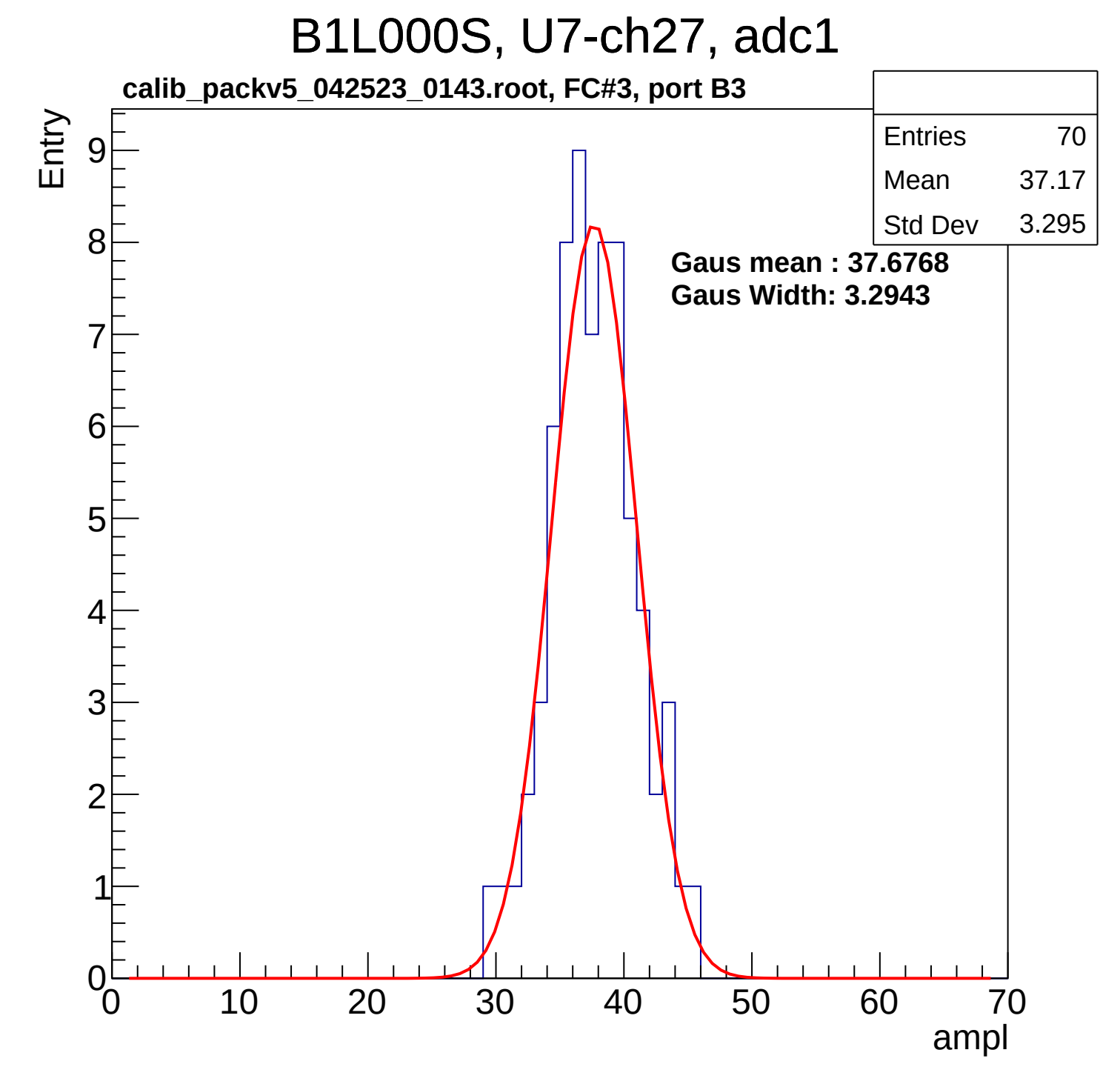

B1L000S, U7-ch26, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 54.59
Std Dev 2.656
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch26, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 57.97
Std Dev 8.019
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch26, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.83
Std Dev 1.28
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch26, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch27, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 29.83
Std Dev 4.772
Gaus mean : 30.4797
Gaus Width: 3.2582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch27, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 37.17
Std Dev 3.295
Gaus mean : 37.6768
Gaus Width: 3.2943
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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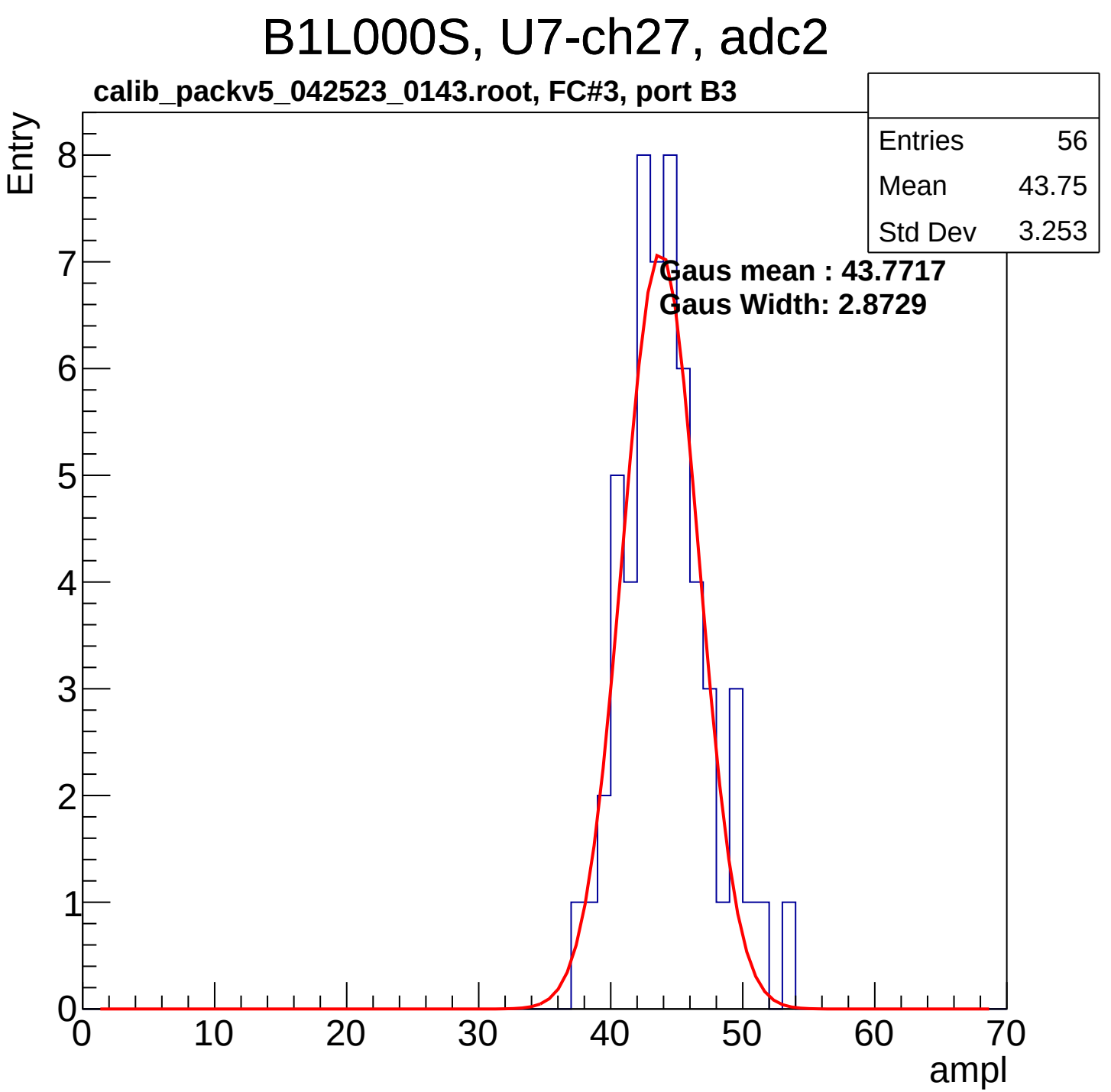
B1L000S, U7-ch27, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 43.75
Std Dev 3.253
Gaus mean : 43.7717
Gaus Width: 2.8729
Entry
ampl
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2
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4
5
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0
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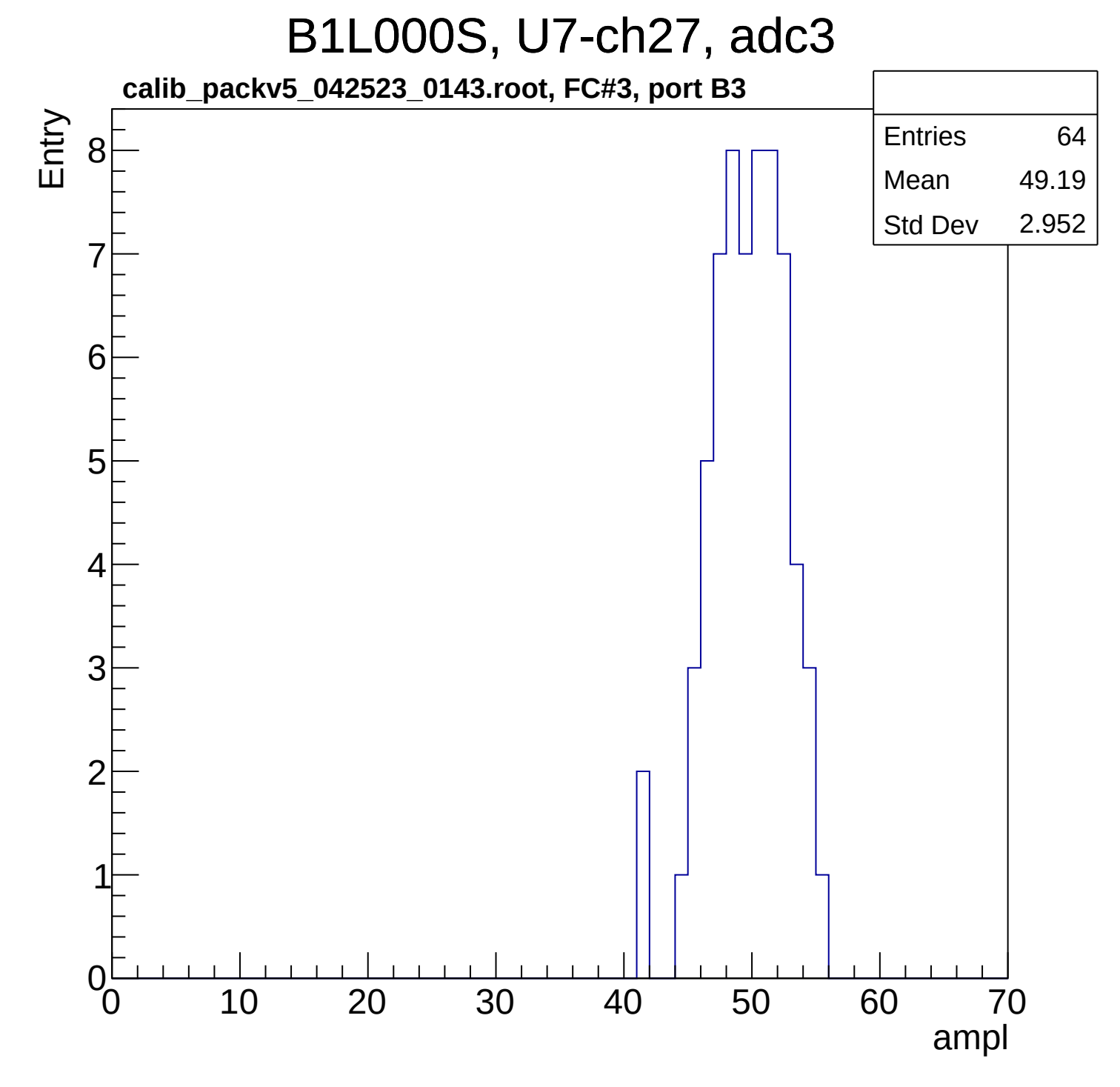
B1L000S, U7-ch27, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 49.19
Std Dev 2.952
Entry
ampl
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2
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5
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7
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0
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60
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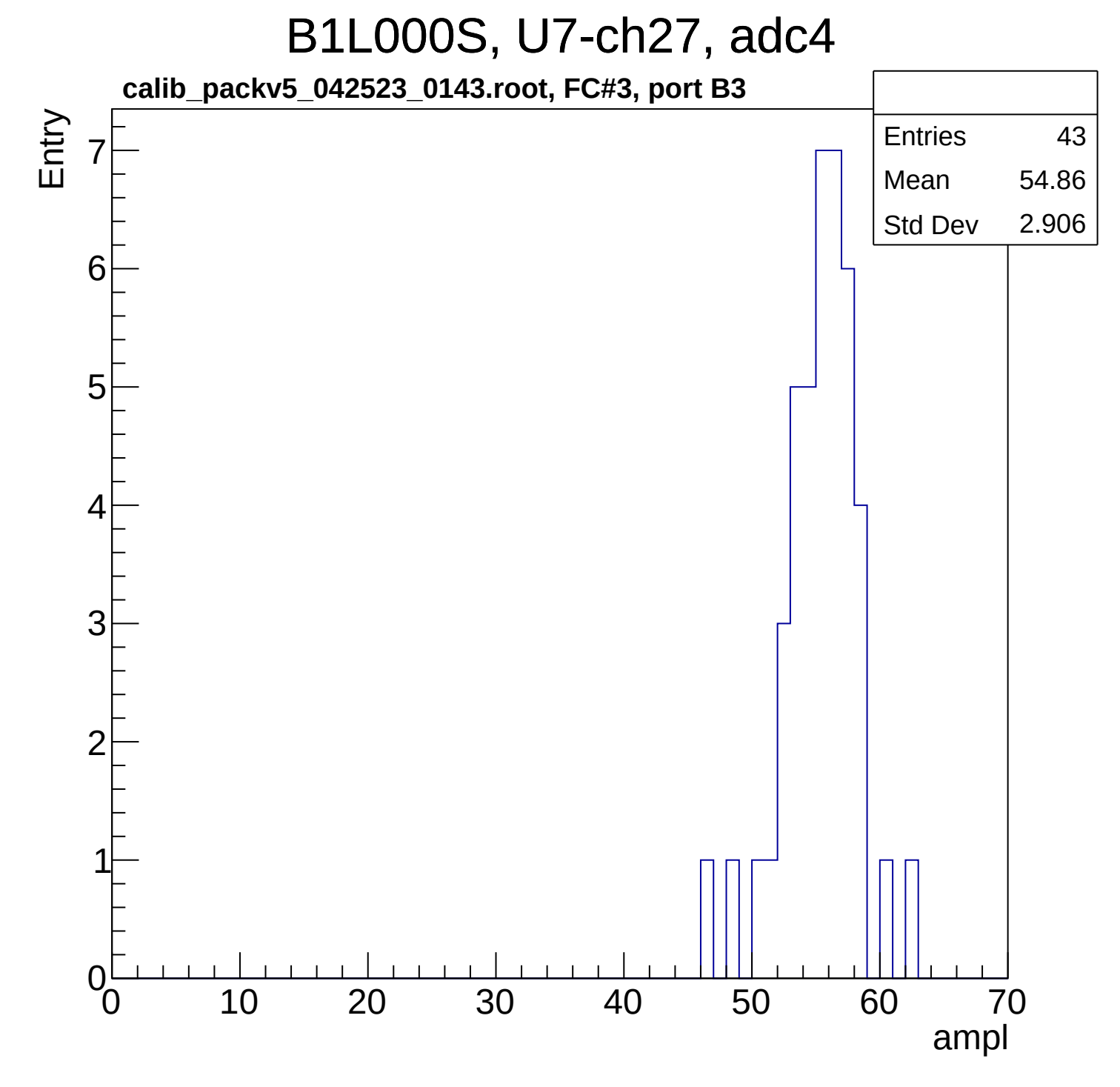
B1L000S, U7-ch27, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 43
Mean 54.86
Std Dev 2.906
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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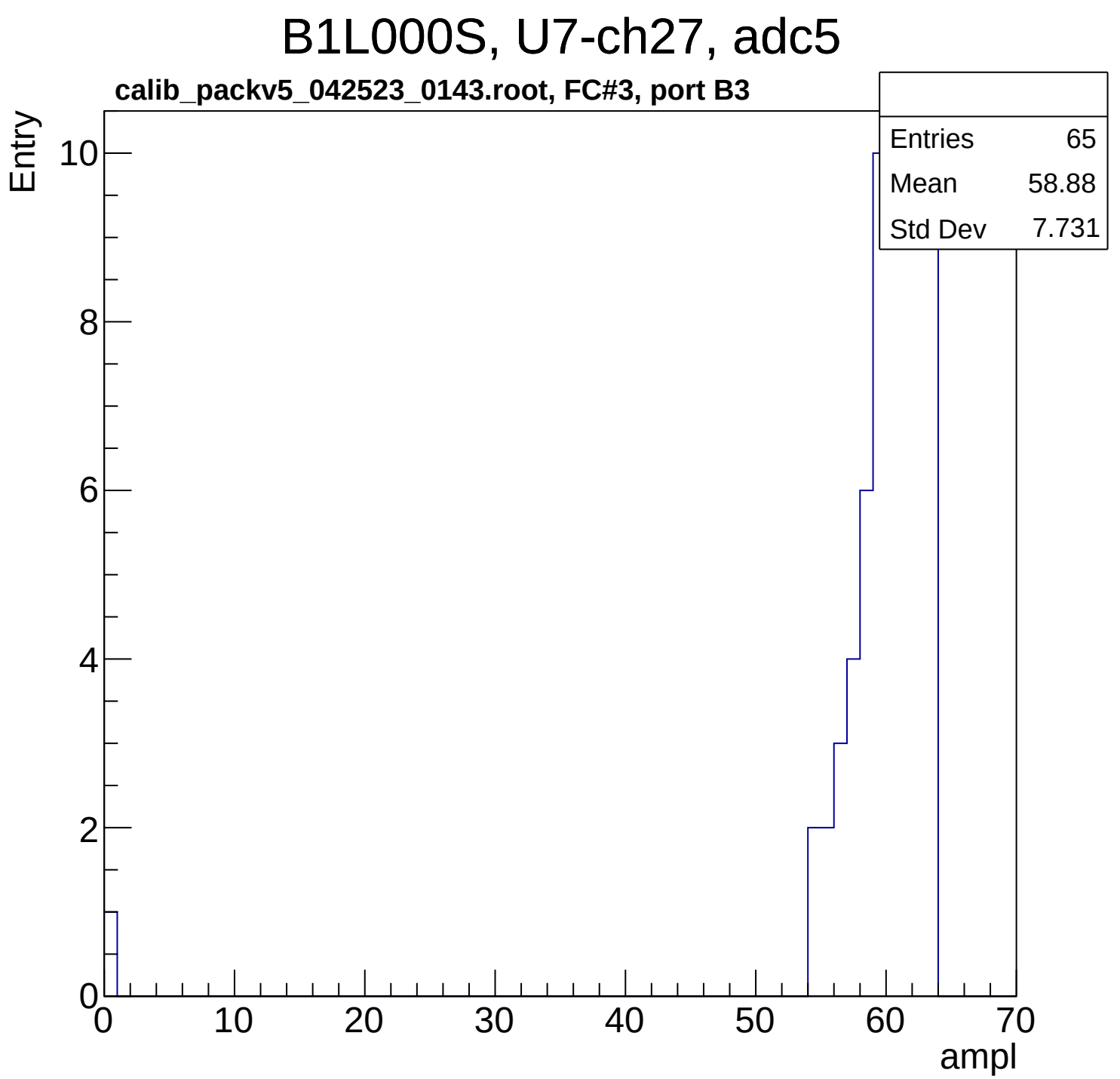
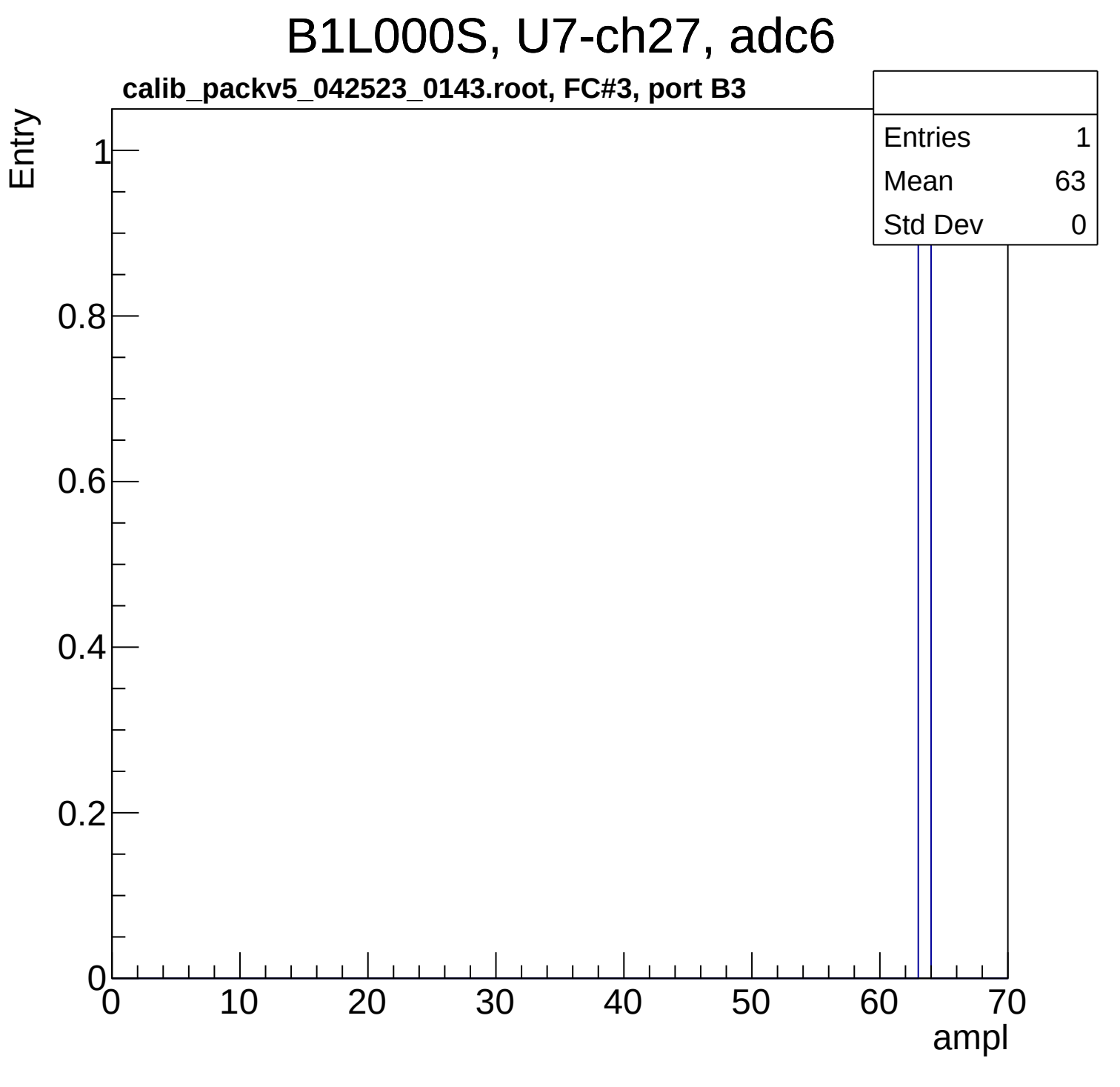
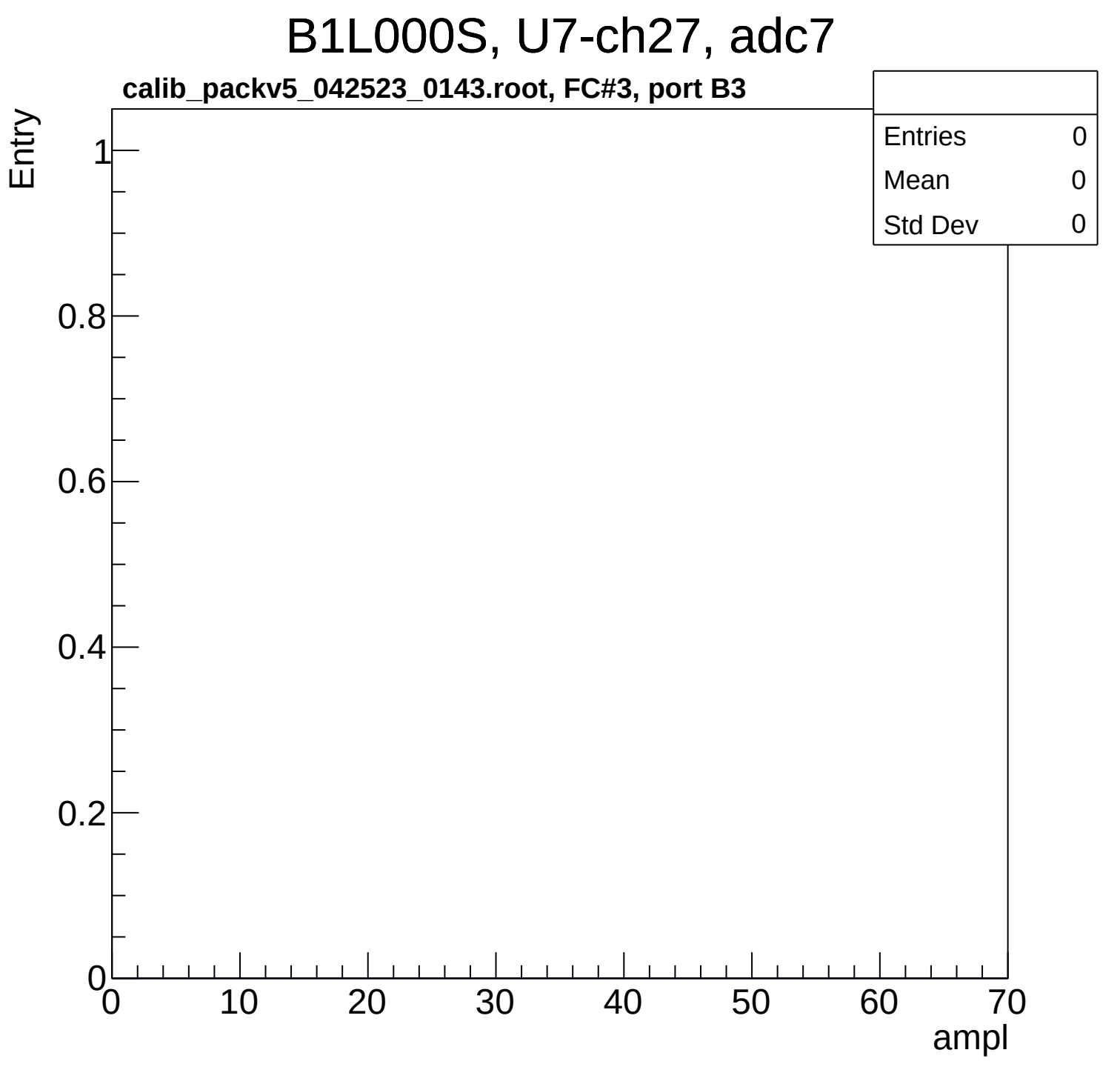
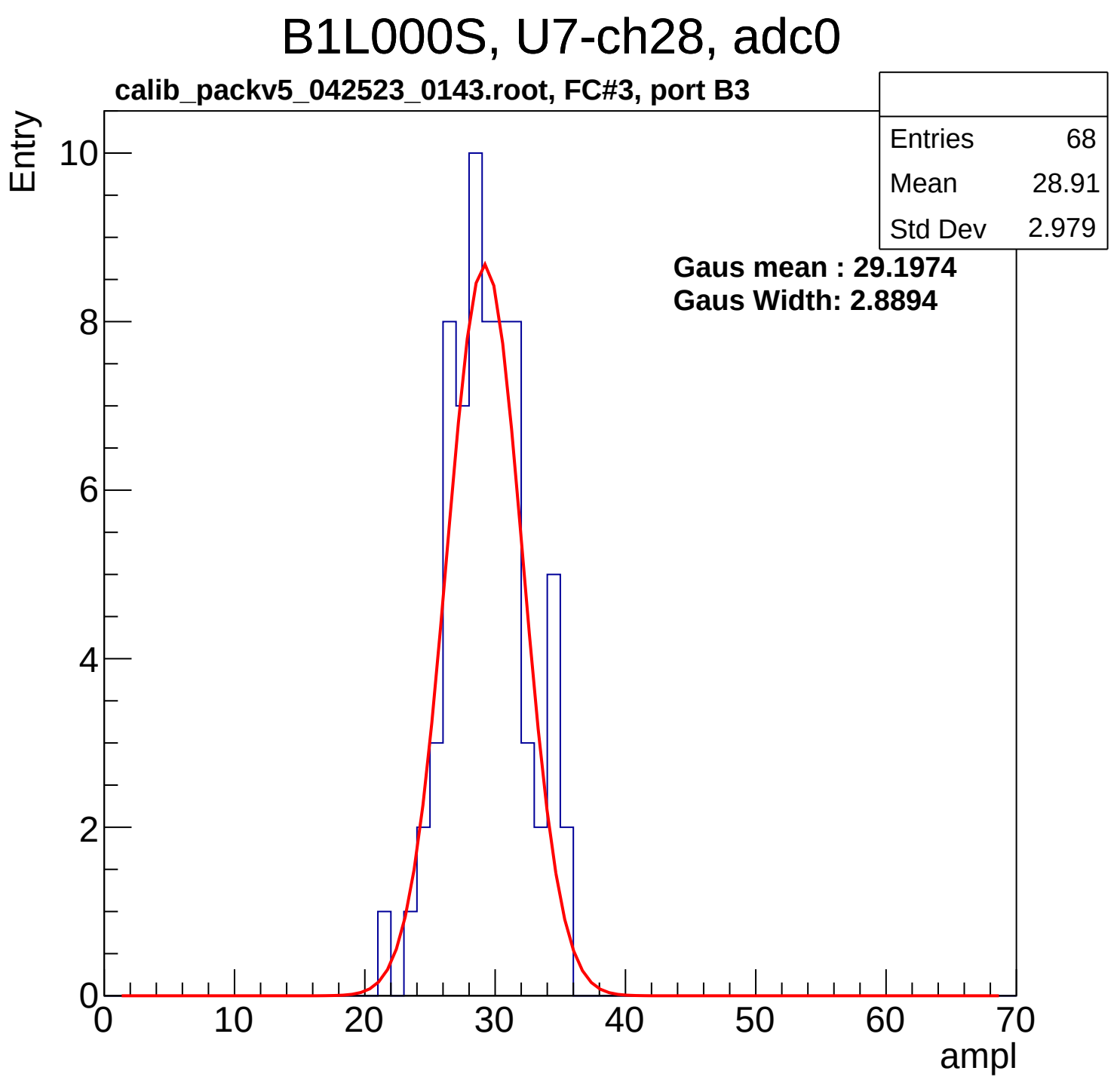
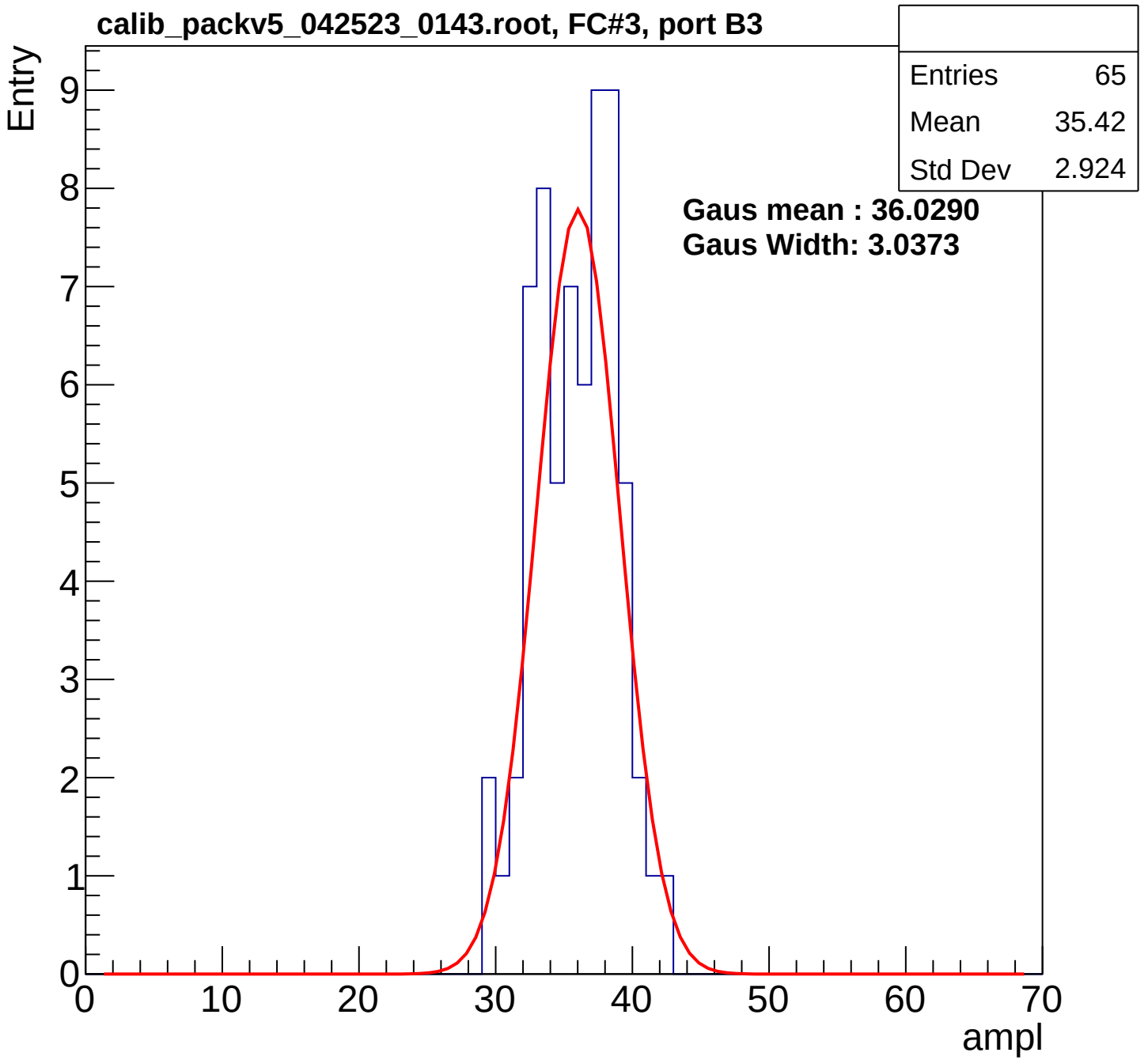

B1L000S, U7-ch27, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 58.88
Std Dev 7.731
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch27, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch27, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch28, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 28.91
Std Dev 2.979
Gaus mean : 29.1974
Gaus Width: 2.8894
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch28, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.42
Std Dev 2.924
Gaus mean : 36.0290
Gaus Width: 3.0373
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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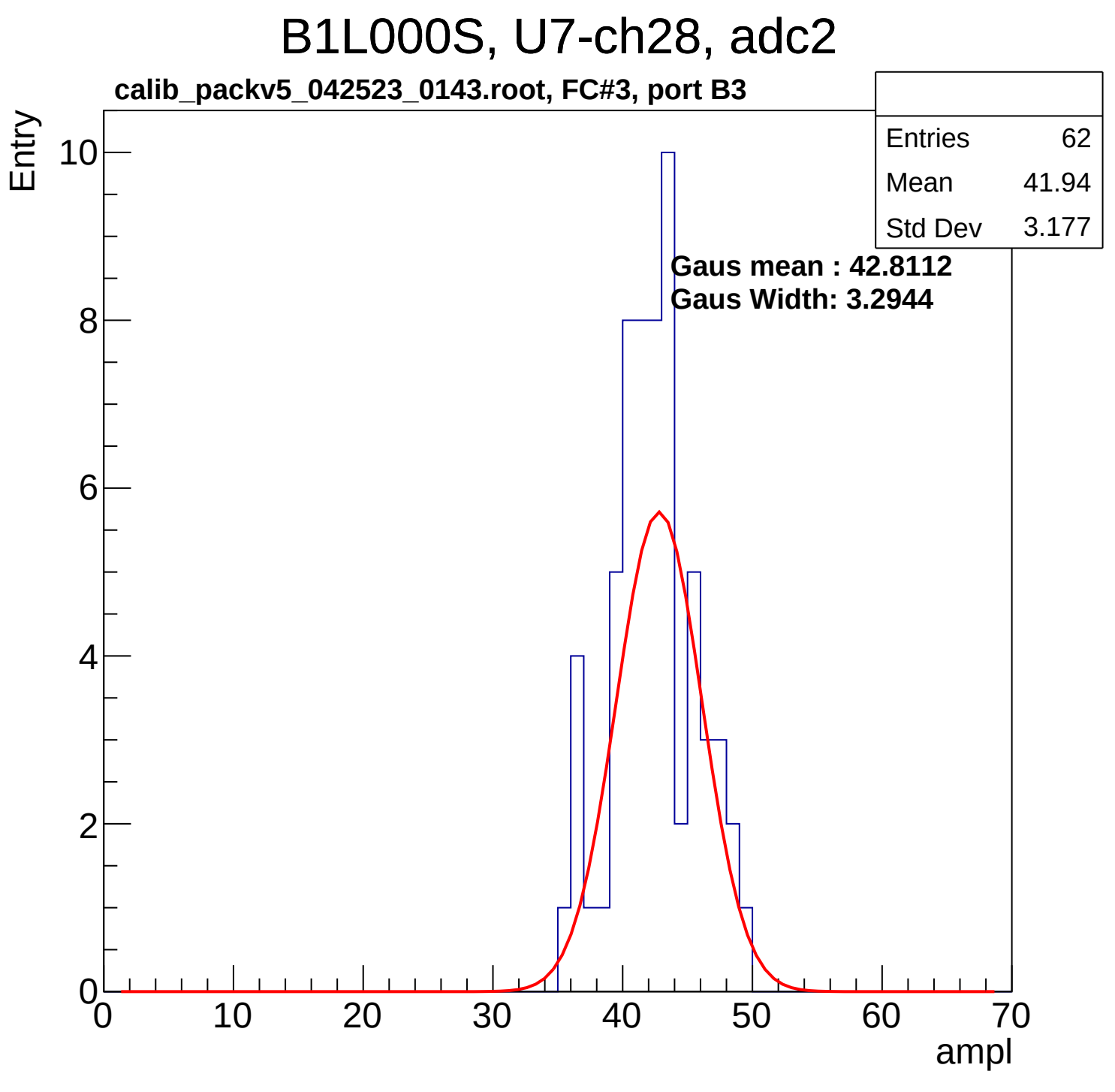
B1L000S, U7-ch28, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.94
Std Dev 3.177
Gaus mean : 42.8112
Gaus Width: 3.2944
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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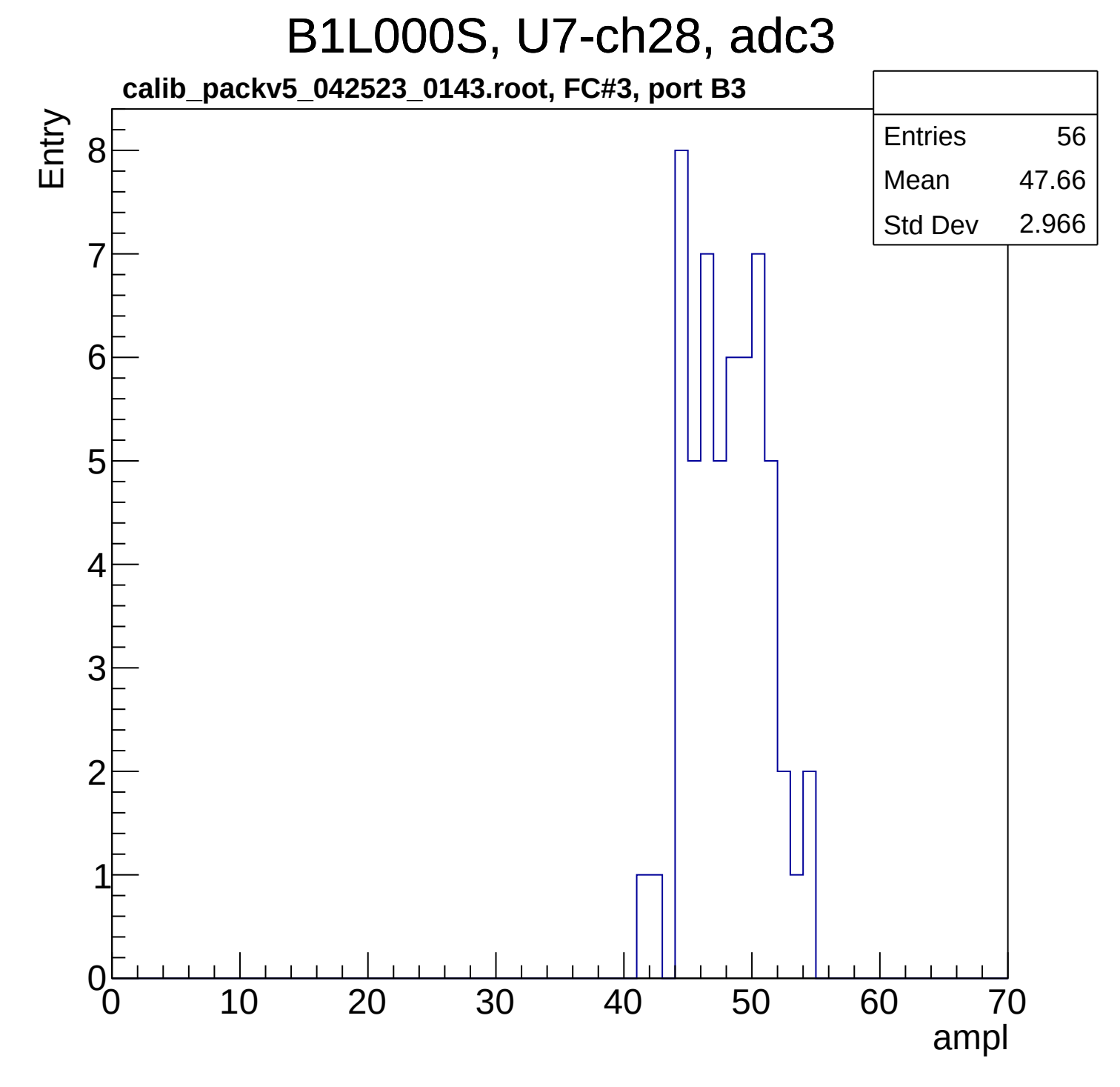
B1L000S, U7-ch28, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 47.66
Std Dev 2.966
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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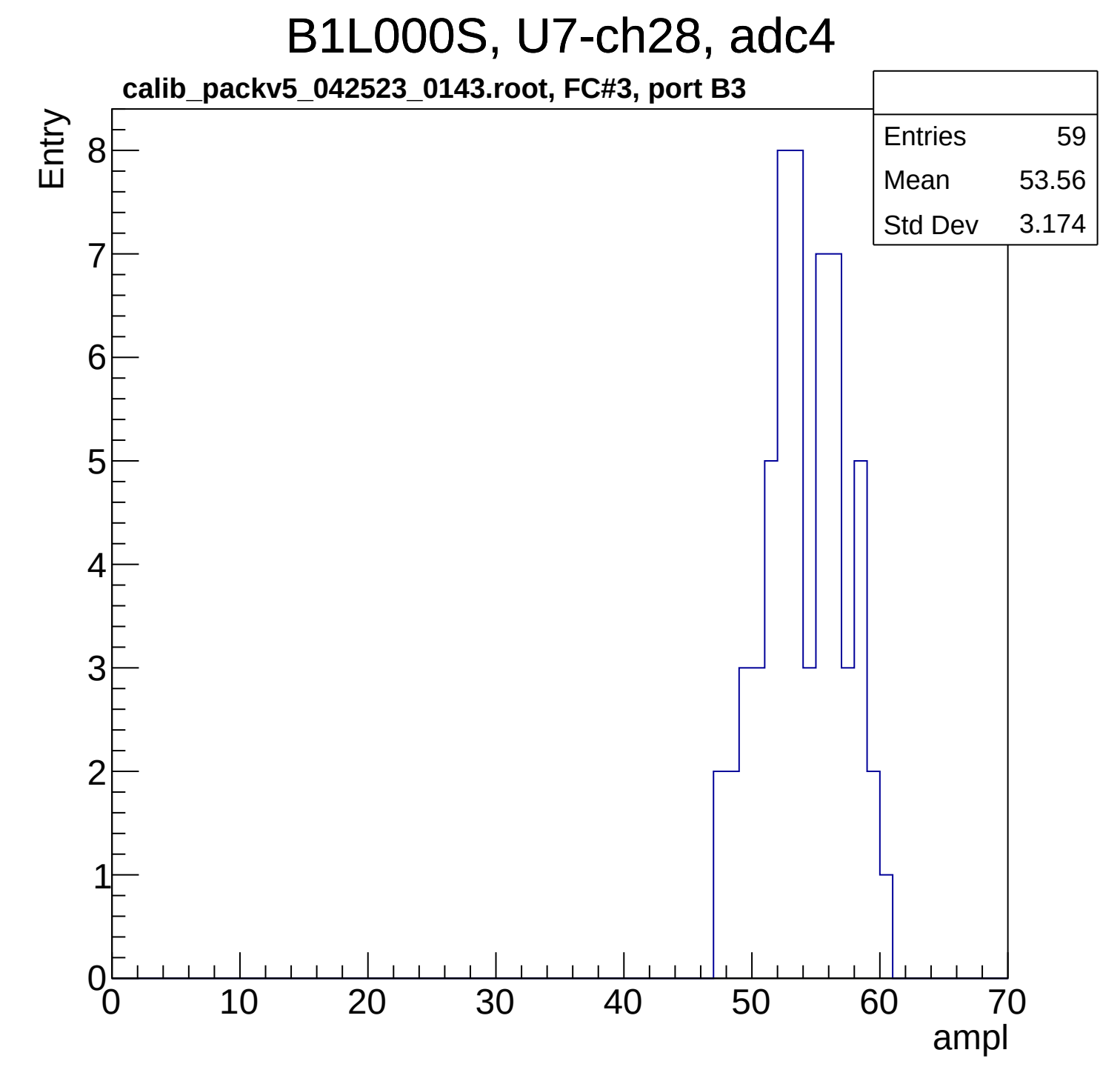
B1L000S, U7-ch28, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.56
Std Dev 3.174
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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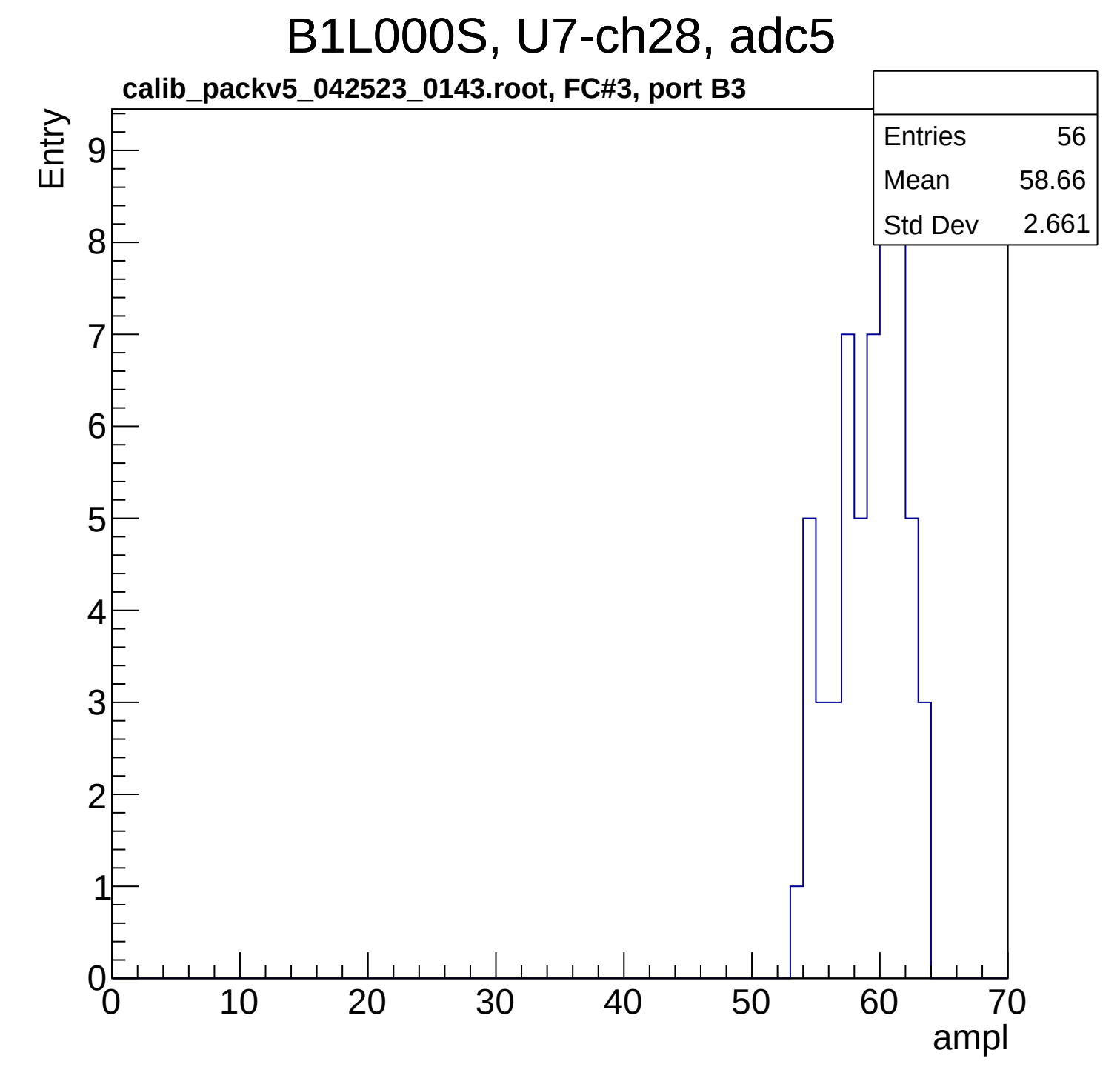
B1L000S, U7-ch28, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 58.66
Std Dev 2.661
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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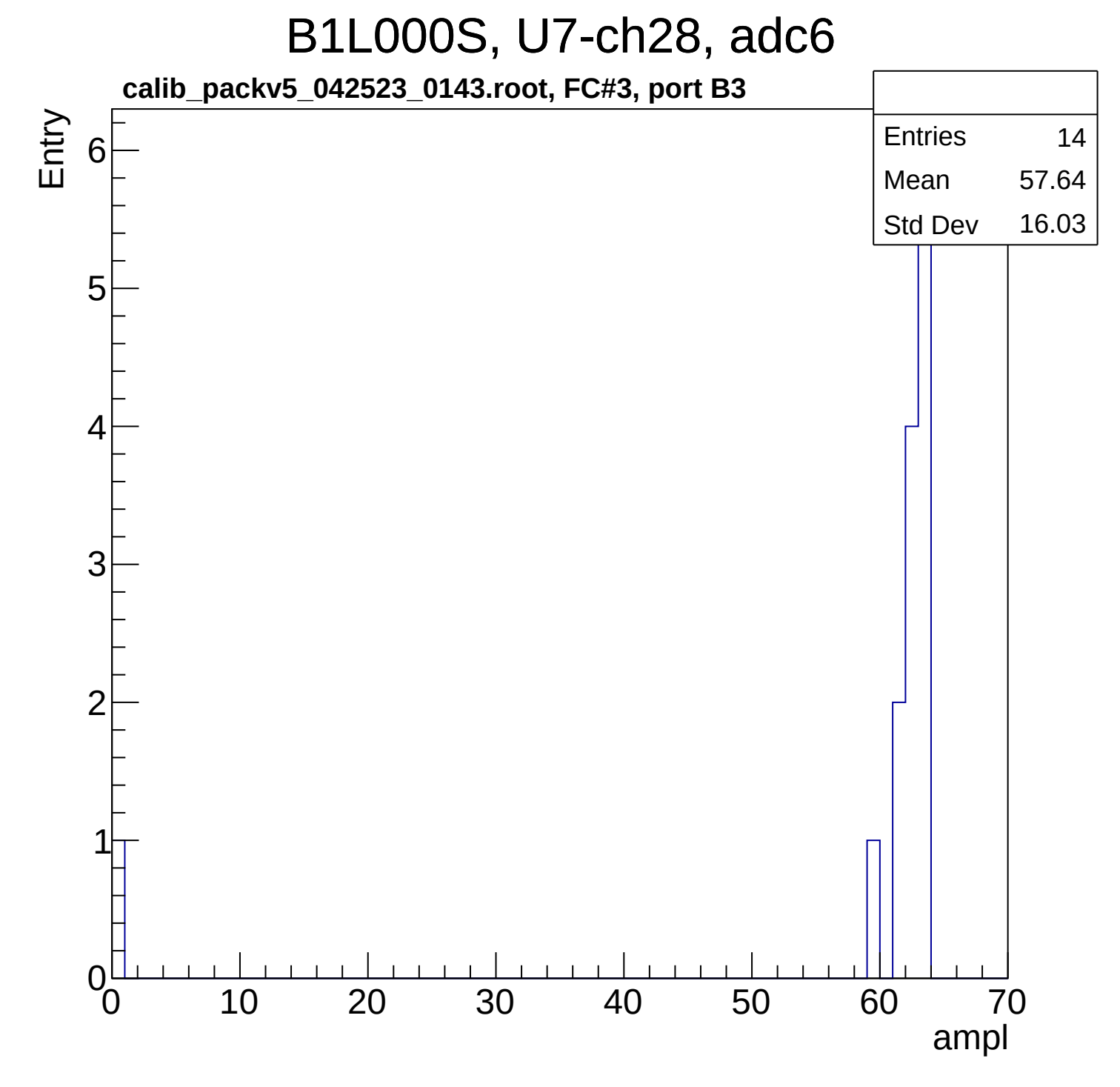
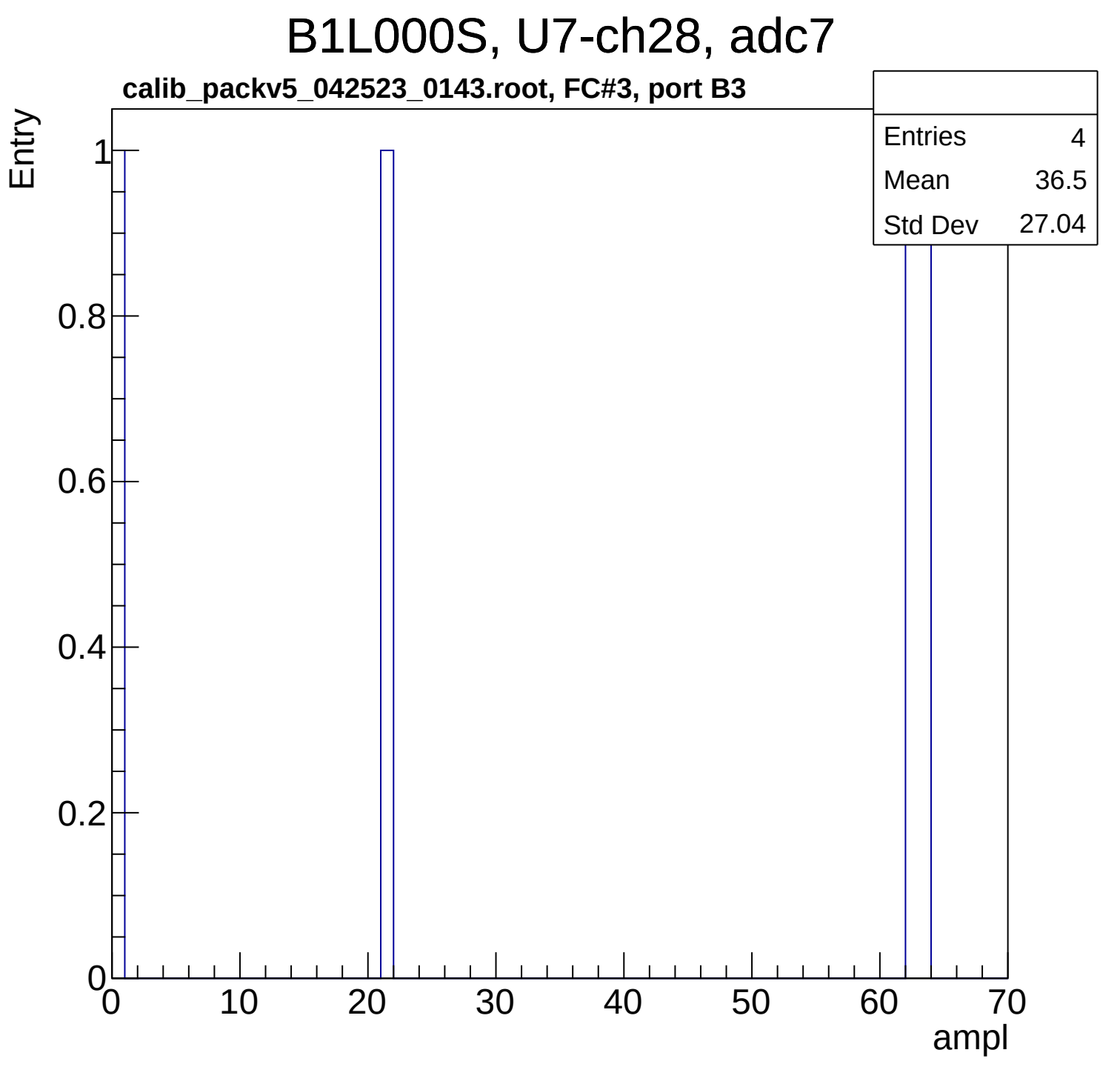

B1L000S, U7-ch28, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.64
Std Dev 16.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch28, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 36.5
Std Dev 27.04
Entry
ampl
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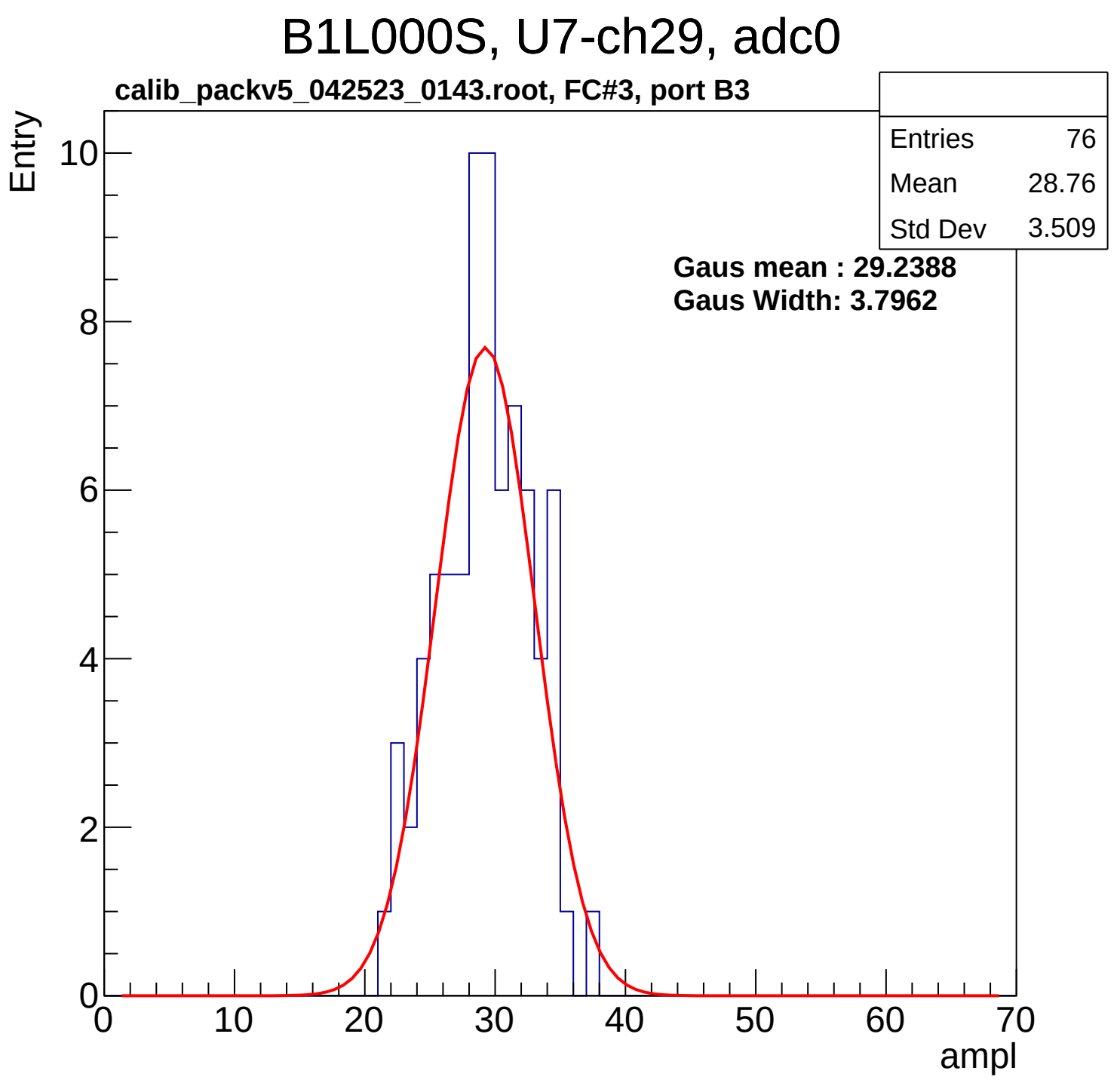
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U7-ch29, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 28.76
Std Dev 3.509
Gaus mean : 29.2388
Gaus Width: 3.7962
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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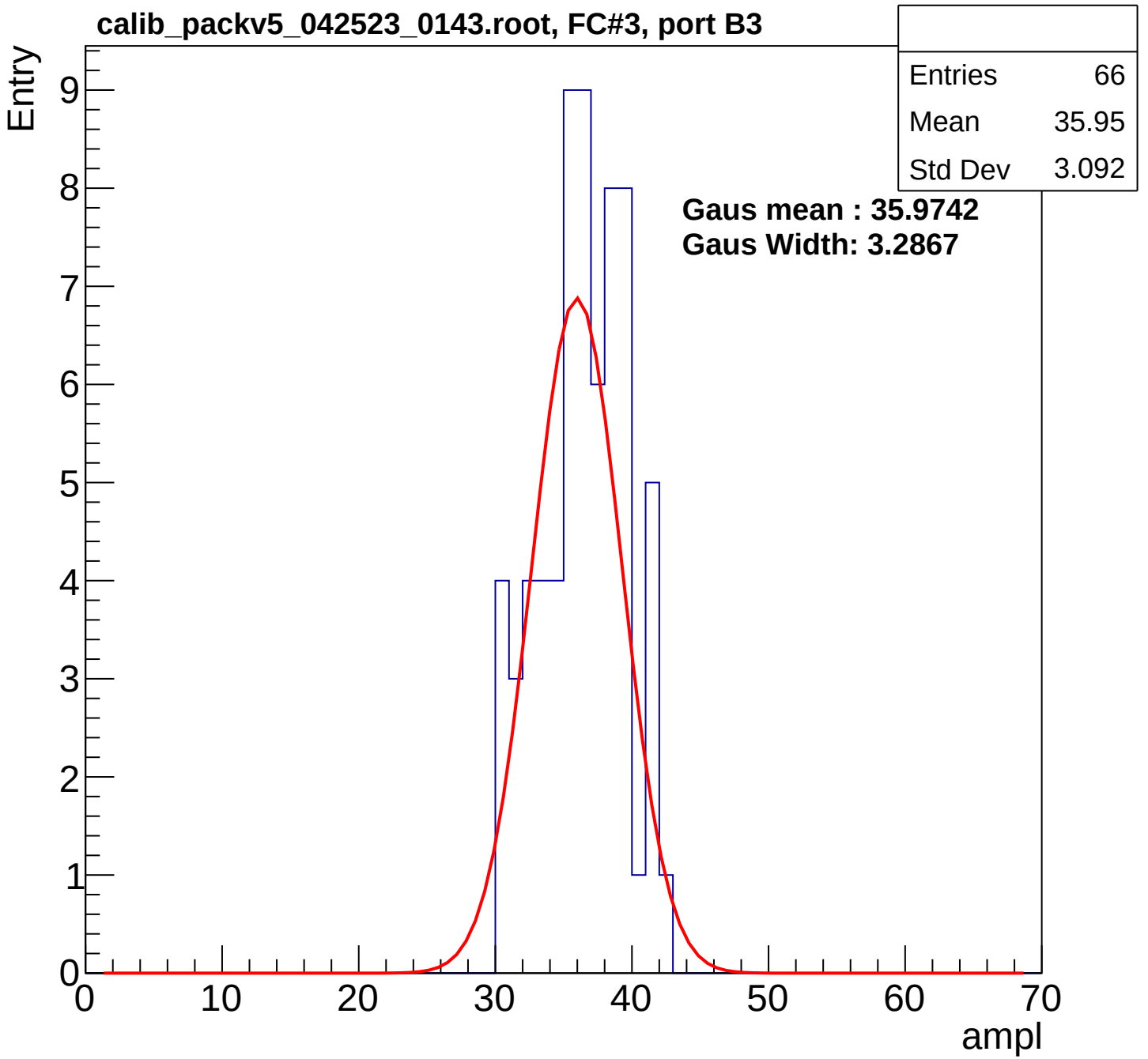
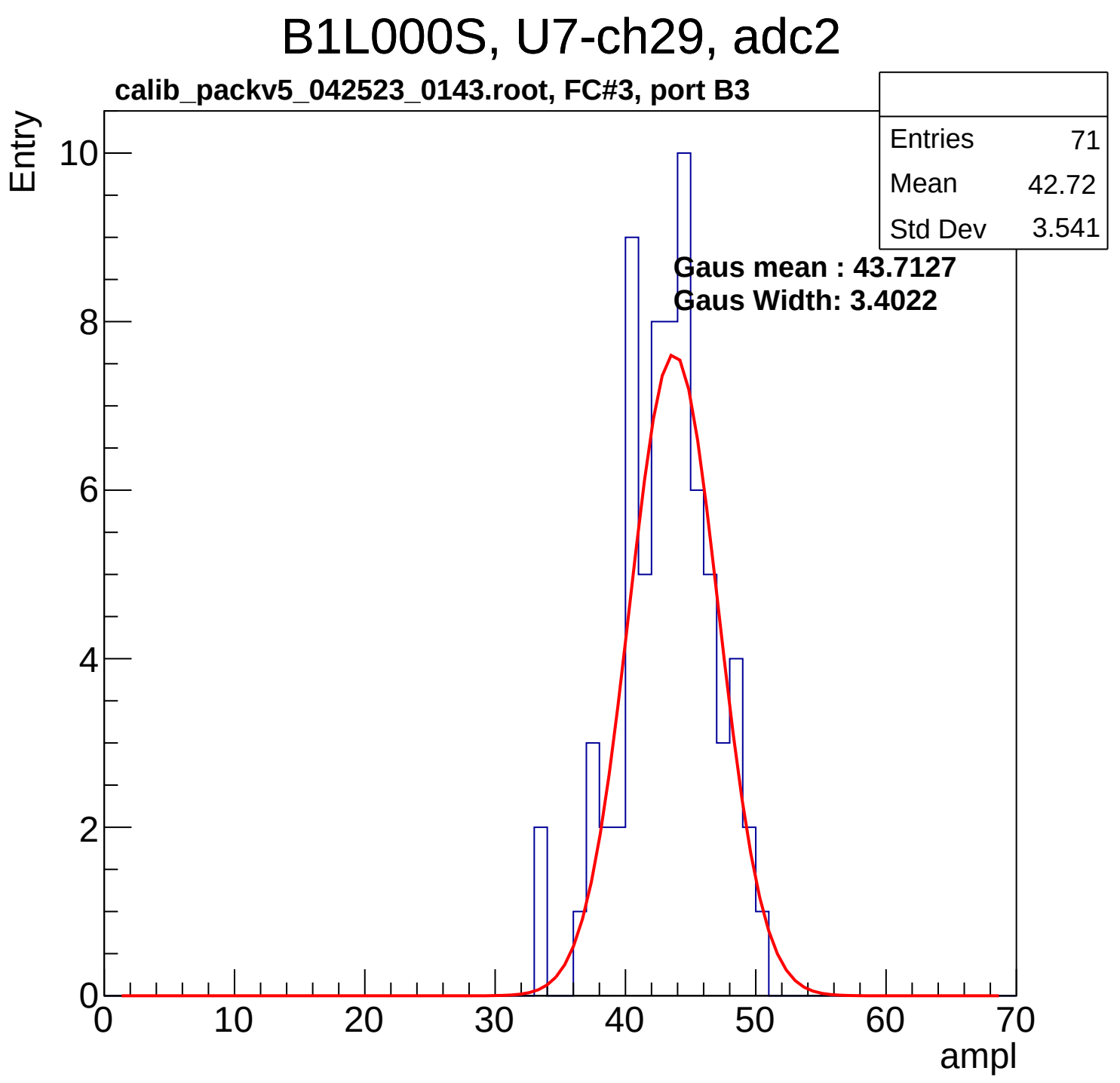
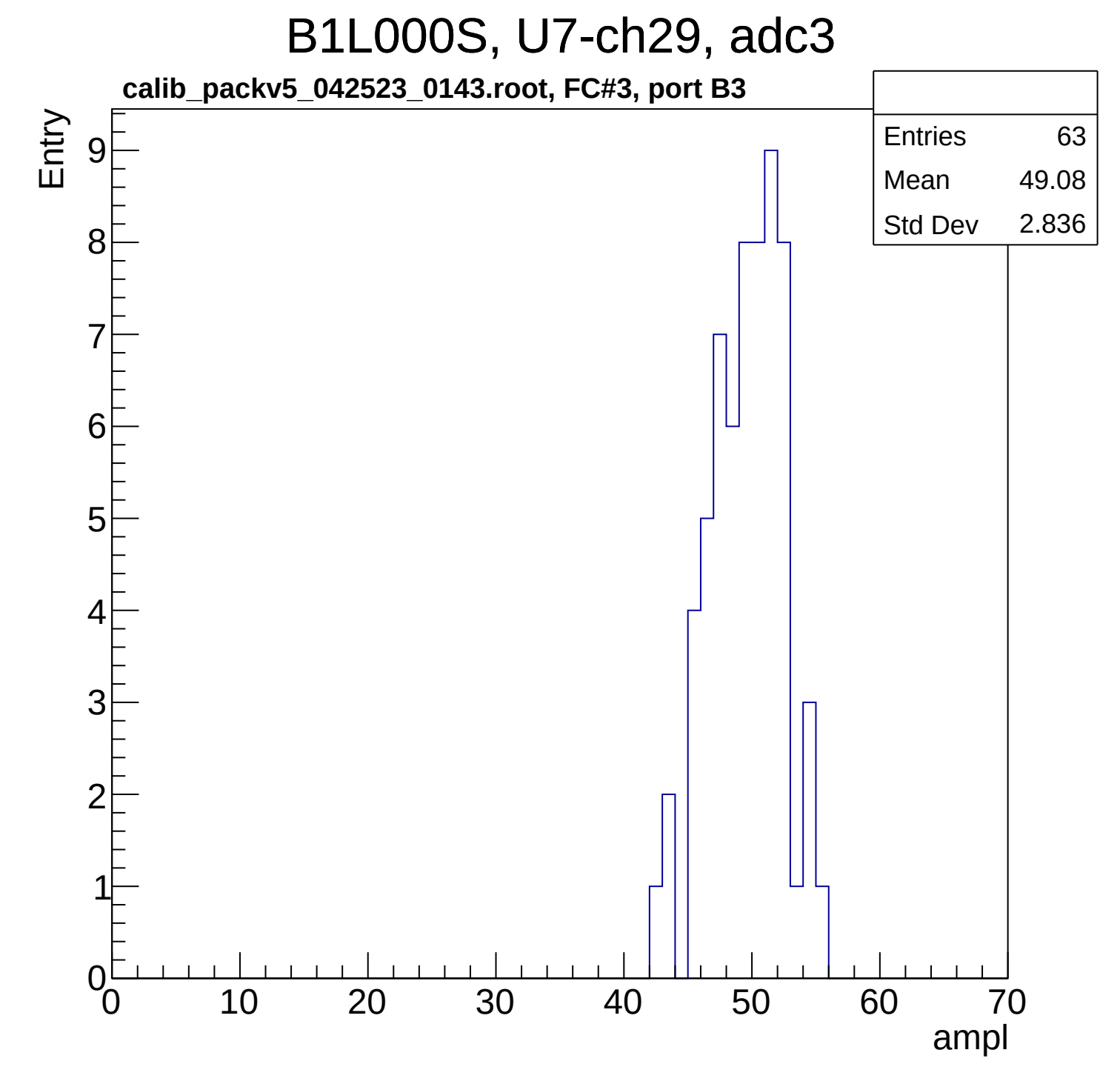

B1L000S, U7-ch29, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 35.95
Std Dev 3.092
Gaus mean : 35.9742
Gaus Width: 3.2867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch29, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 42.72
Std Dev 3.541
Gaus mean : 43.7127
Gaus Width: 3.4022
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch29, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 49.08
Std Dev 2.836
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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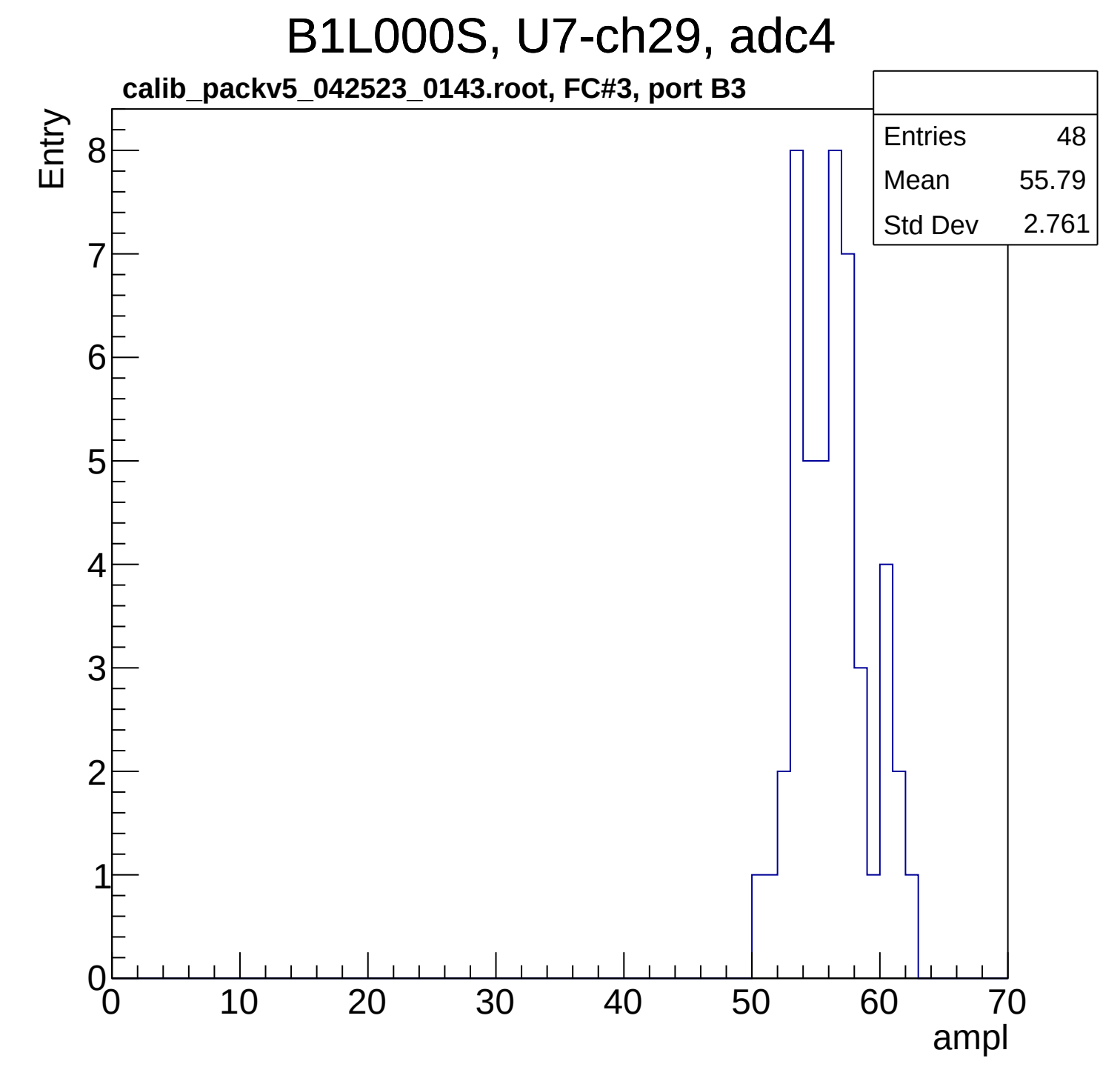
B1L000S, U7-ch29, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 55.79
Std Dev 2.761
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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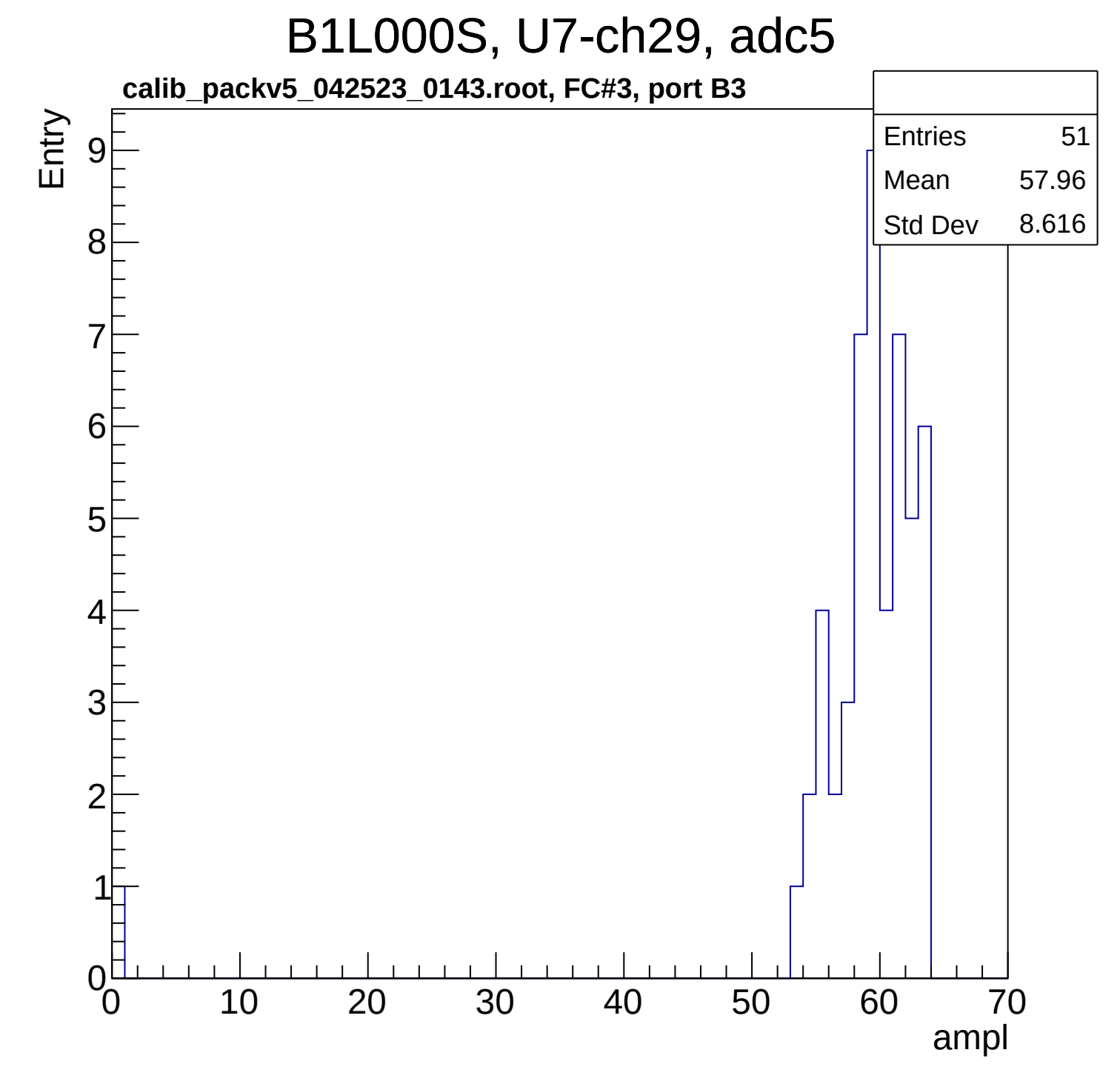
B1L000S, U7-ch29, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 57.96
Std Dev 8.616
Entry
ampl
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3
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6
7
8
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0
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60
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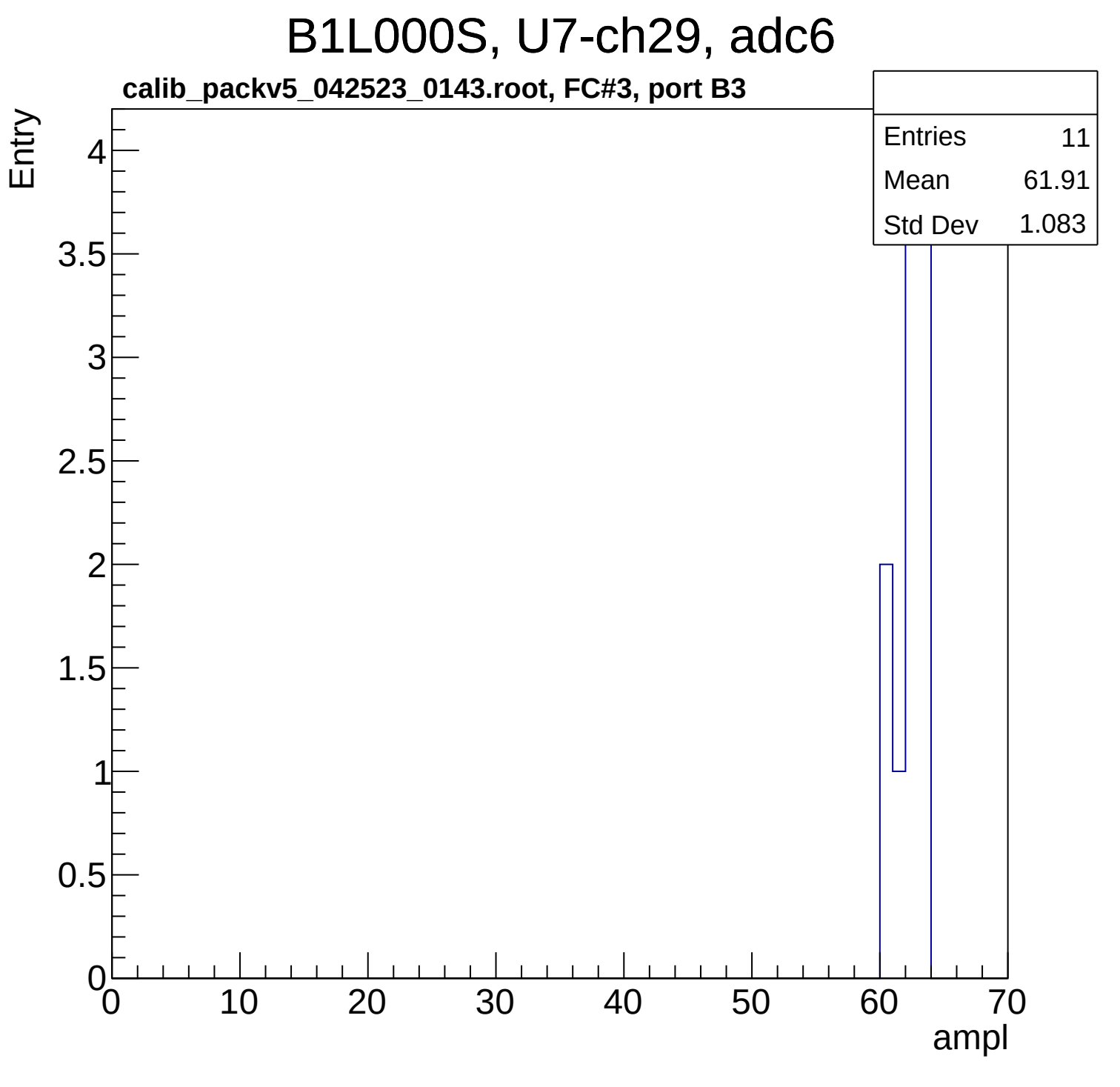
B1L000S, U7-ch29, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61.91
Std Dev 1.083
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
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40
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60
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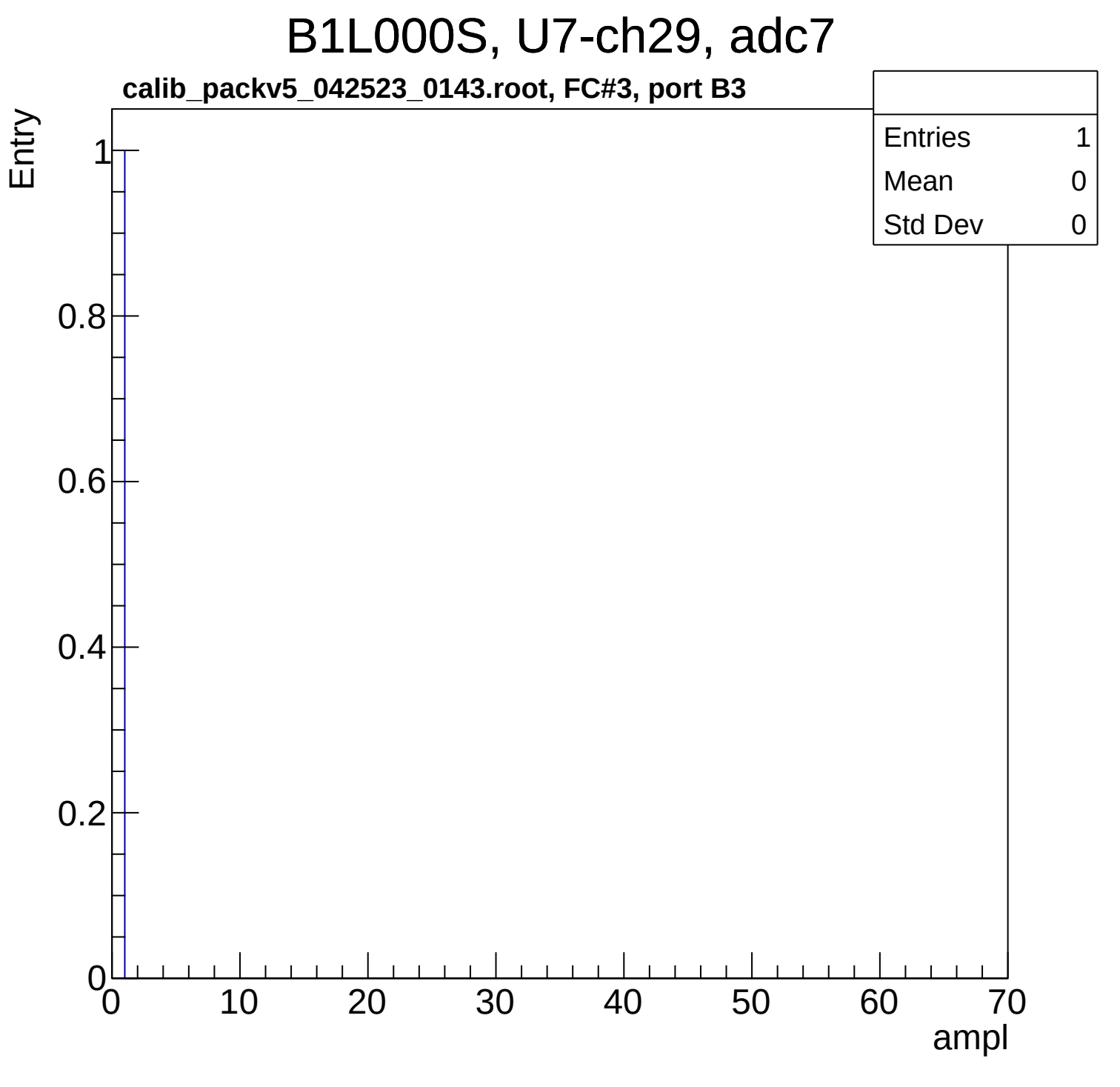
B1L000S, U7-ch29, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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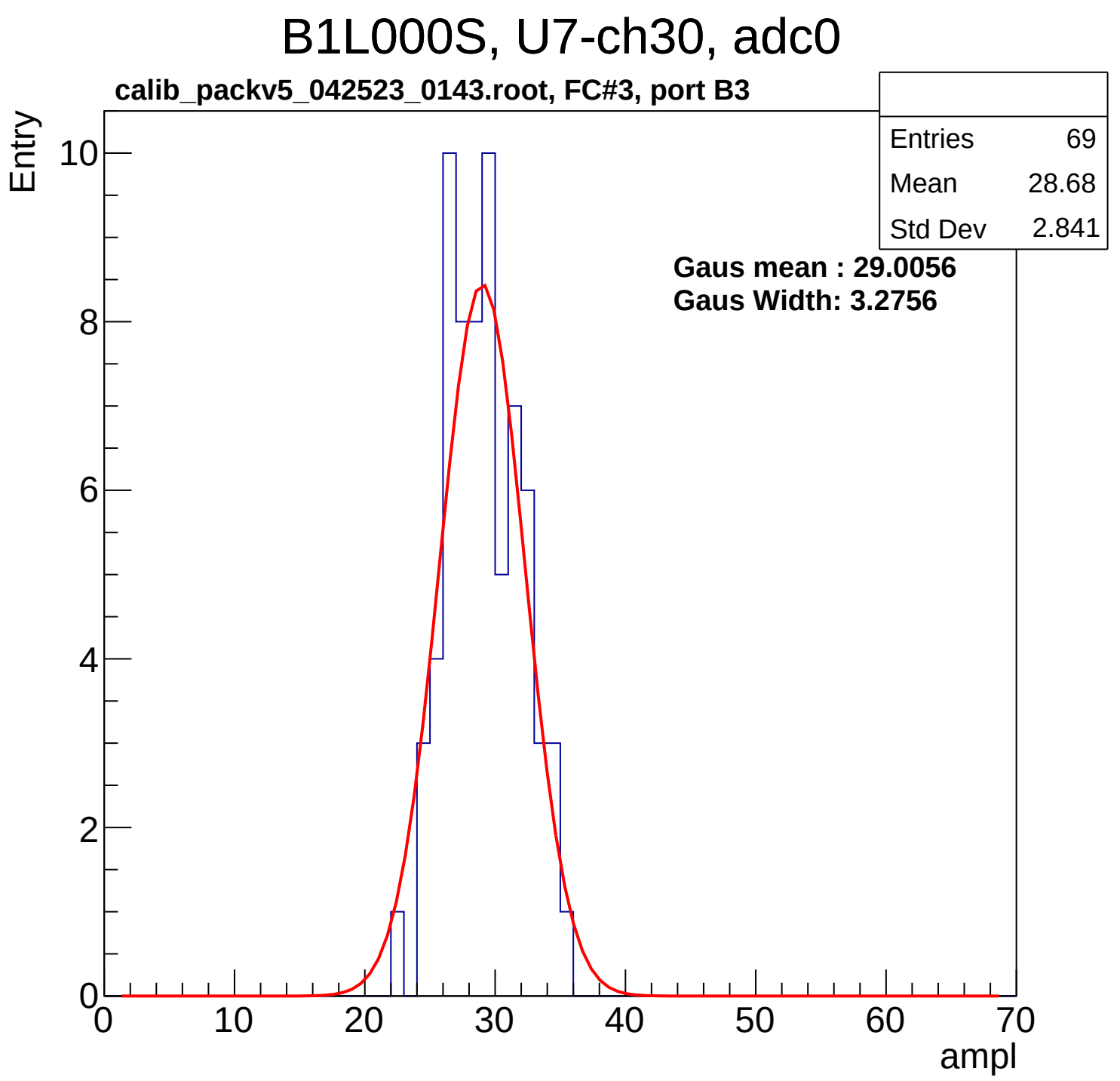
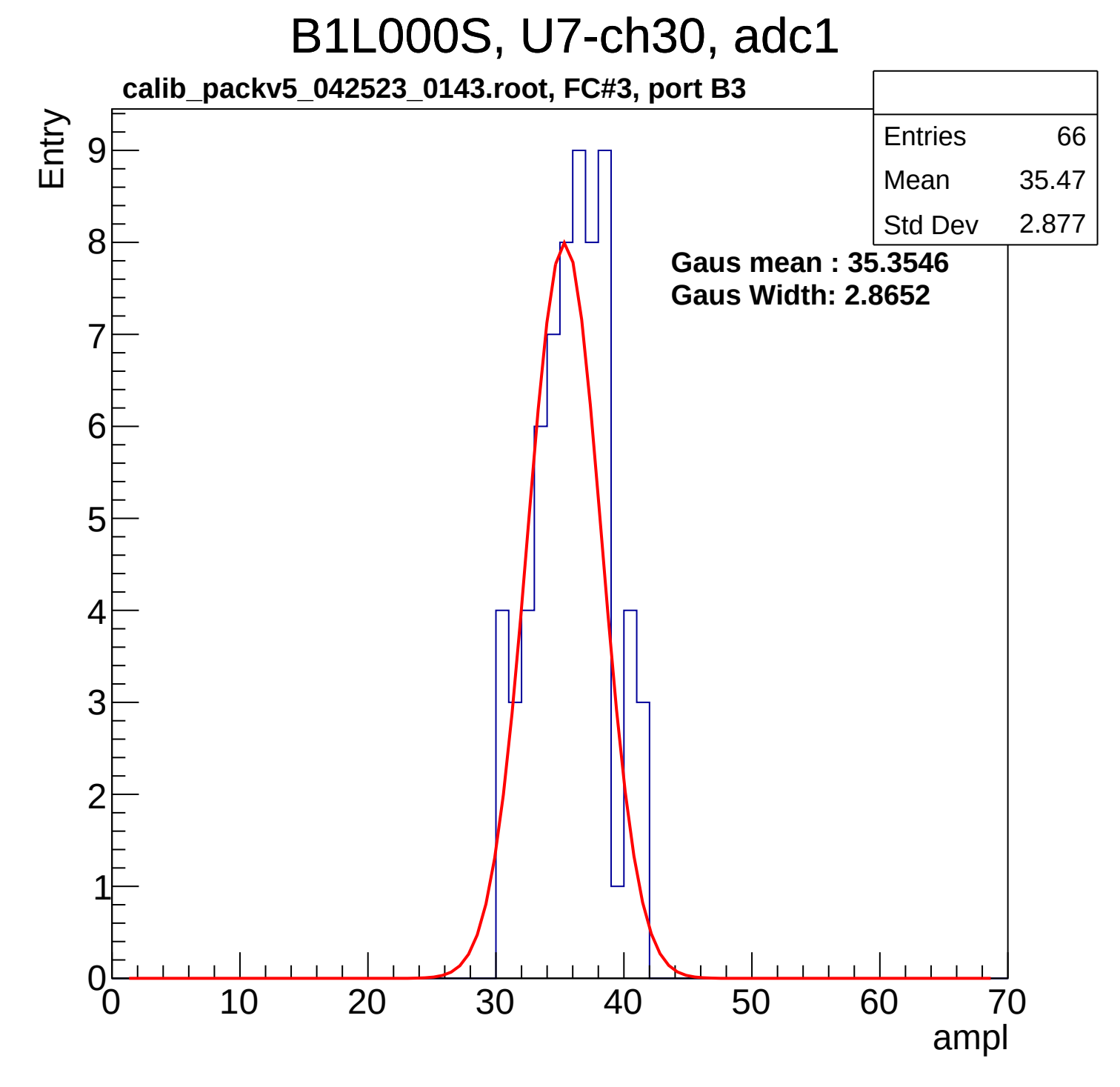
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch30, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 28.68
Std Dev 2.841
Gaus mean : 29.0056
Gaus Width: 3.2756
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch30, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 35.47
Std Dev 2.877
Gaus mean : 35.3546
Gaus Width: 2.8652
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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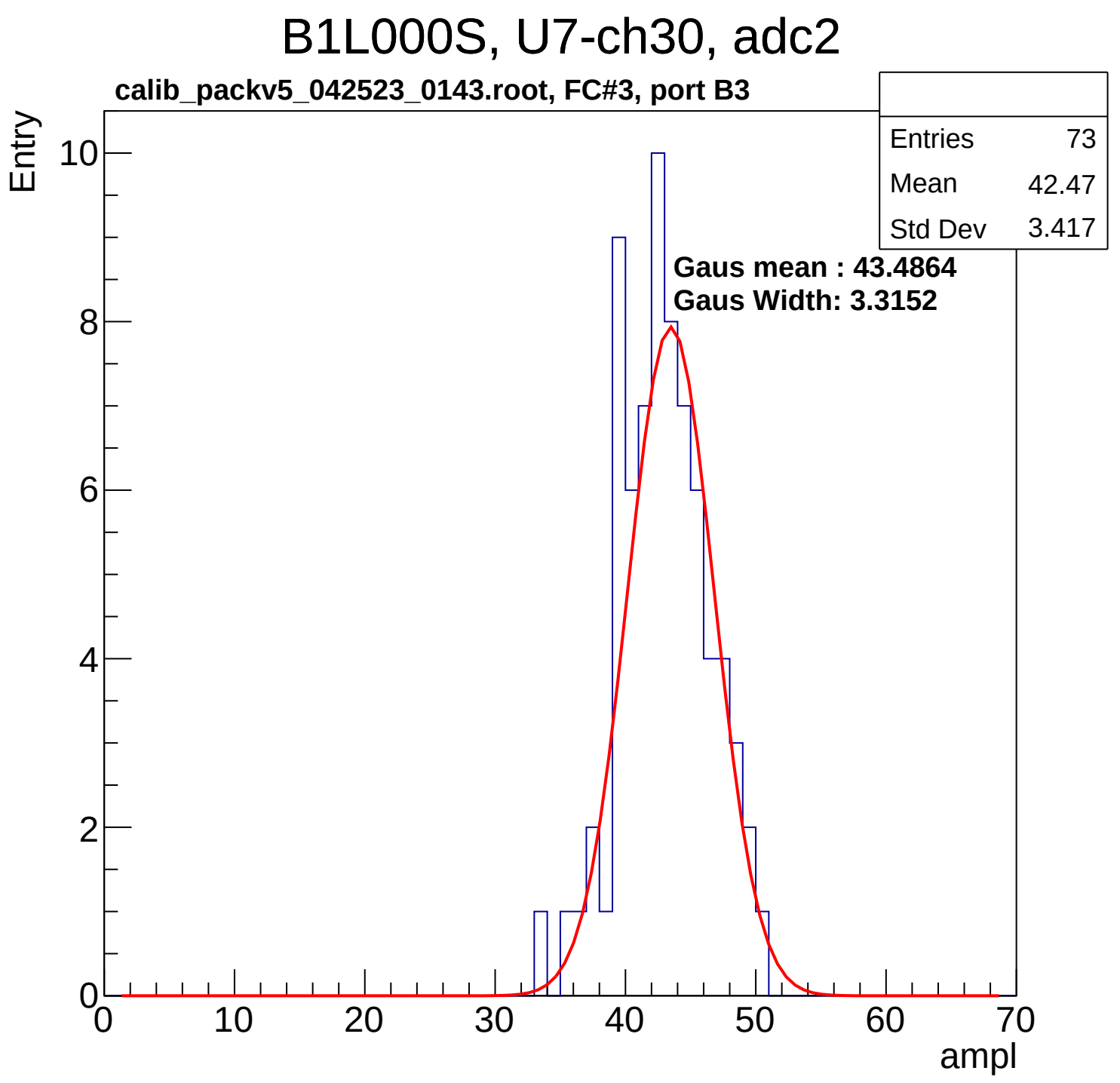
B1L000S, U7-ch30, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 42.47
Std Dev 3.417
Gaus mean : 43.4864
Gaus Width: 3.3152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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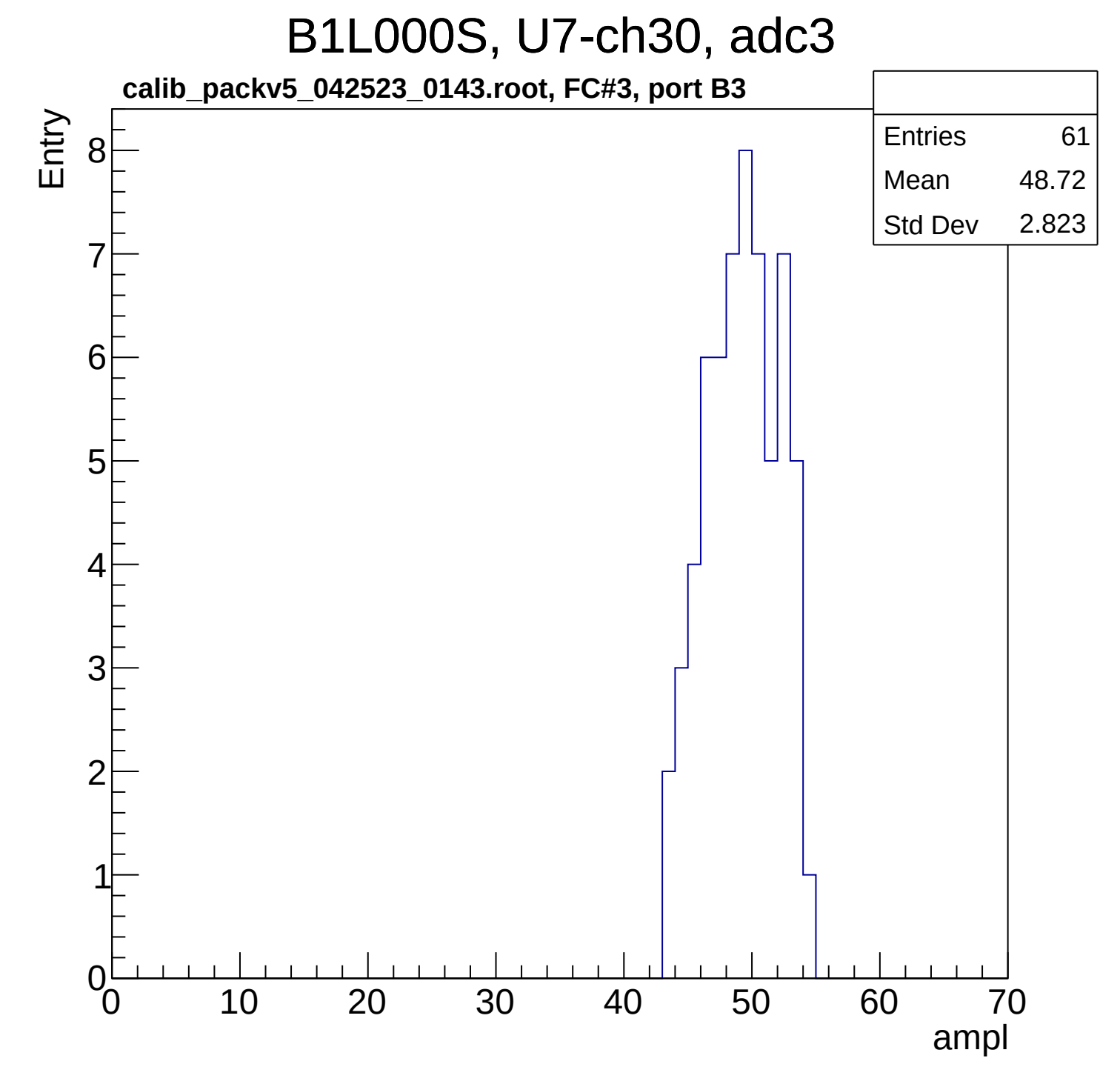
B1L000S, U7-ch30, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 48.72
Std Dev 2.823
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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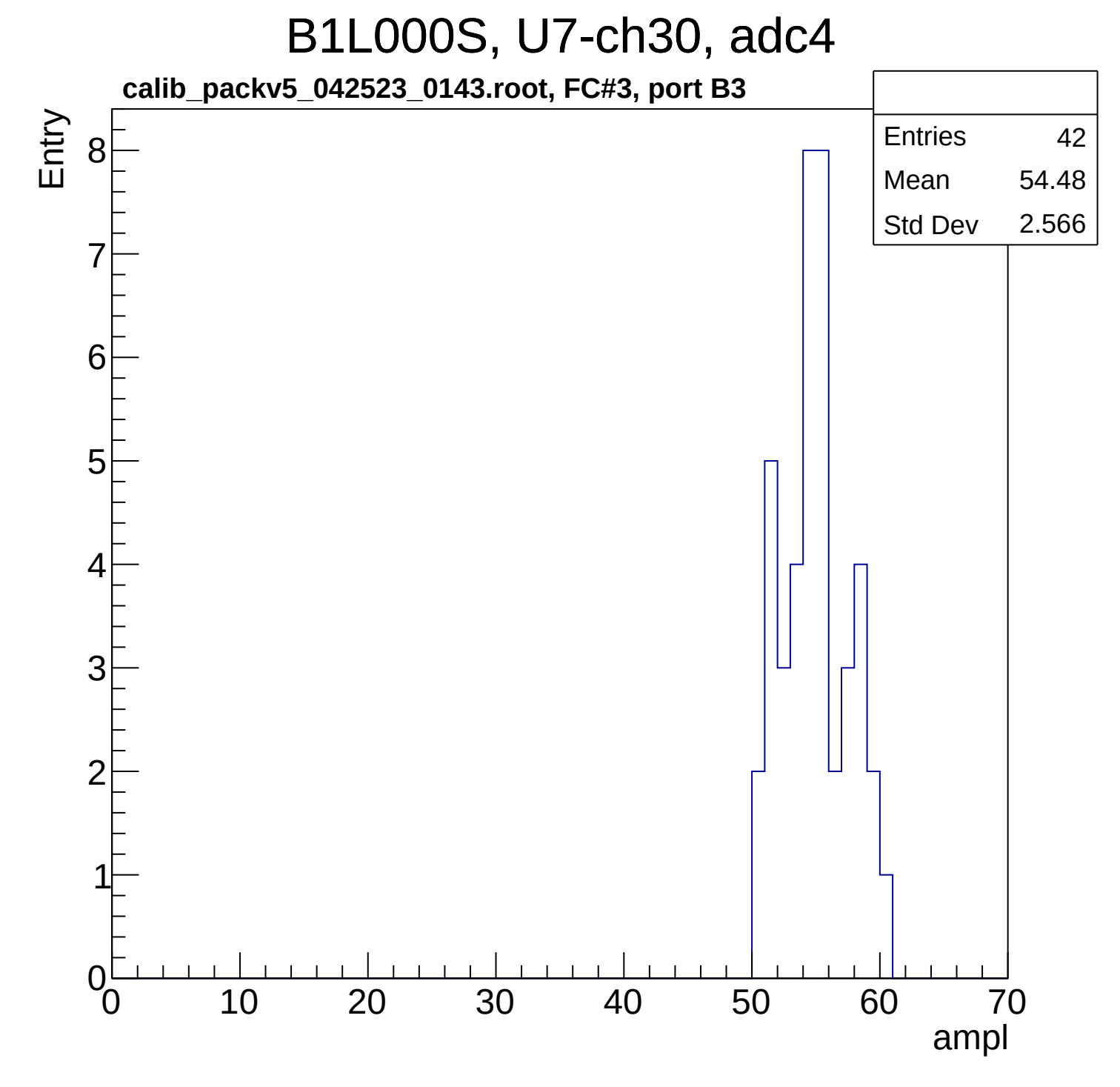
B1L000S, U7-ch30, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 54.48
Std Dev 2.566
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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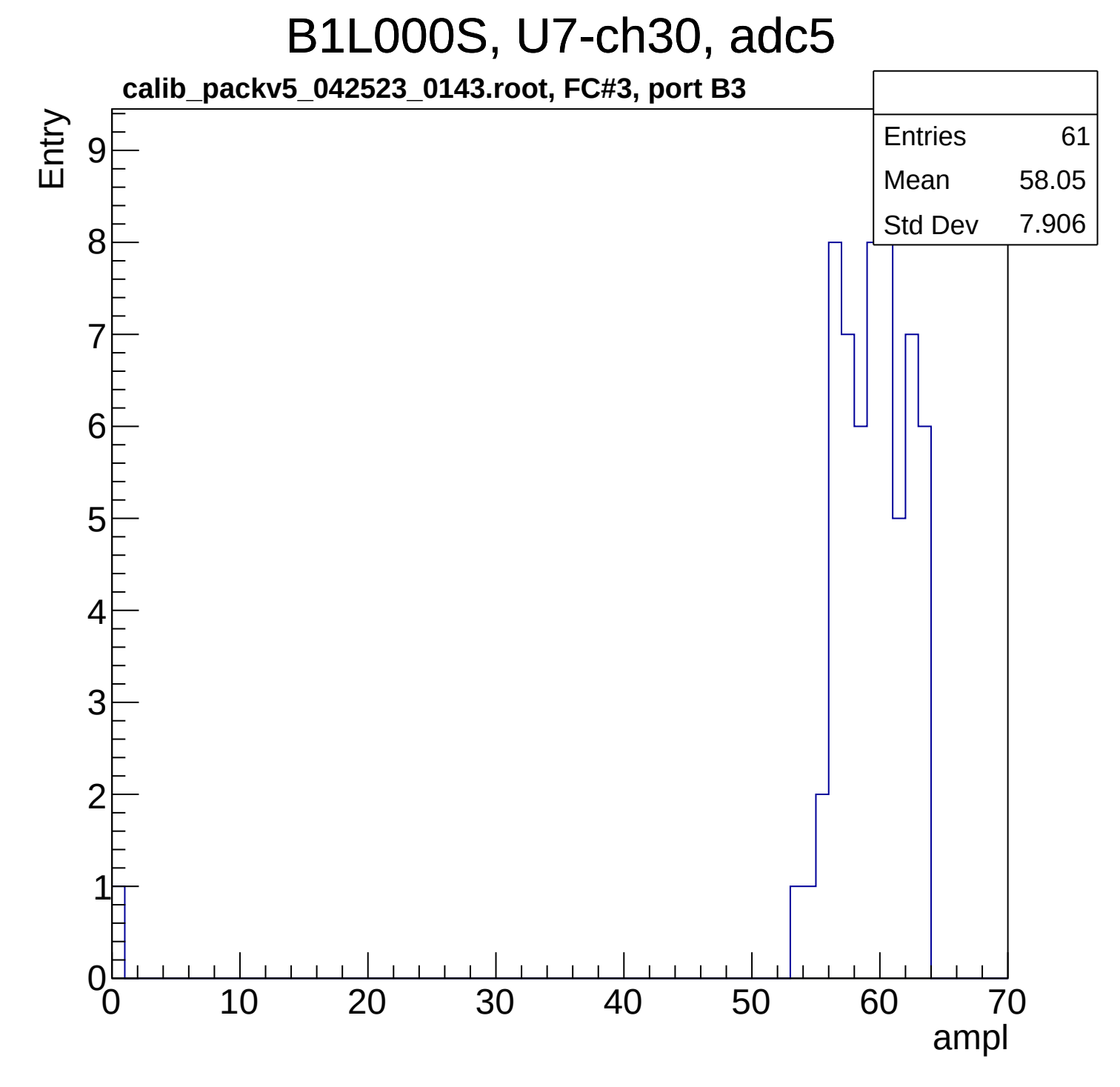
B1L000S, U7-ch30, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.05
Std Dev 7.906
Entry
ampl
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2
3
4
5
6
7
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0
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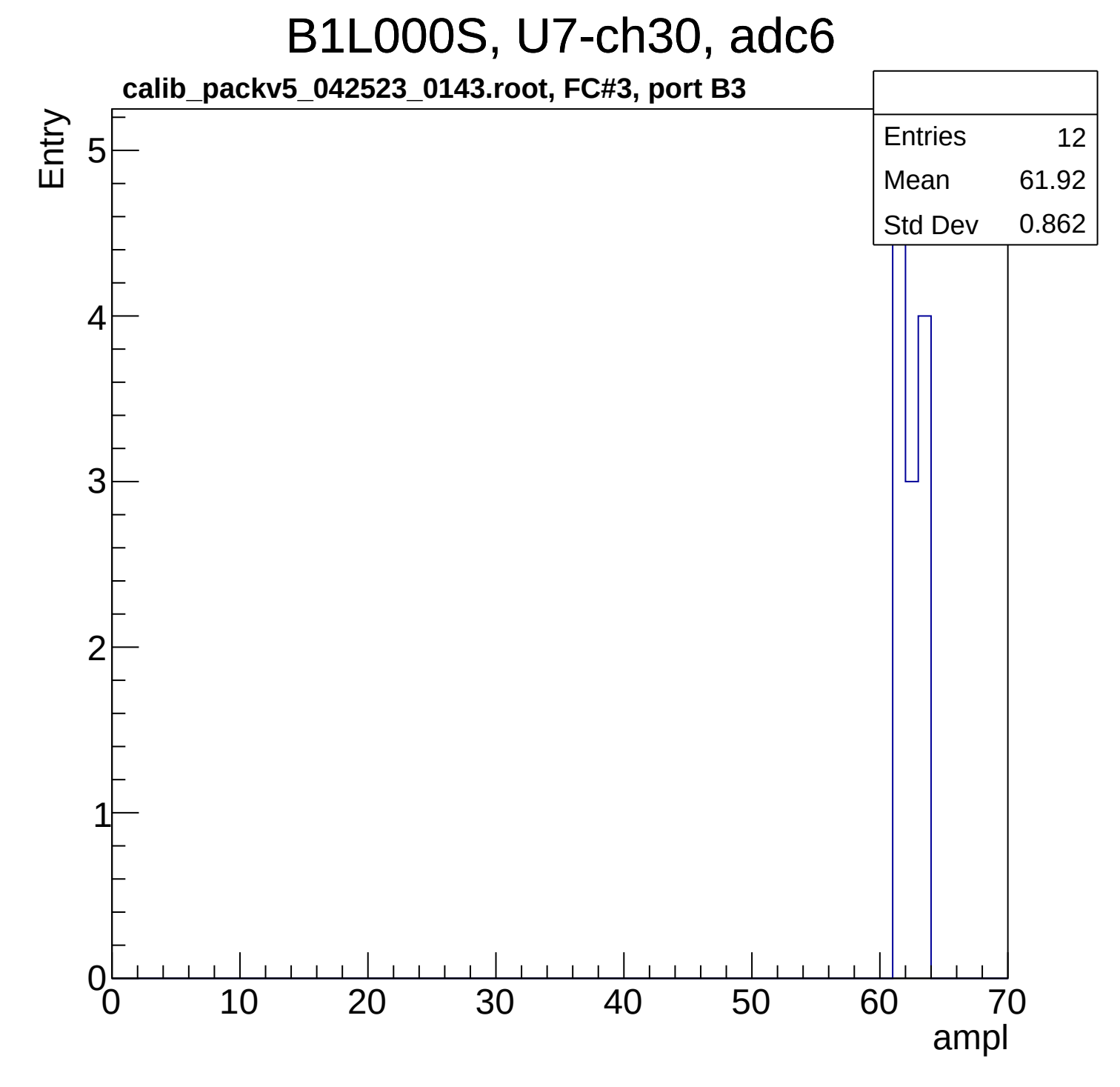
B1L000S, U7-ch30, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 61.92
Std Dev 0.862
Entry
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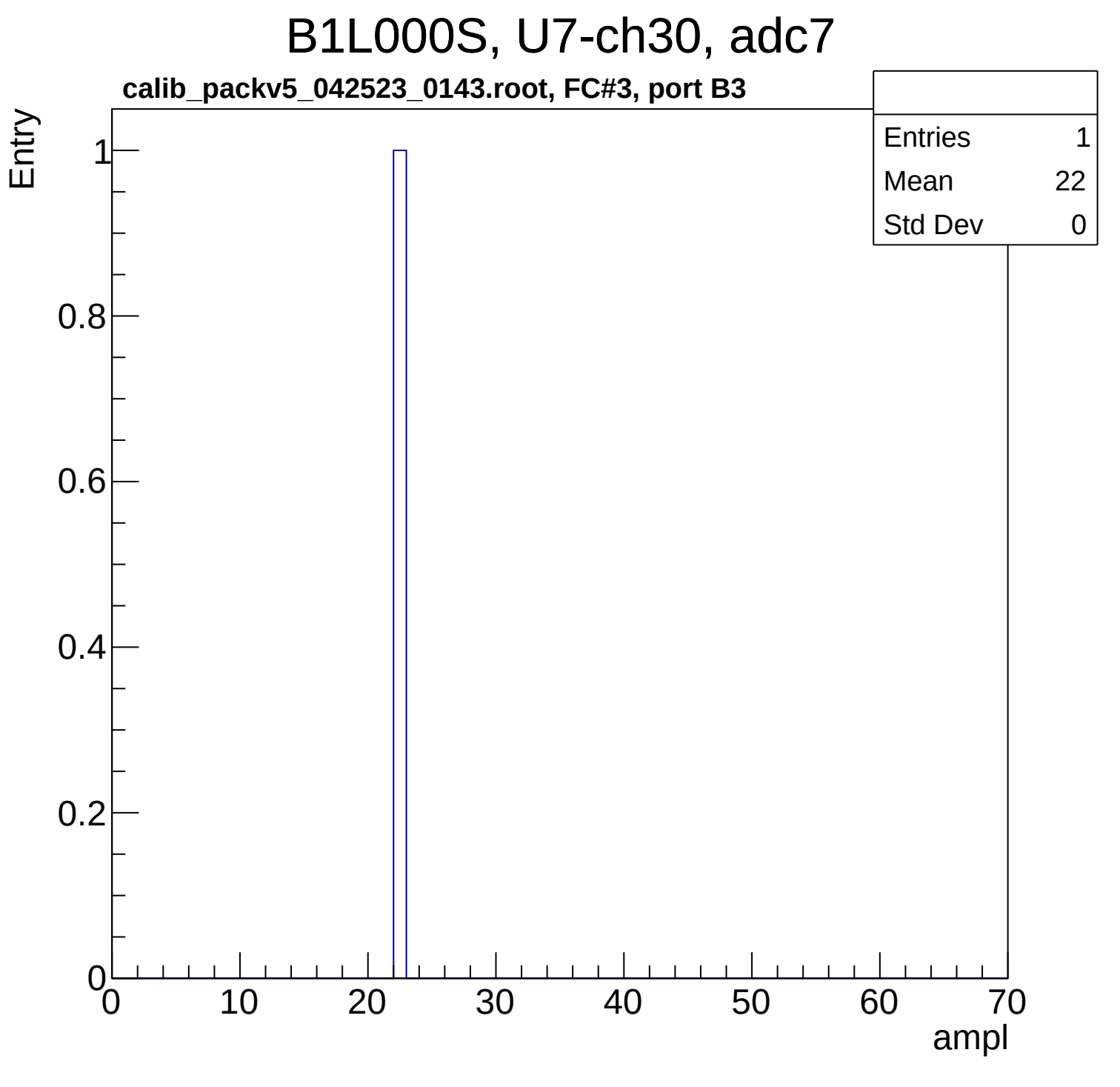
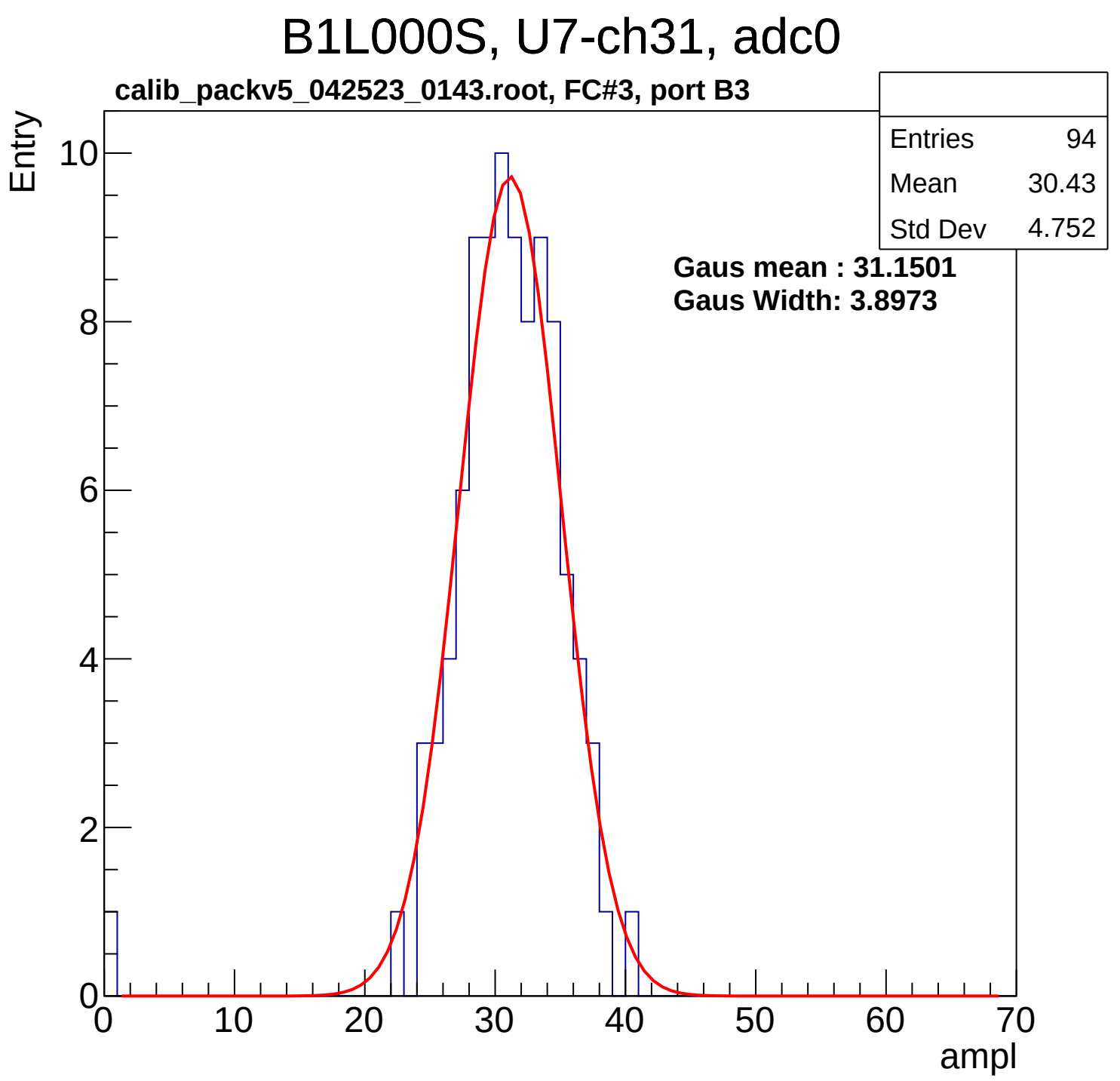
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch30, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch31, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 94
Mean 30.43
Std Dev 4.752
Gaus mean : 31.1501
Gaus Width: 3.8973
Entry
ampl
0
2
4
6
8
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0
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40
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60
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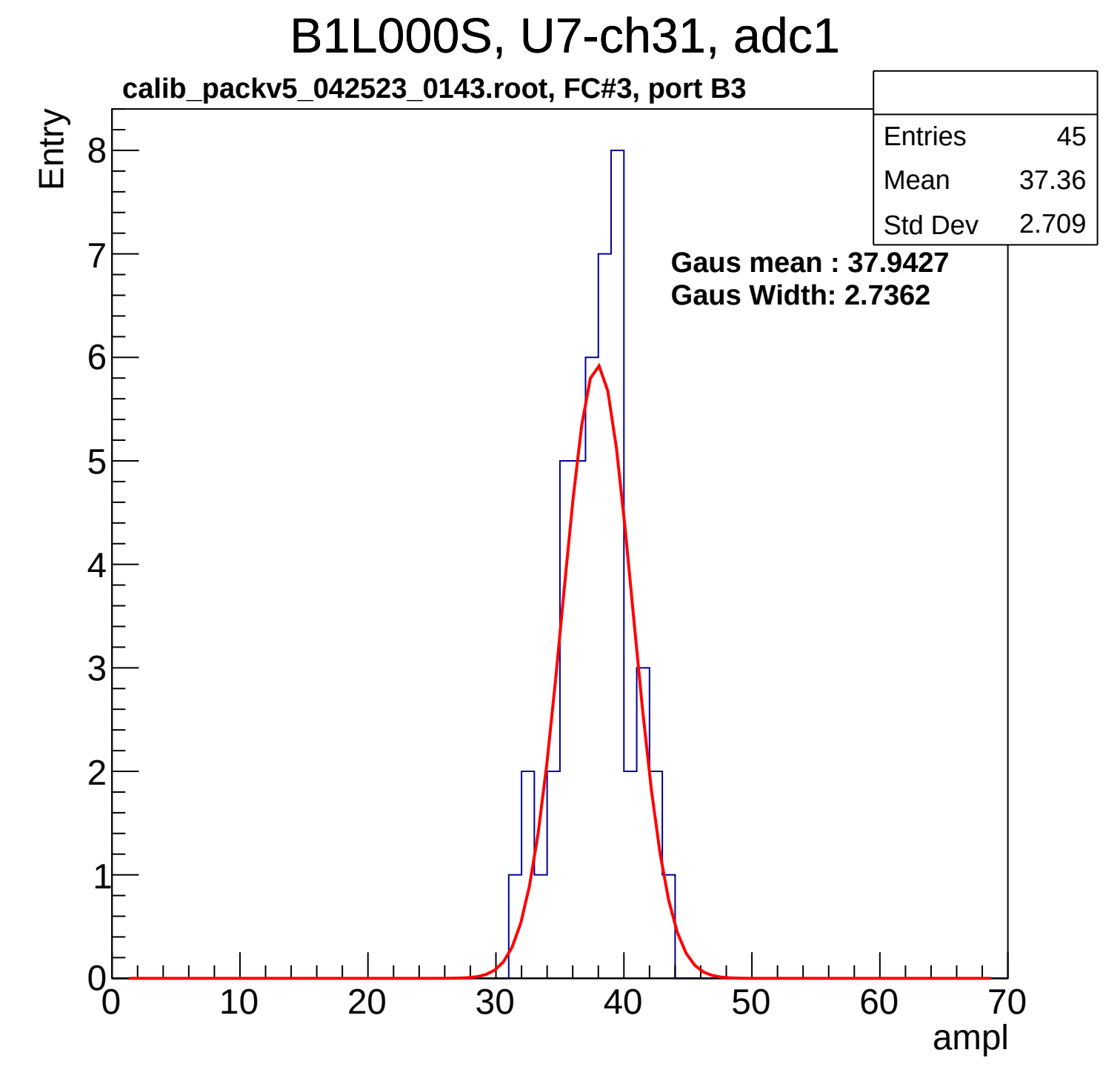
B1L000S, U7-ch31, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 37.36
Std Dev 2.709
Gaus mean : 37.9427
Gaus Width: 2.7362
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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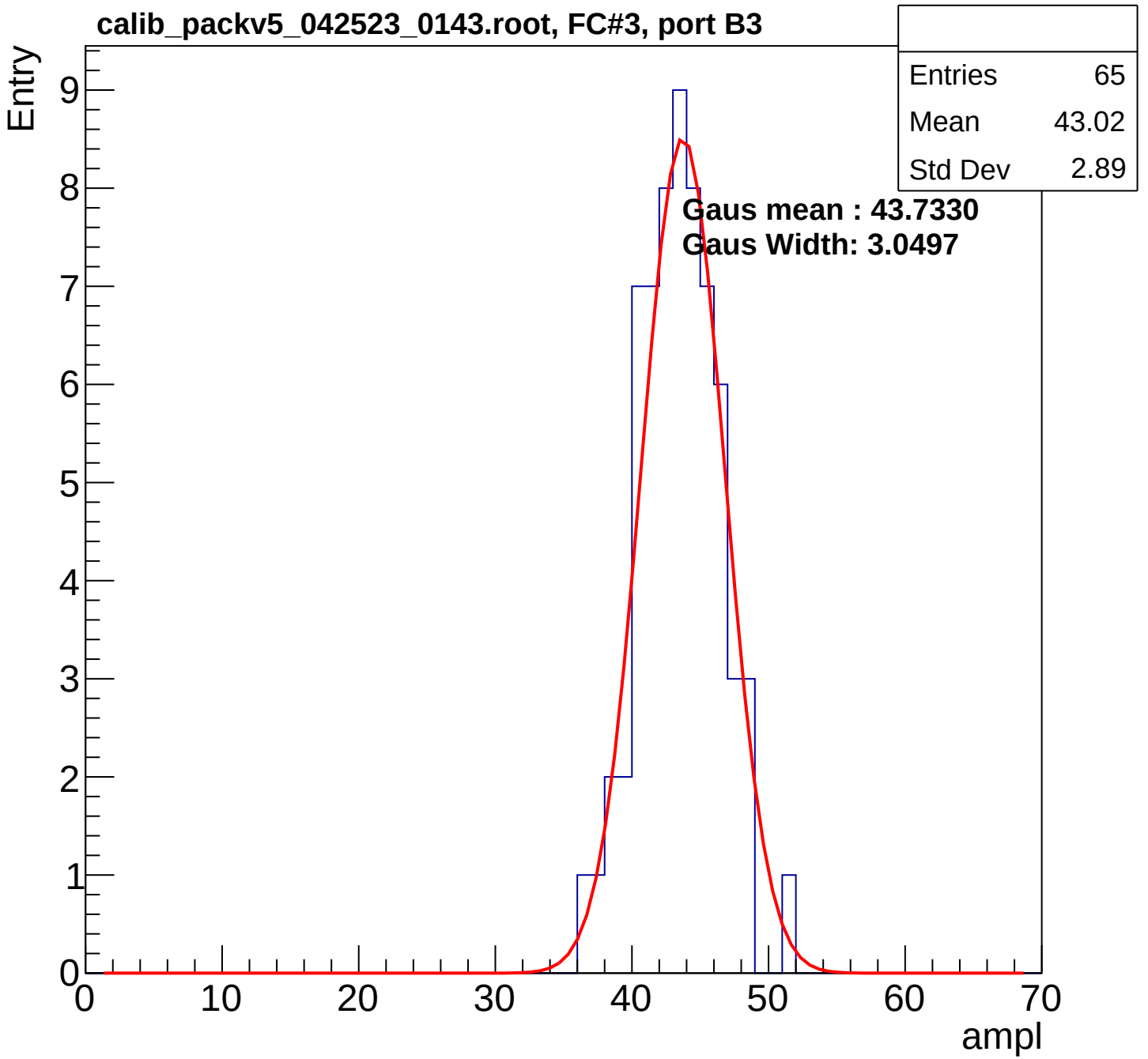
B1L000S, U7-ch31, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 43.02
Std Dev 2.89
Gaus mean : 43.7330
Gaus Width: 3.0497
Entry
ampl
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3
4
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6
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0
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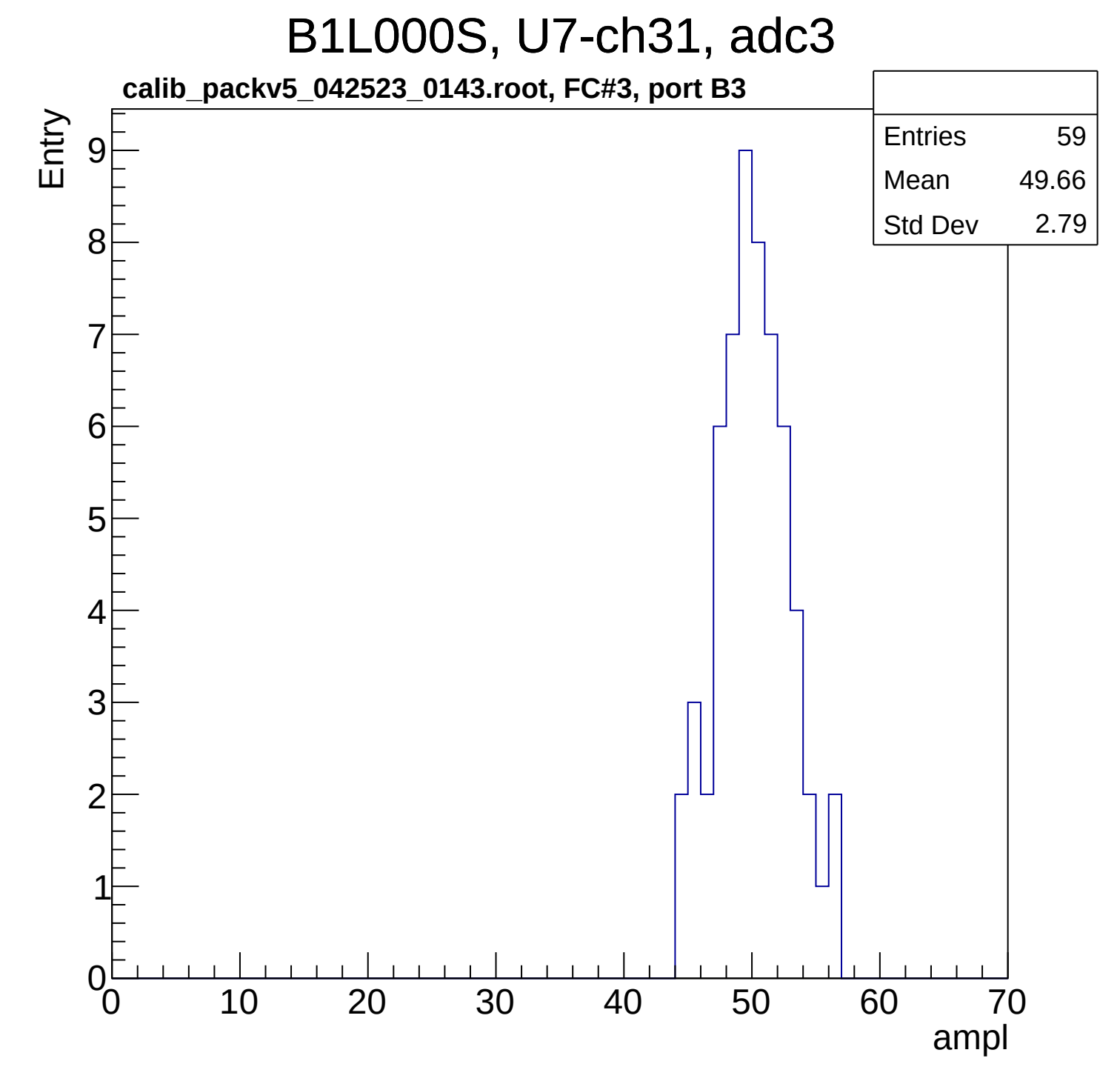
B1L000S, U7-ch31, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 49.66
Std Dev 2.79
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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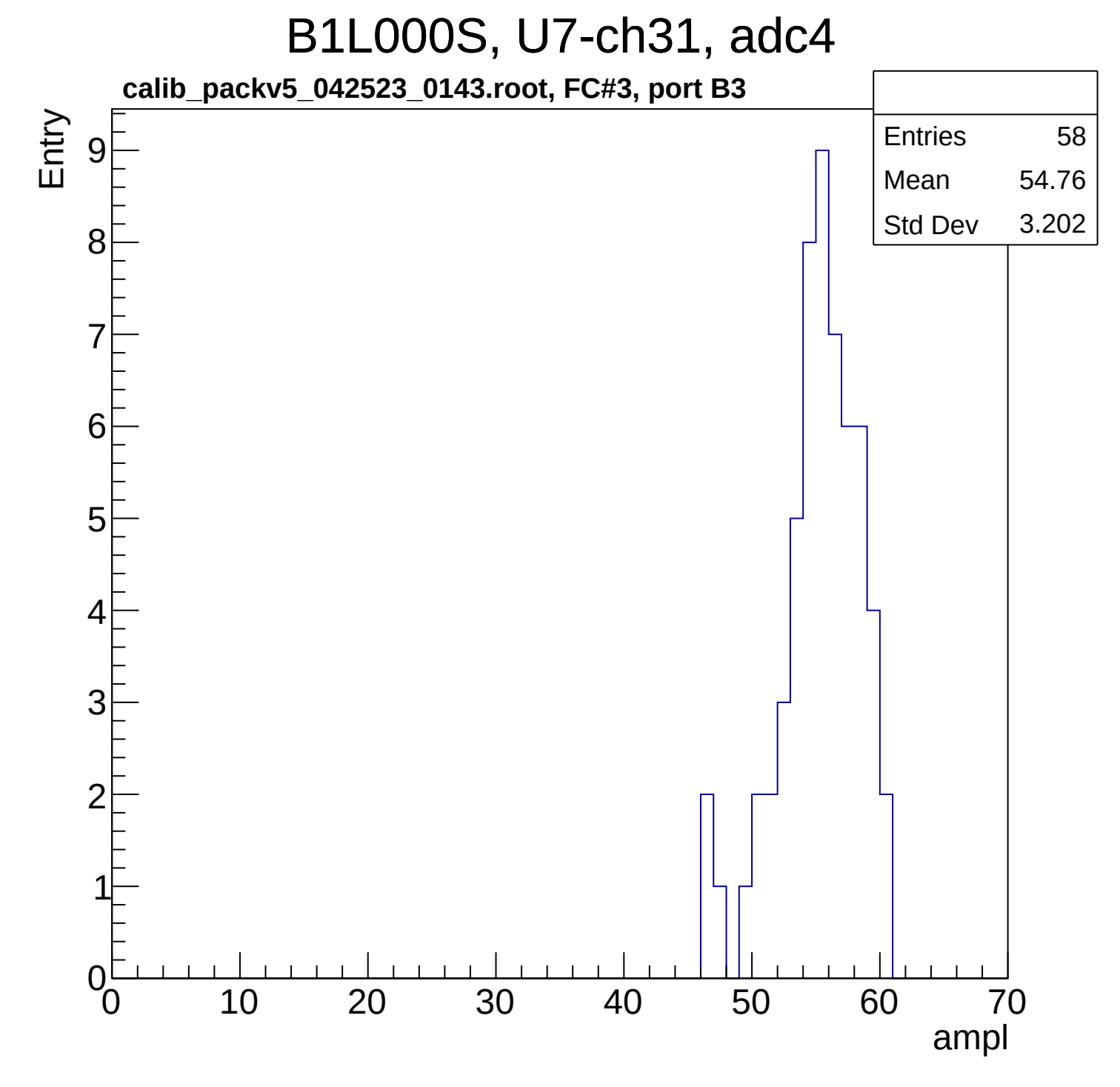
B1L000S, U7-ch31, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.76
Std Dev 3.202
Entry
ampl
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2
3
4
5
6
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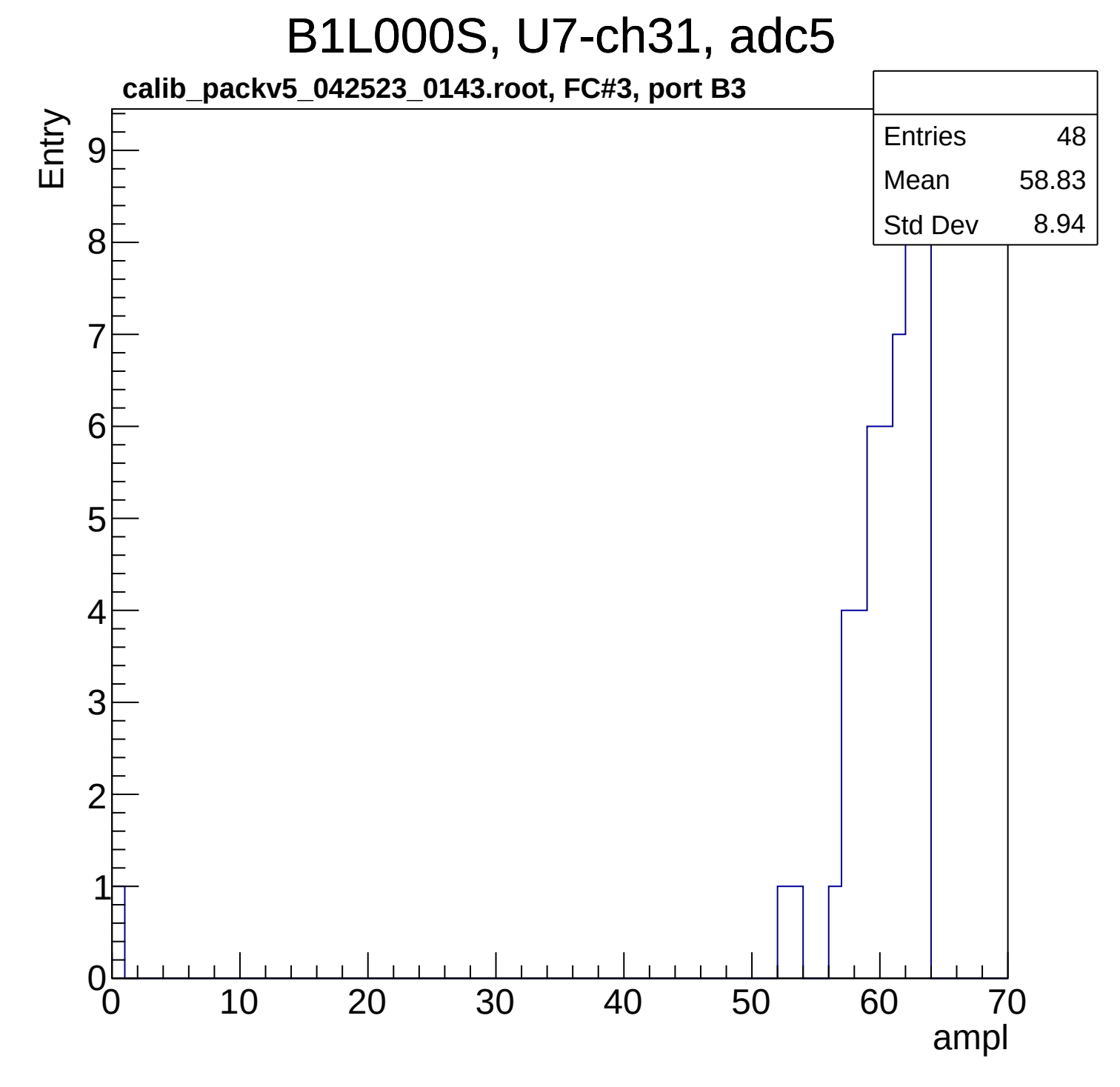
B1L000S, U7-ch31, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.83
Std Dev 8.94
Entry
ampl
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2
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6
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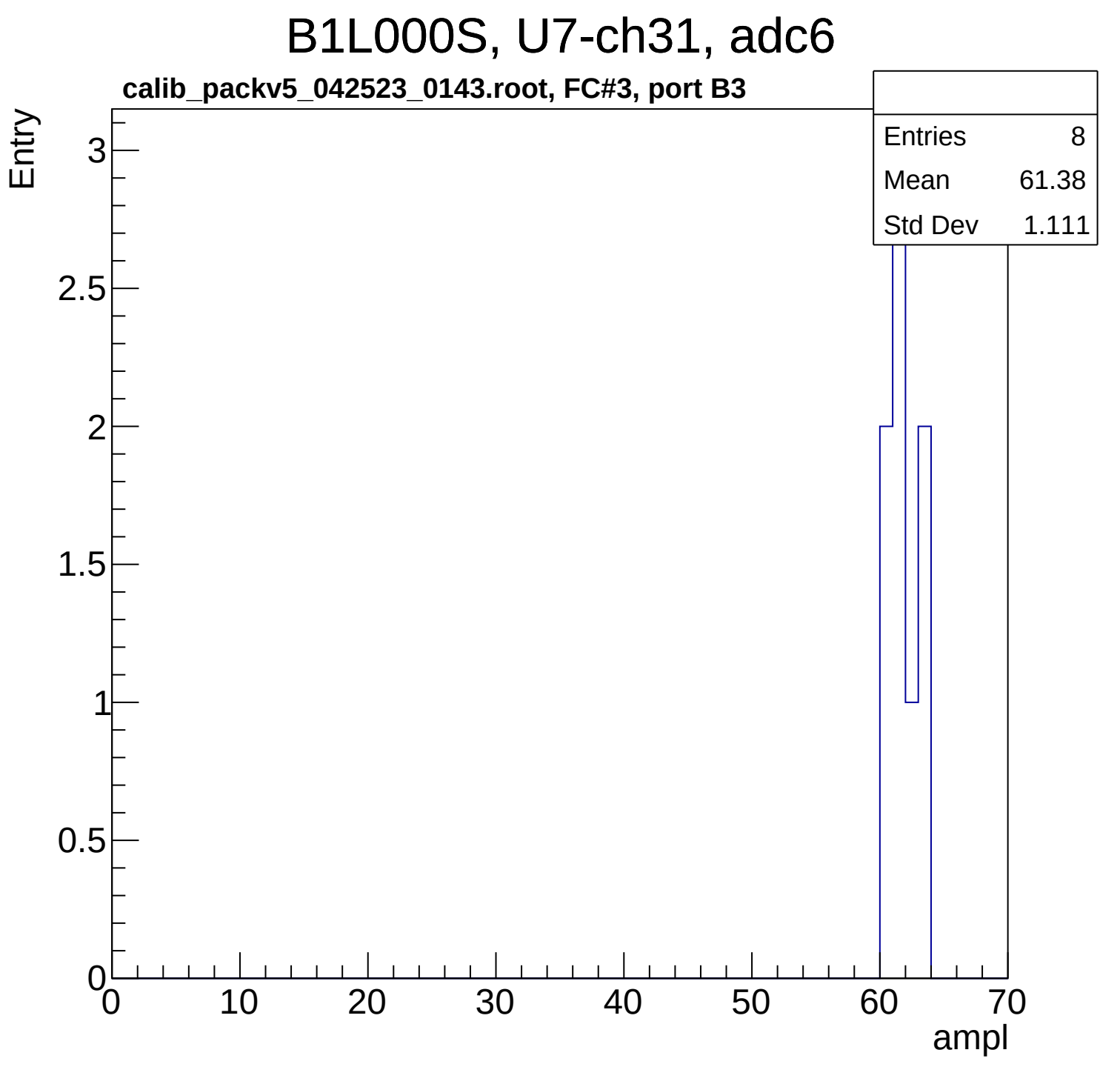
B1L000S, U7-ch31, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.38
Std Dev 1.111
Entry
ampl
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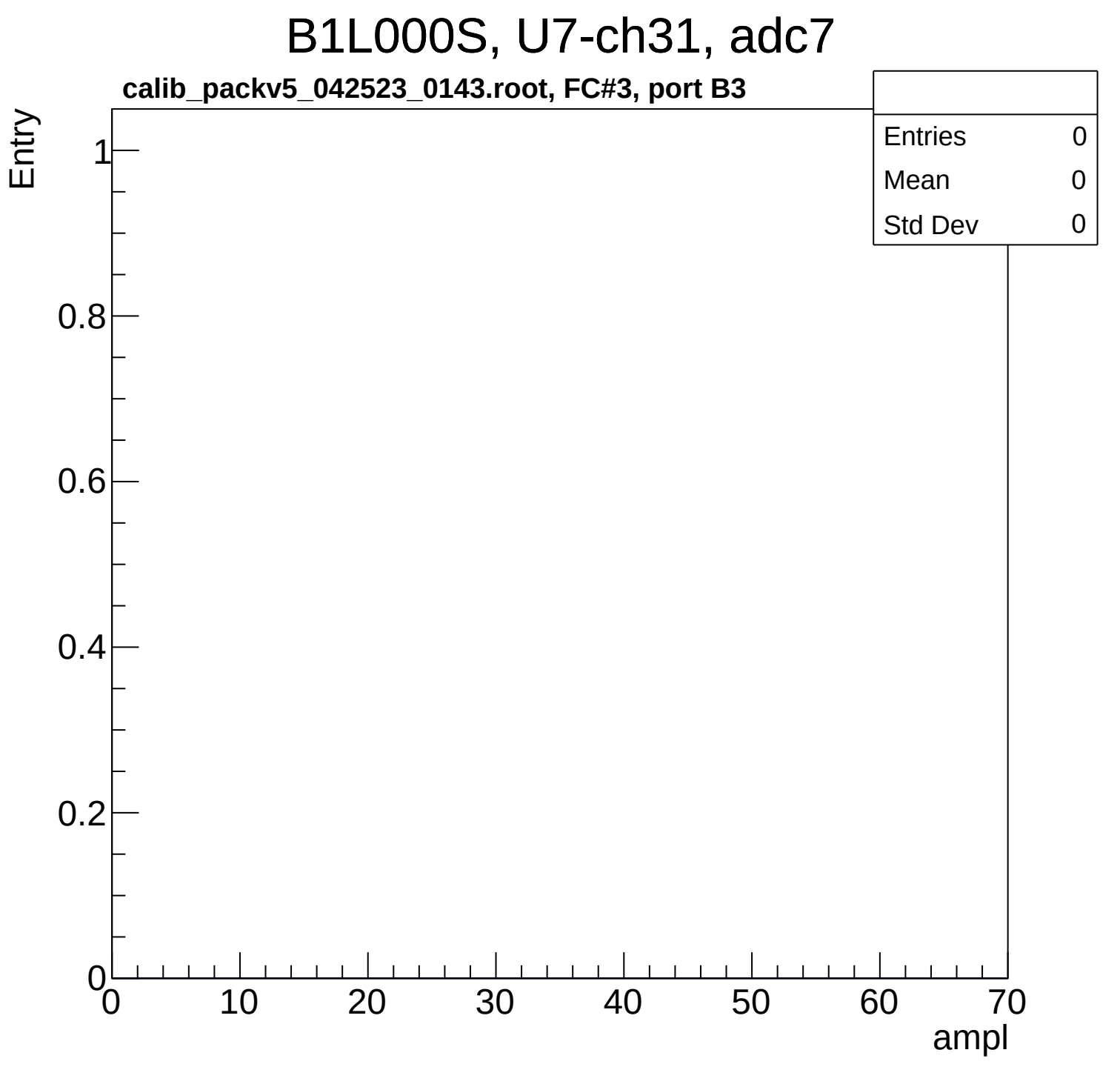
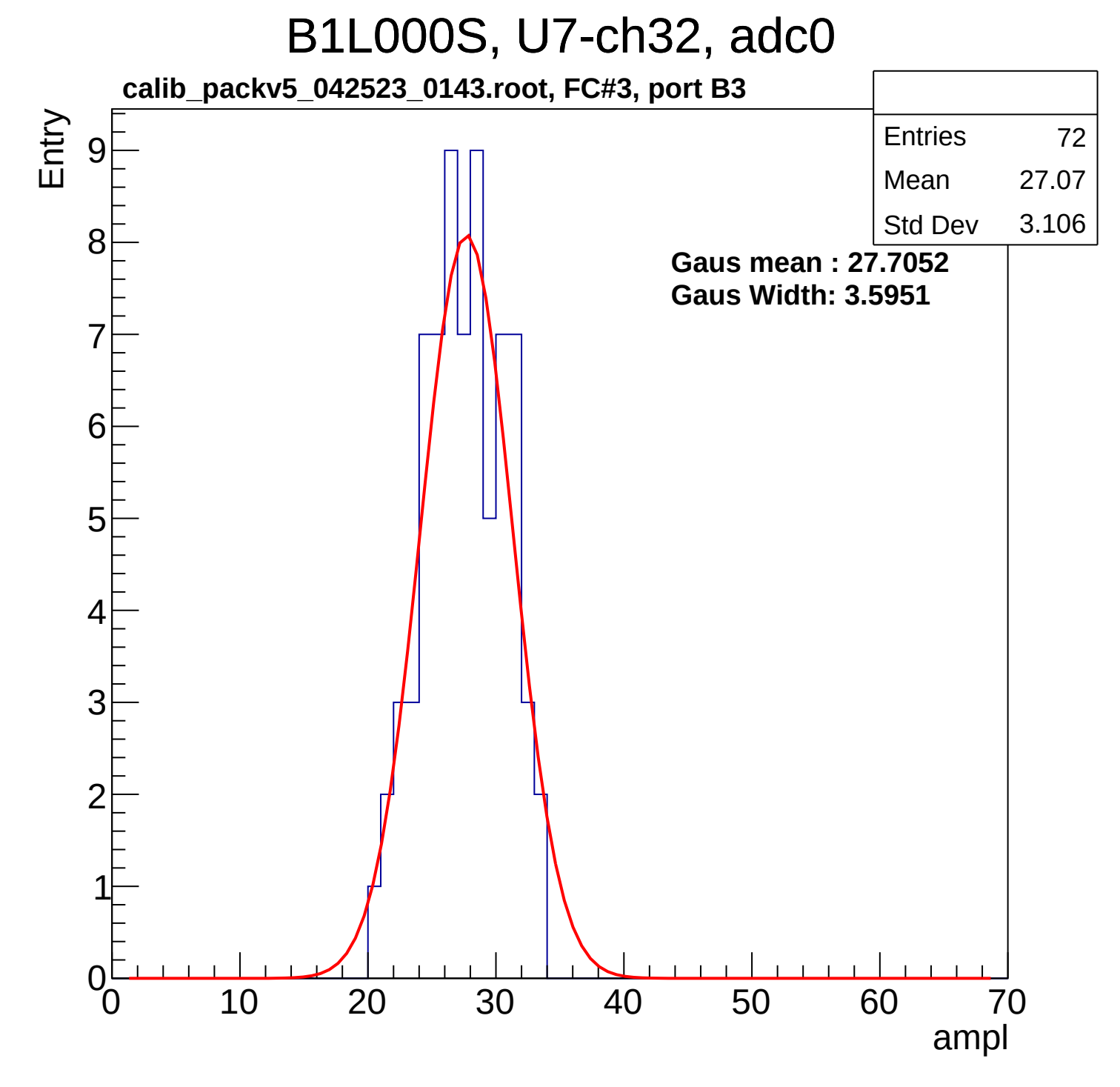
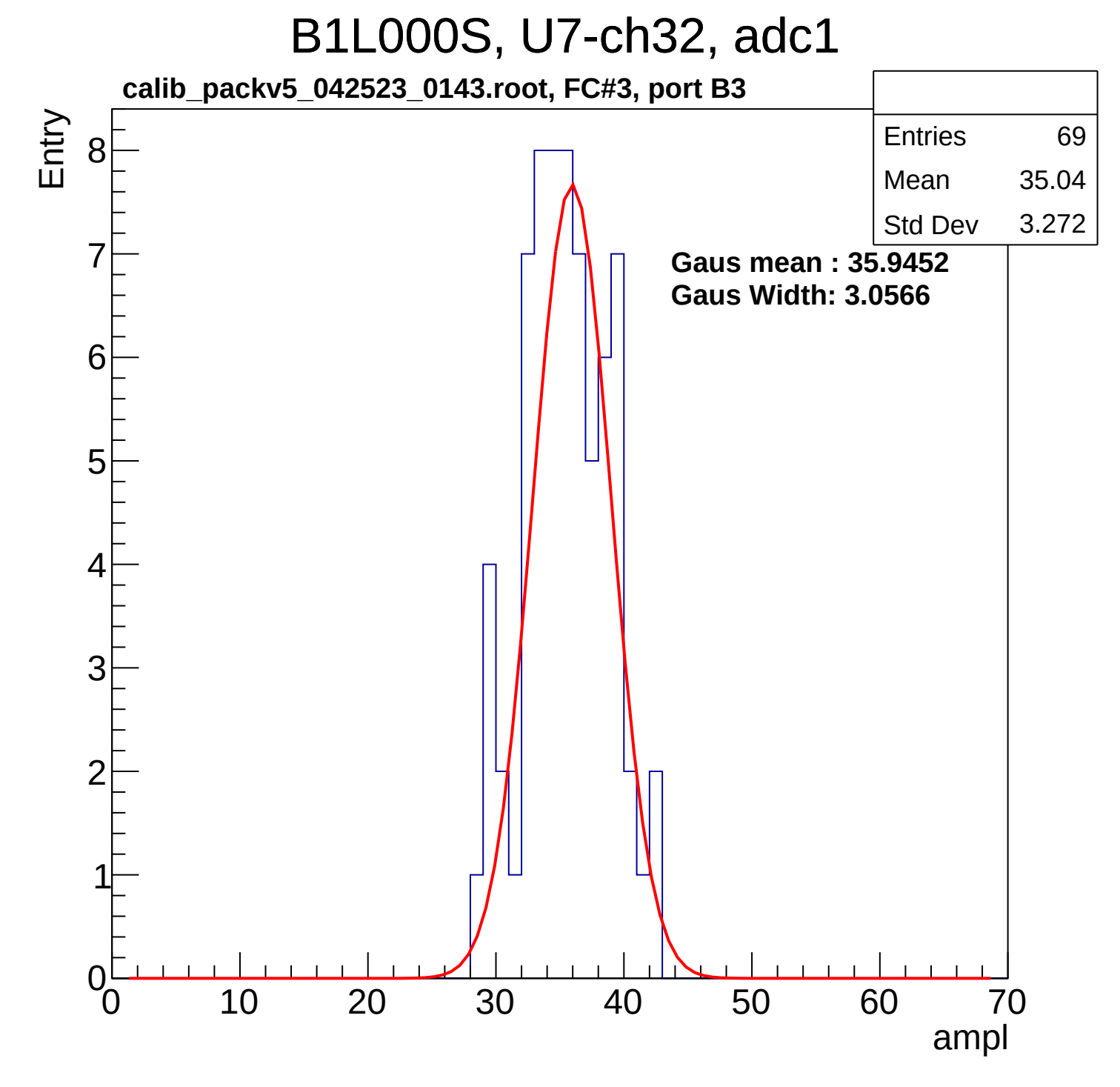
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch31, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch32, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 27.07
Std Dev 3.106
Gaus mean : 27.7052
Gaus Width: 3.5951
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch32, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 35.04
Std Dev 3.272
Gaus mean : 35.9452
Gaus Width: 3.0566
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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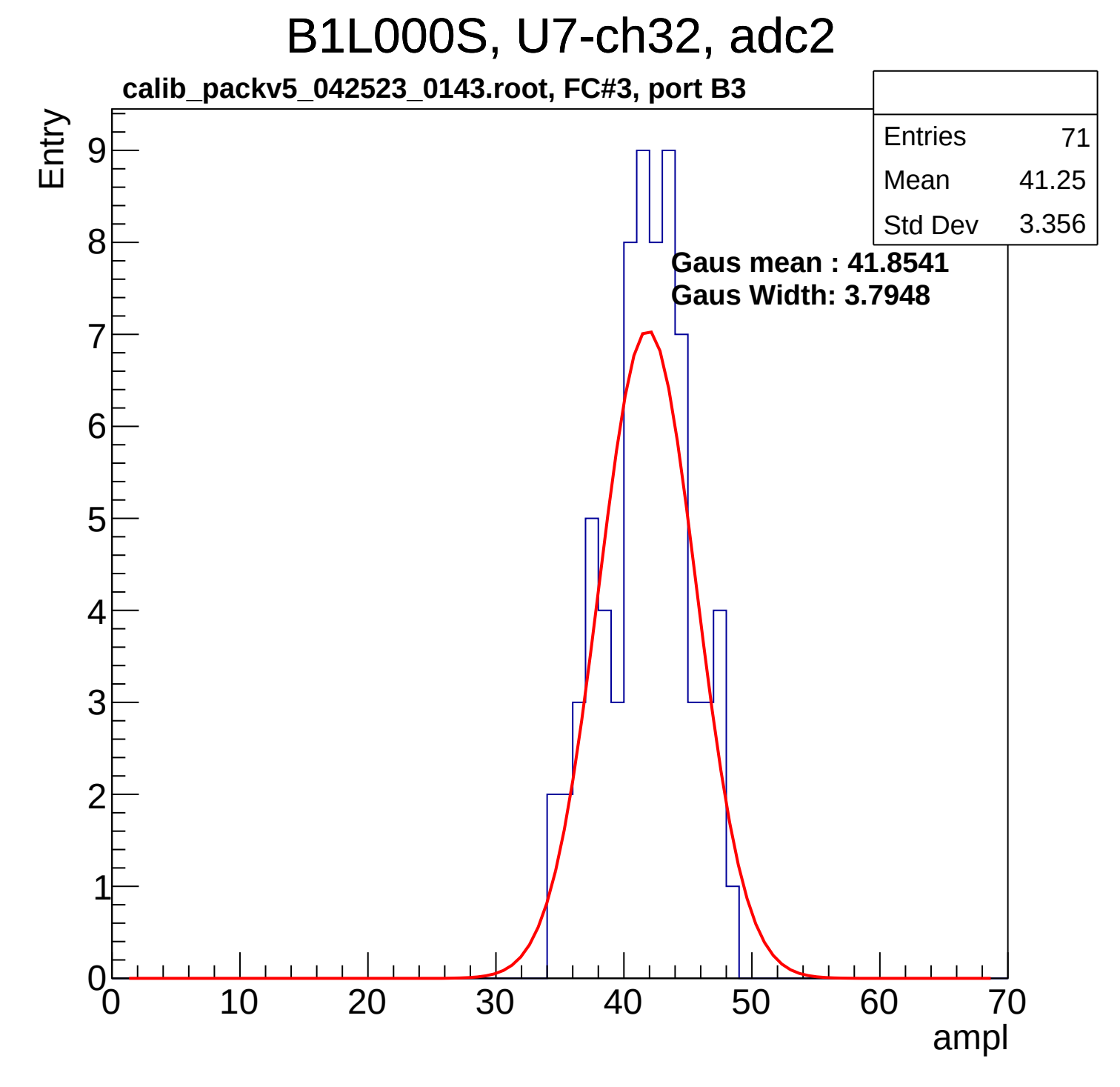
B1L000S, U7-ch32, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 41.25
Std Dev 3.356
Gaus mean : 41.8541
Gaus Width: 3.7948
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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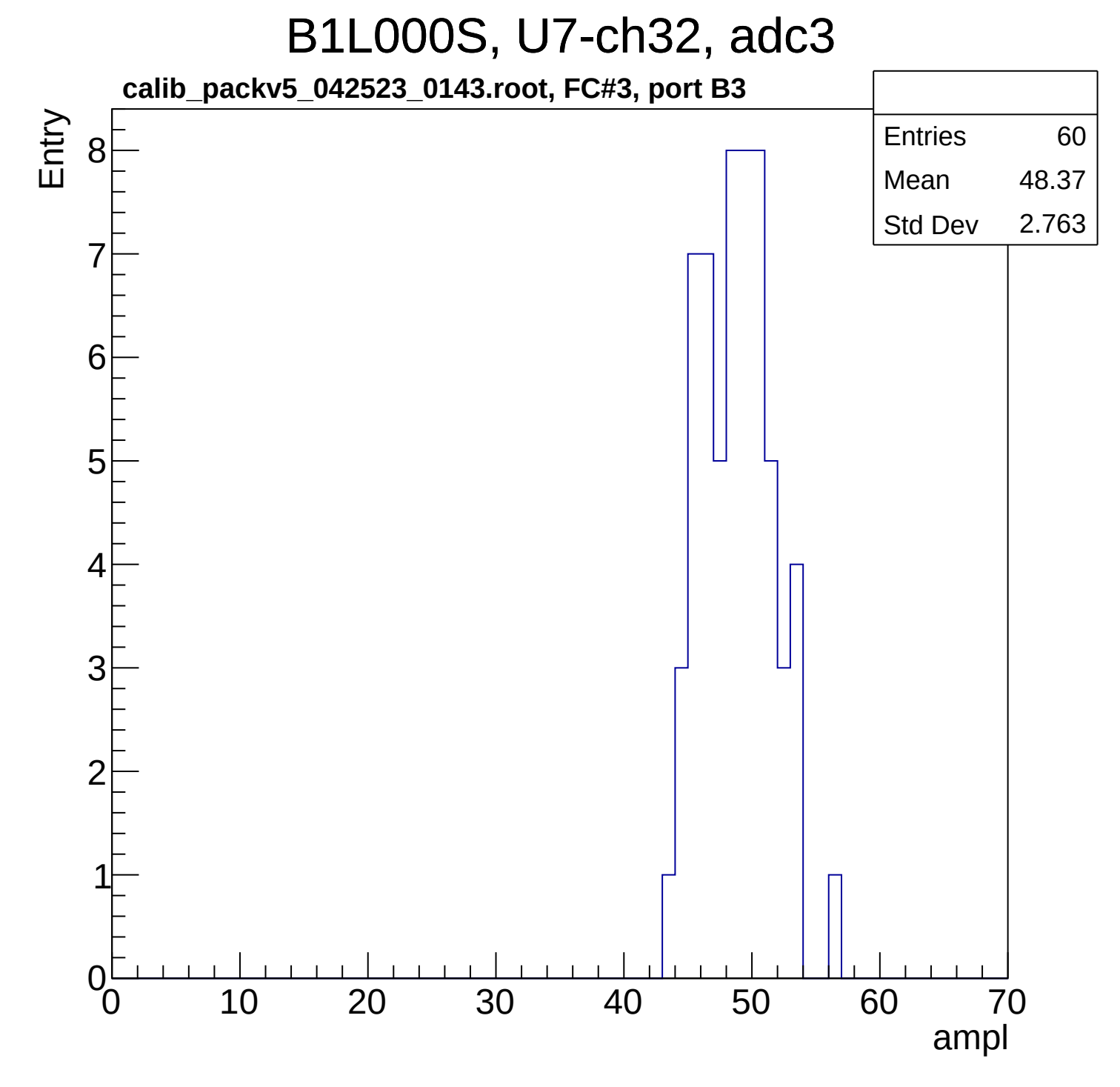
B1L000S, U7-ch32, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.37
Std Dev 2.763
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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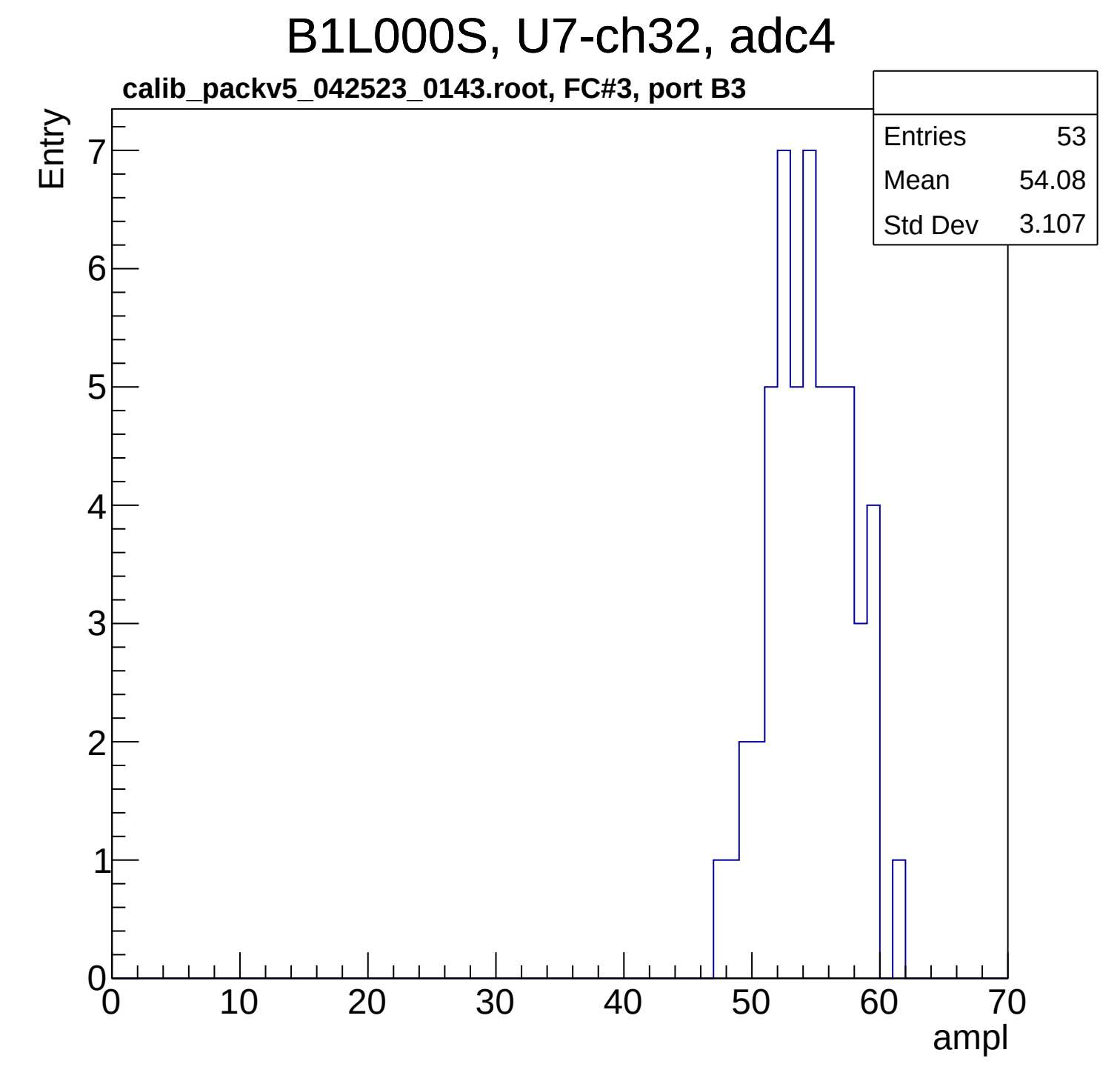
B1L000S, U7-ch32, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 54.08
Std Dev 3.107
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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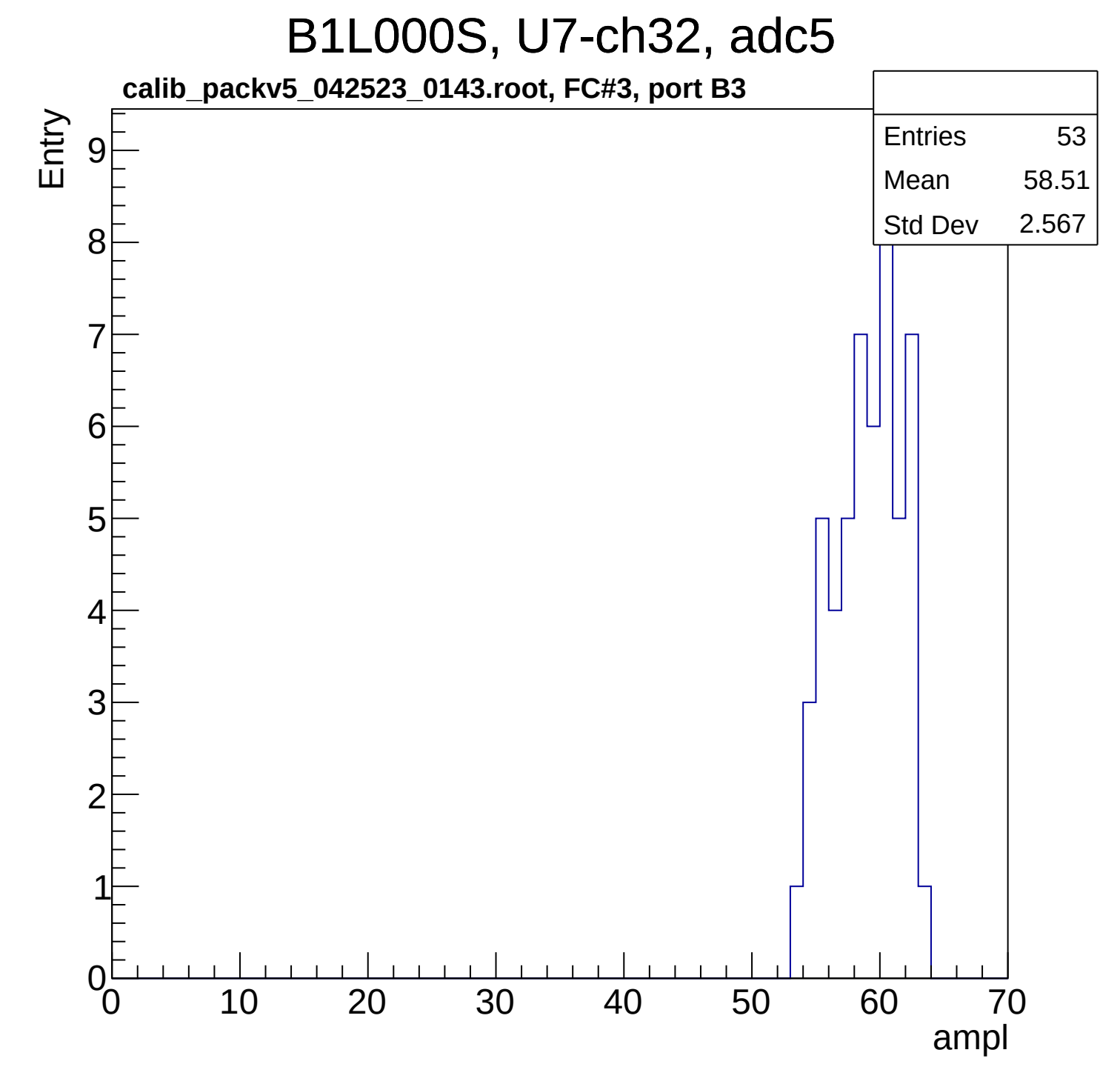
B1L000S, U7-ch32, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.51
Std Dev 2.567
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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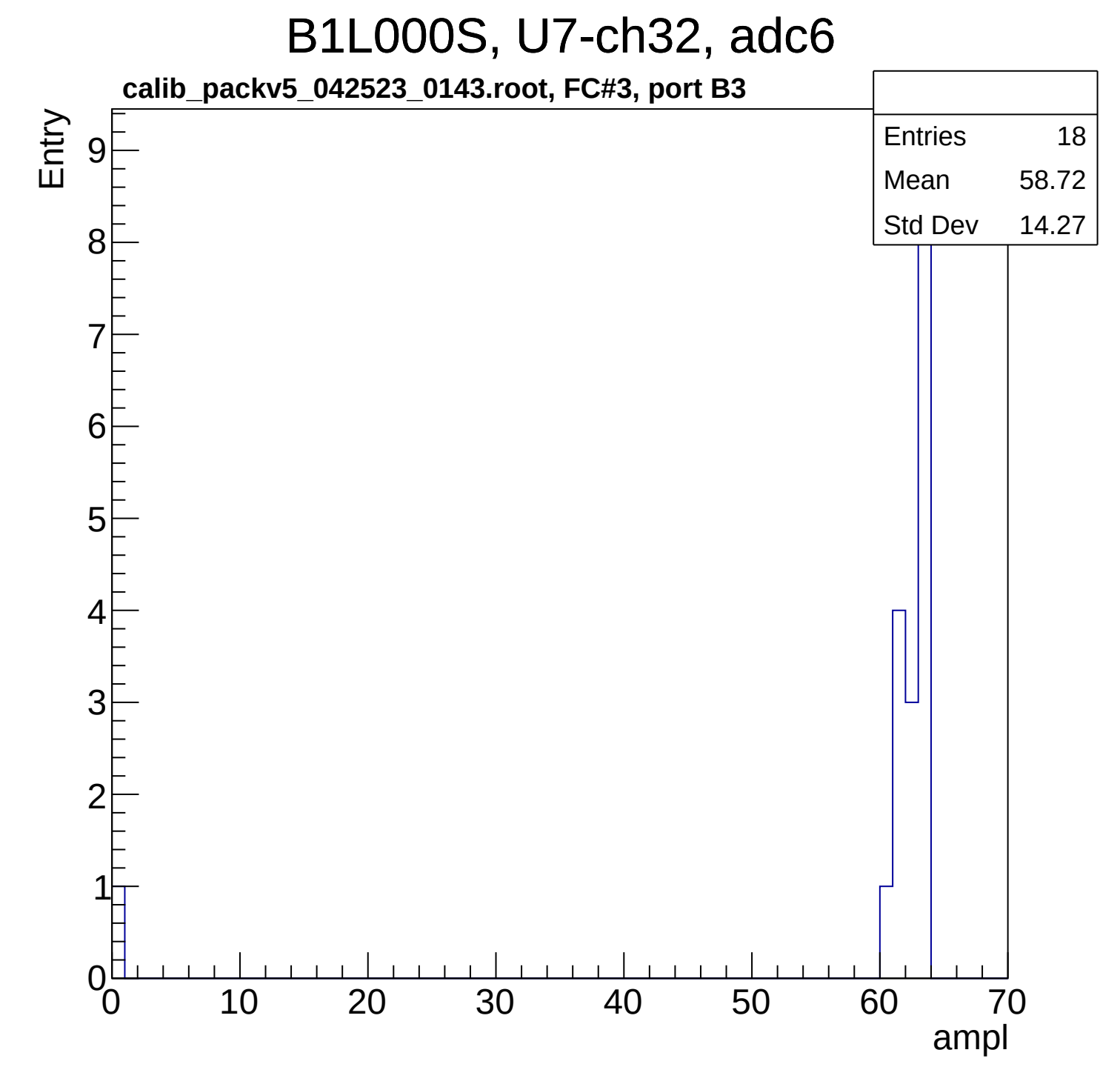
B1L000S, U7-ch32, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 58.72
Std Dev 14.27
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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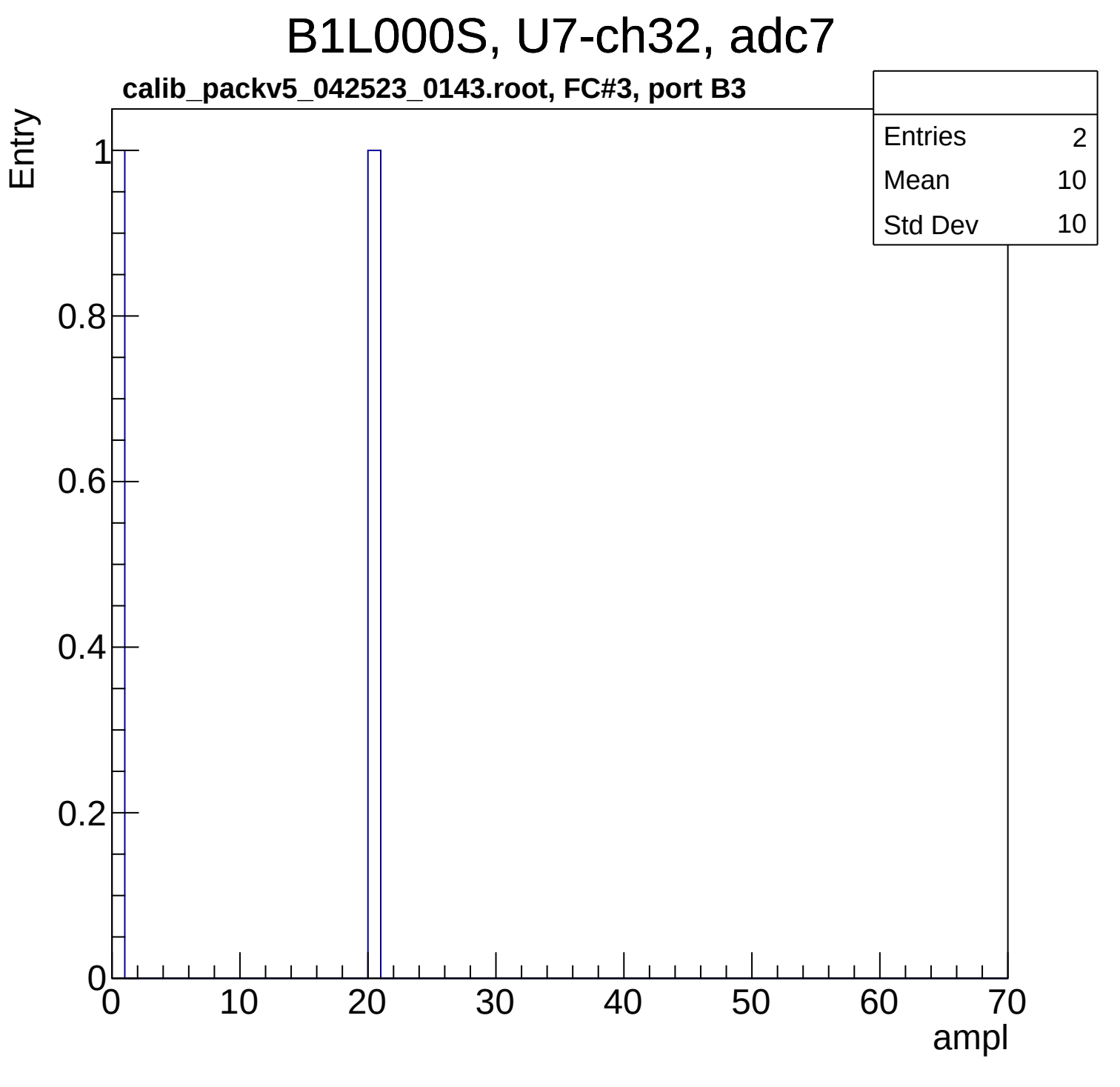
B1L000S, U7-ch32, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
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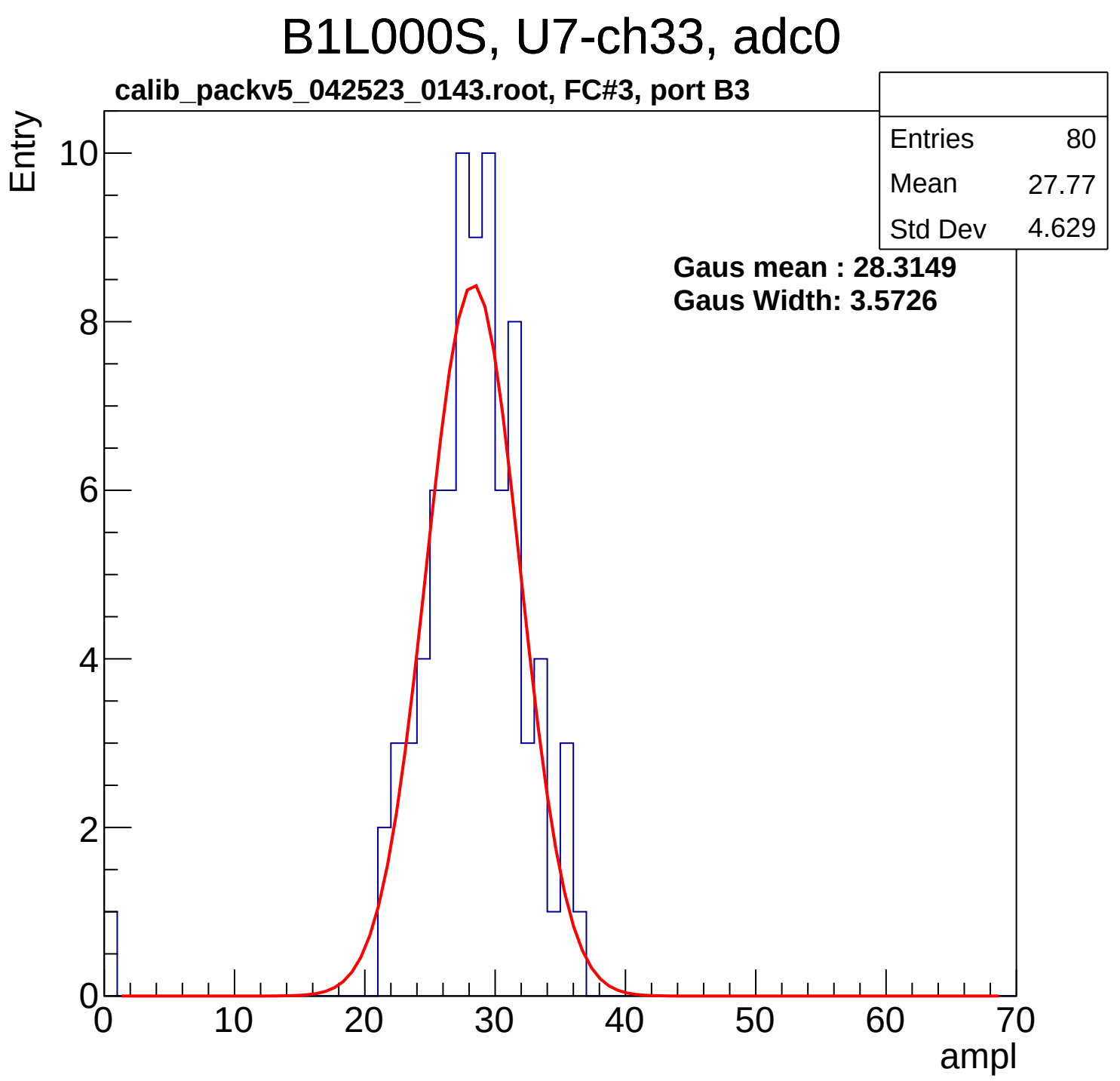
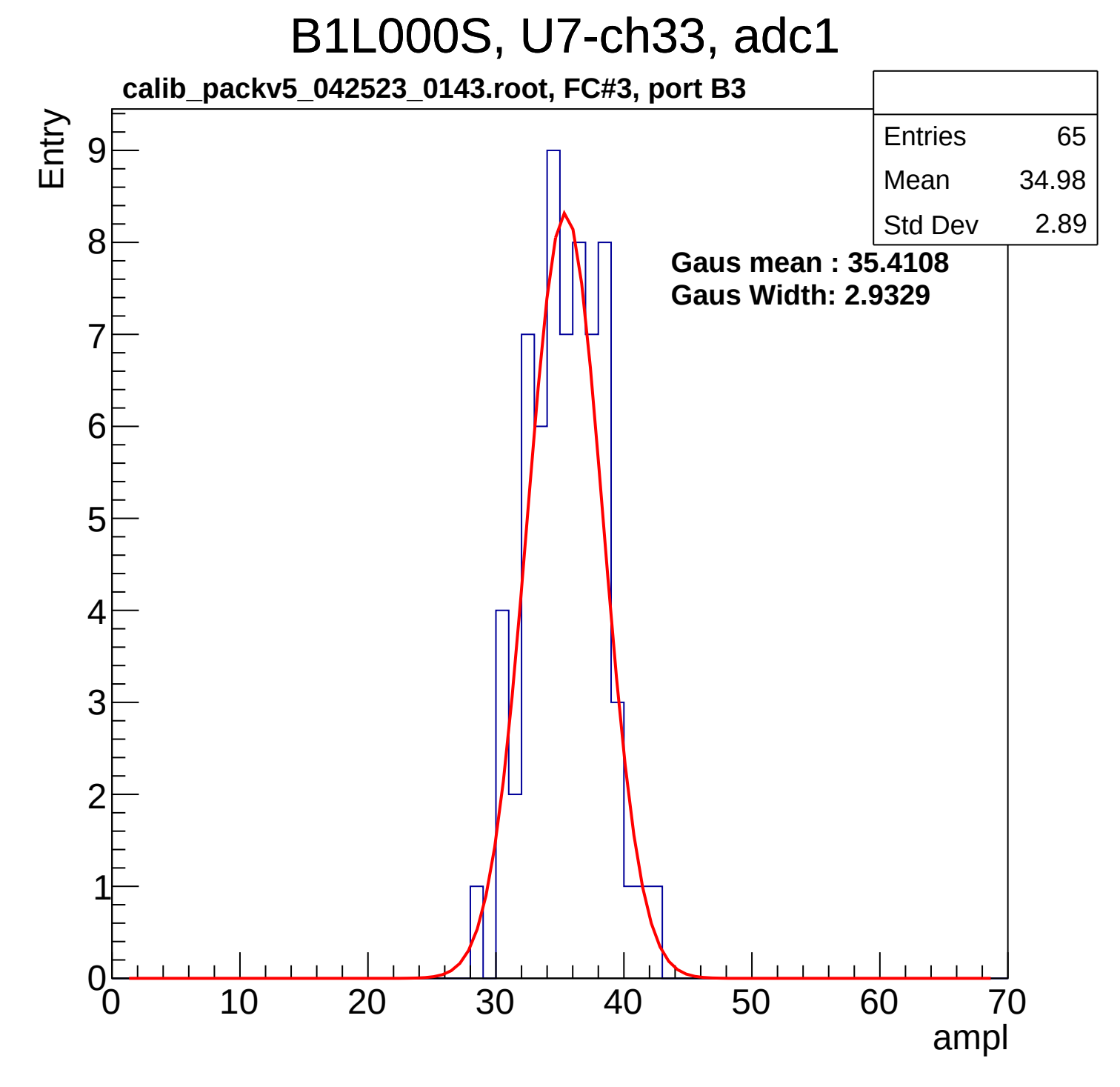
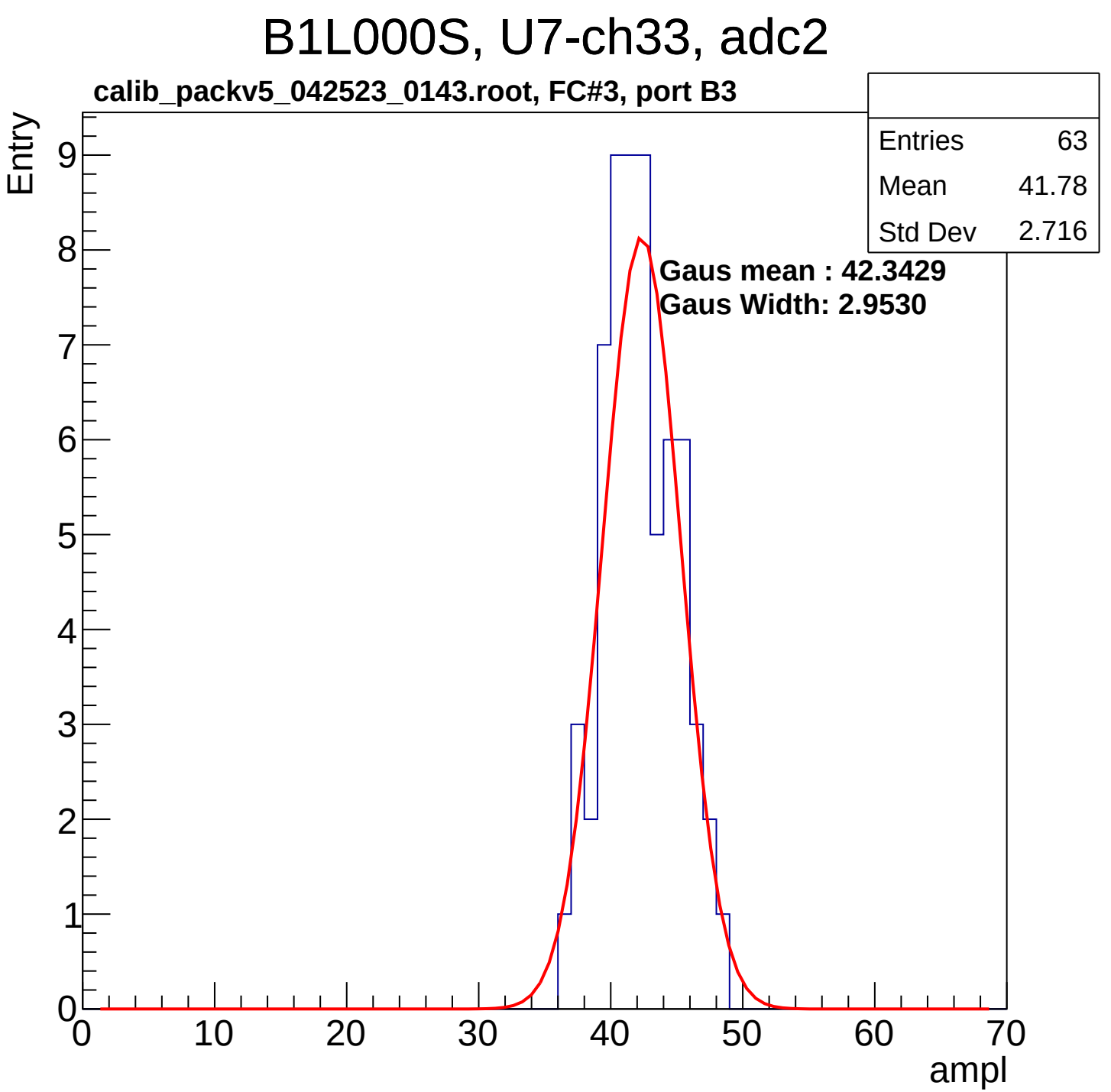
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch33, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 27.77
Std Dev 4.629
Gaus mean : 28.3149
Gaus Width: 3.5726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch33, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 34.98
Std Dev 2.89
Gaus mean : 35.4108
Gaus Width: 2.9329
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch33, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 41.78
Std Dev 2.716
Gaus mean : 42.3429
Gaus Width: 2.9530
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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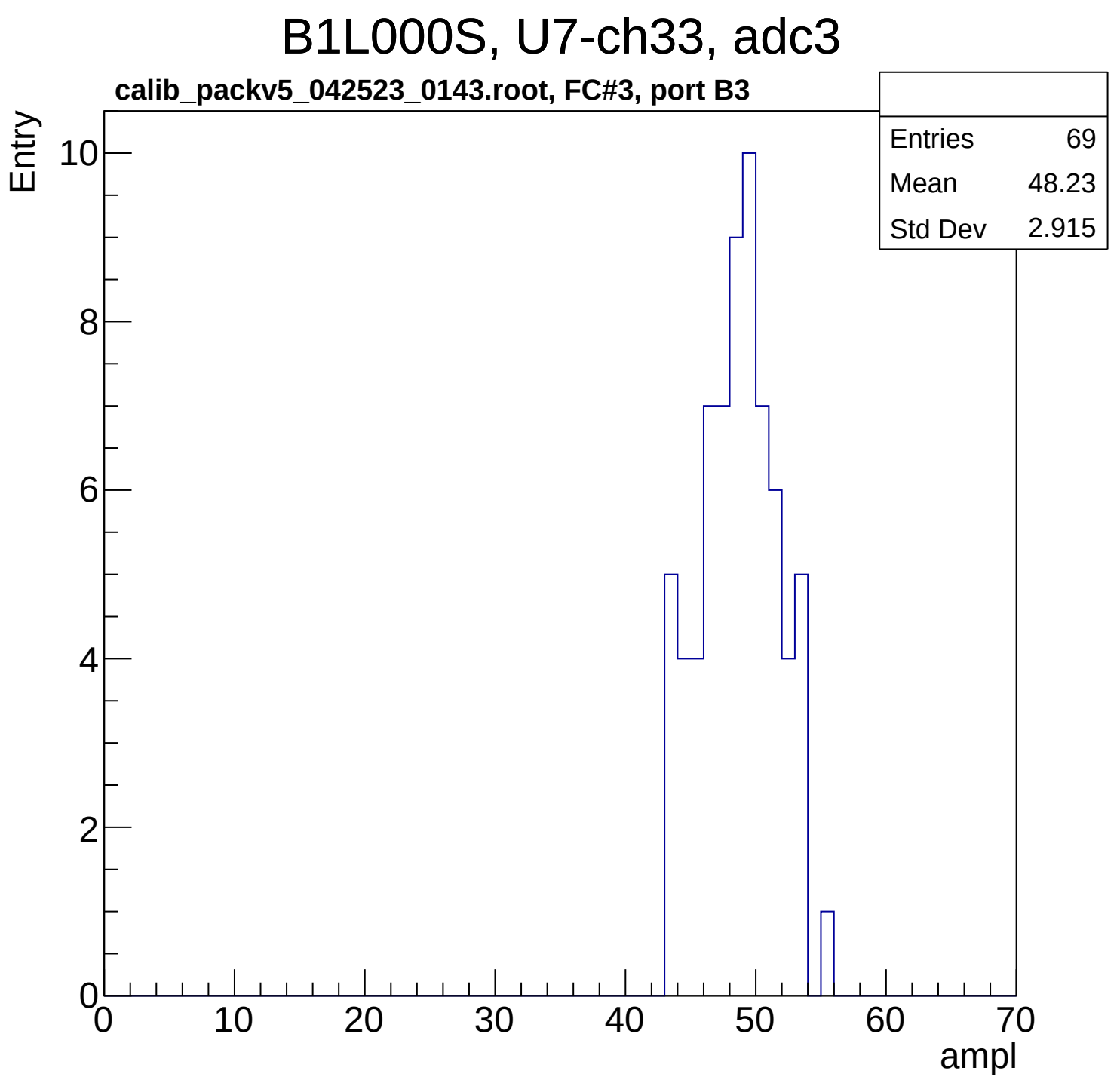
B1L000S, U7-ch33, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.23
Std Dev 2.915
Entry
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4
6
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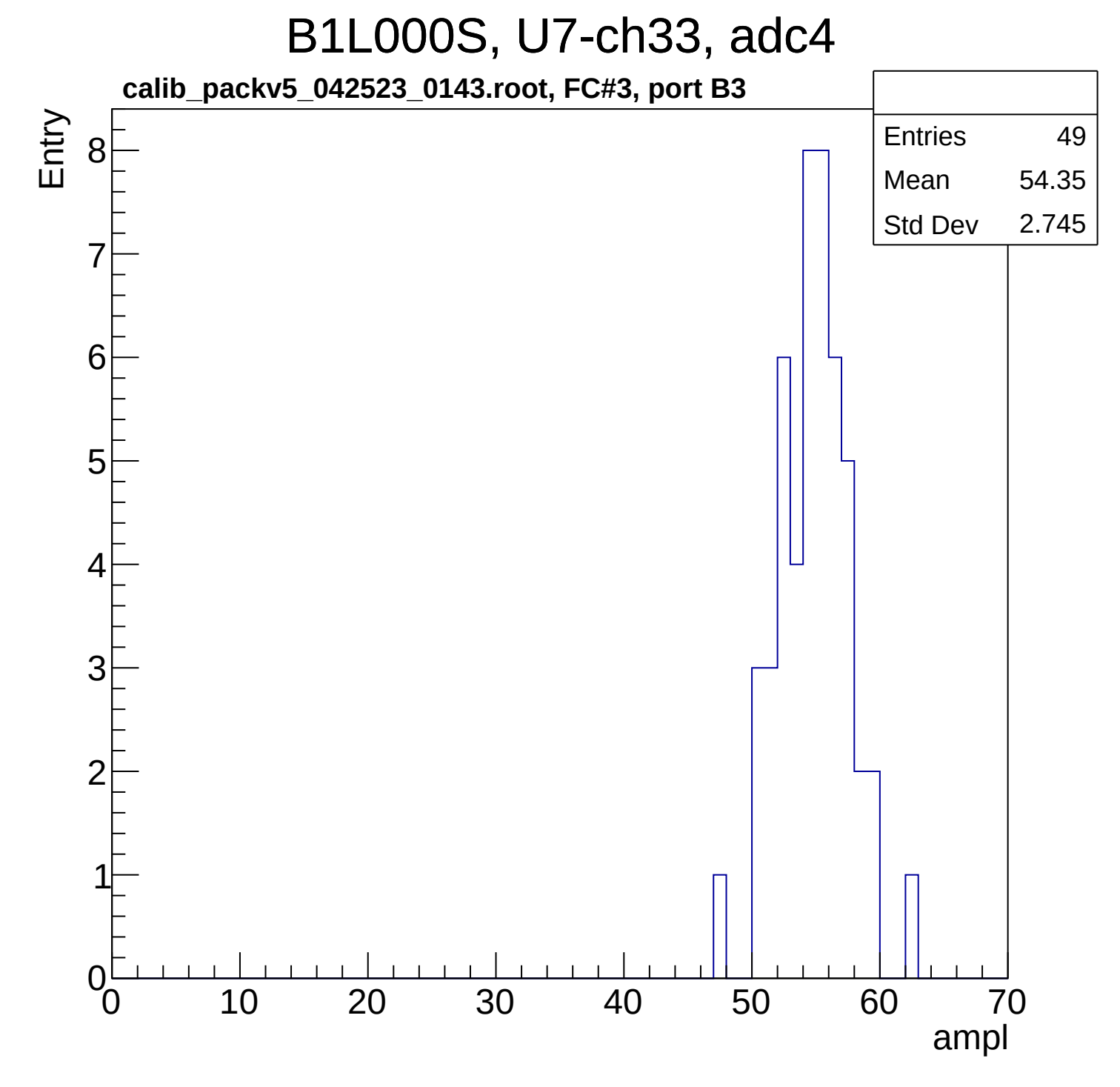
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch33, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 54.35
Std Dev 2.745
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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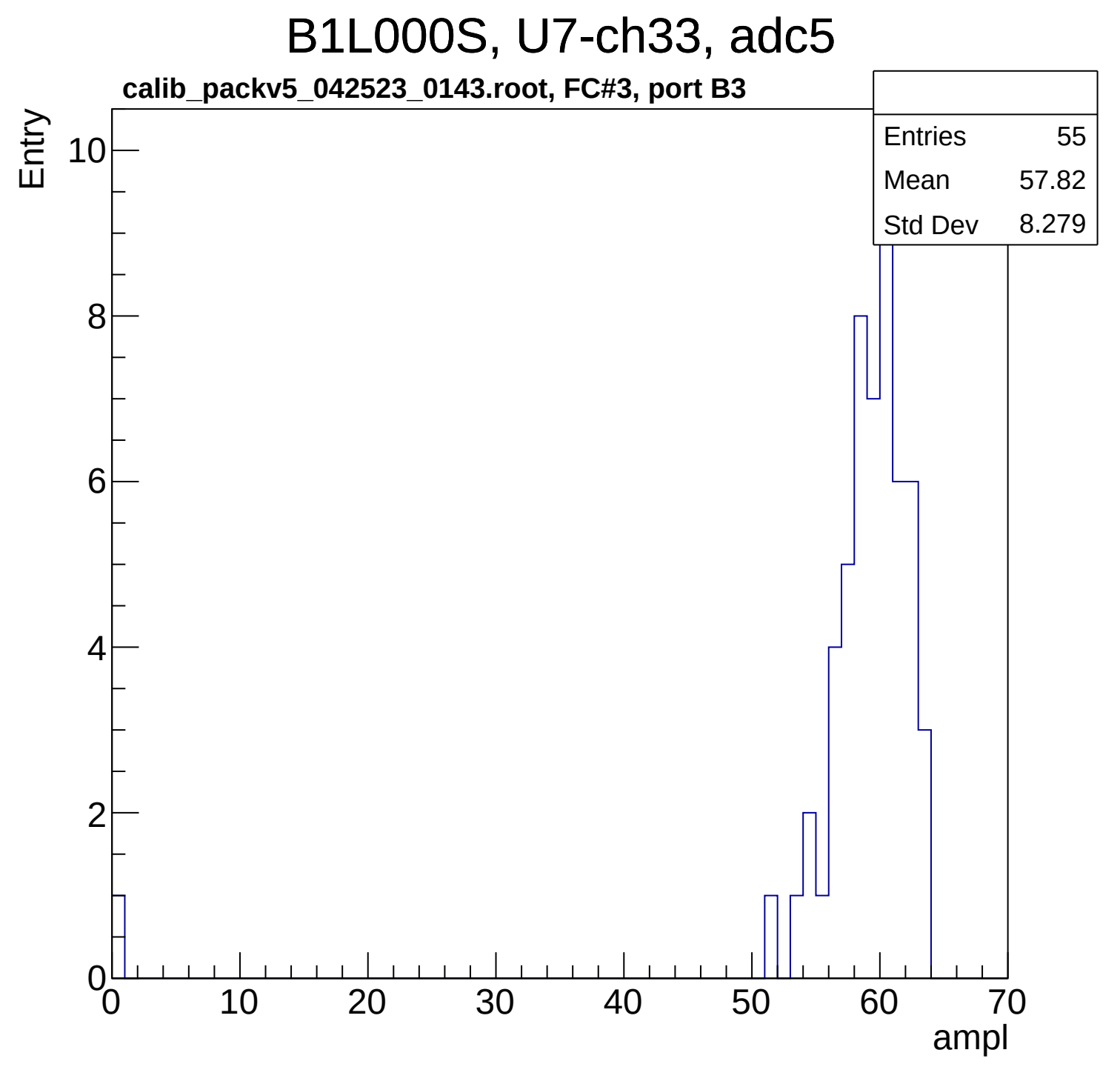
B1L000S, U7-ch33, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 57.82
Std Dev 8.279
Entry
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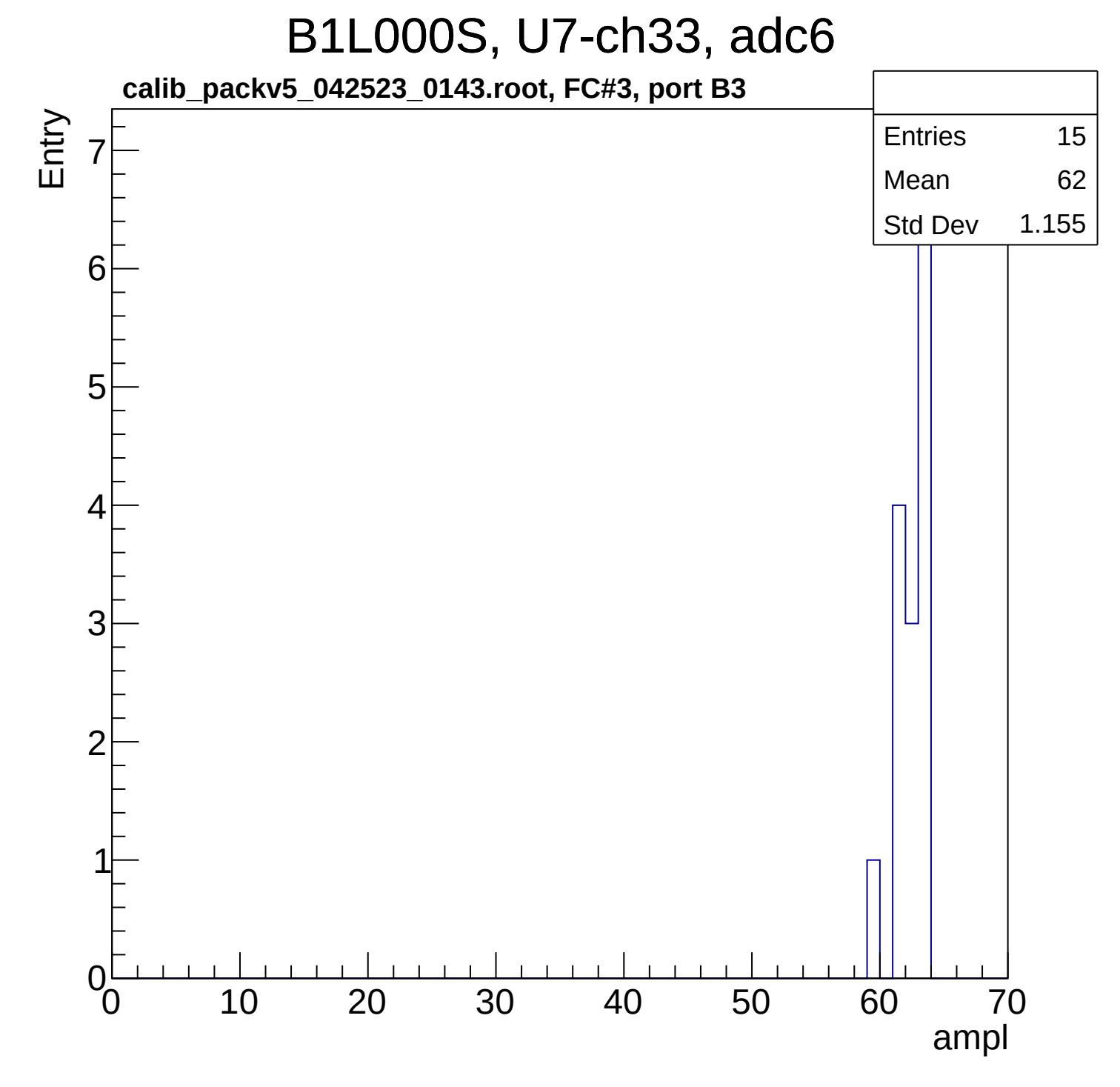
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch33, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 62
Std Dev 1.155
Entry
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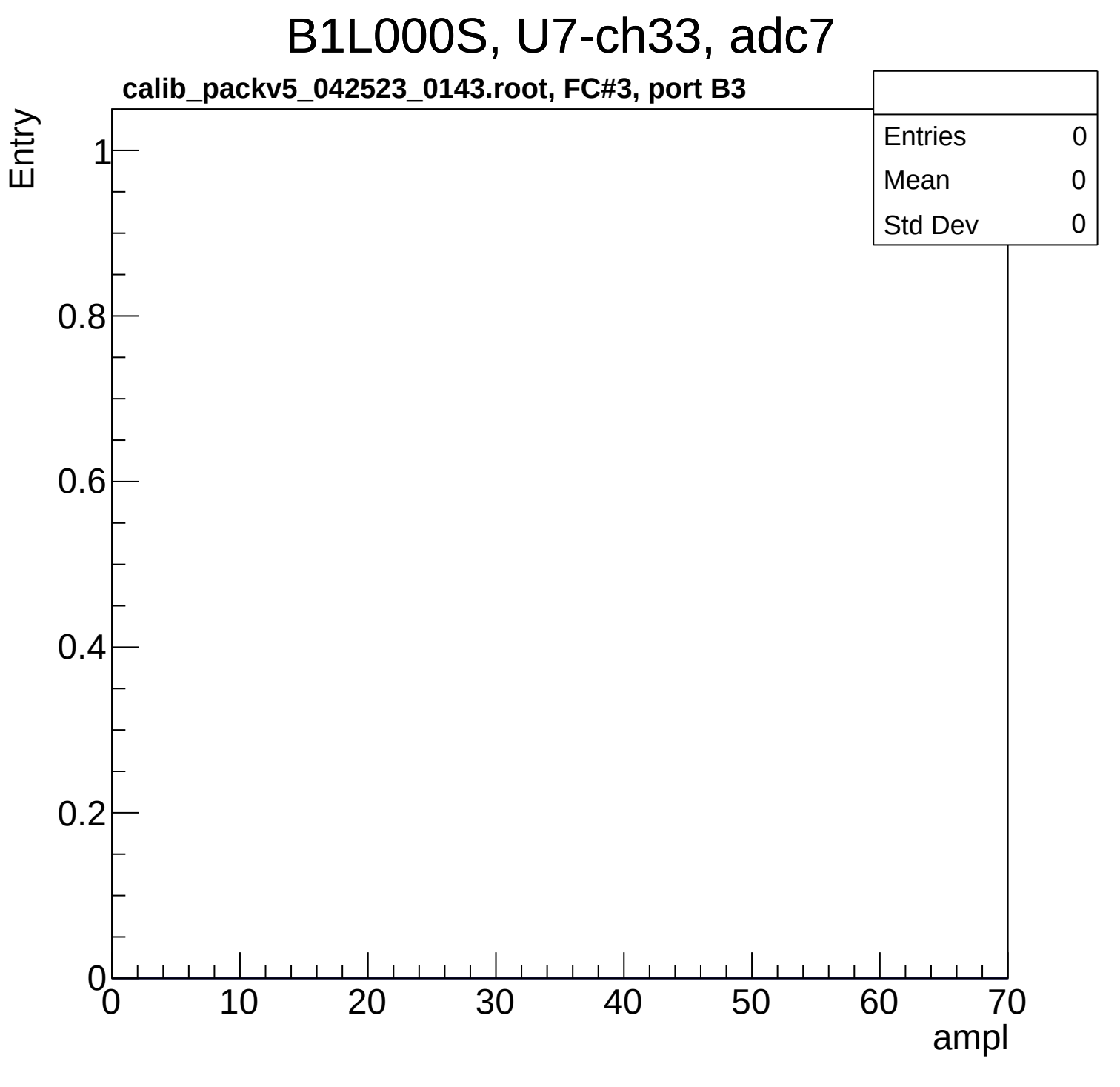
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch33, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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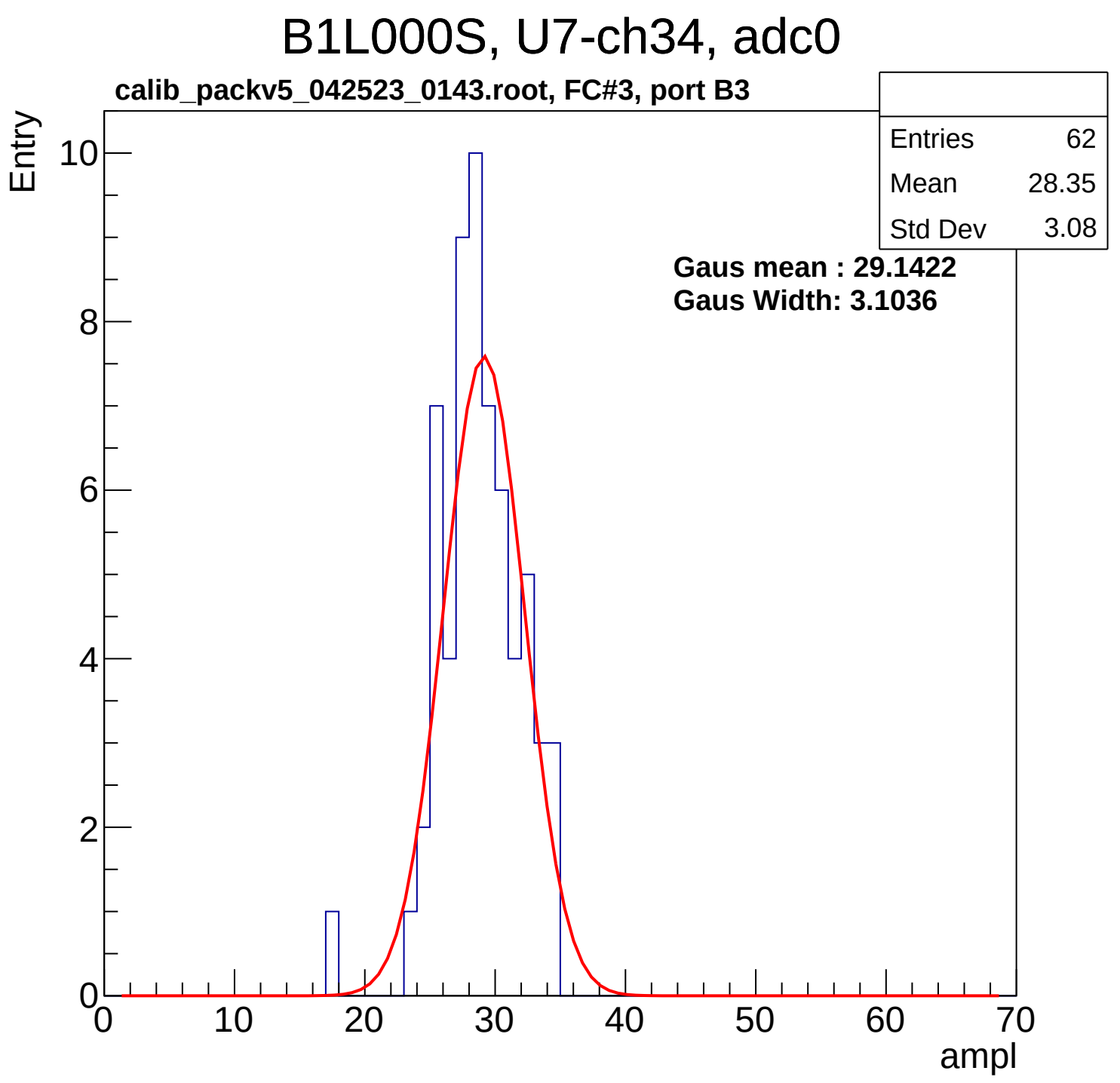
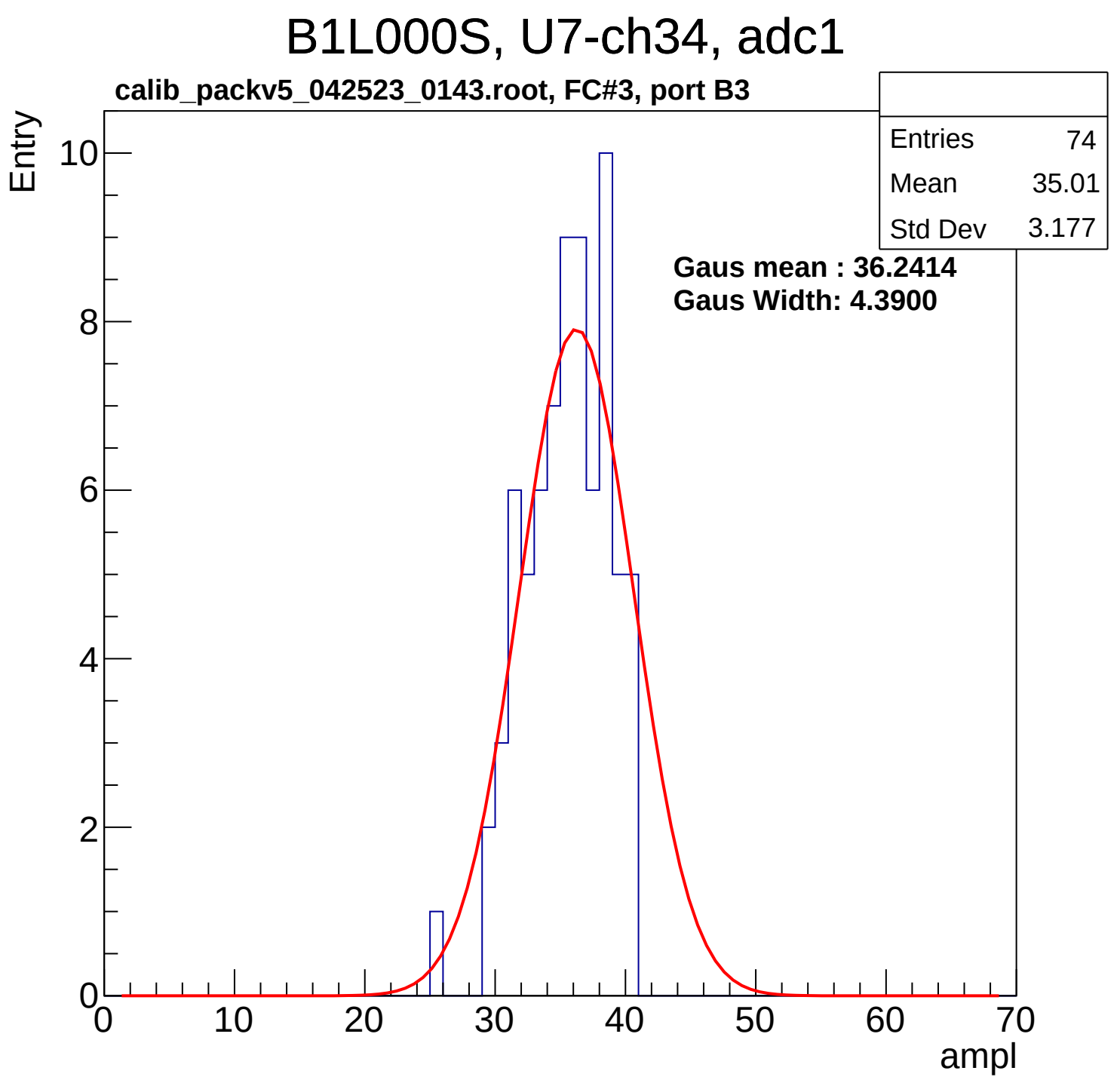
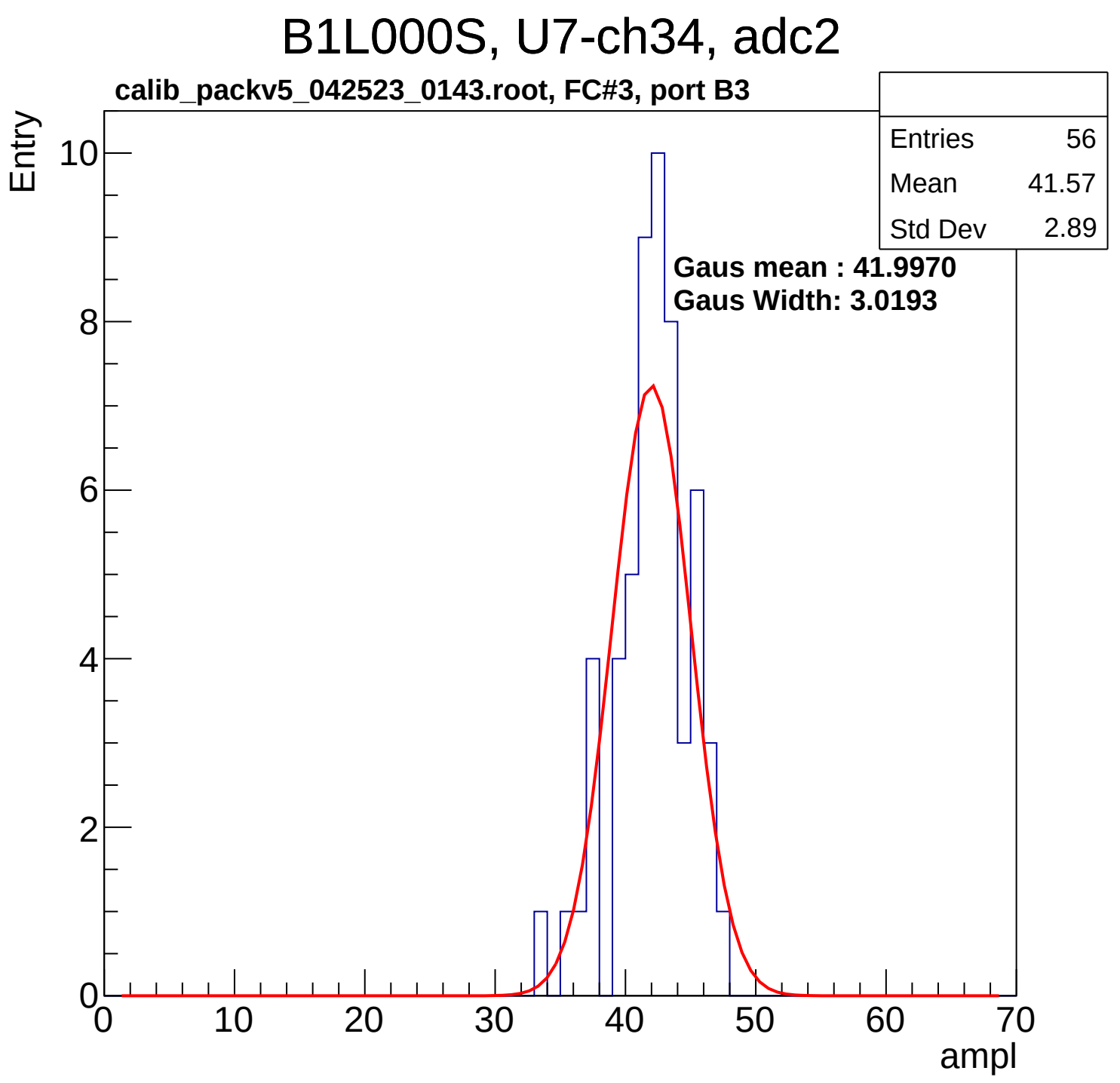
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch34, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.35
Std Dev 3.08
Gaus mean : 29.1422
Gaus Width: 3.1036
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch34, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 35.01
Std Dev 3.177
Gaus mean : 36.2414
Gaus Width: 4.3900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch34, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.57
Std Dev 2.89
Gaus mean : 41.9970
Gaus Width: 3.0193
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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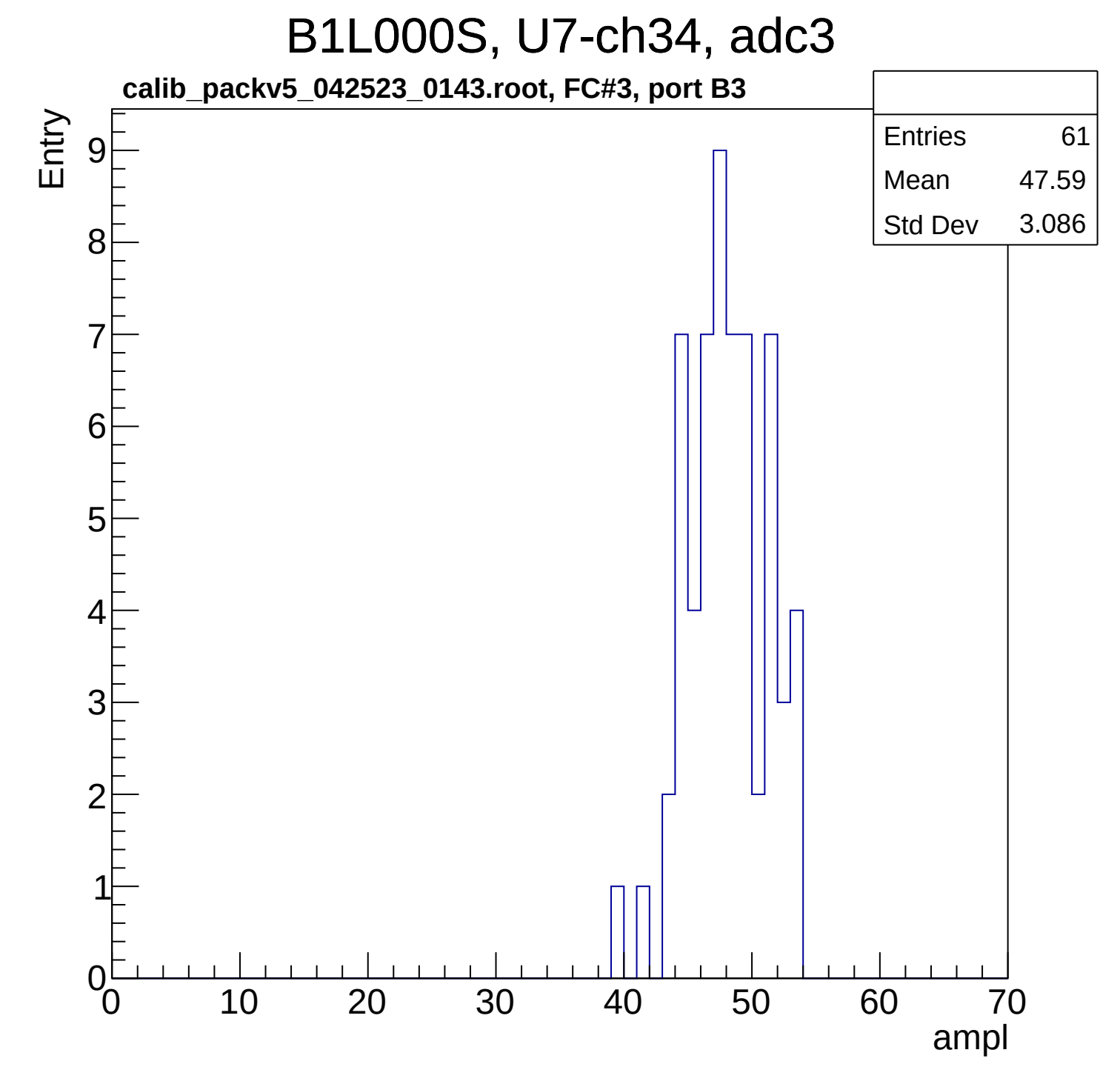
B1L000S, U7-ch34, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 47.59
Std Dev 3.086
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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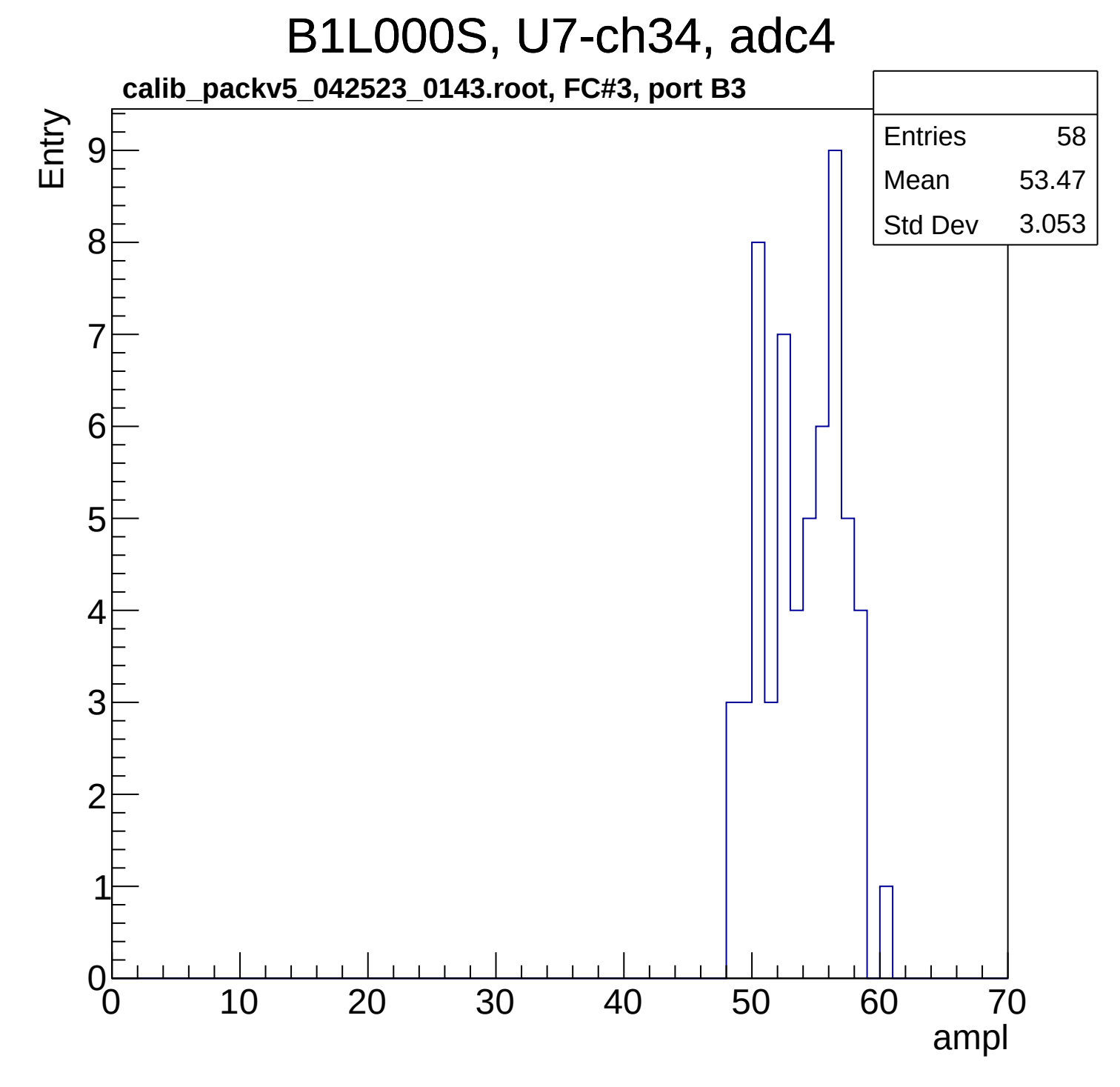
B1L000S, U7-ch34, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 53.47
Std Dev 3.053
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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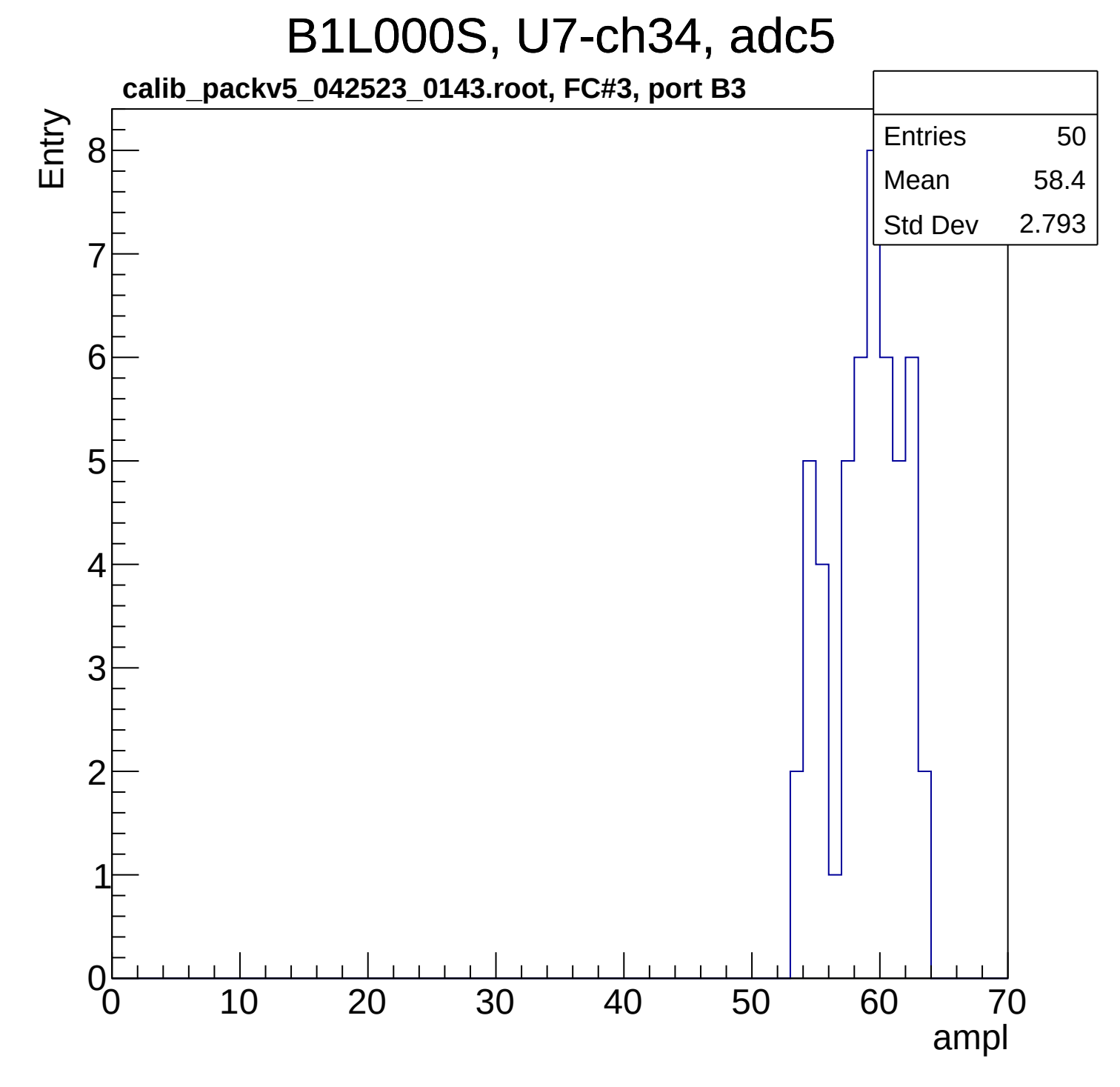
B1L000S, U7-ch34, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.4
Std Dev 2.793
Entry
ampl
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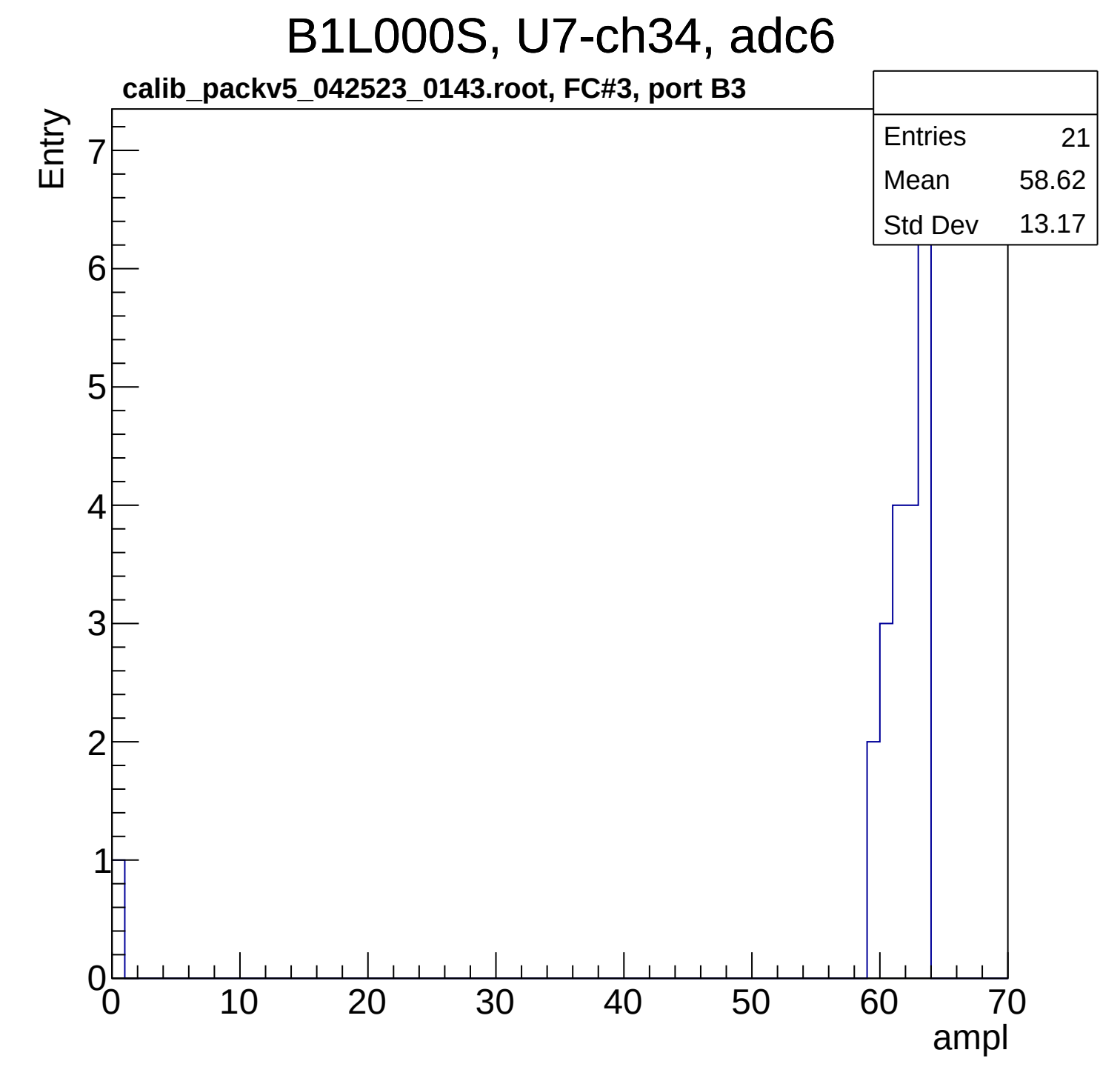
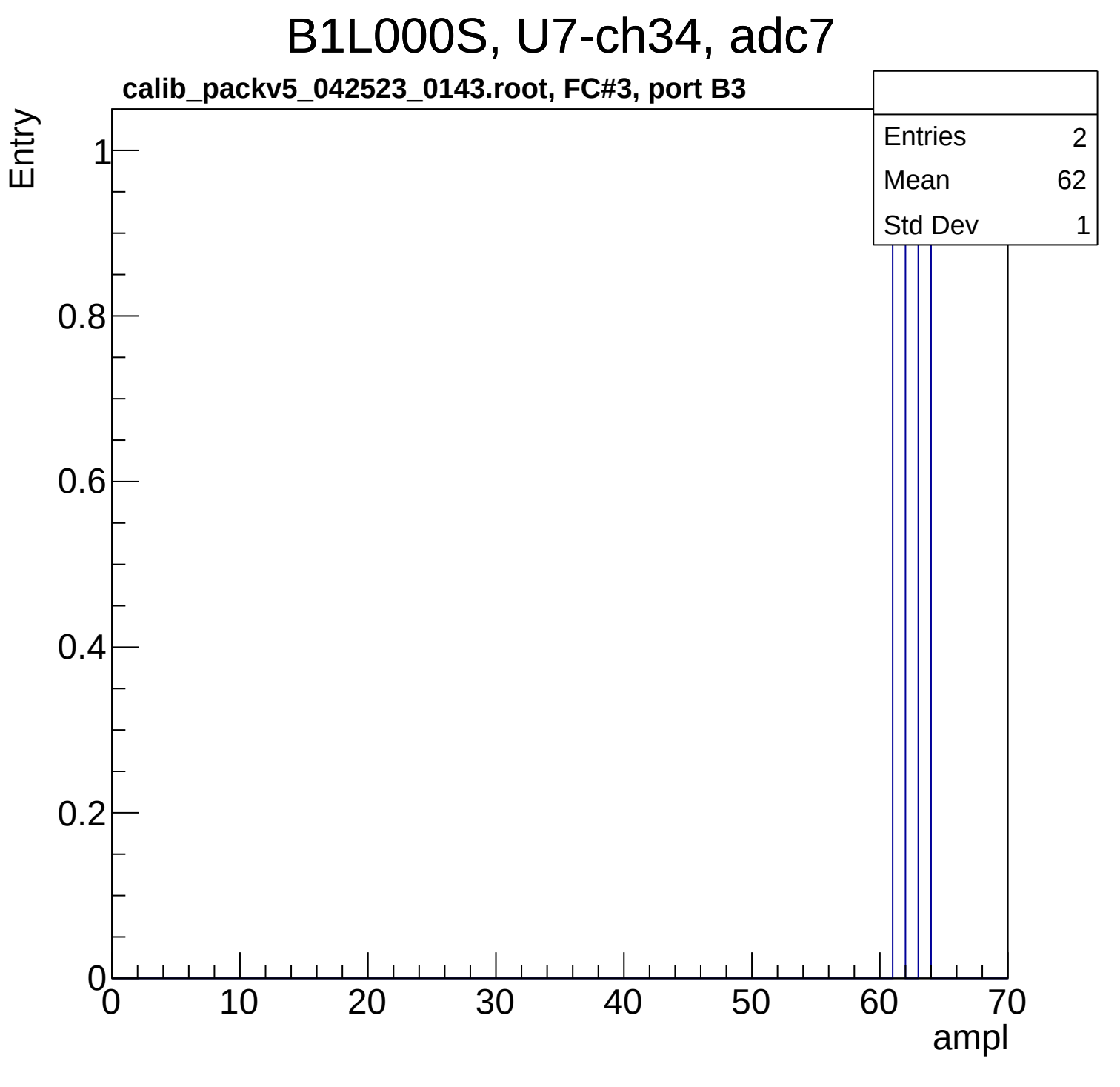
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch34, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 58.62
Std Dev 13.17
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch34, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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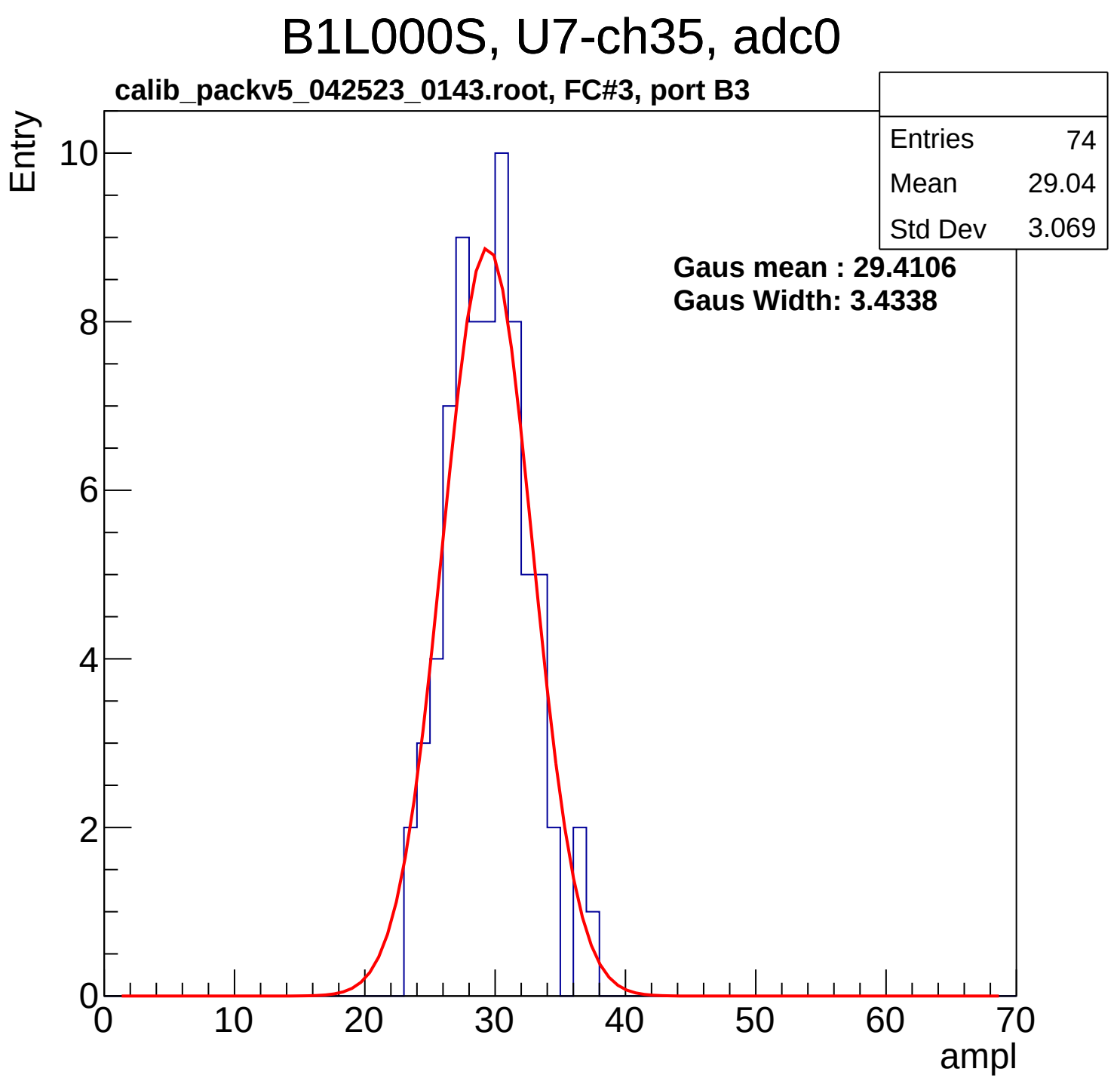
50
60
70
ampl


B1L000S, U7-ch35, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.04
Std Dev 3.069
Gaus mean : 29.4106
Gaus Width: 3.4338
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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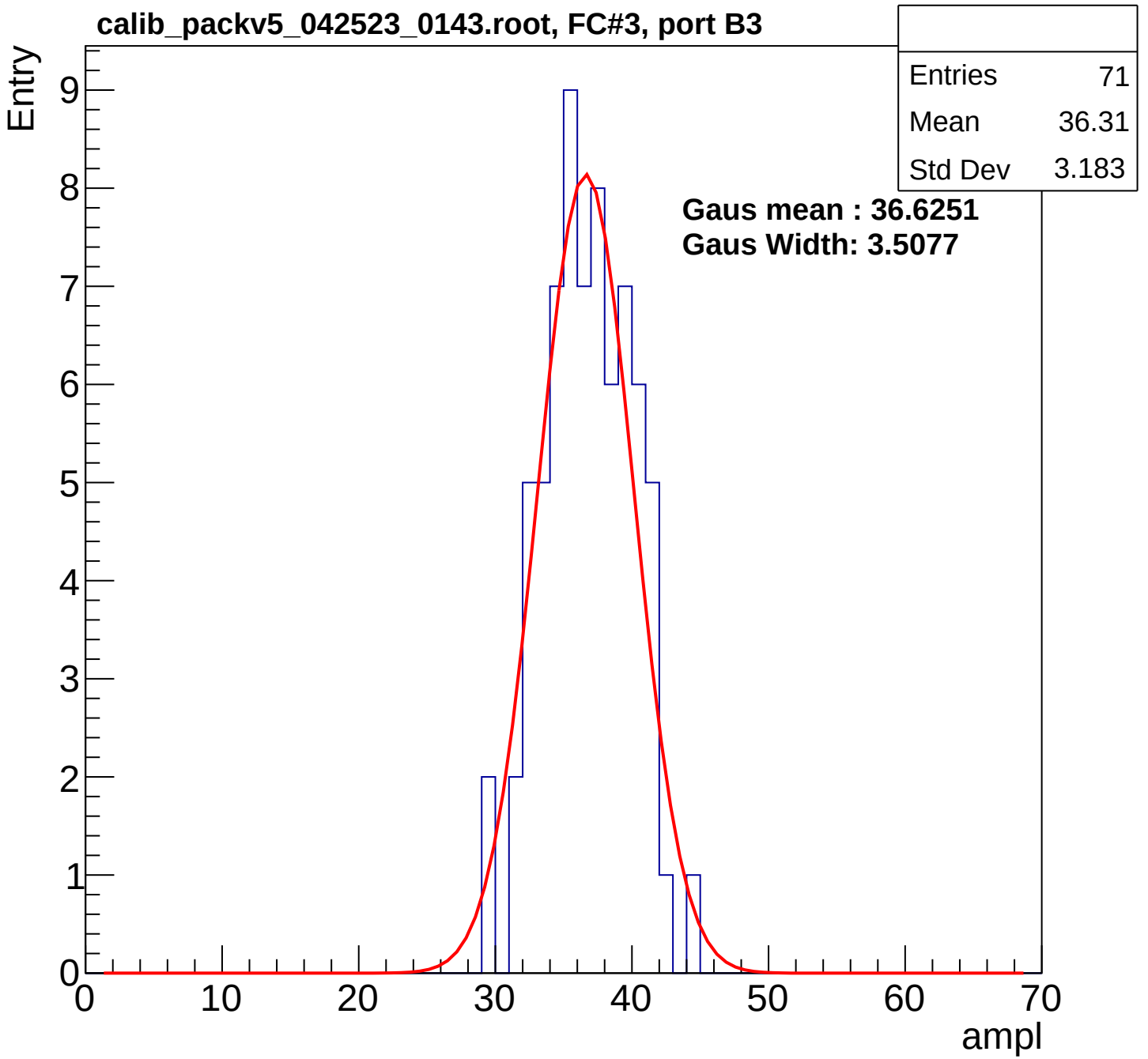
B1L000S, U7-ch35, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 36.31
Std Dev 3.183
Gaus mean : 36.6251
Gaus Width: 3.5077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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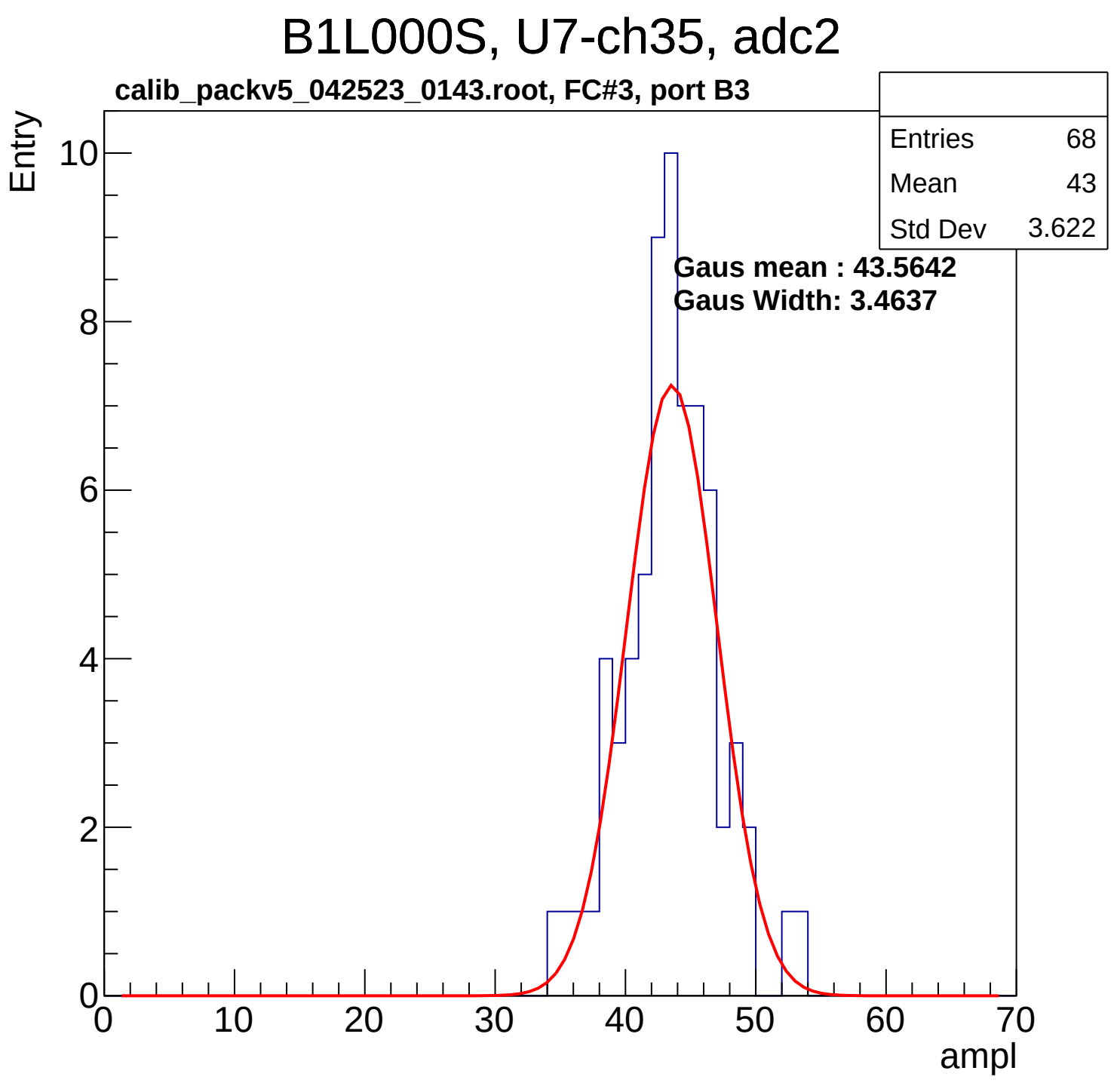
B1L000S, U7-ch35, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 43
Std Dev 3.622
Gaus mean : 43.5642
Gaus Width: 3.4637
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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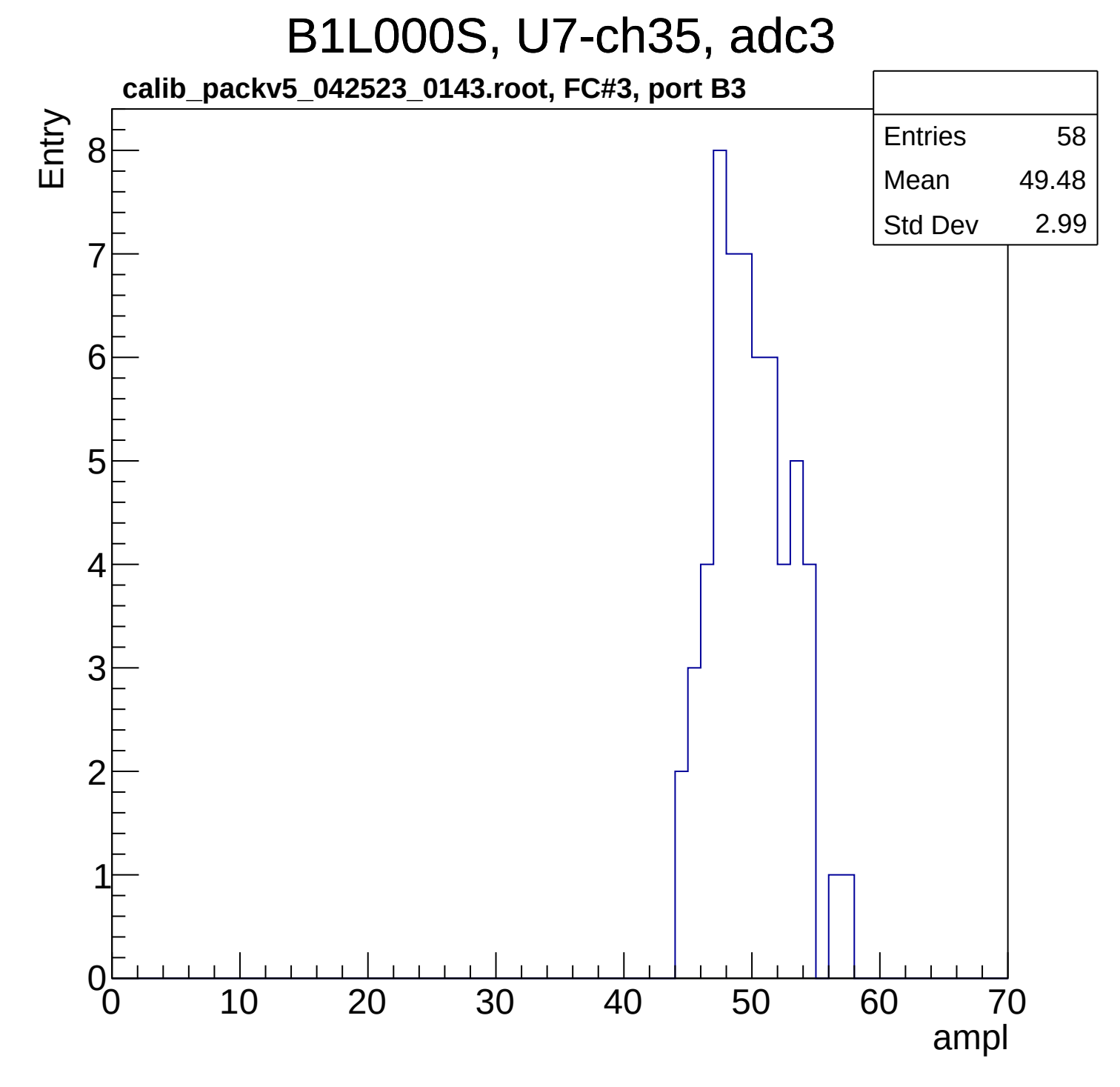
B1L000S, U7-ch35, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 49.48
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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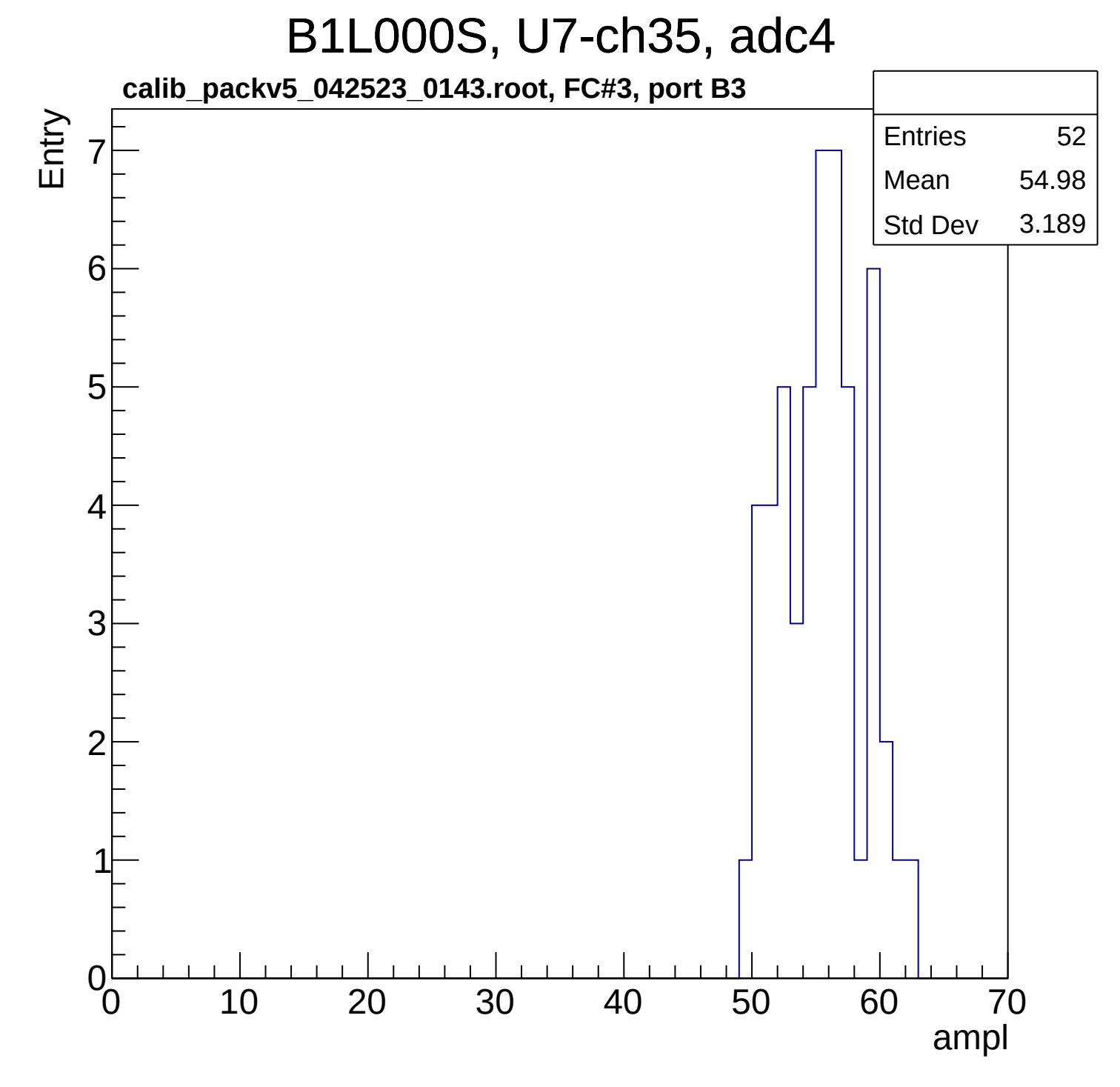
B1L000S, U7-ch35, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 54.98
Std Dev 3.189
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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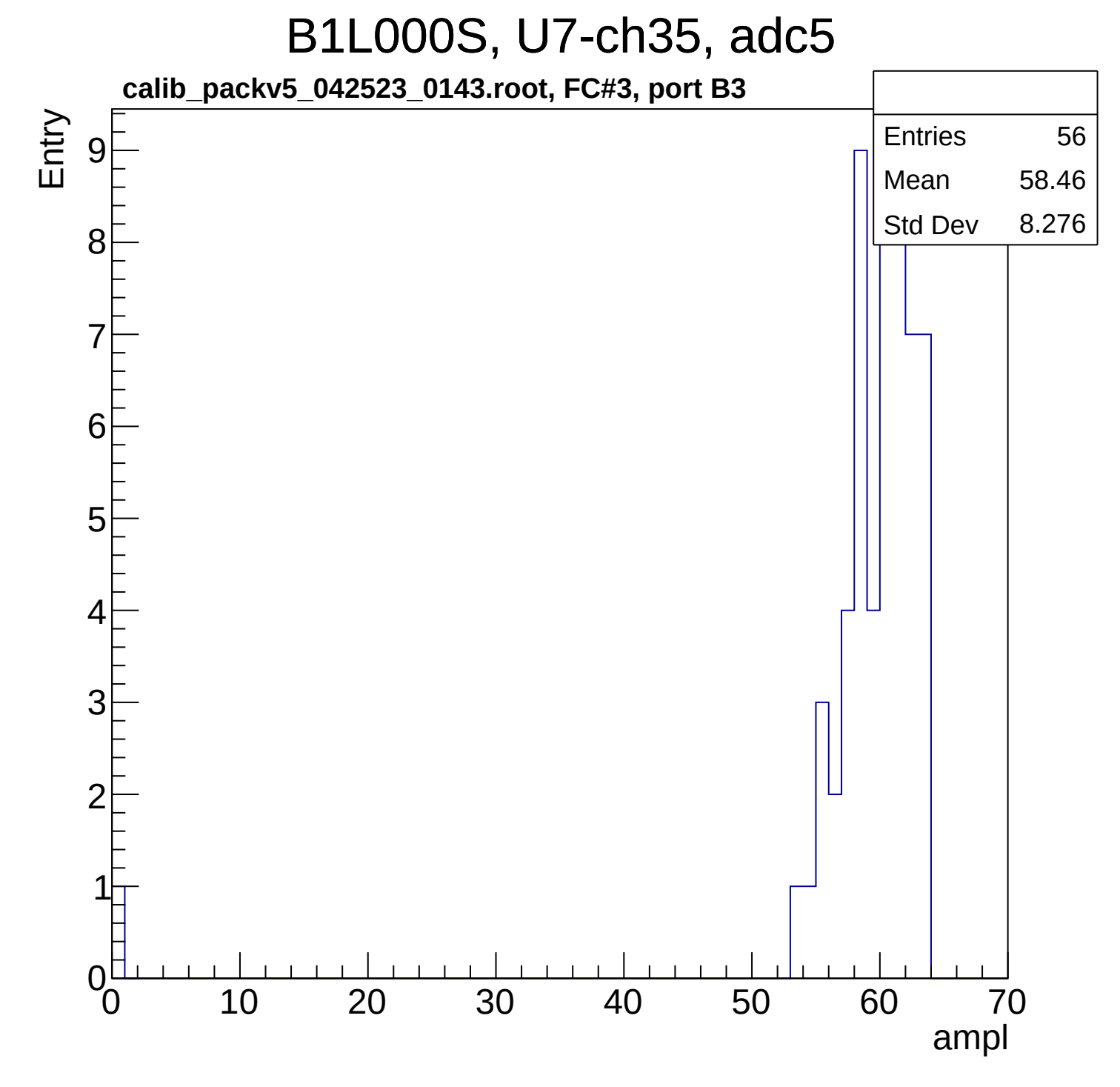
B1L000S, U7-ch35, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 58.46
Std Dev 8.276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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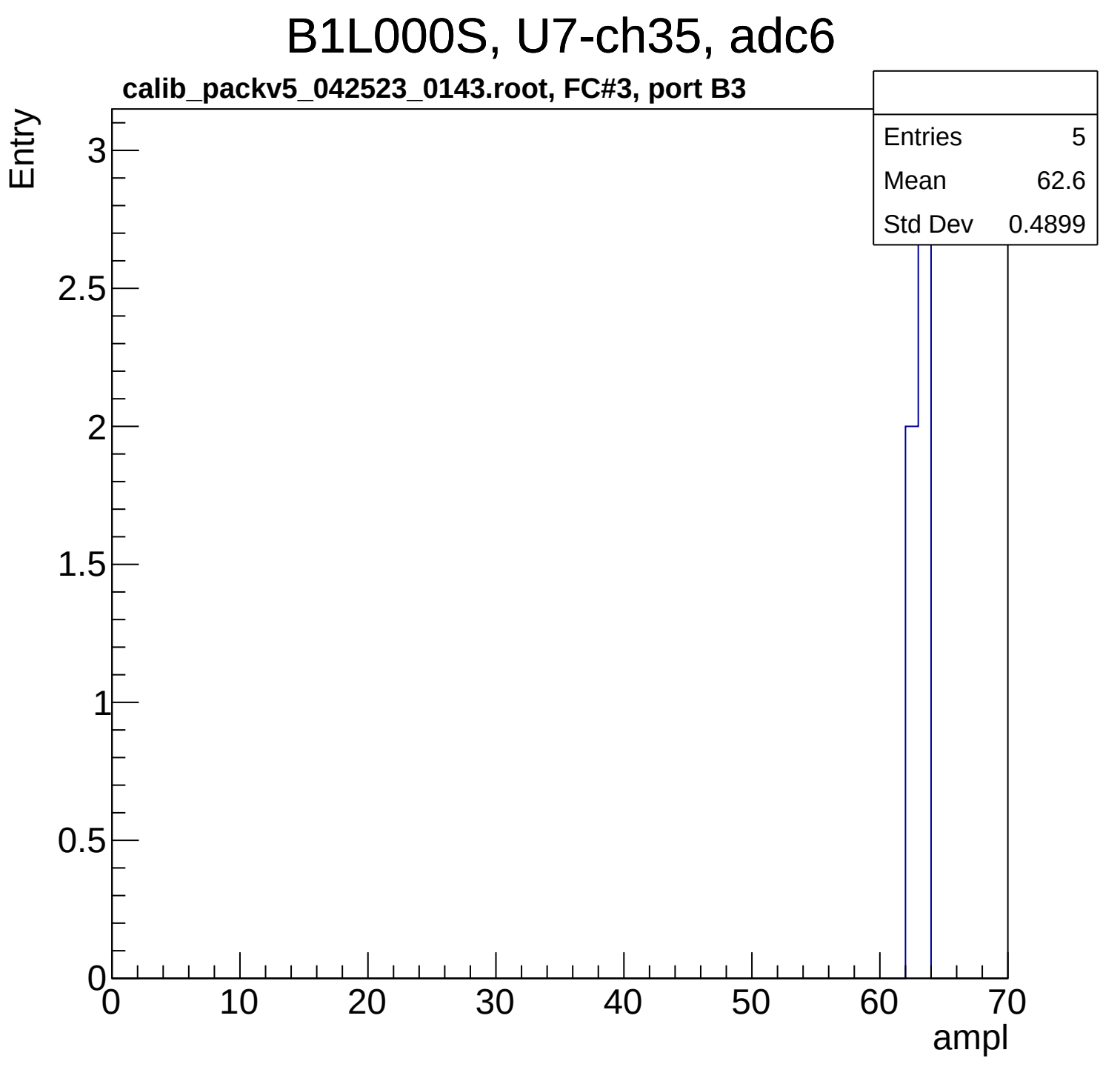
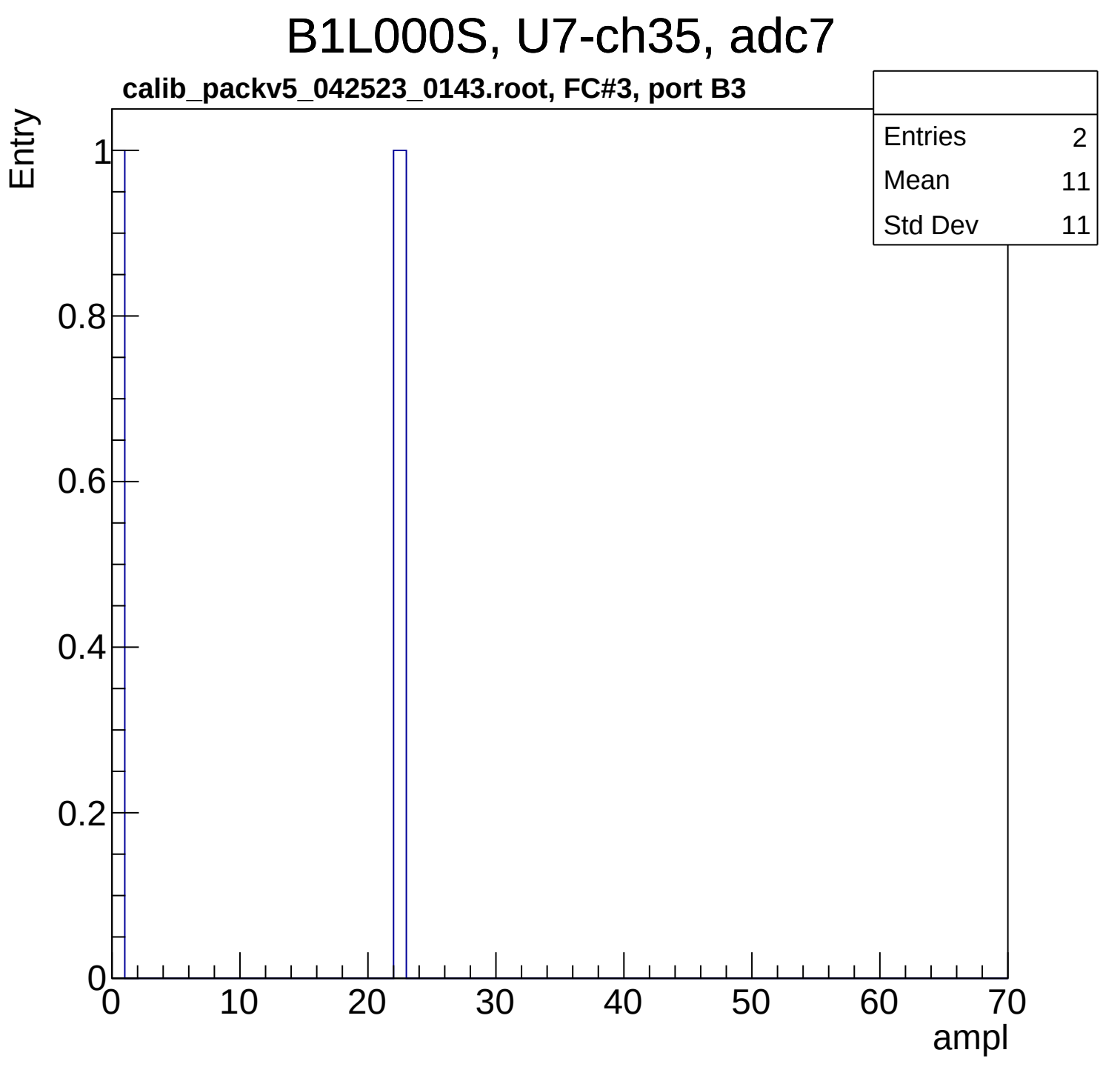
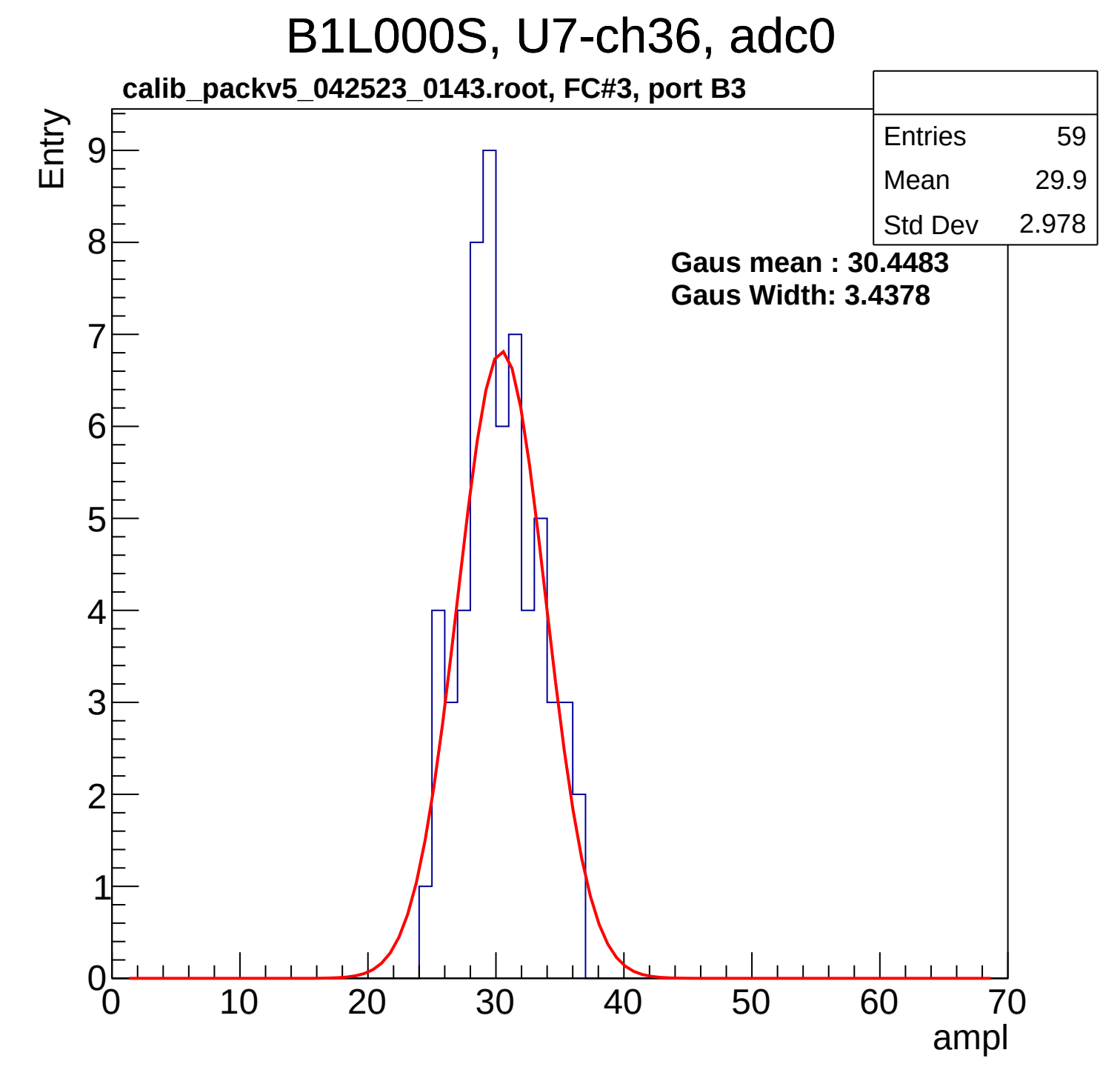
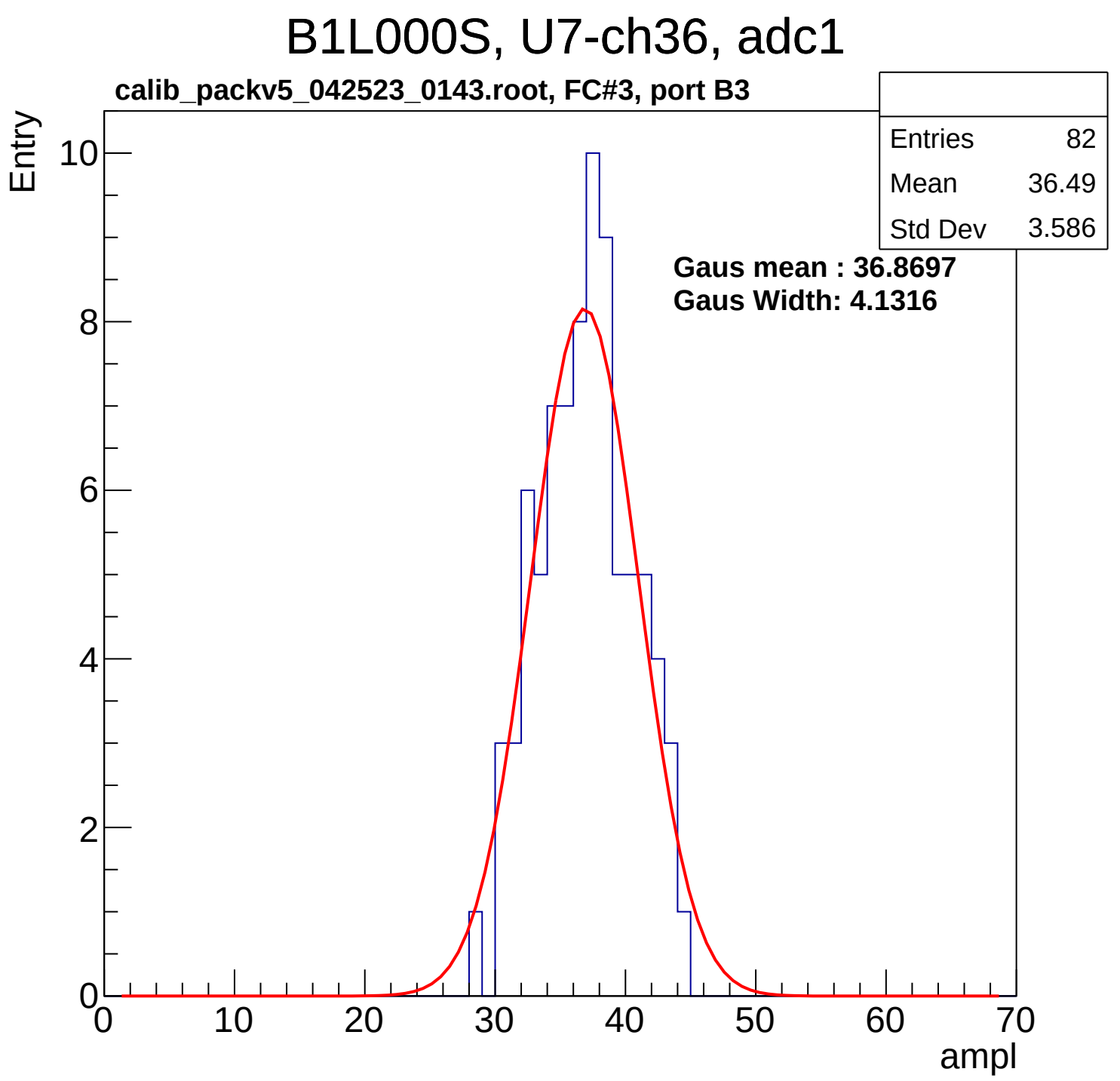

B1L000S, U7-ch35, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch35, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch36, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 29.9
Std Dev 2.978
Gaus mean : 30.4483
Gaus Width: 3.4378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch36, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 36.49
Std Dev 3.586
Gaus mean : 36.8697
Gaus Width: 4.1316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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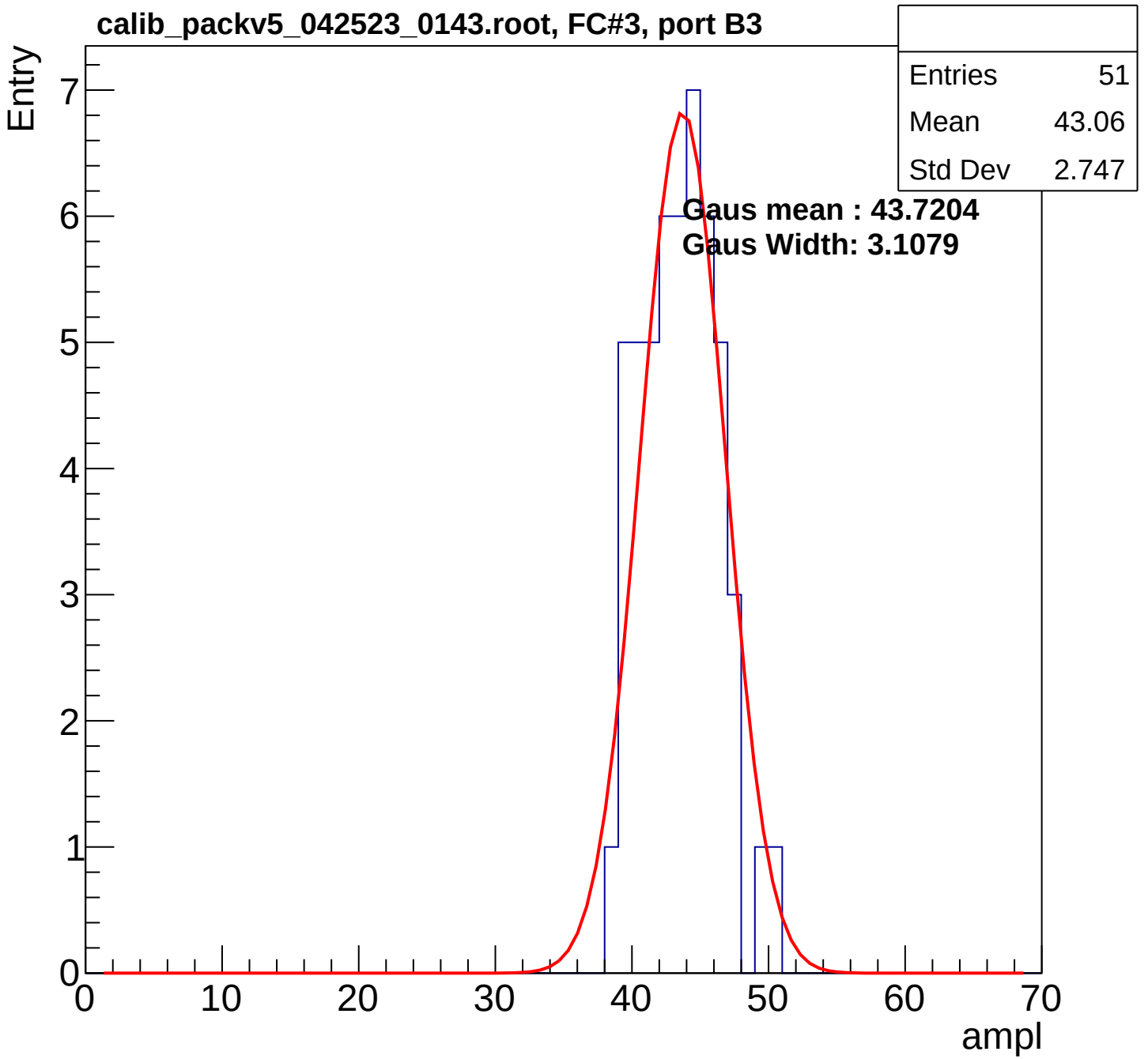
B1L000S, U7-ch36, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 43.06
Std Dev 2.747
Gaus mean : 43.7204
Gaus Width: 3.1079
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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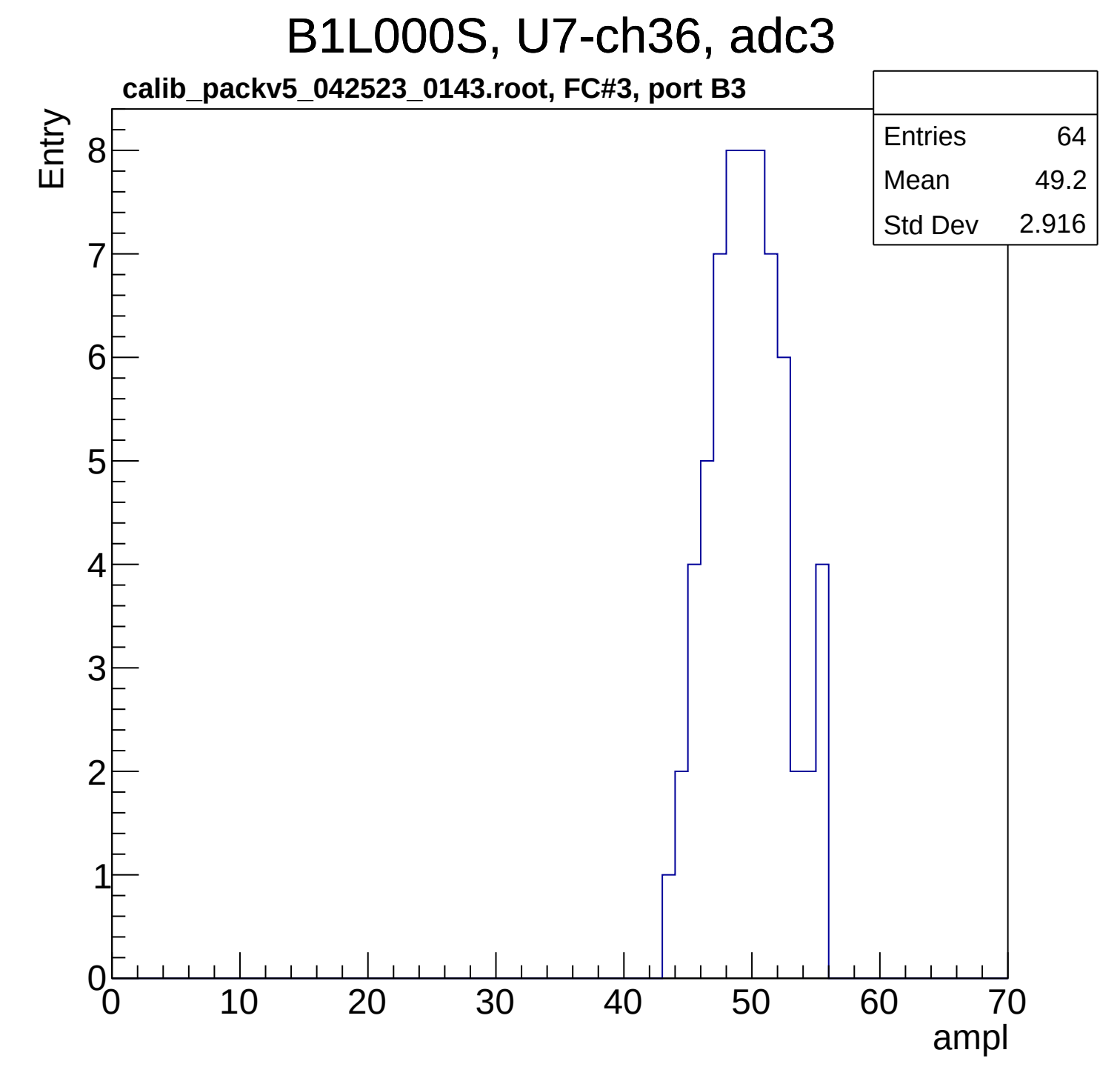
B1L000S, U7-ch36, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 49.2
Std Dev 2.916
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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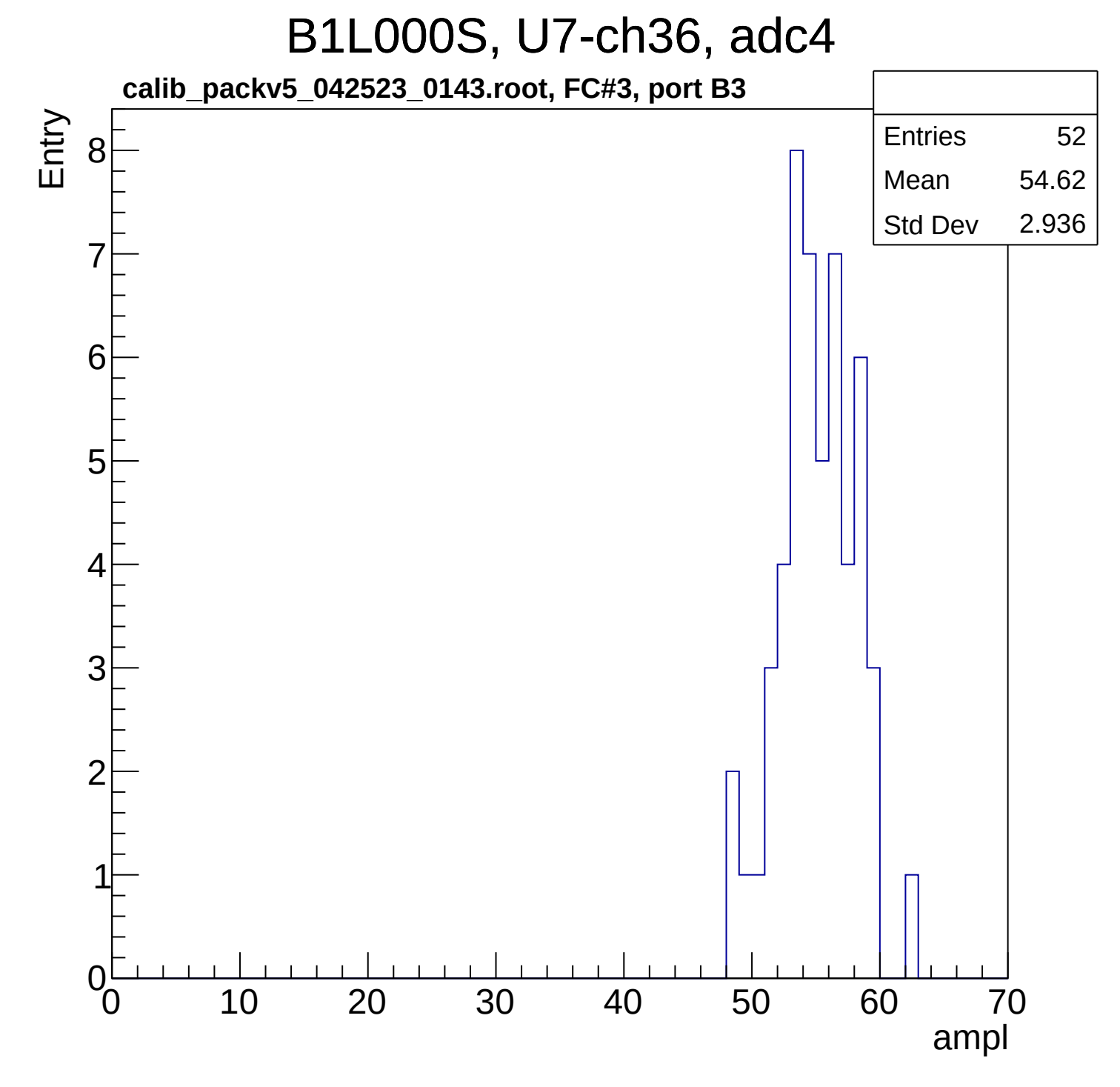
40
50
60
70


B1L000S, U7-ch36, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 54.62
Std Dev 2.936
Entry
ampl
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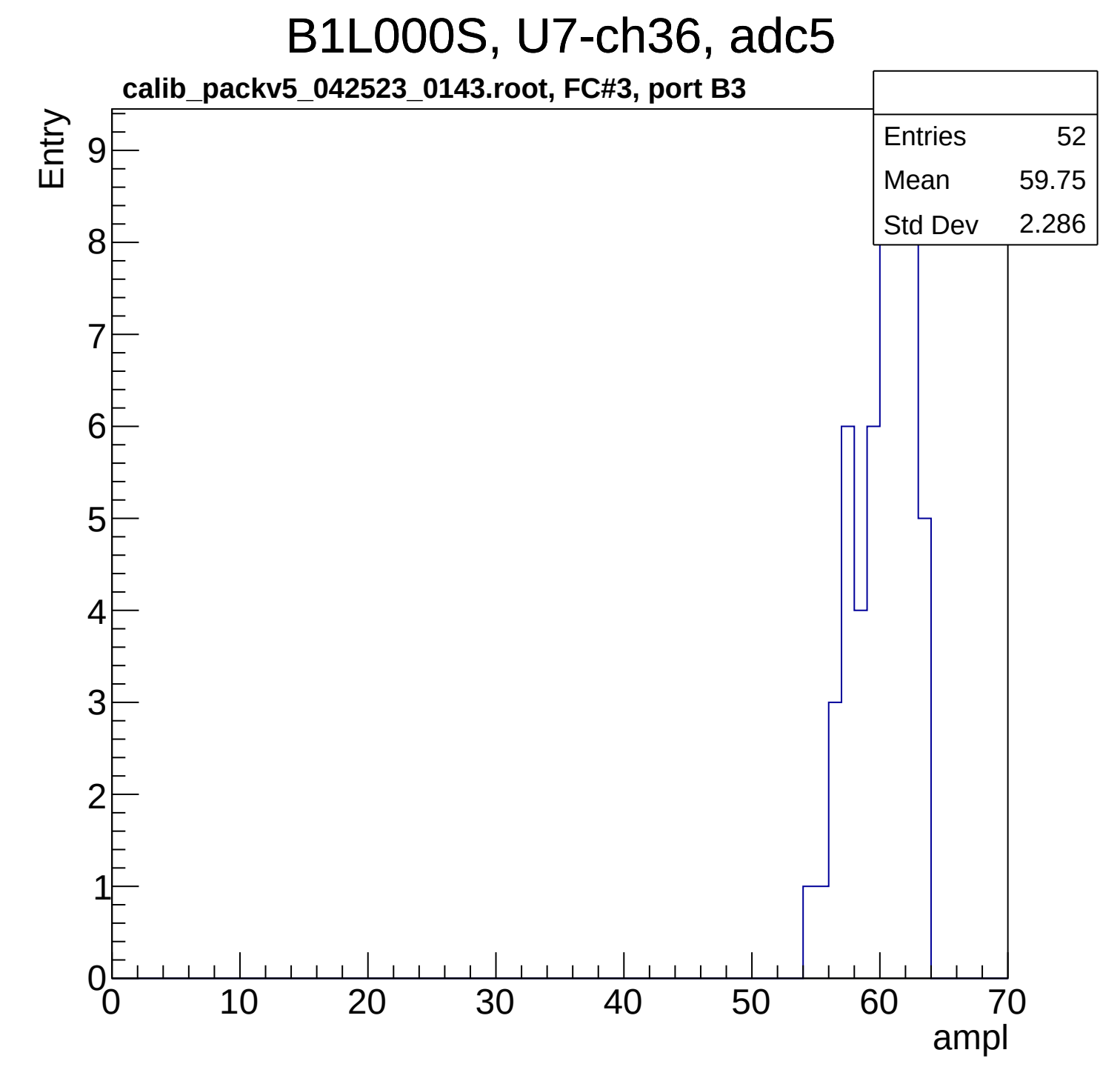
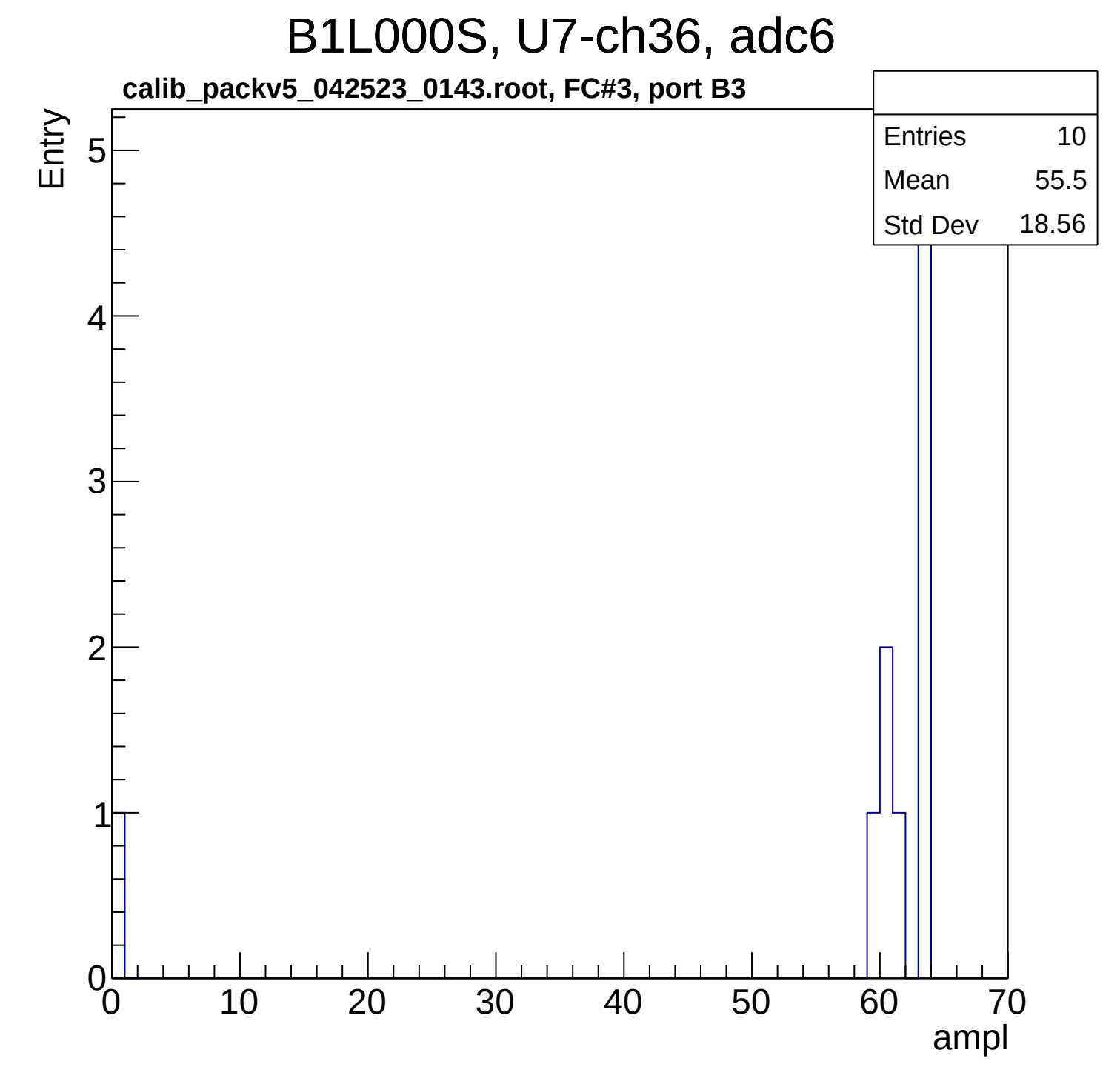
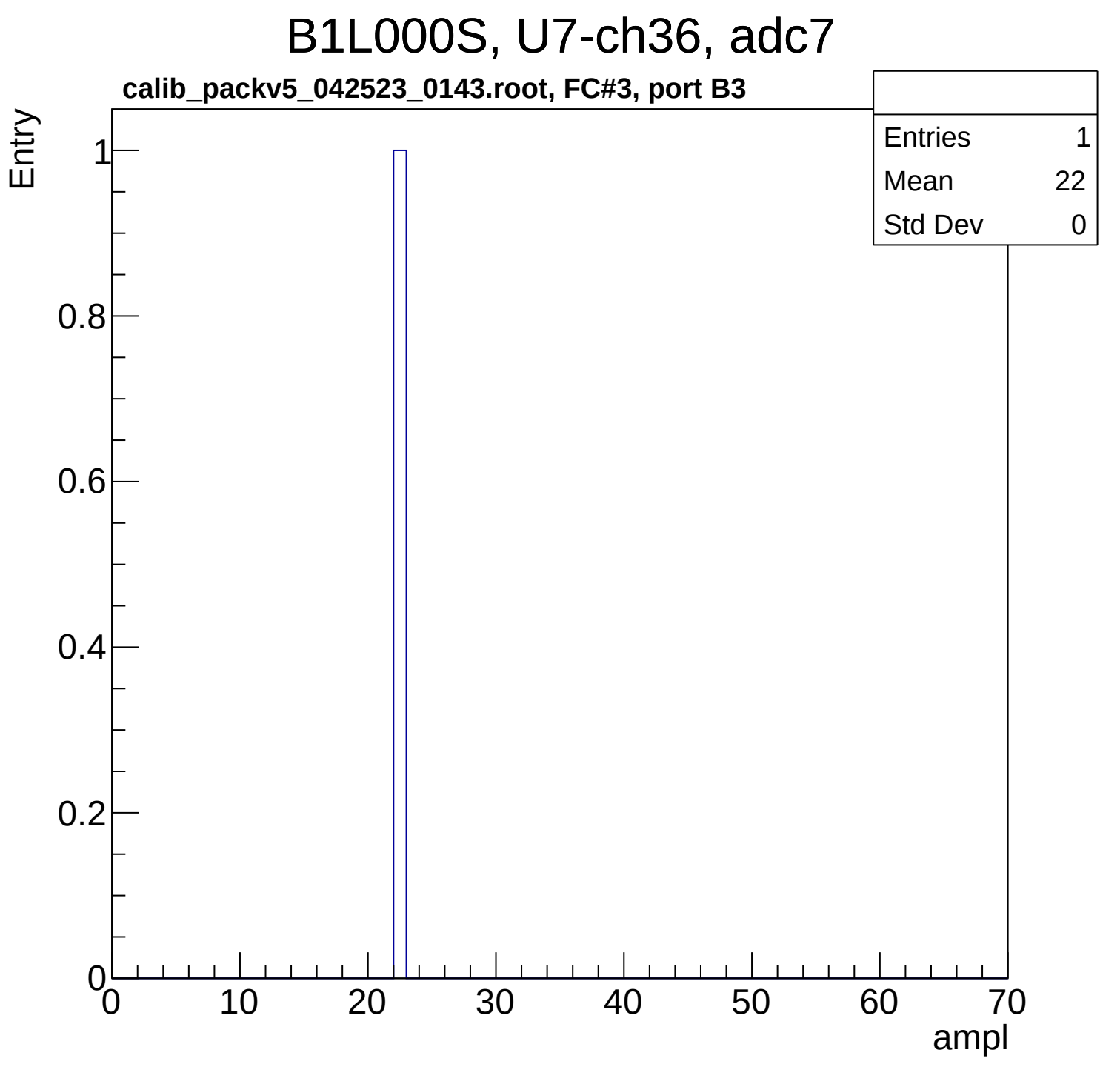
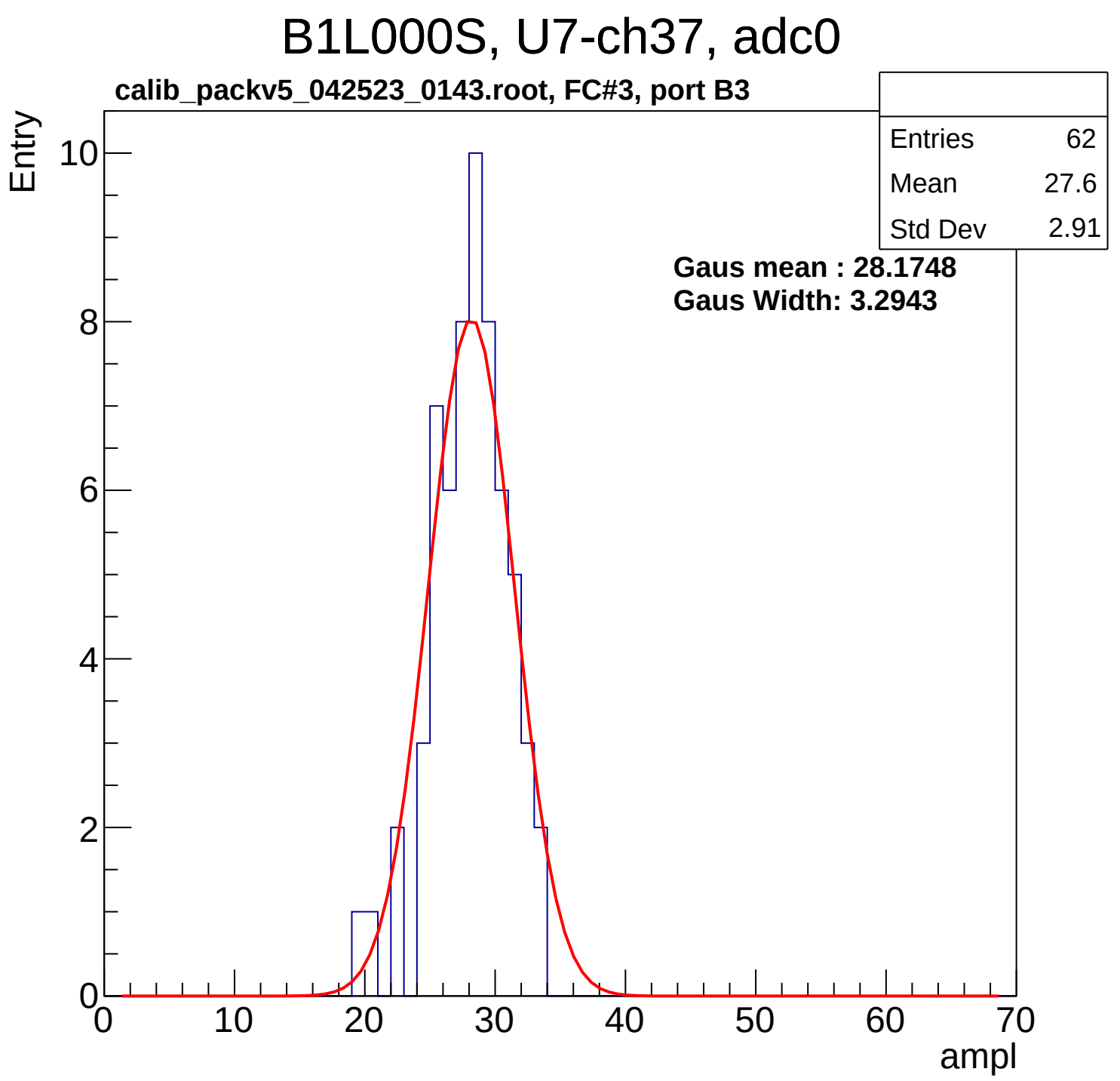
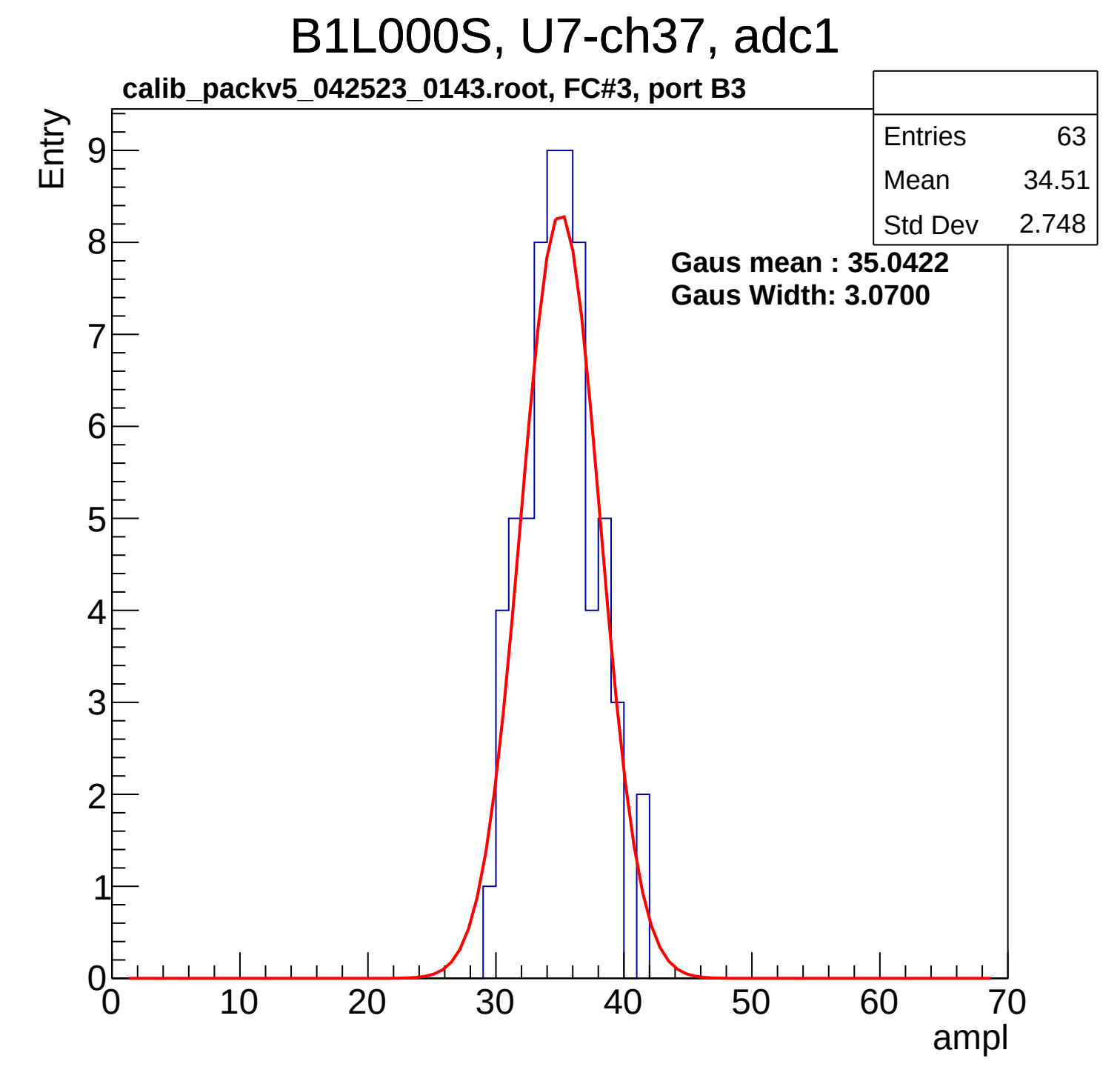
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

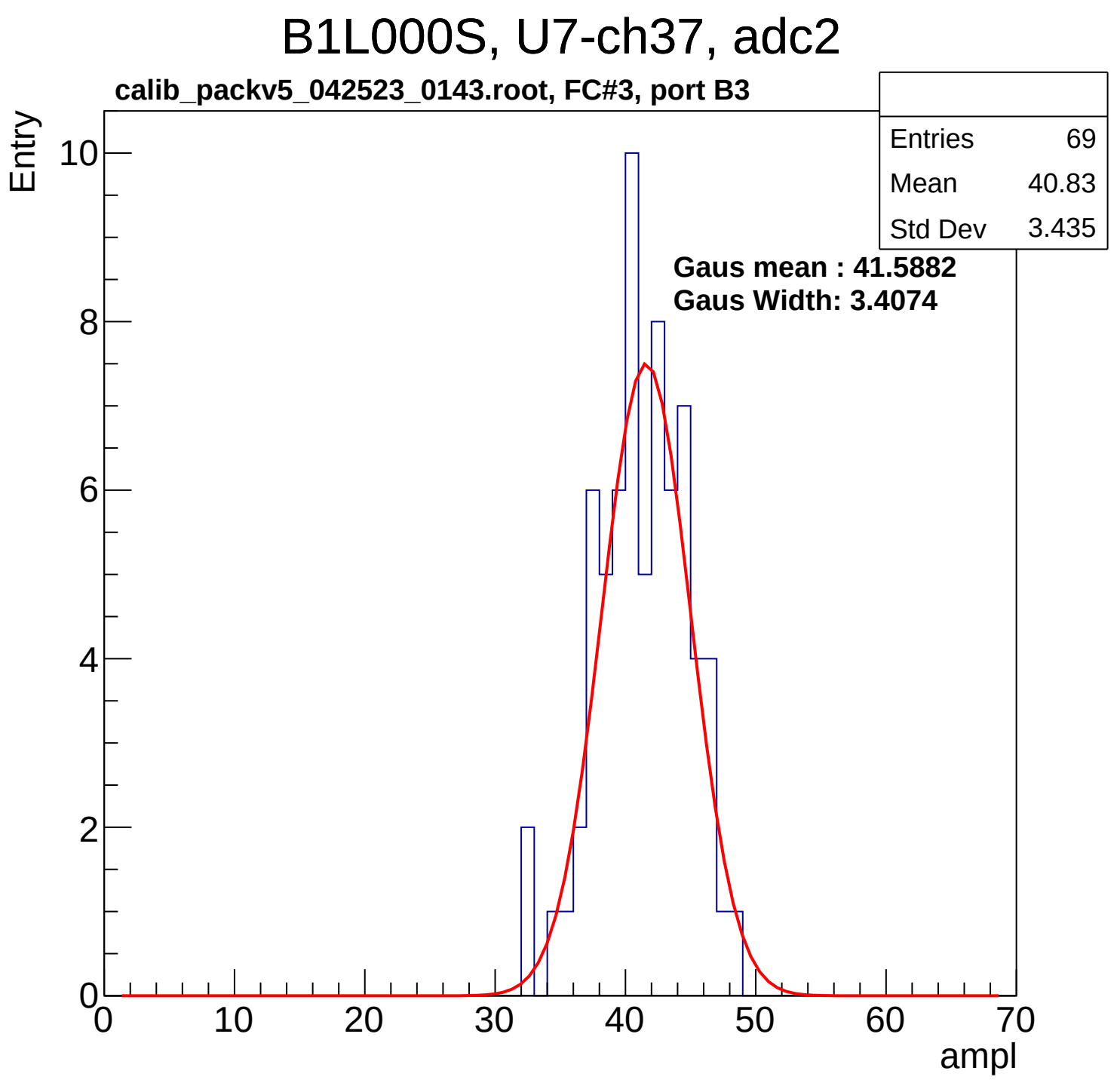

B1L000S, U7-ch36, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 59.75
Std Dev 2.286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch36, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.5
Std Dev 18.56
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch36, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch37, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.6
Std Dev 2.91
Gaus mean : 28.1748
Gaus Width: 3.2943
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch37, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.51
Std Dev 2.748
Gaus mean : 35.0422
Gaus Width: 3.0700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch37, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 40.83
Std Dev 3.435
Gaus mean : 41.5882
Gaus Width: 3.4074
Entry
ampl
0
2
4
6
8
10
0
10
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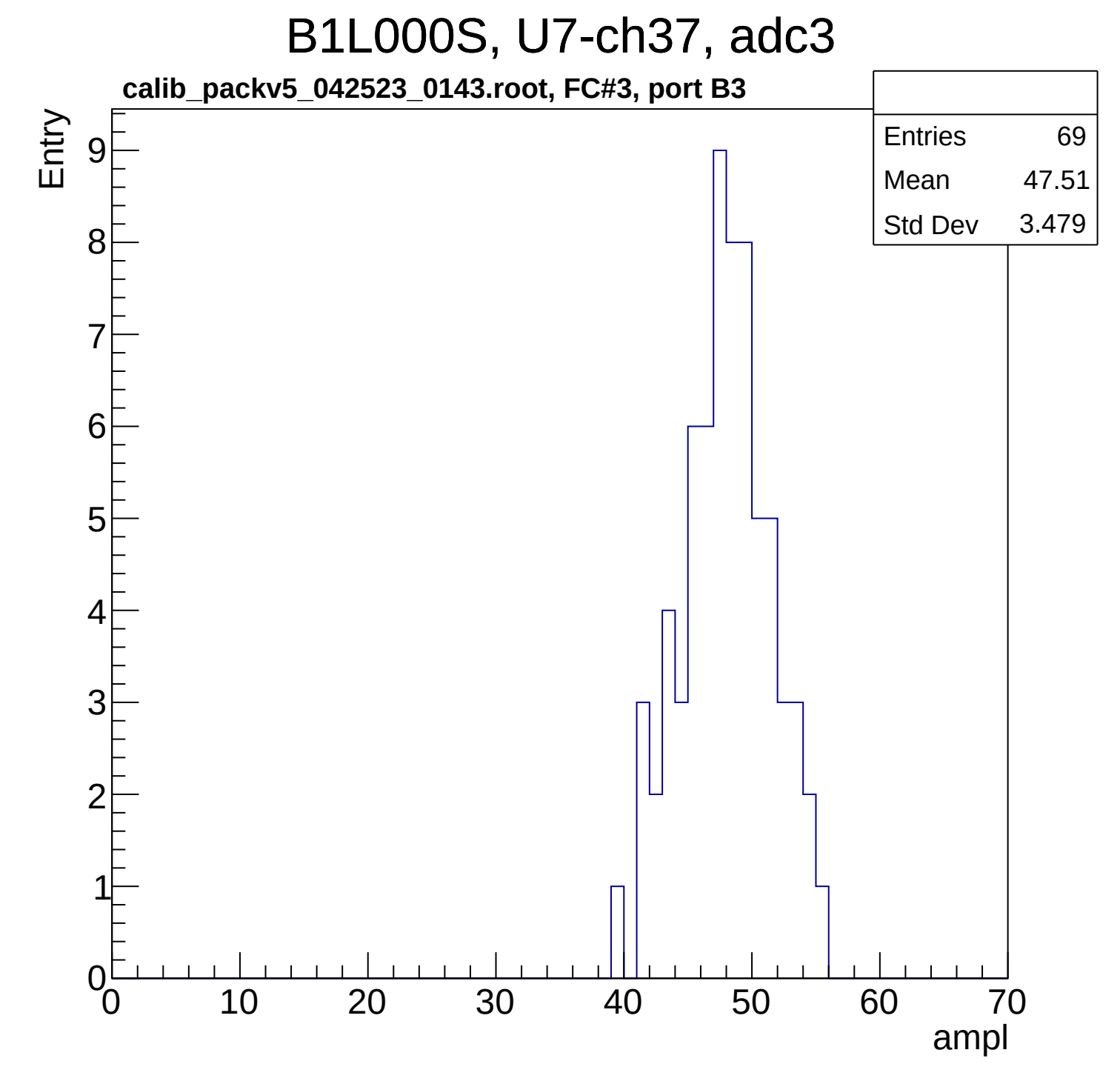
40
50
60
70


B1L000S, U7-ch37, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 47.51
Std Dev 3.479
Entry
ampl
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0
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60
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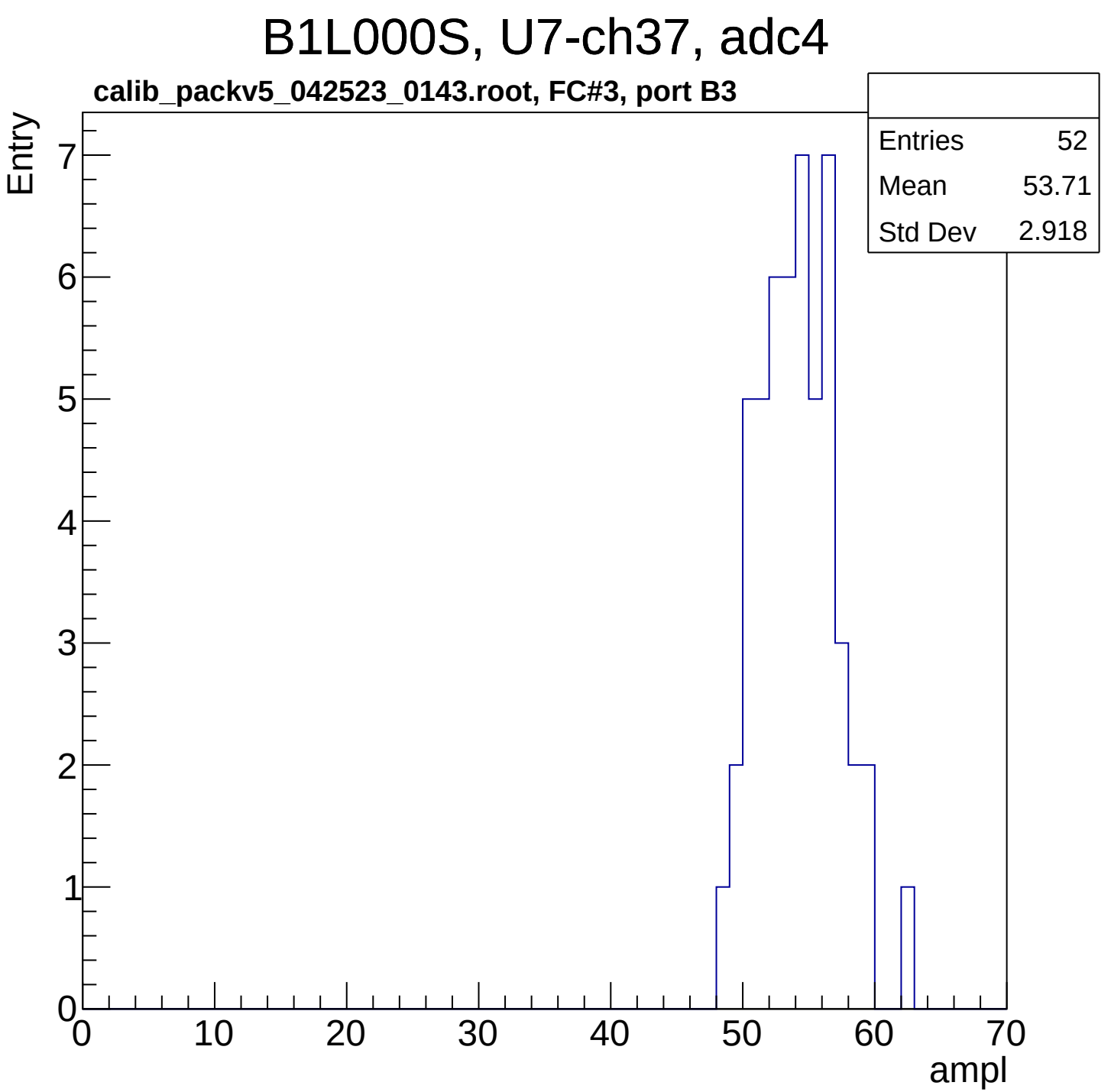
B1L000S, U7-ch37, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 53.71
Std Dev 2.918
Entry
ampl
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1
2
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4
5
6
7
0
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50
60
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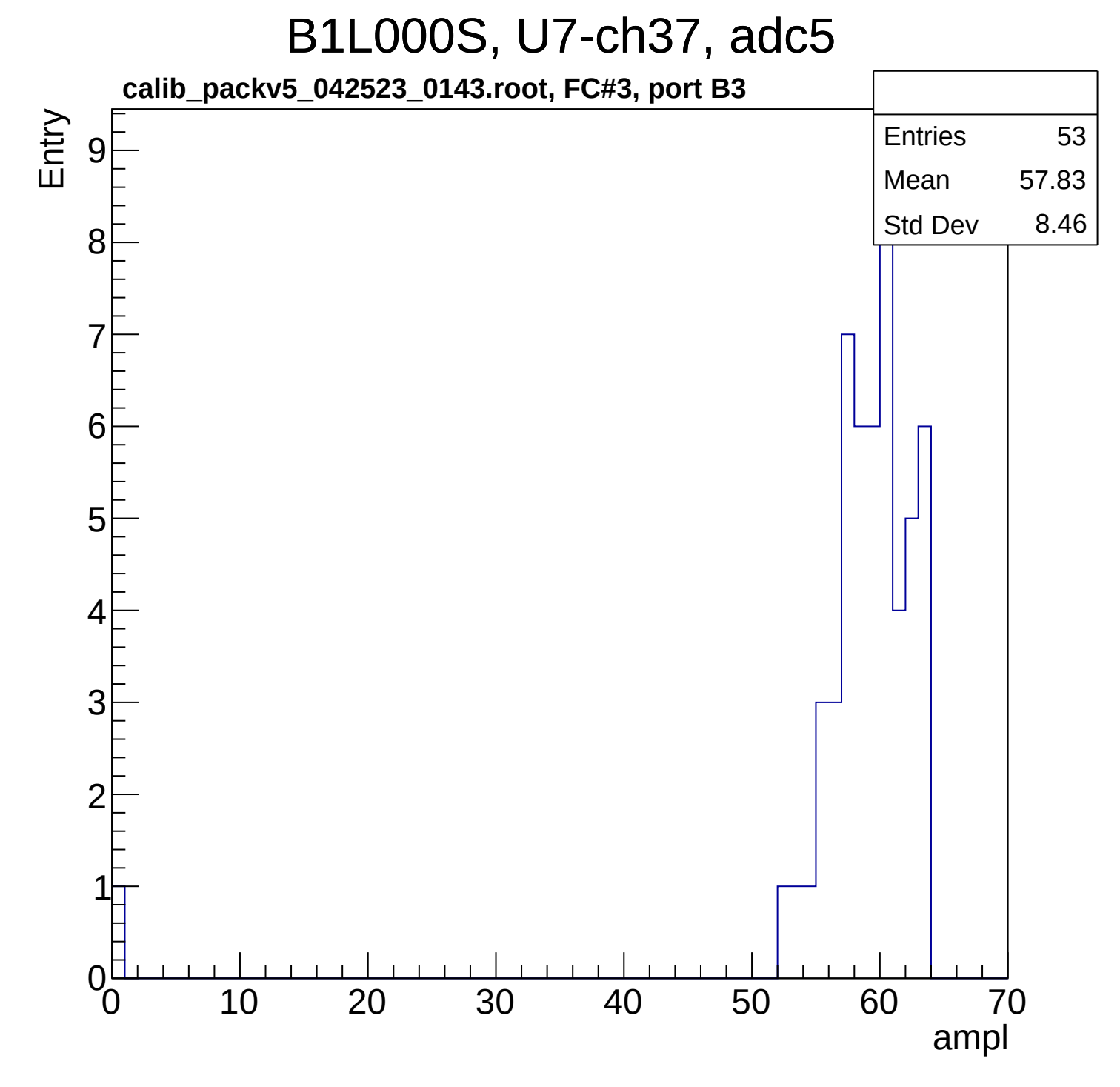
B1L000S, U7-ch37, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 57.83
Std Dev 8.46
Entry
ampl
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2
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4
5
6
7
8
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0
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60
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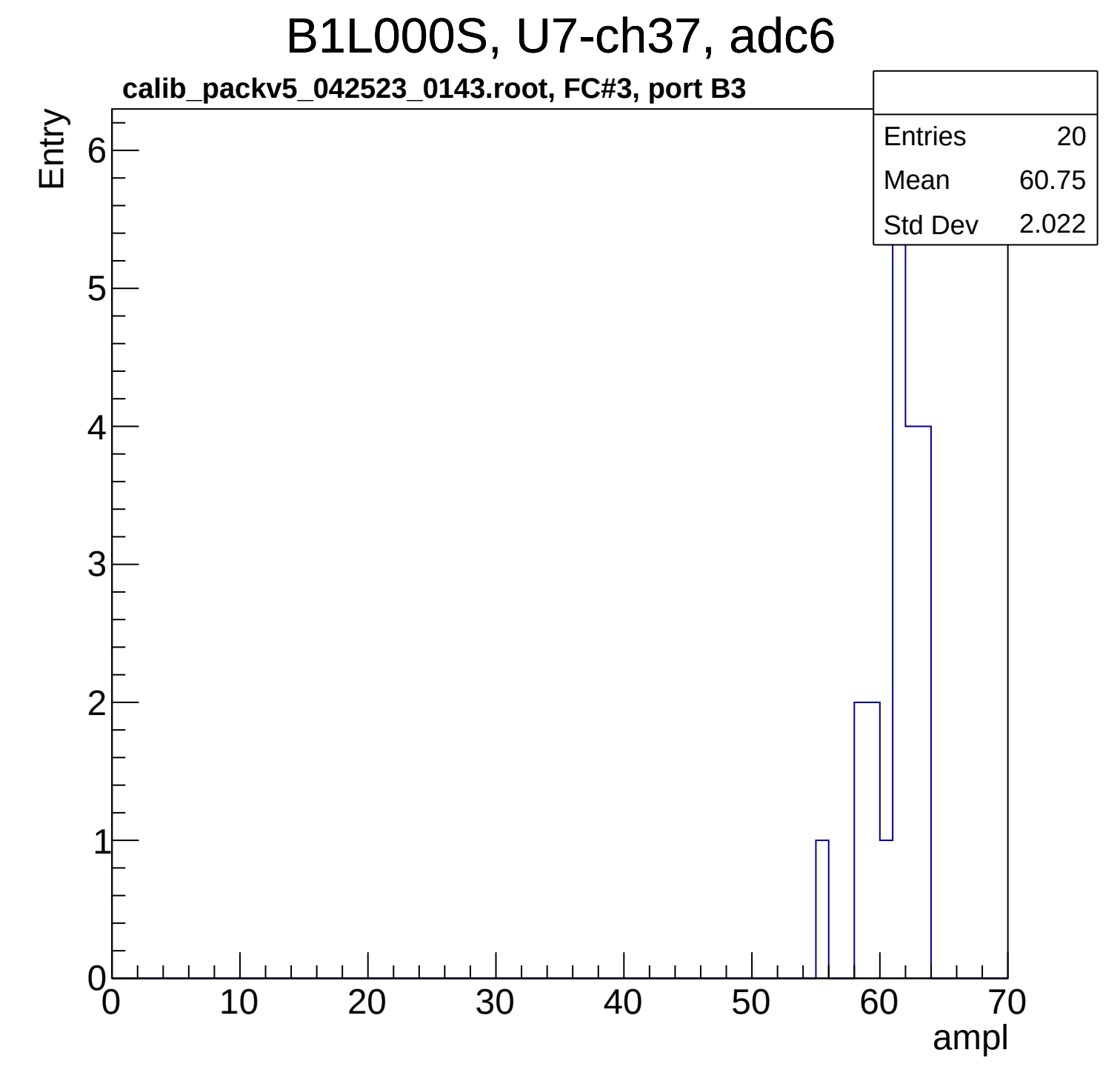
B1L000S, U7-ch37, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 60.75
Std Dev 2.022
Entry
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2
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4
5
6
0
10
20
30
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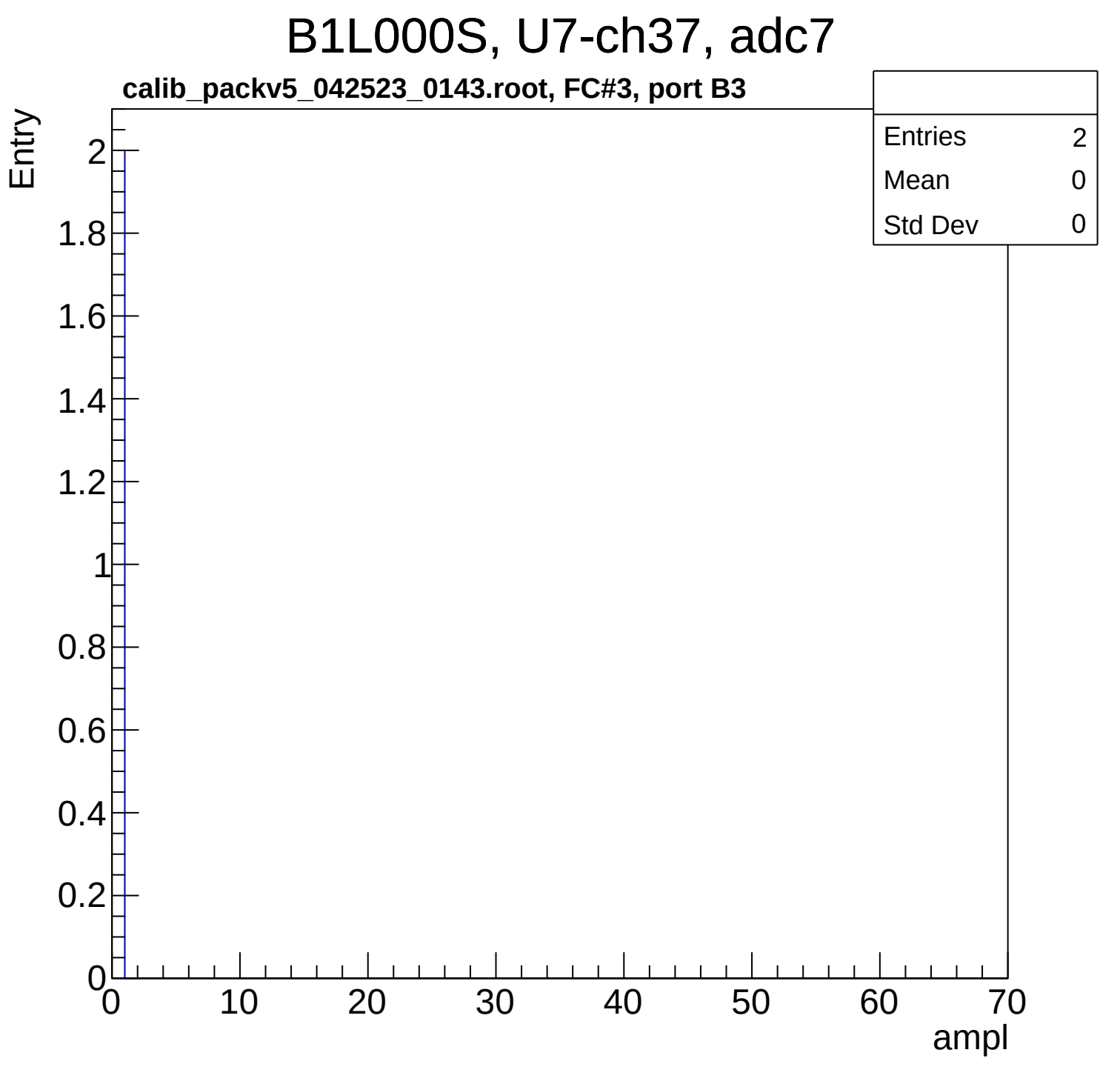
50
60
70
ampl


B1L000S, U7-ch37, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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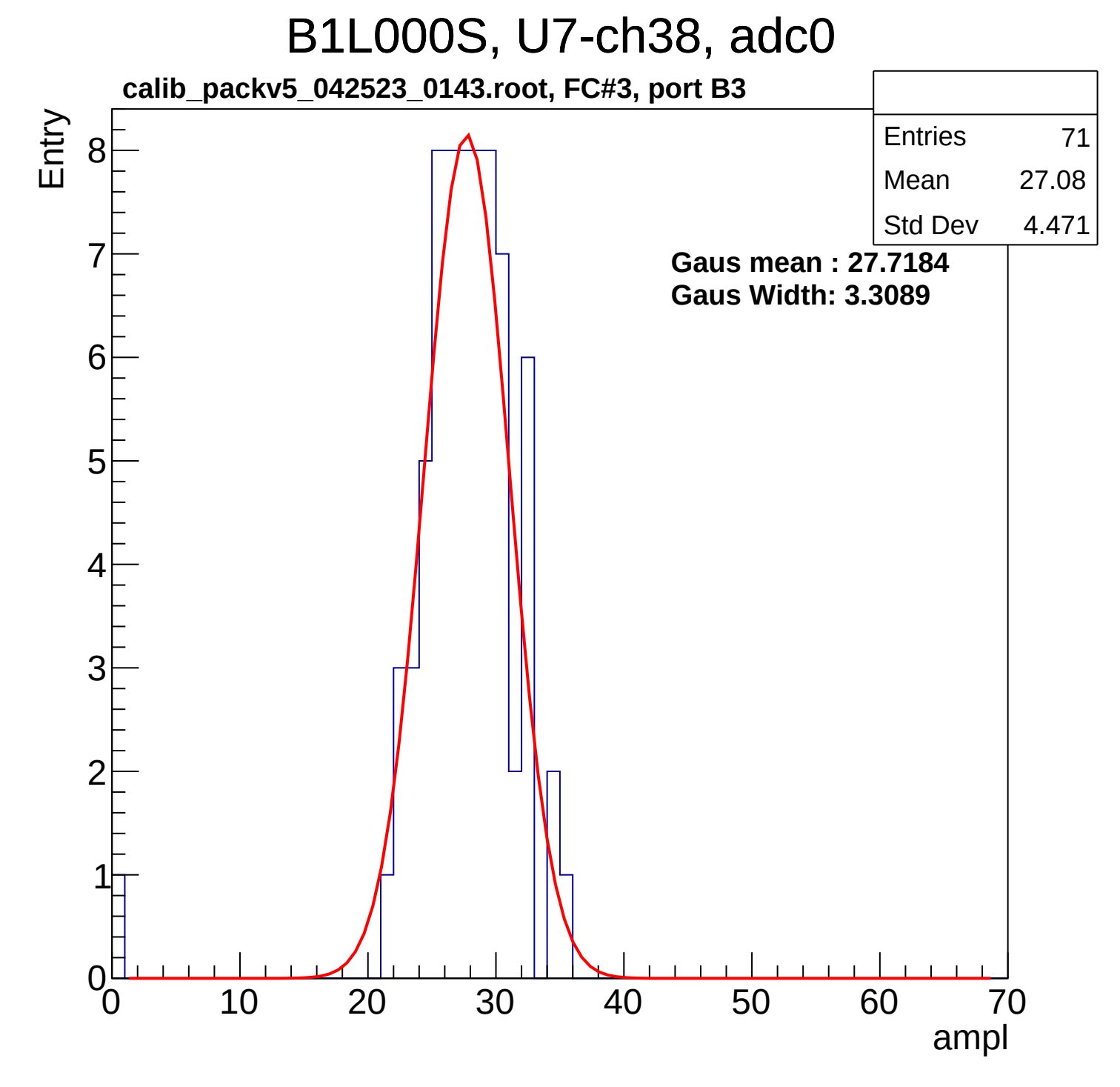
50
60
70
ampl


B1L000S, U7-ch38, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.08
Std Dev 4.471
Gaus mean : 27.7184
Gaus Width: 3.3089
Entry
ampl
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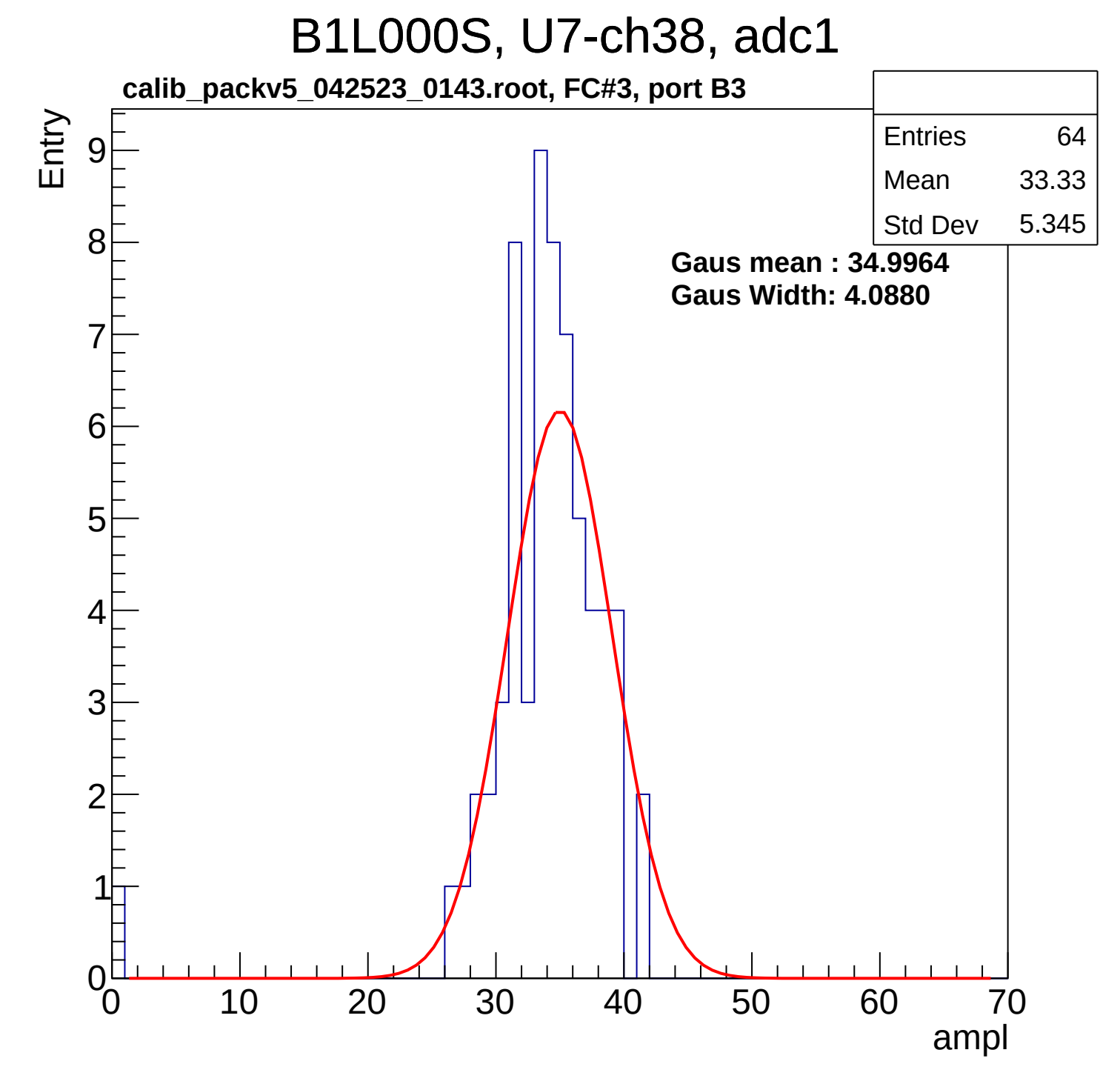
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch38, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 33.33
Std Dev 5.345
Gaus mean : 34.9964
Gaus Width: 4.0880
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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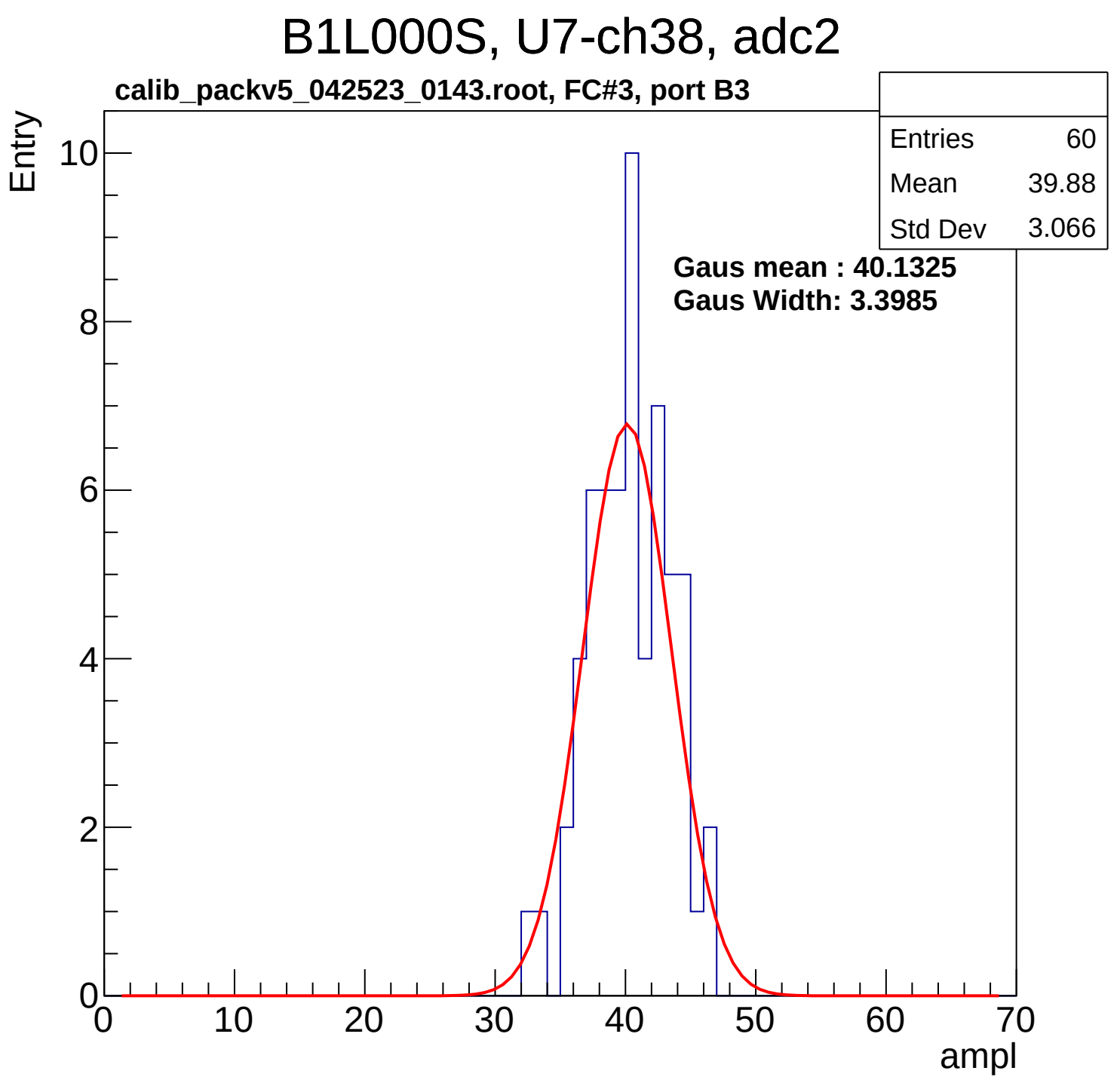
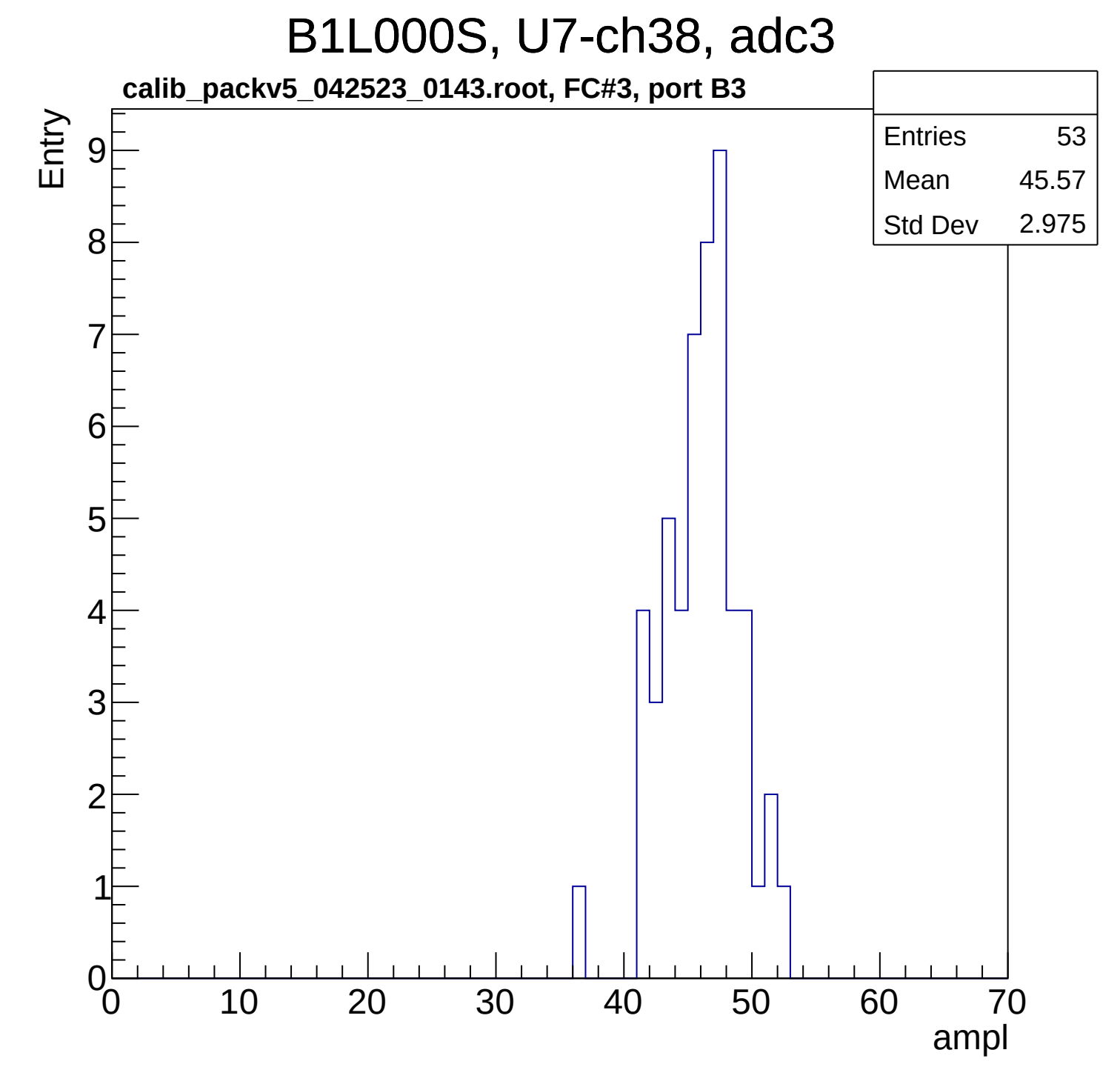

B1L000S, U7-ch38, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 39.88
Std Dev 3.066
Gaus mean : 40.1325
Gaus Width: 3.3985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch38, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 45.57
Std Dev 2.975
Entry
ampl
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3
4
5
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7
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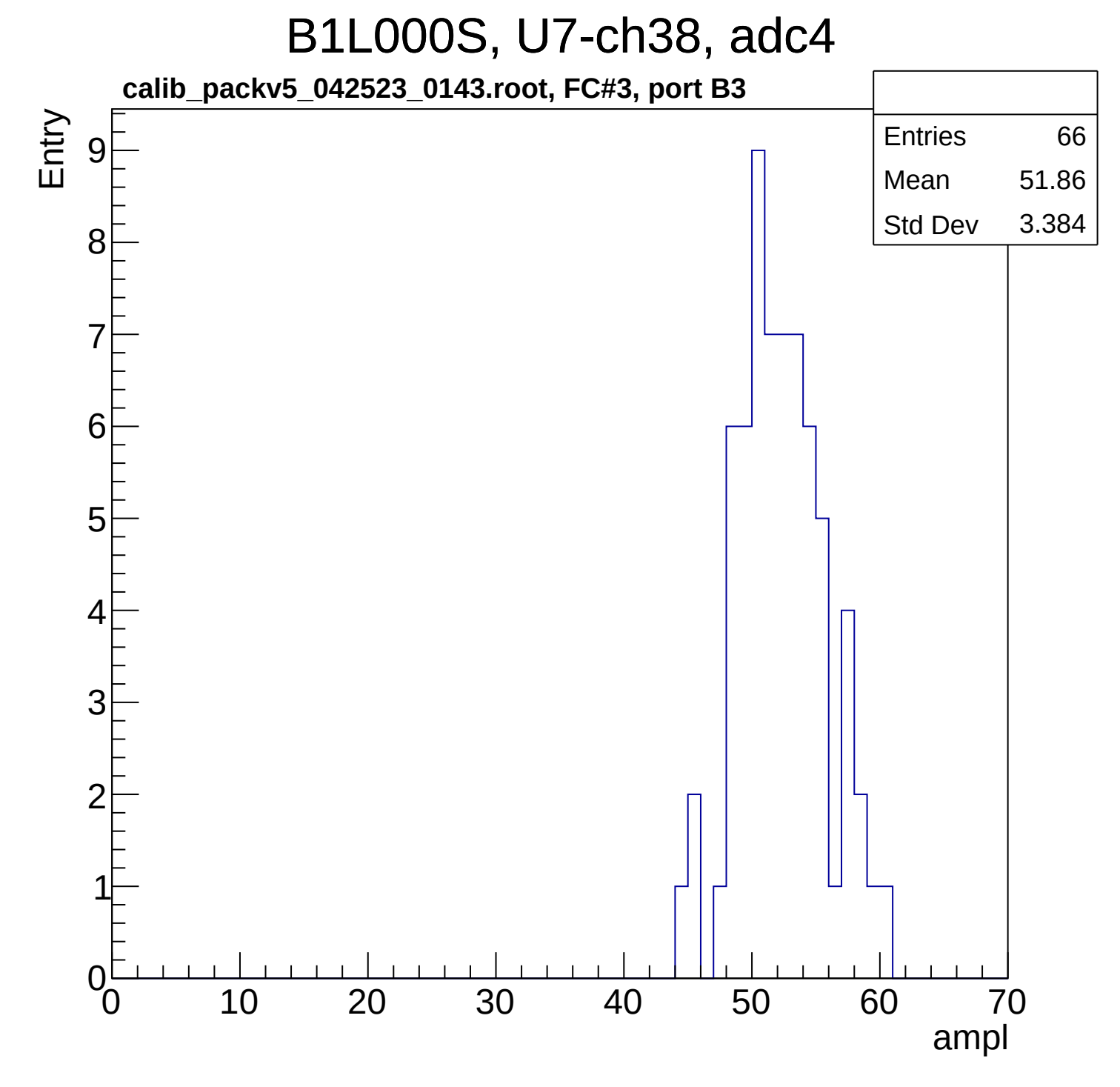
B1L000S, U7-ch38, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 51.86
Std Dev 3.384
Entry
ampl
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2
3
4
5
6
7
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0
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60
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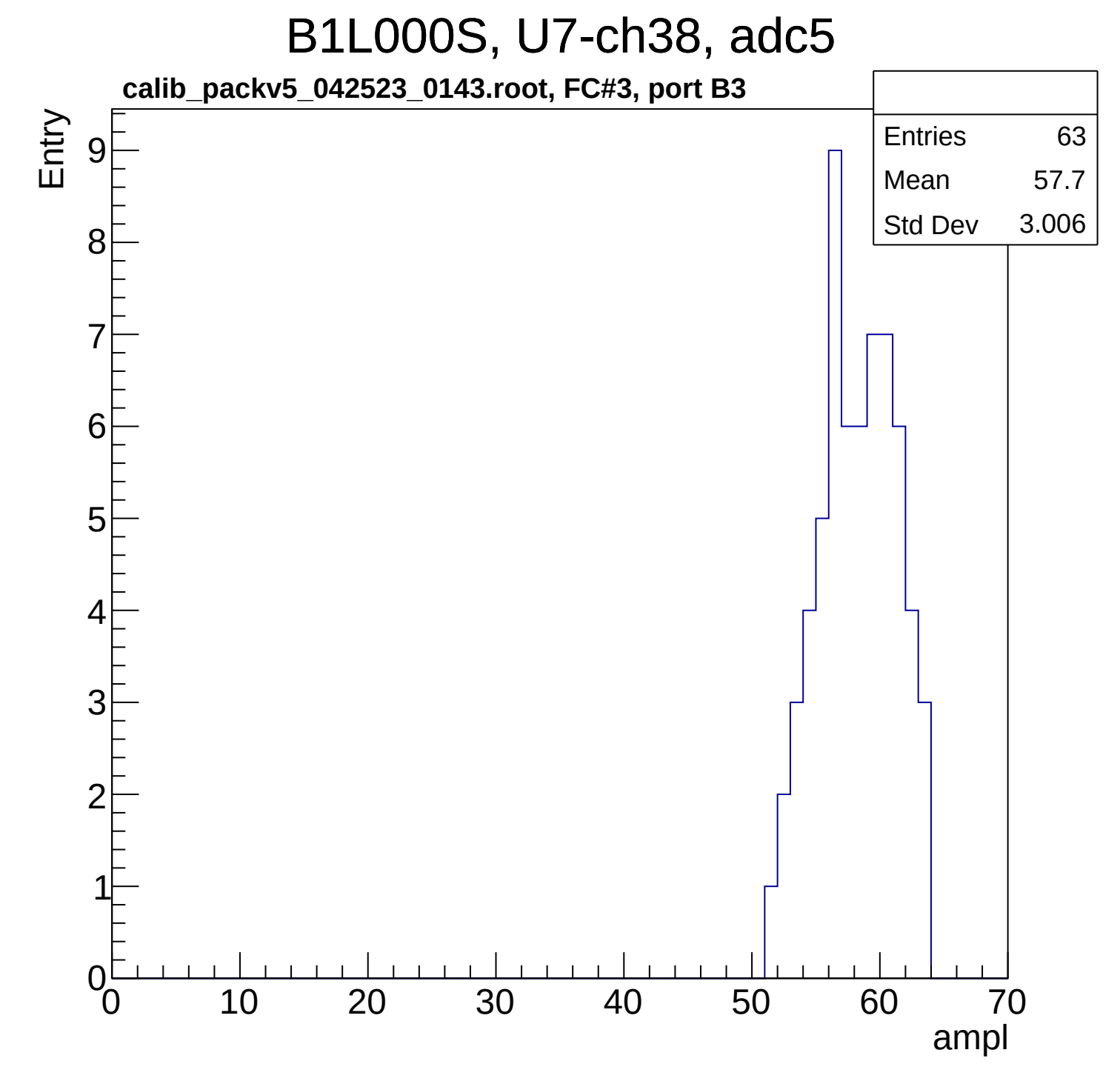
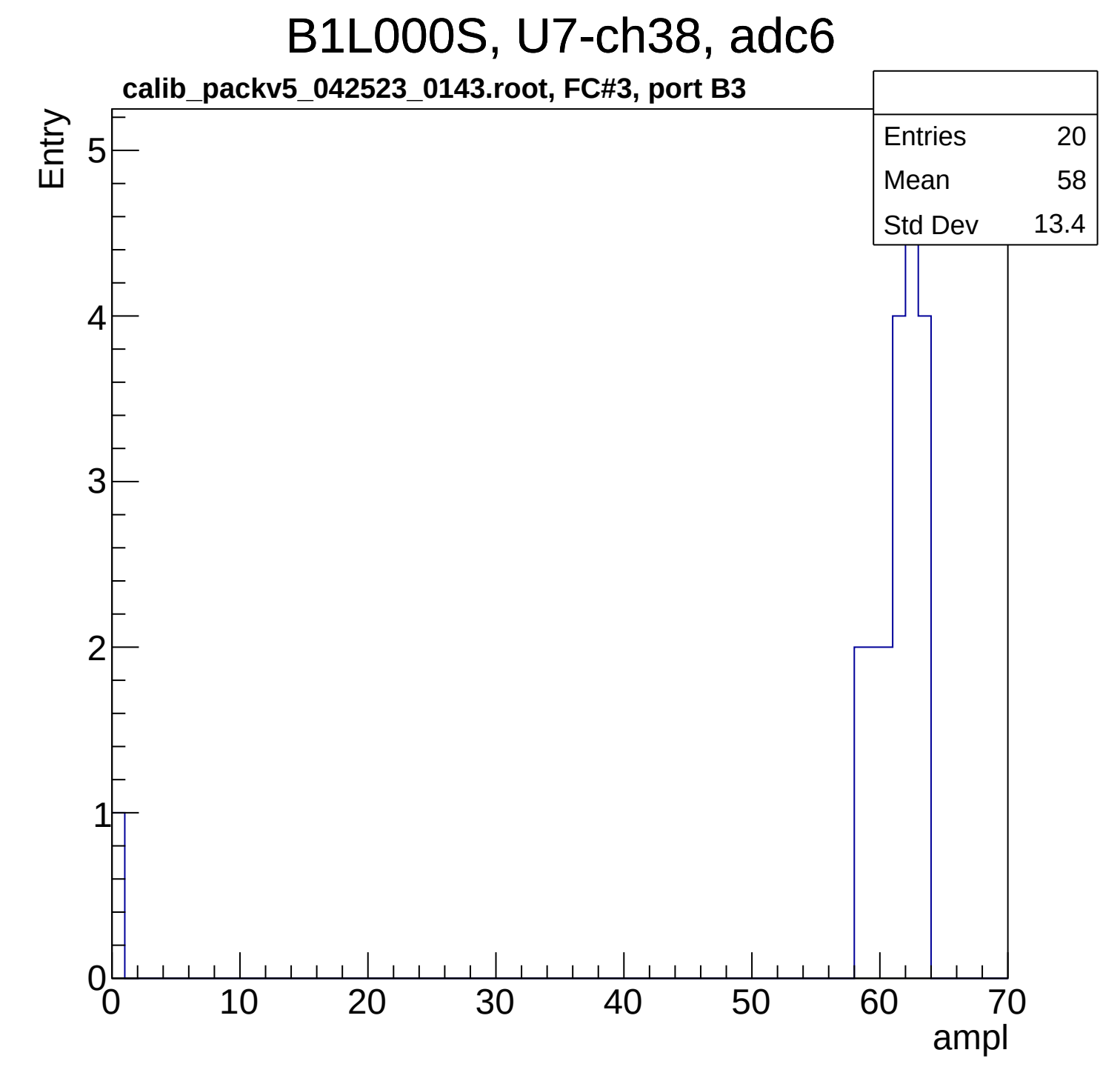

B1L000S, U7-ch38, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 57.7
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch38, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 58
Std Dev 13.4
Entry
0
1
2
3
4
5
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10
20
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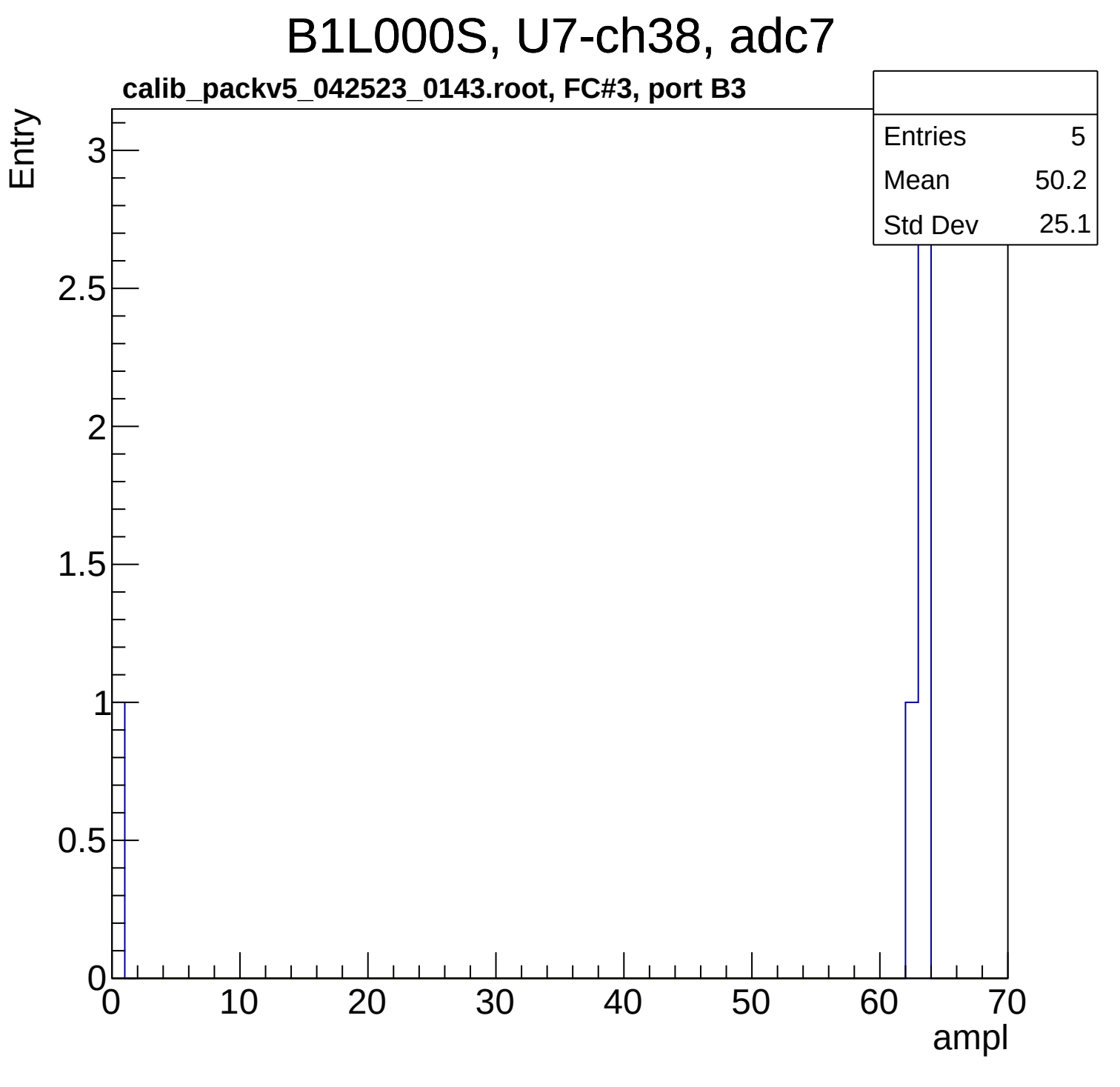
70
ampl


B1L000S, U7-ch38, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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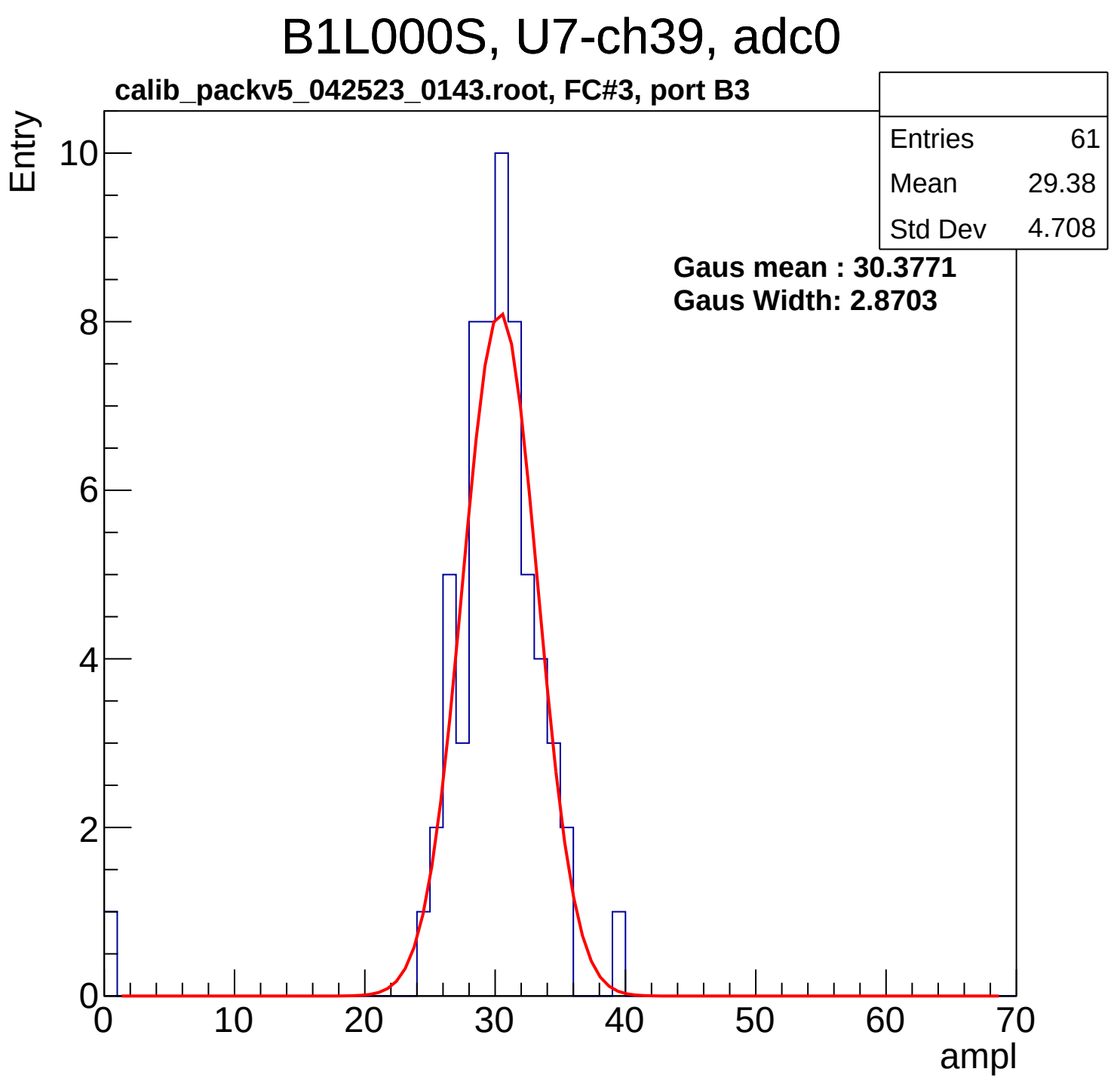
B1L000S, U7-ch39, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 29.38
Std Dev 4.708
Gaus mean : 30.3771
Gaus Width: 2.8703
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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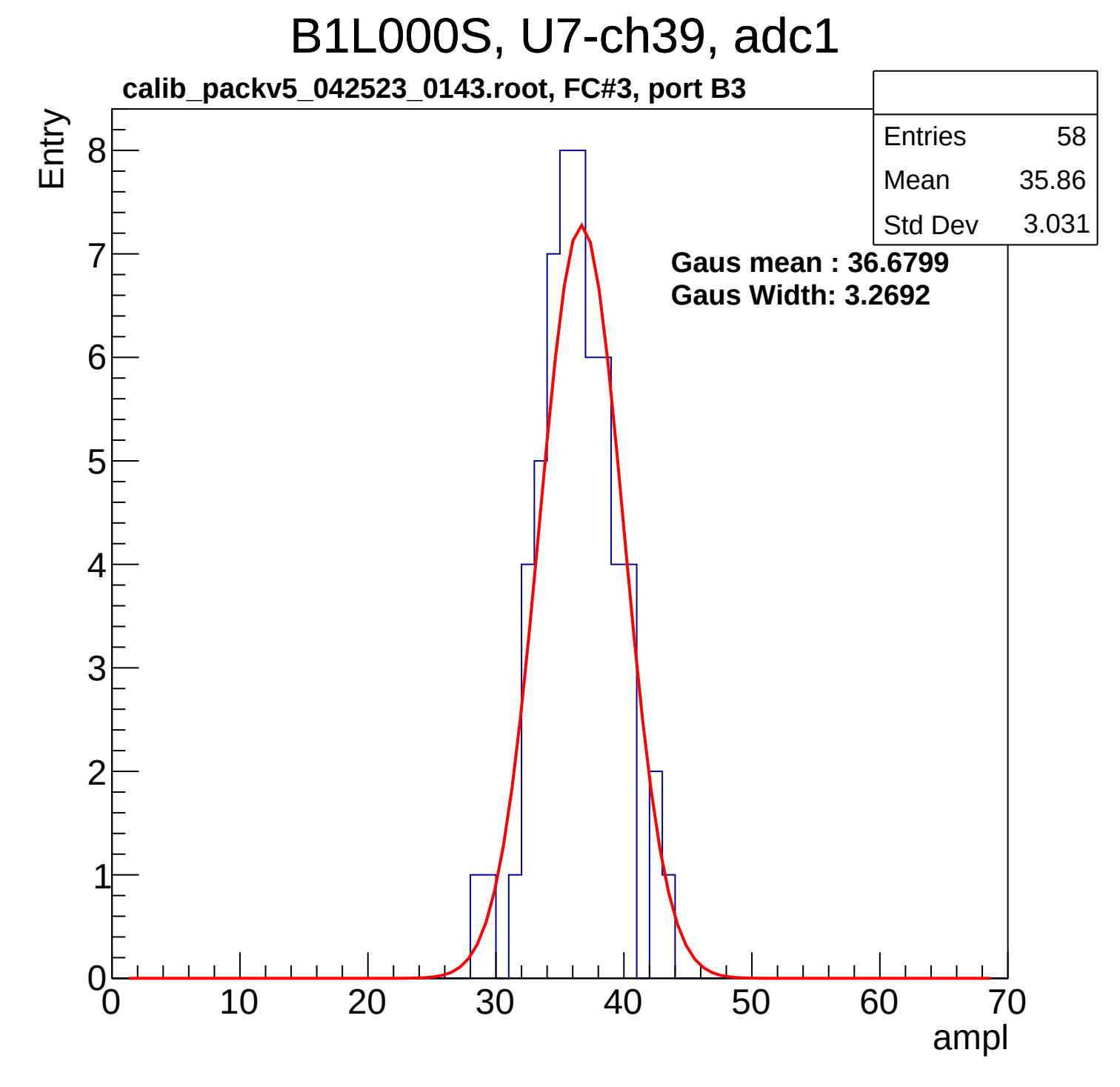
B1L000S, U7-ch39, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 35.86
Std Dev 3.031
Gaus mean : 36.6799
Gaus Width: 3.2692
Entry
ampl
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1
2
3
4
5
6
7
8
0
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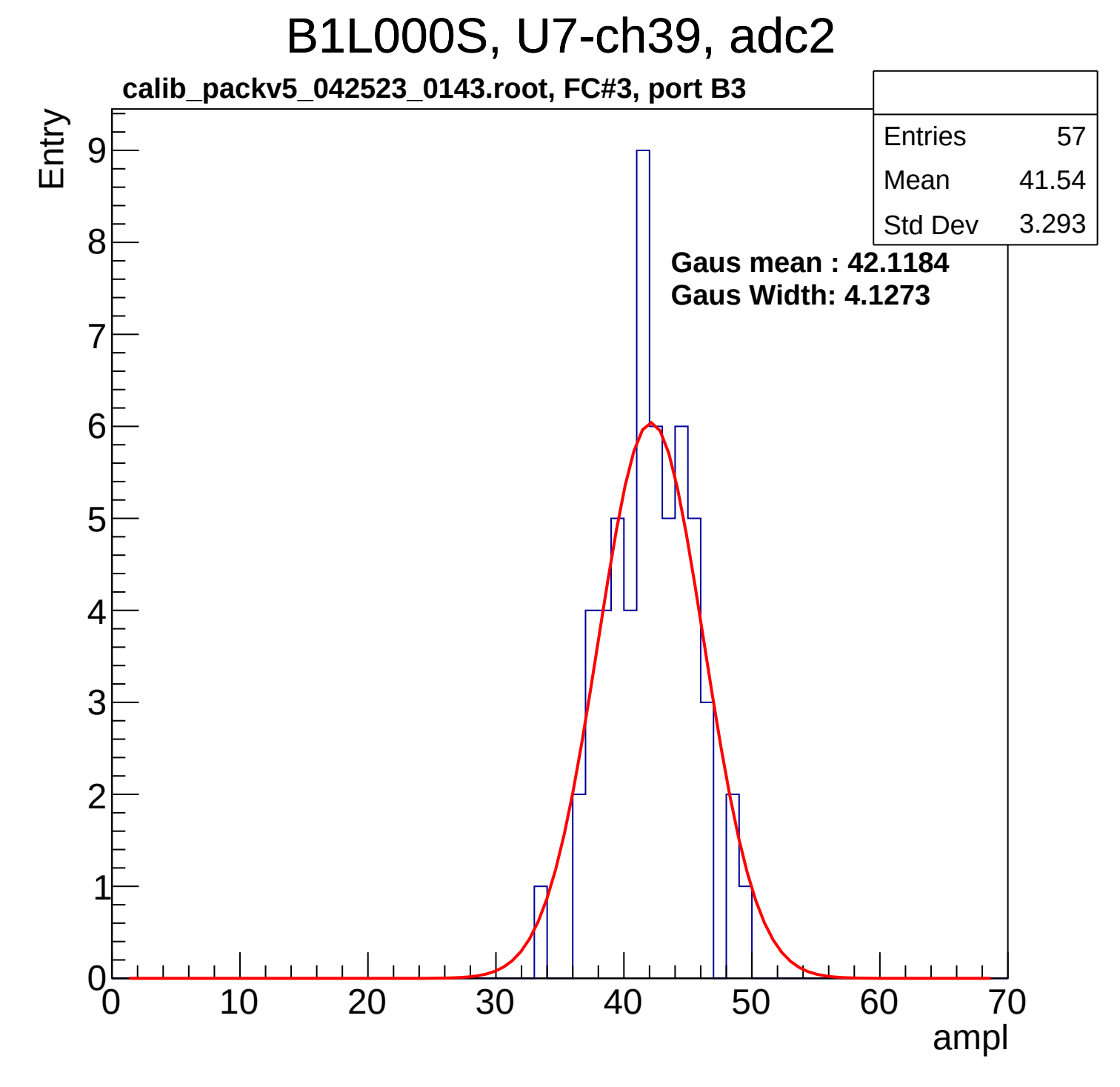
B1L000S, U7-ch39, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.54
Std Dev 3.293
Gaus mean : 42.1184
Gaus Width: 4.1273
Entry
ampl
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2
3
4
5
6
7
8
9
0
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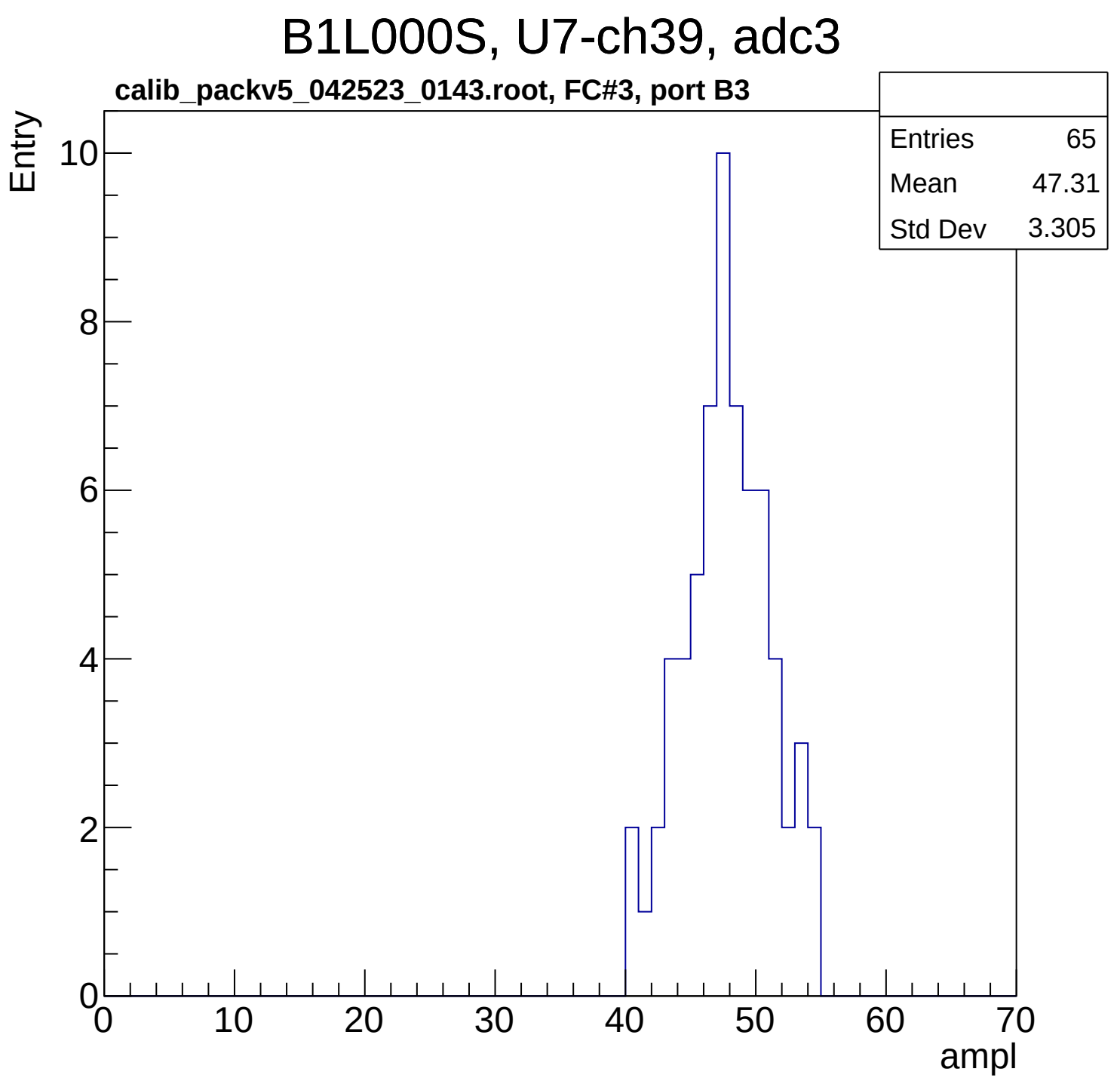
B1L000S, U7-ch39, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 47.31
Std Dev 3.305
Entry
ampl
0
2
4
6
8
10
0
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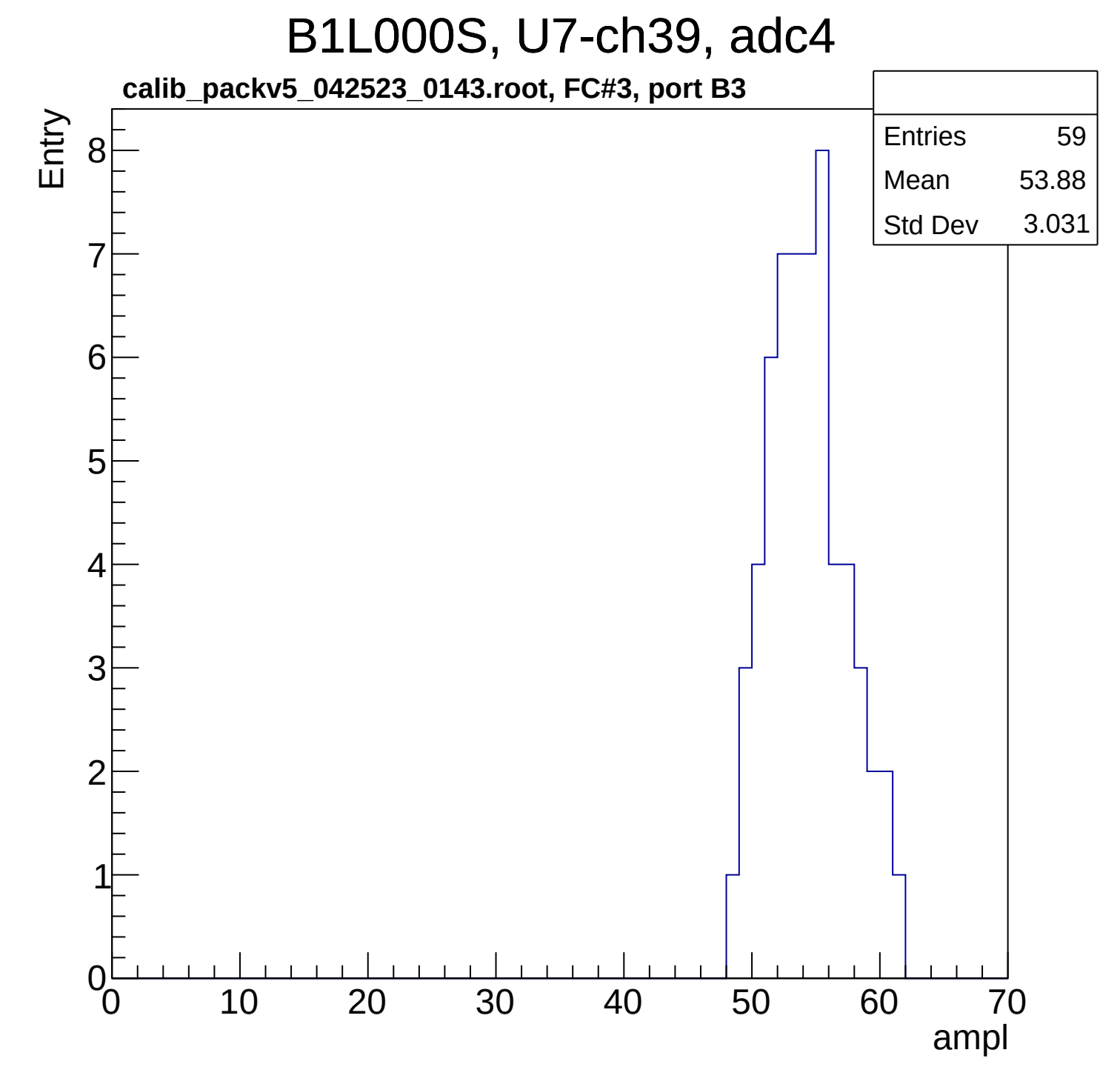
B1L000S, U7-ch39, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 53.88
Std Dev 3.031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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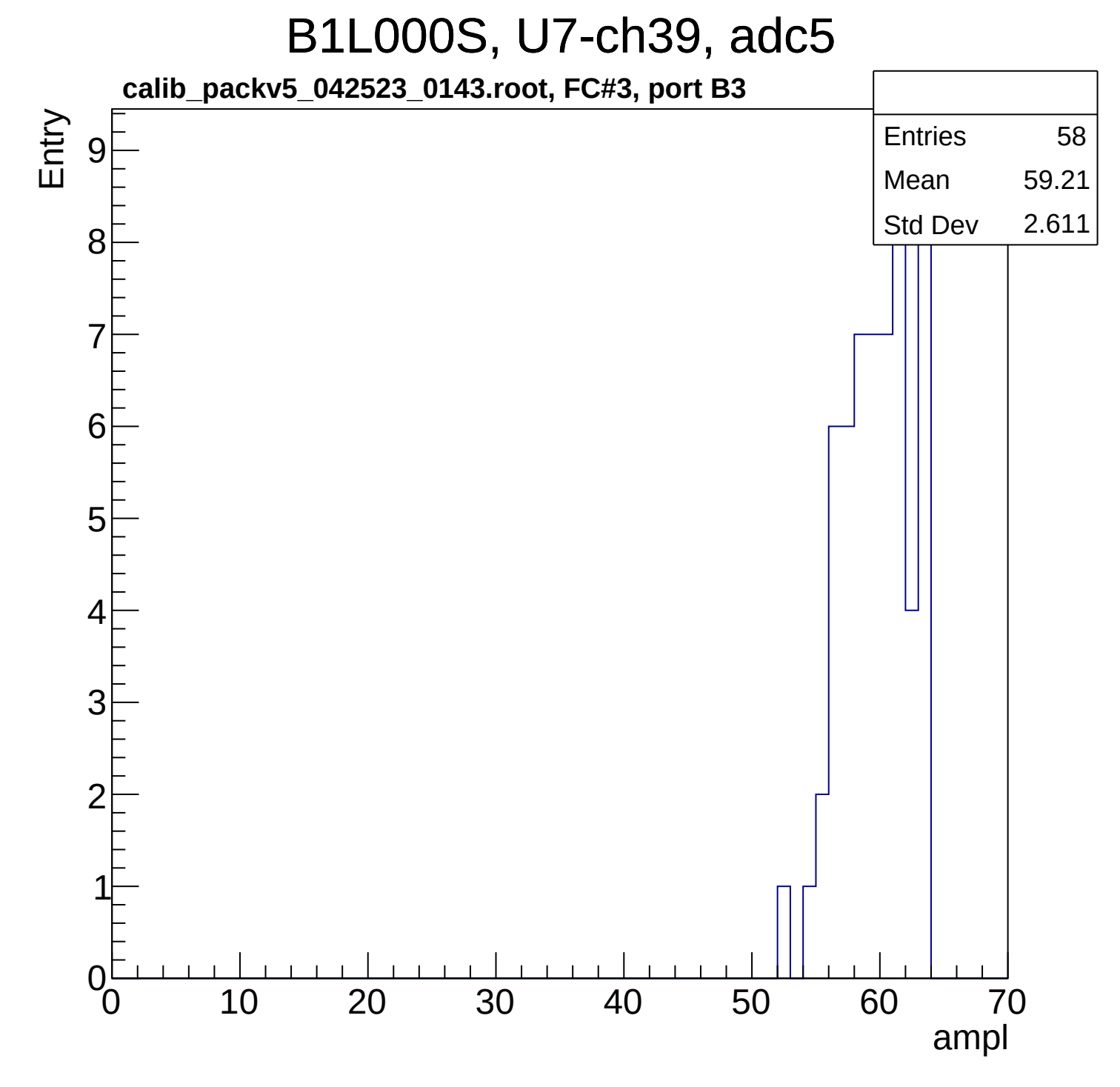
B1L000S, U7-ch39, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 59.21
Std Dev 2.611
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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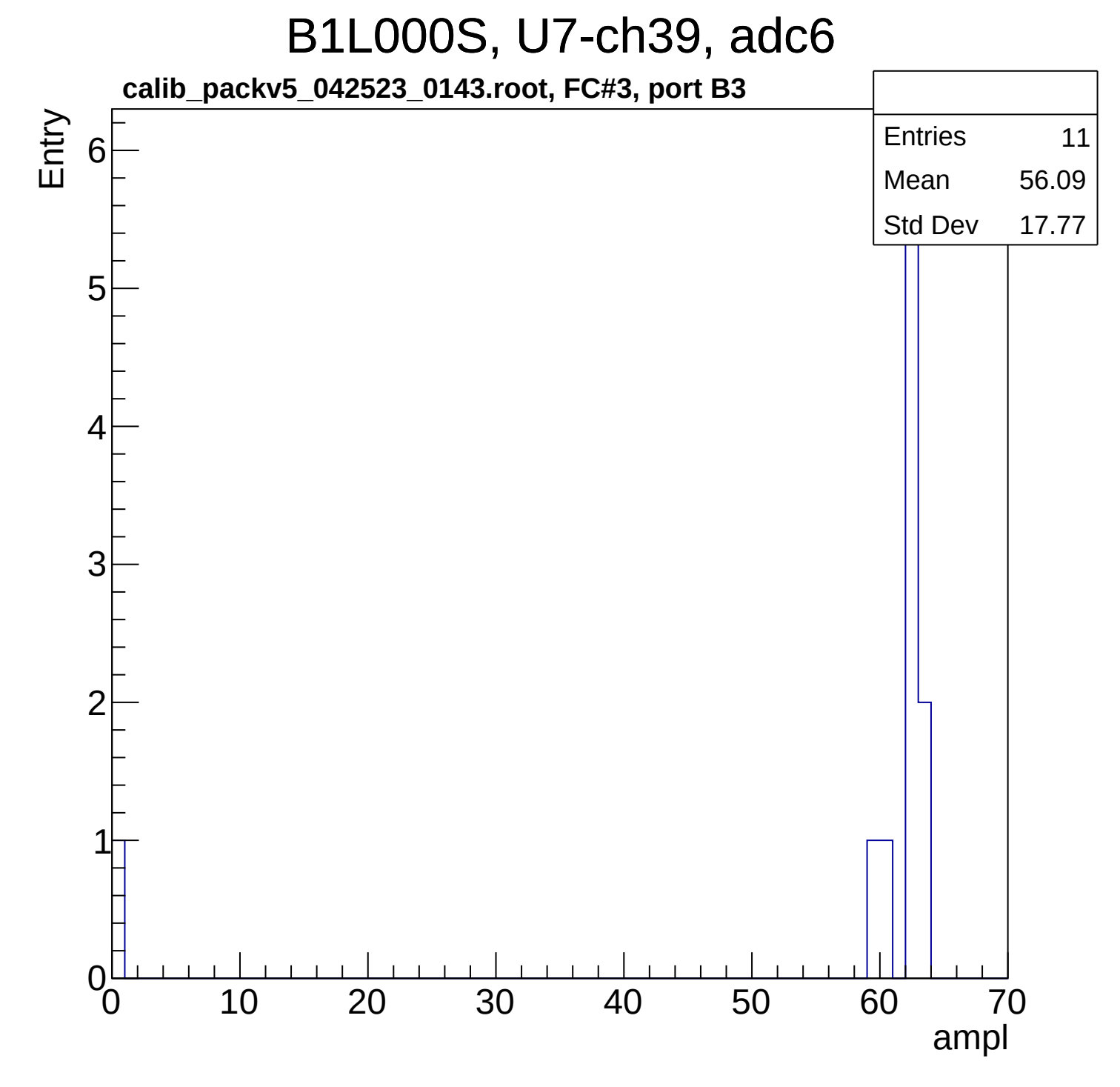
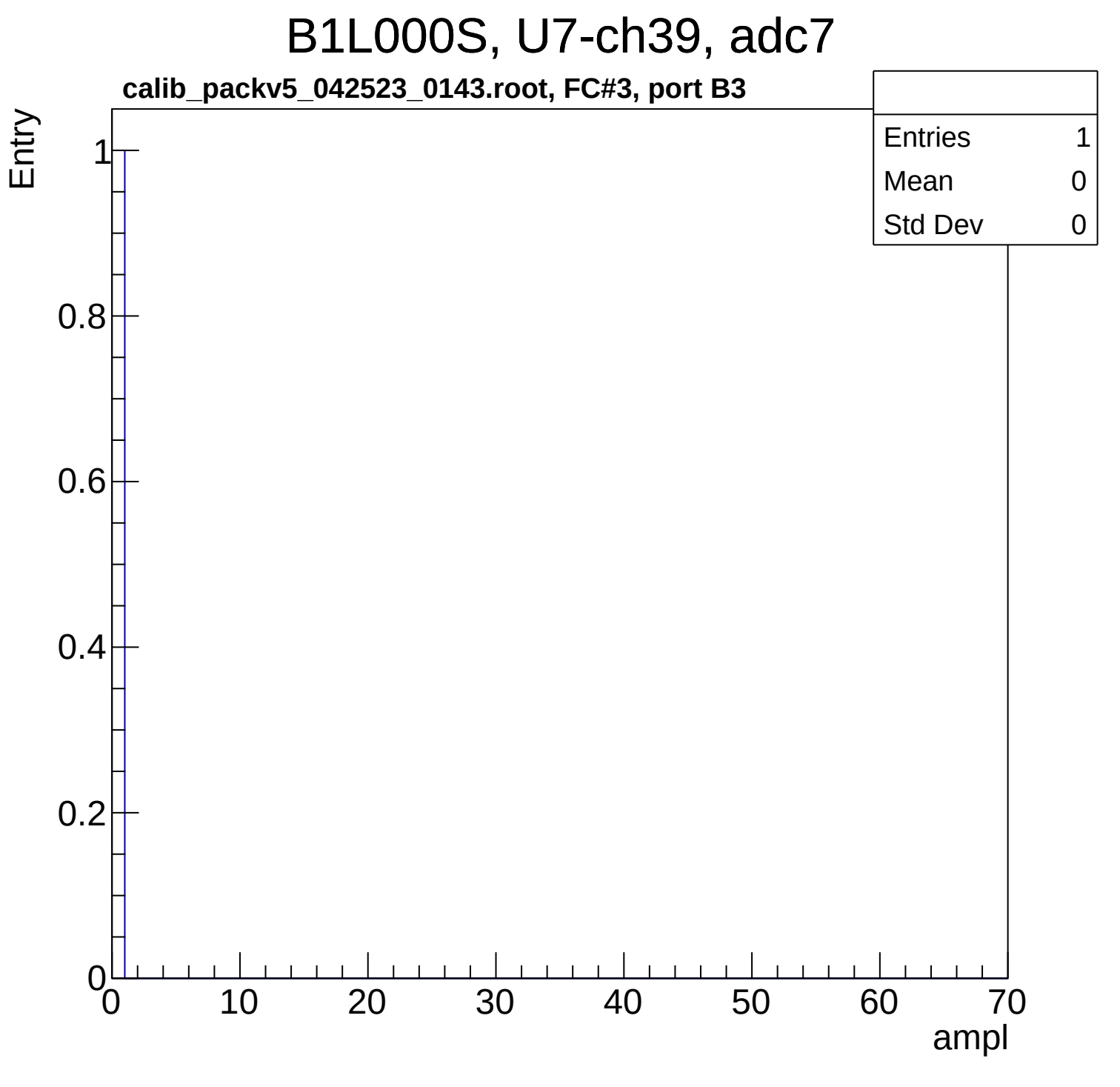
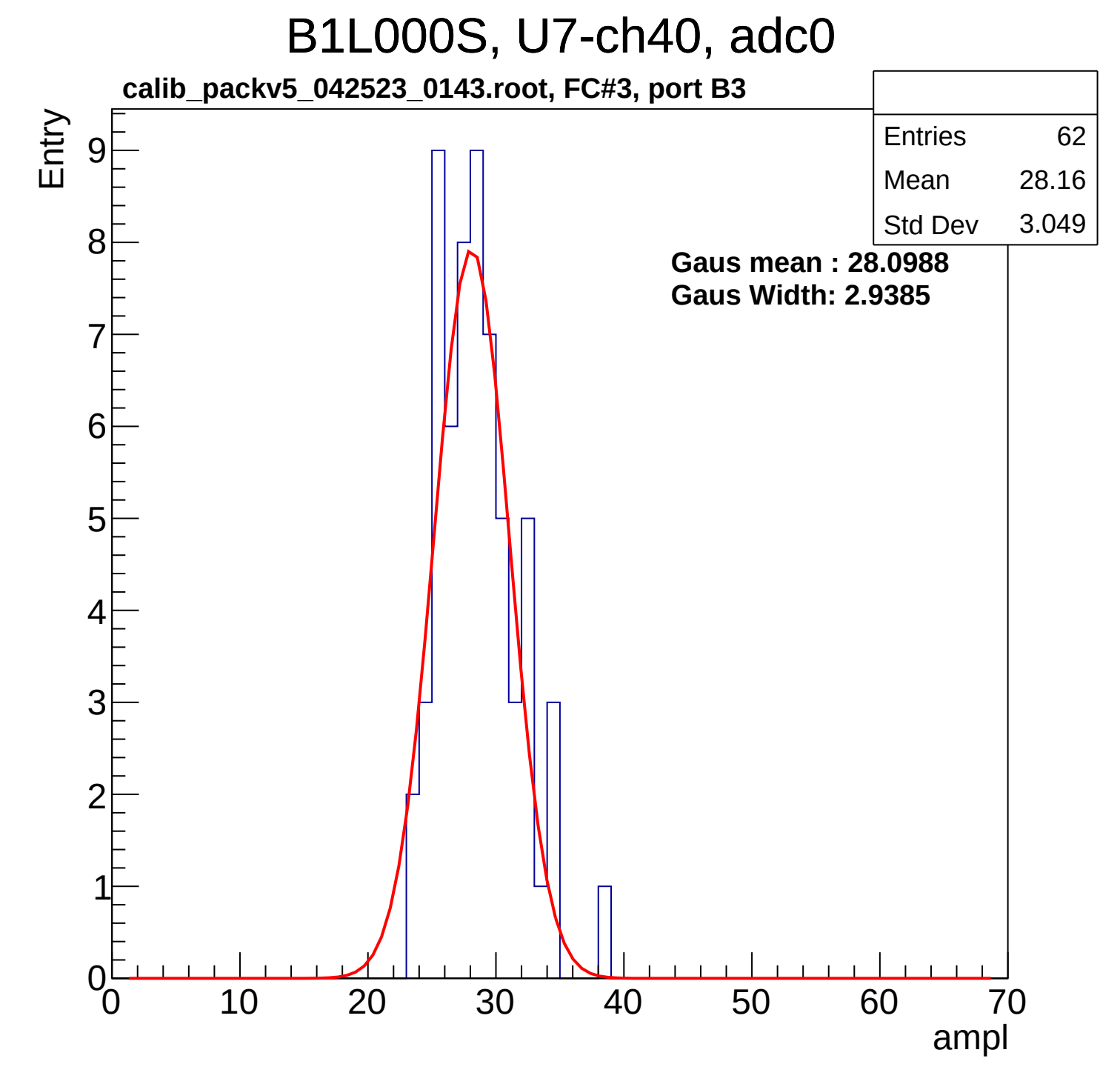

B1L000S, U7-ch39, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.09
Std Dev 17.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch39, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch40, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.16
Std Dev 3.049
Gaus mean : 28.0988
Gaus Width: 2.9385
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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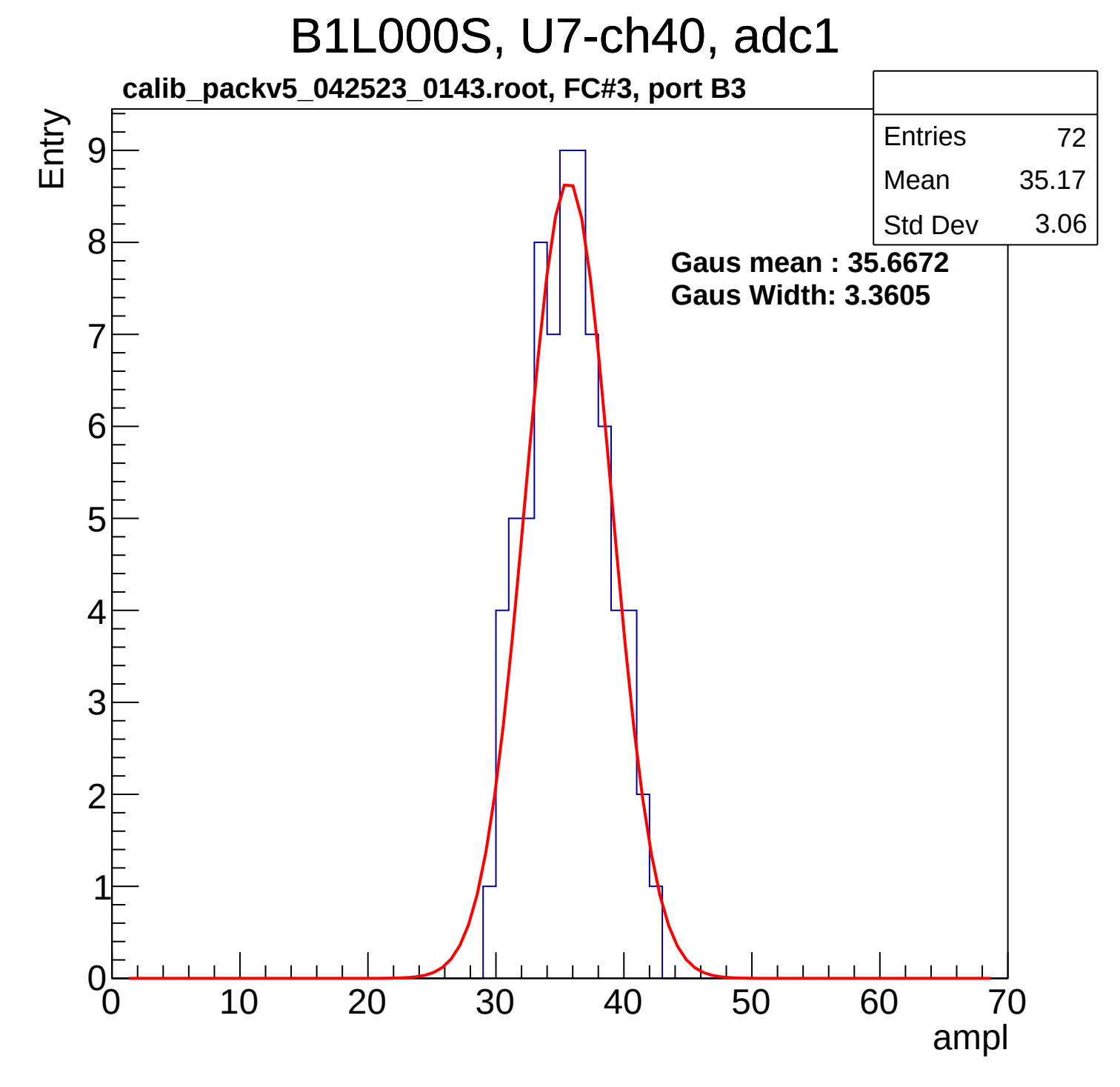
B1L000S, U7-ch40, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.17
Std Dev 3.06
Gaus mean : 35.6672
Gaus Width: 3.3605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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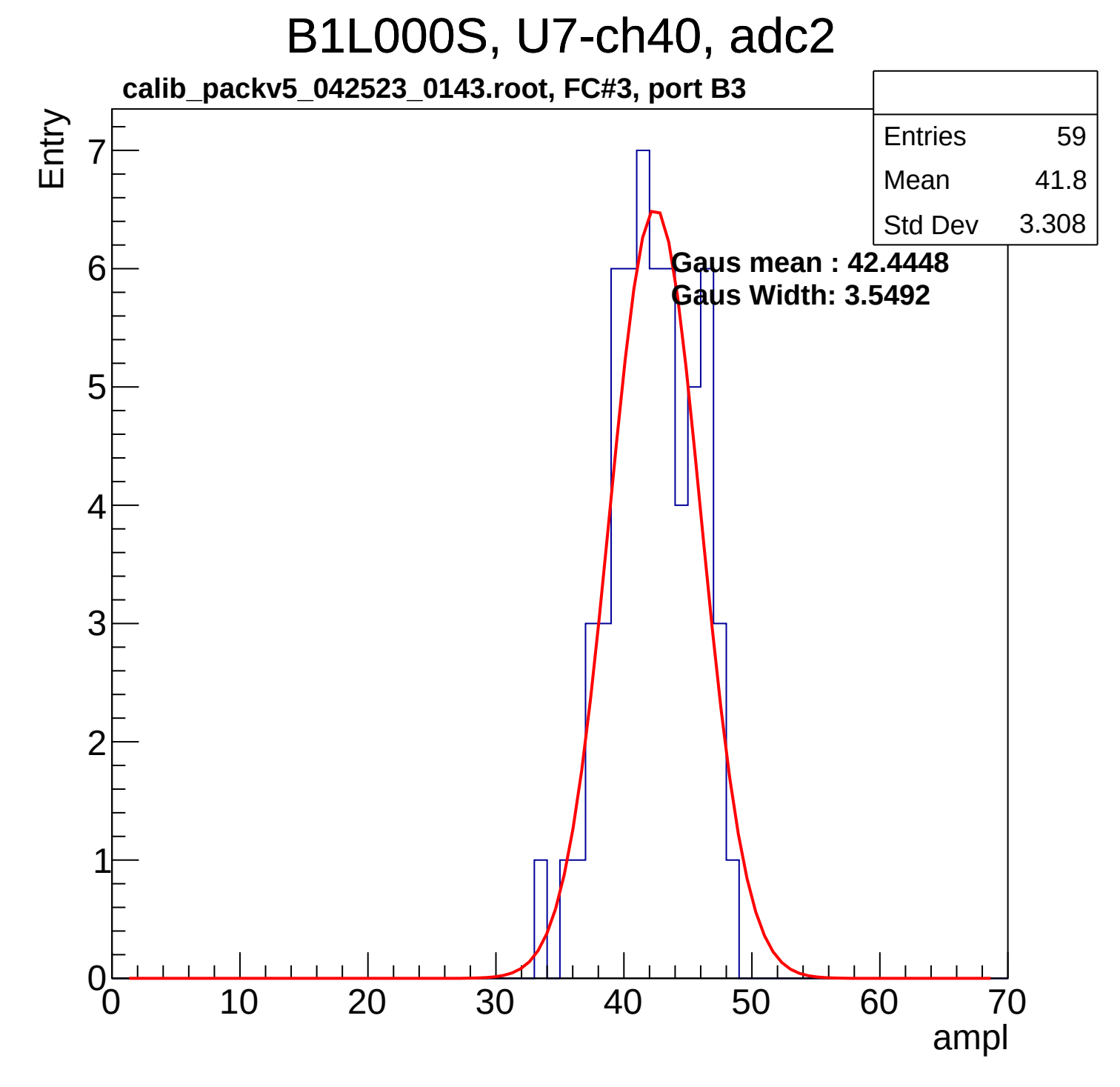
B1L000S, U7-ch40, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 41.8
Std Dev 3.308
Gaus mean : 42.4448
Gaus Width: 3.5492
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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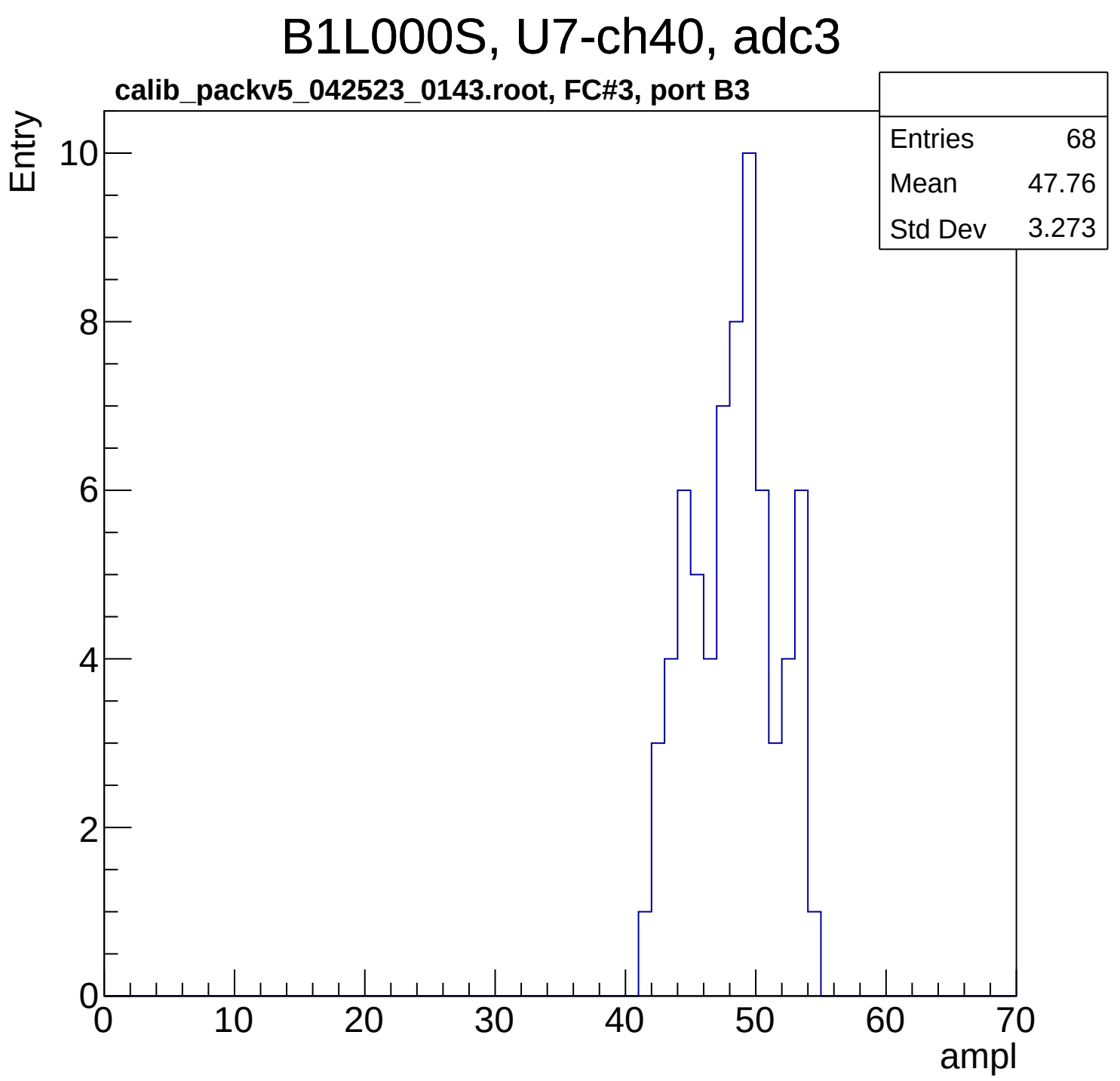
B1L000S, U7-ch40, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 47.76
Std Dev 3.273
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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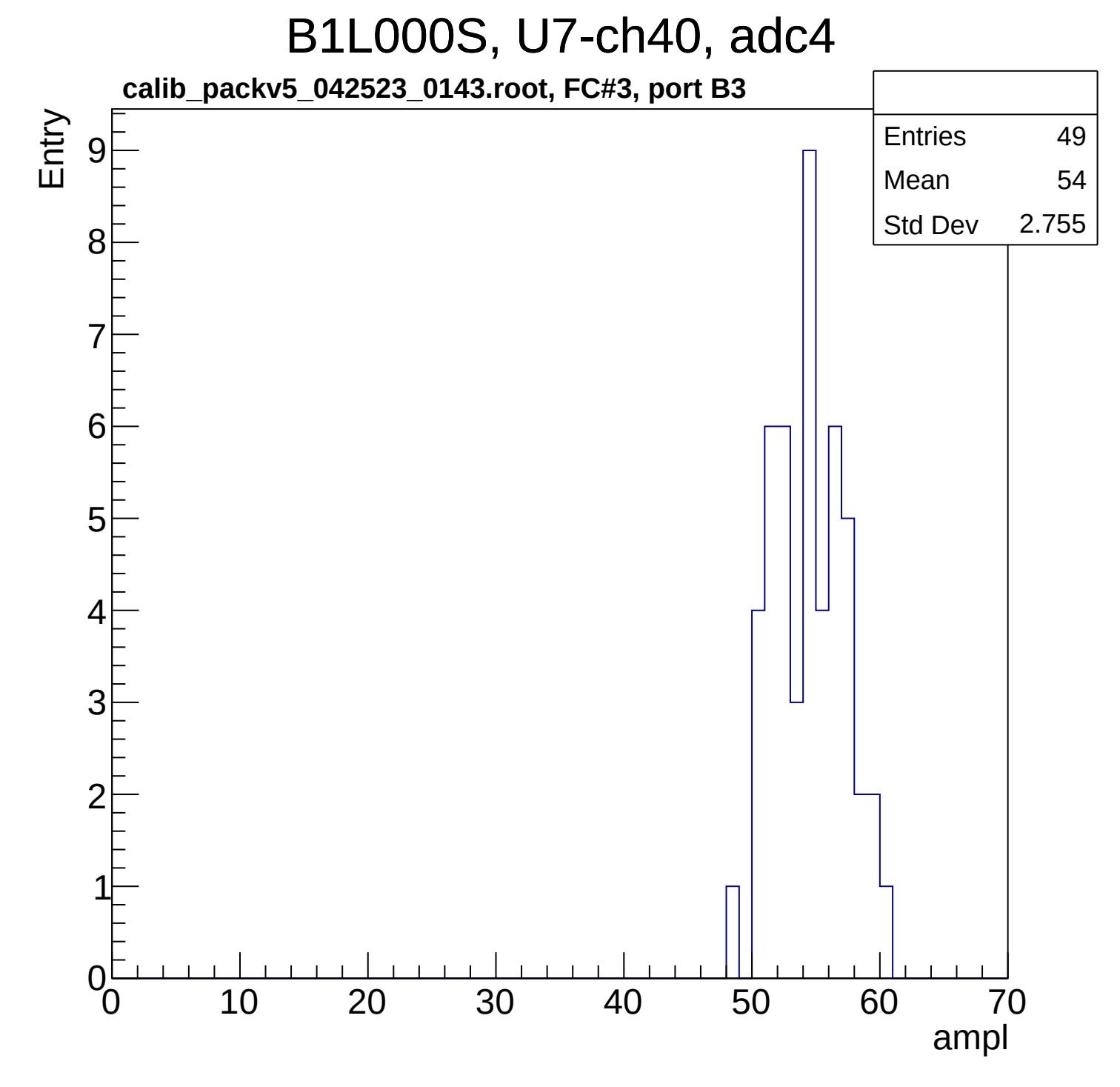
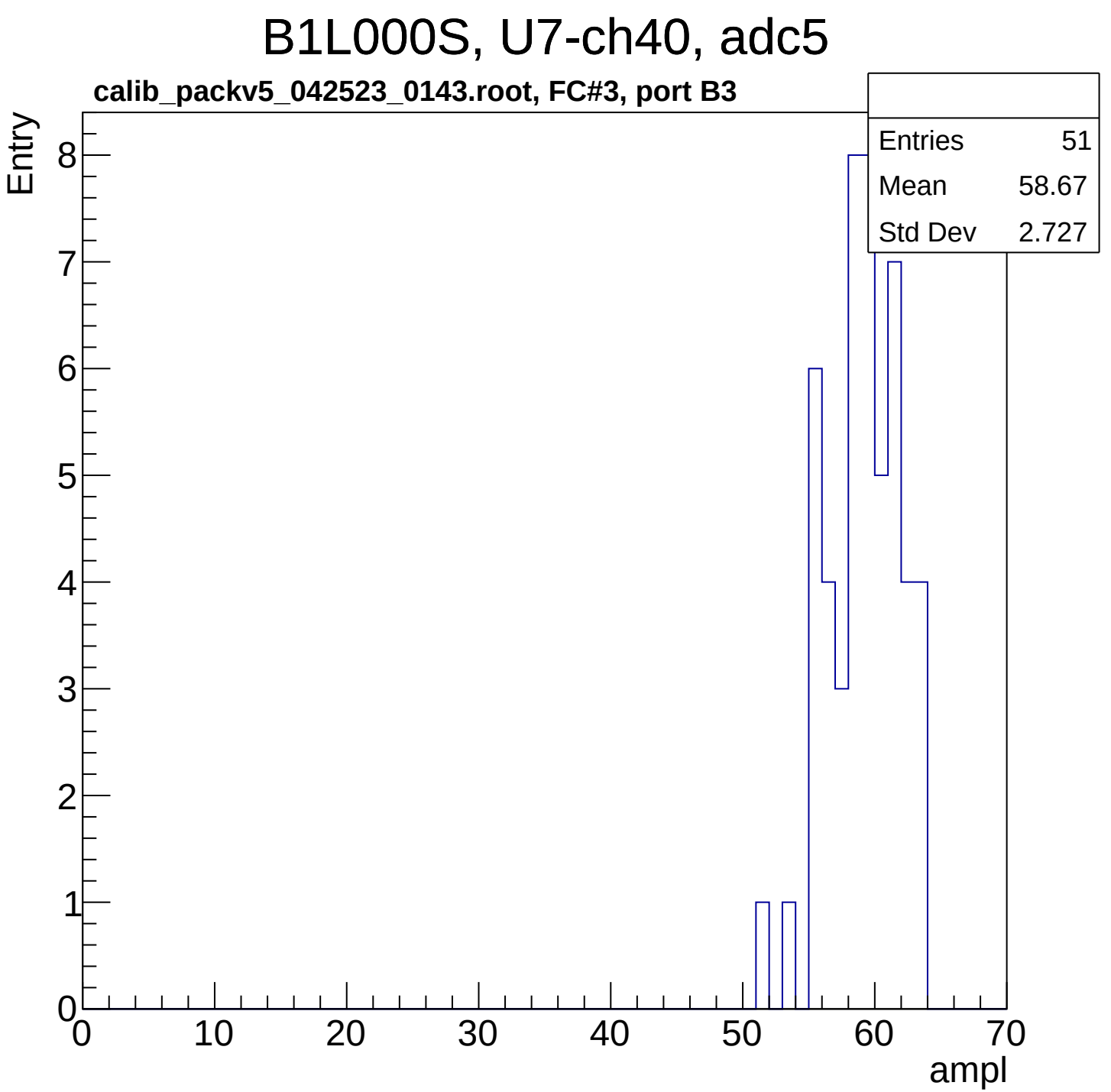

B1L000S, U7-ch40, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 54
Std Dev 2.755
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch40, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 58.67
Std Dev 2.727
Entry
ampl
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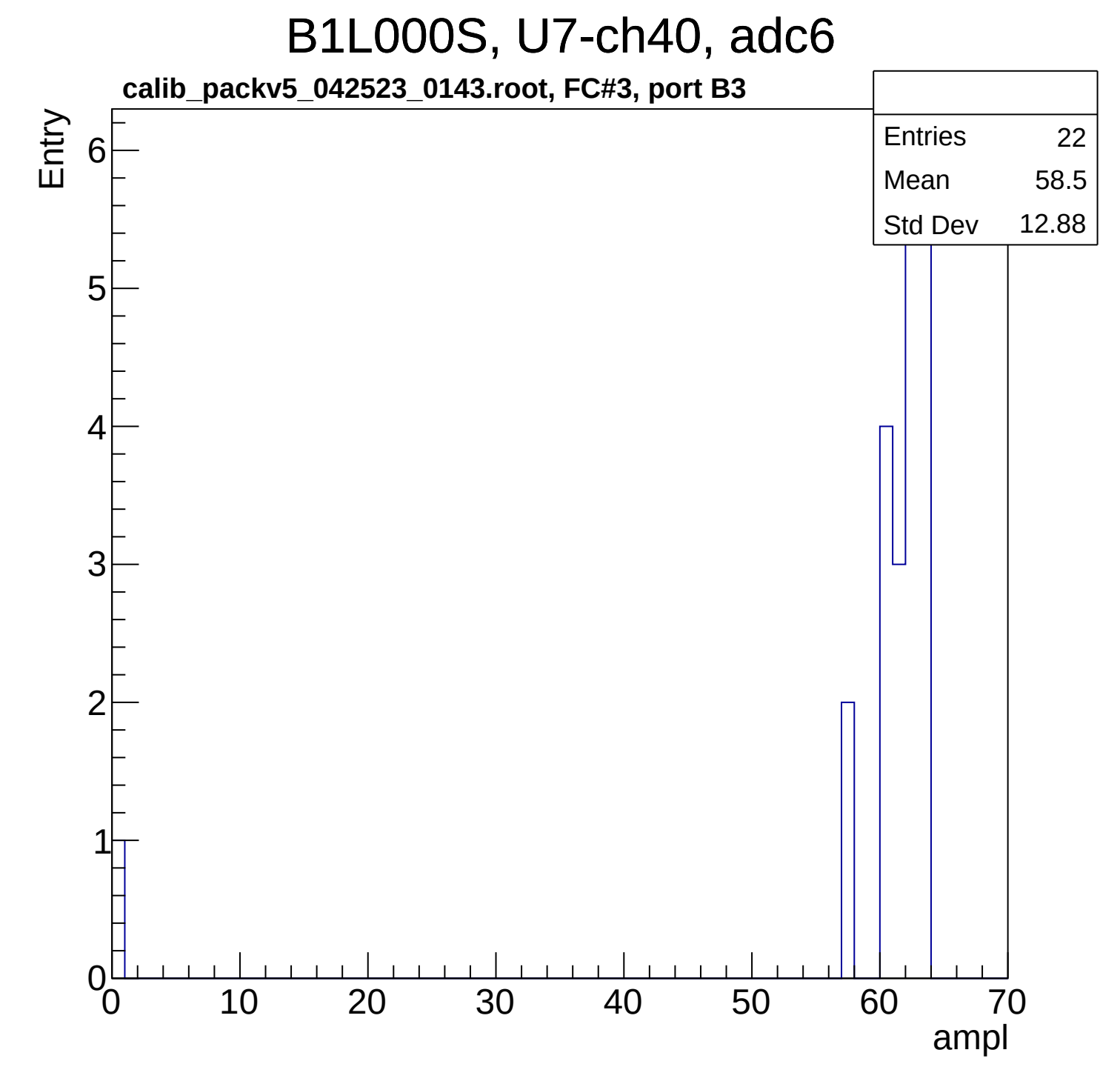
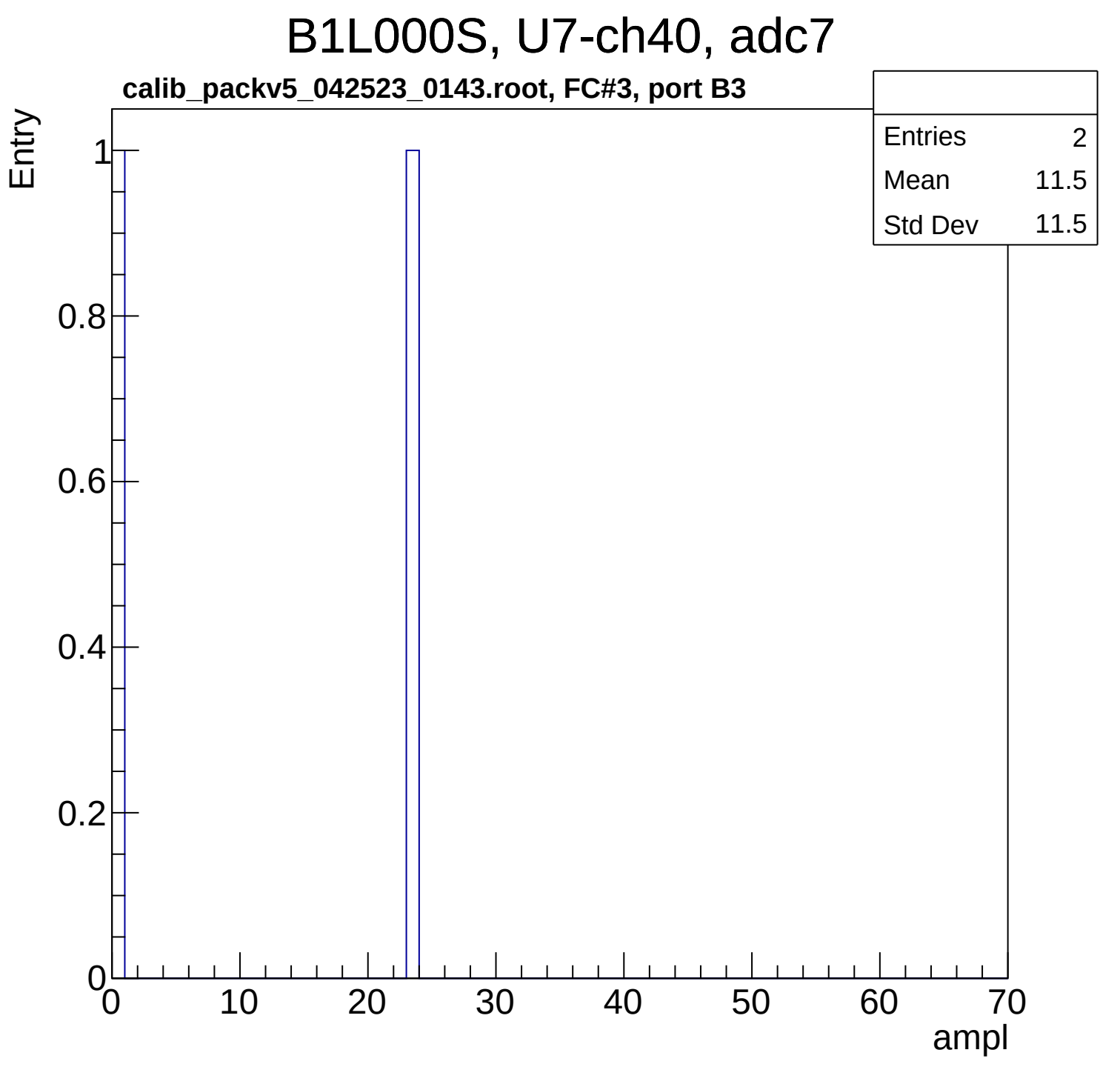
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch40, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 58.5
Std Dev 12.88
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch40, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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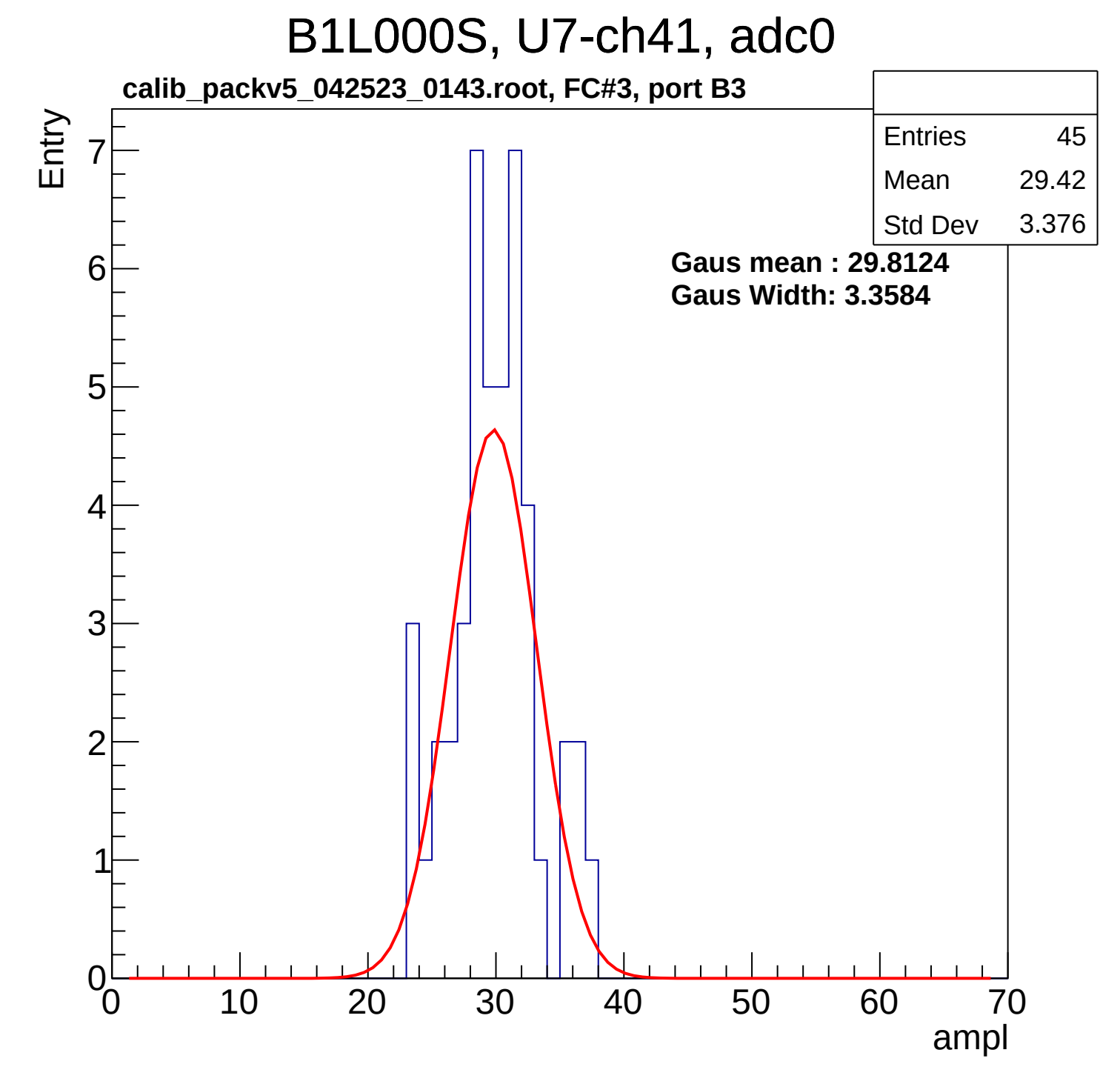
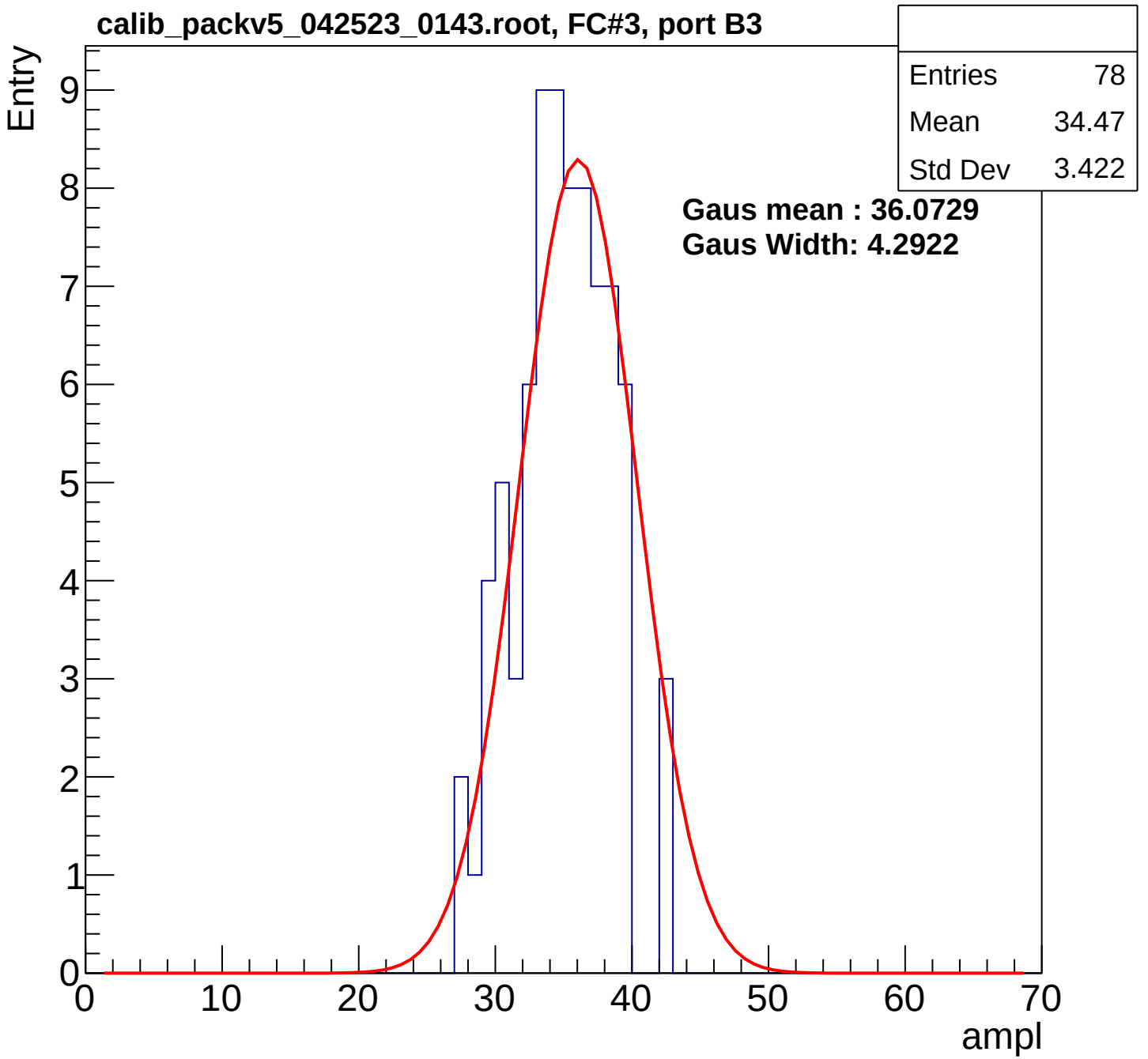
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch41, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 29.42
Std Dev 3.376
Gaus mean : 29.8124
Gaus Width: 3.3584
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch41, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 34.47
Std Dev 3.422
Gaus mean : 36.0729
Gaus Width: 4.2922
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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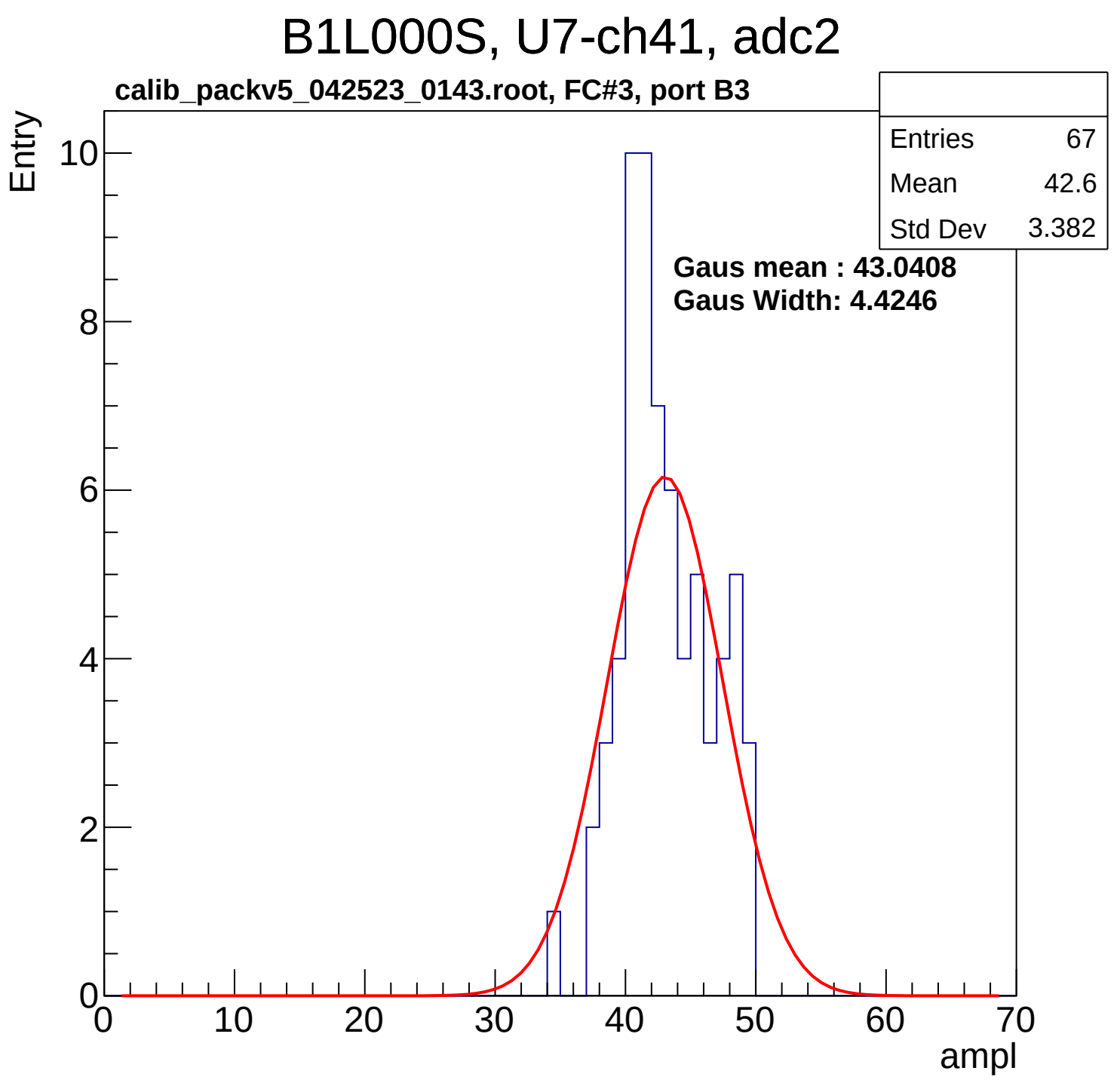
B1L000S, U7-ch41, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 42.6
Std Dev 3.382
Gaus mean : 43.0408
Gaus Width: 4.4246
Entry
ampl
0
2
4
6
8
10
0
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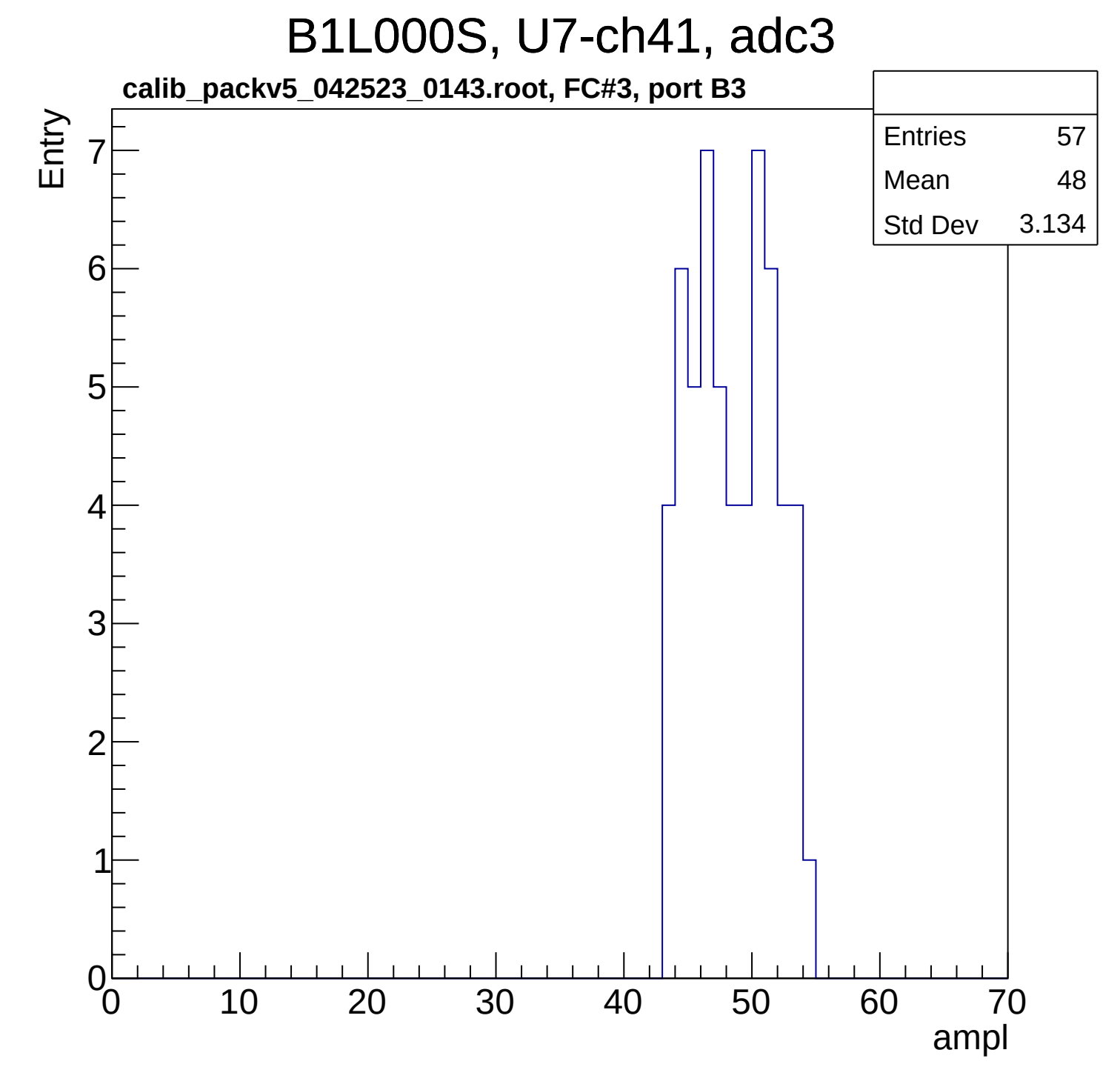
20
30
40
50
60
70


B1L000S, U7-ch41, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 48
Std Dev 3.134
Entry
ampl
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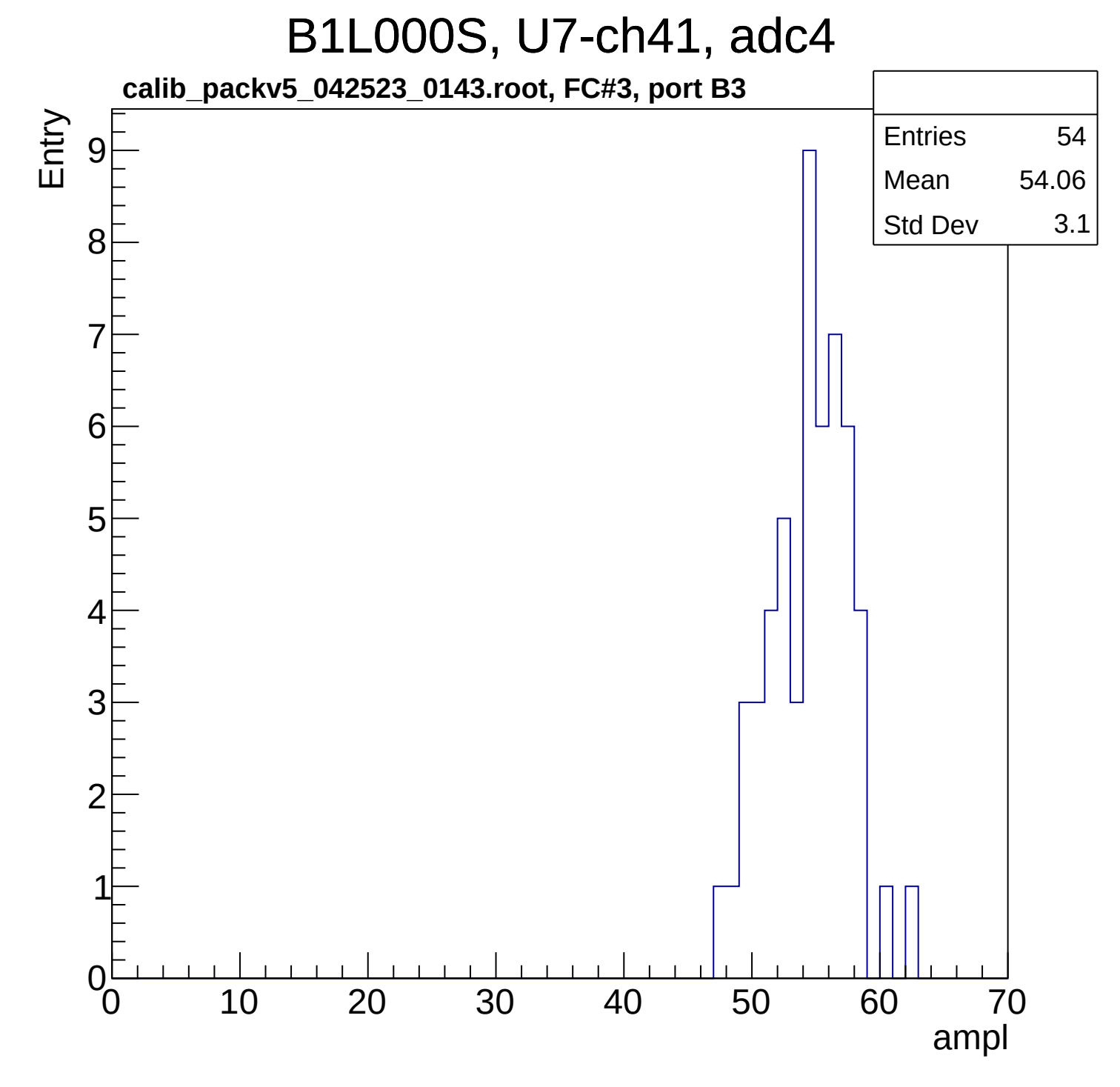
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch41, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.06
Std Dev 3.1
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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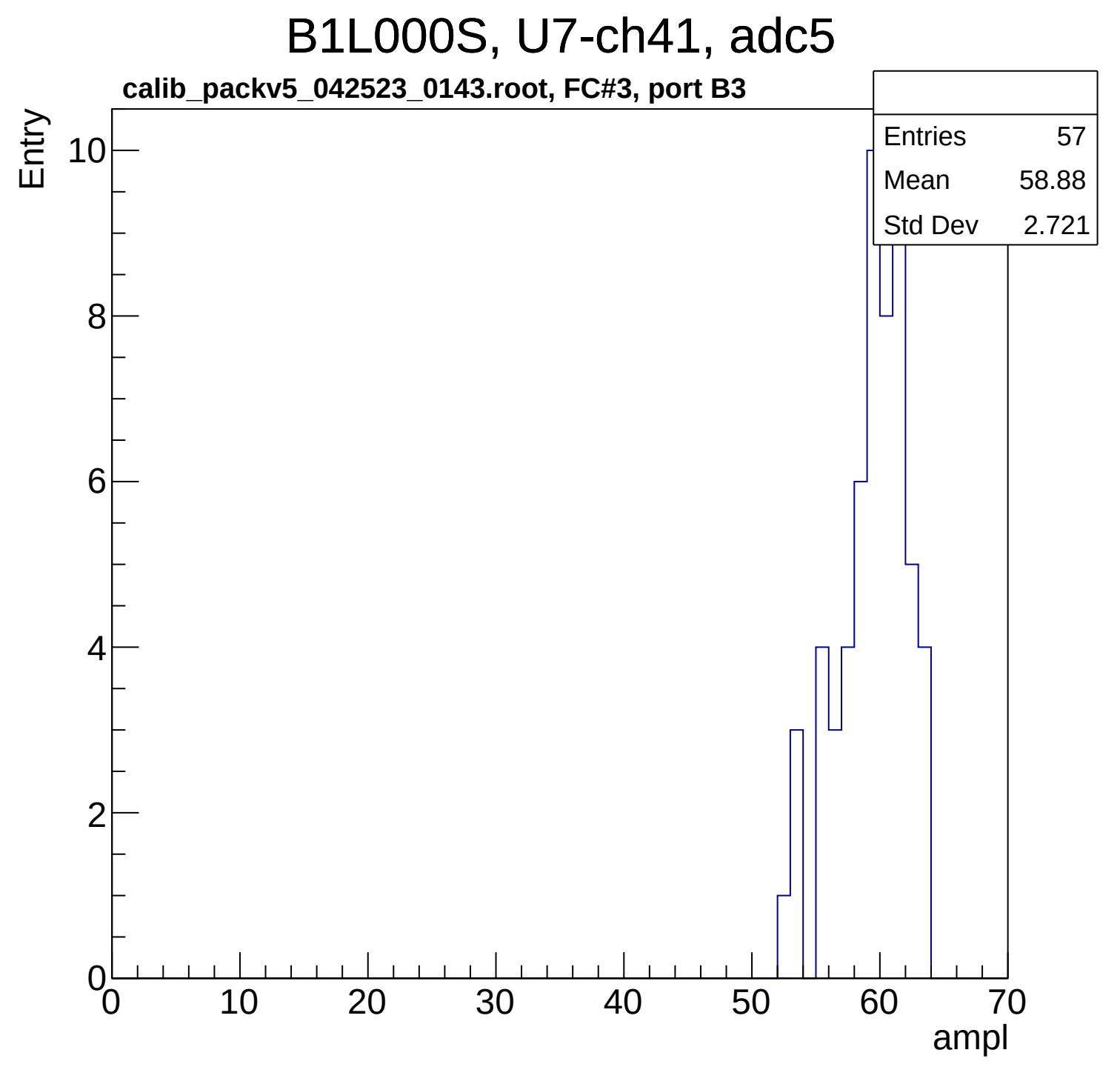
B1L000S, U7-ch41, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.88
Std Dev 2.721
Entry
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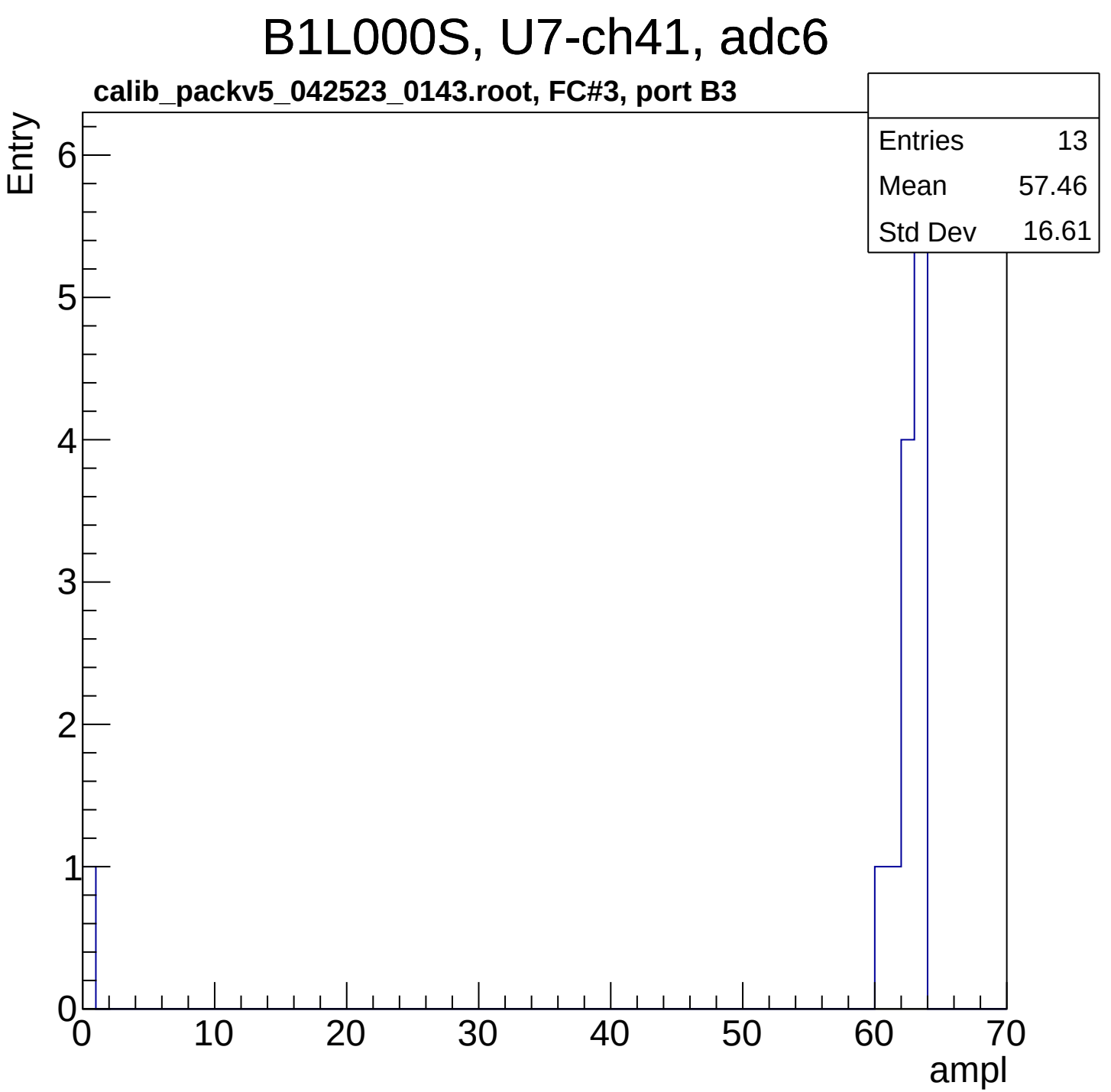
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch41, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.46
Std Dev 16.61
Entry
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4
5
6
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10
20
30
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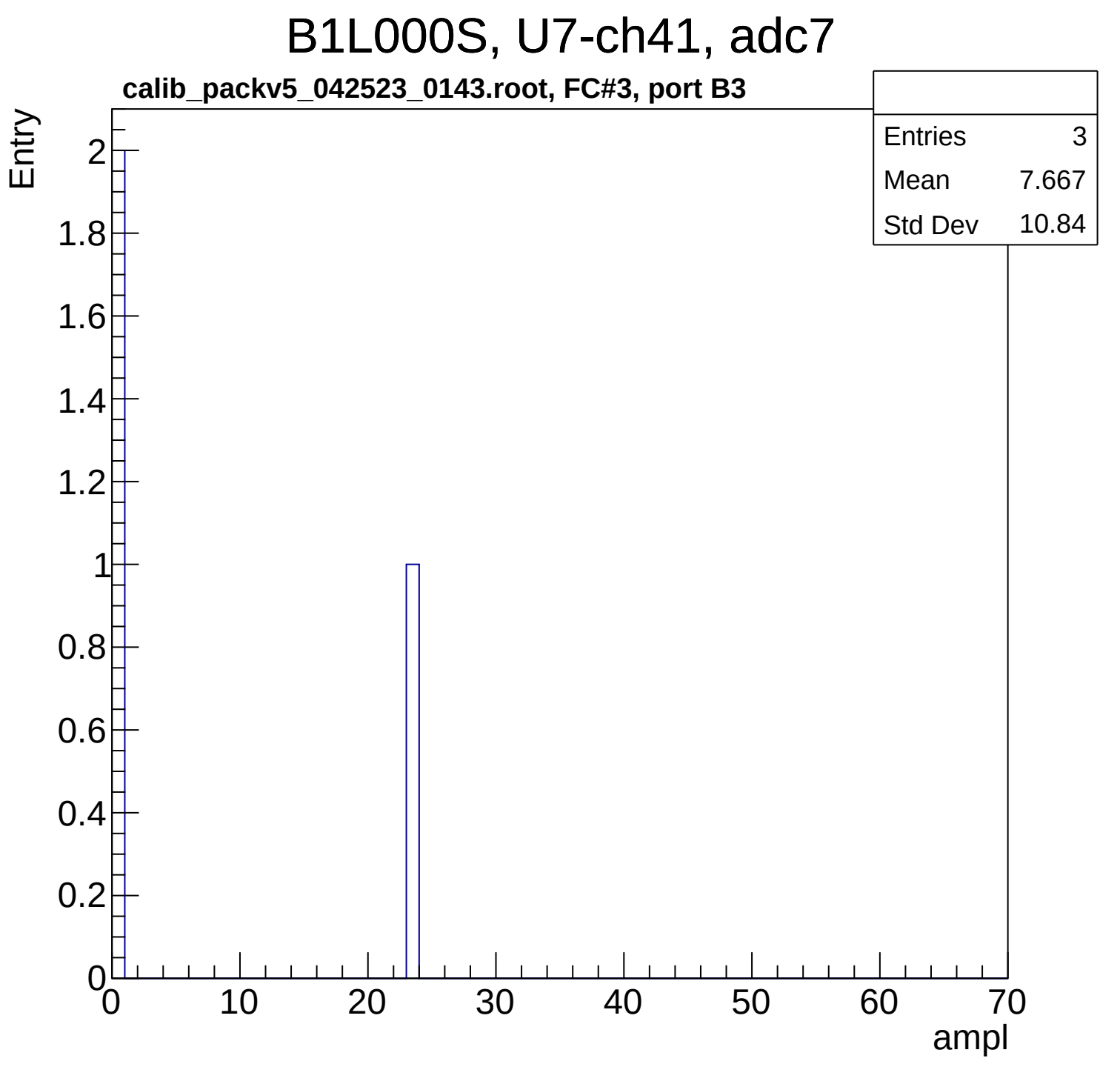
50
60
70
ampl


B1L000S, U7-ch41, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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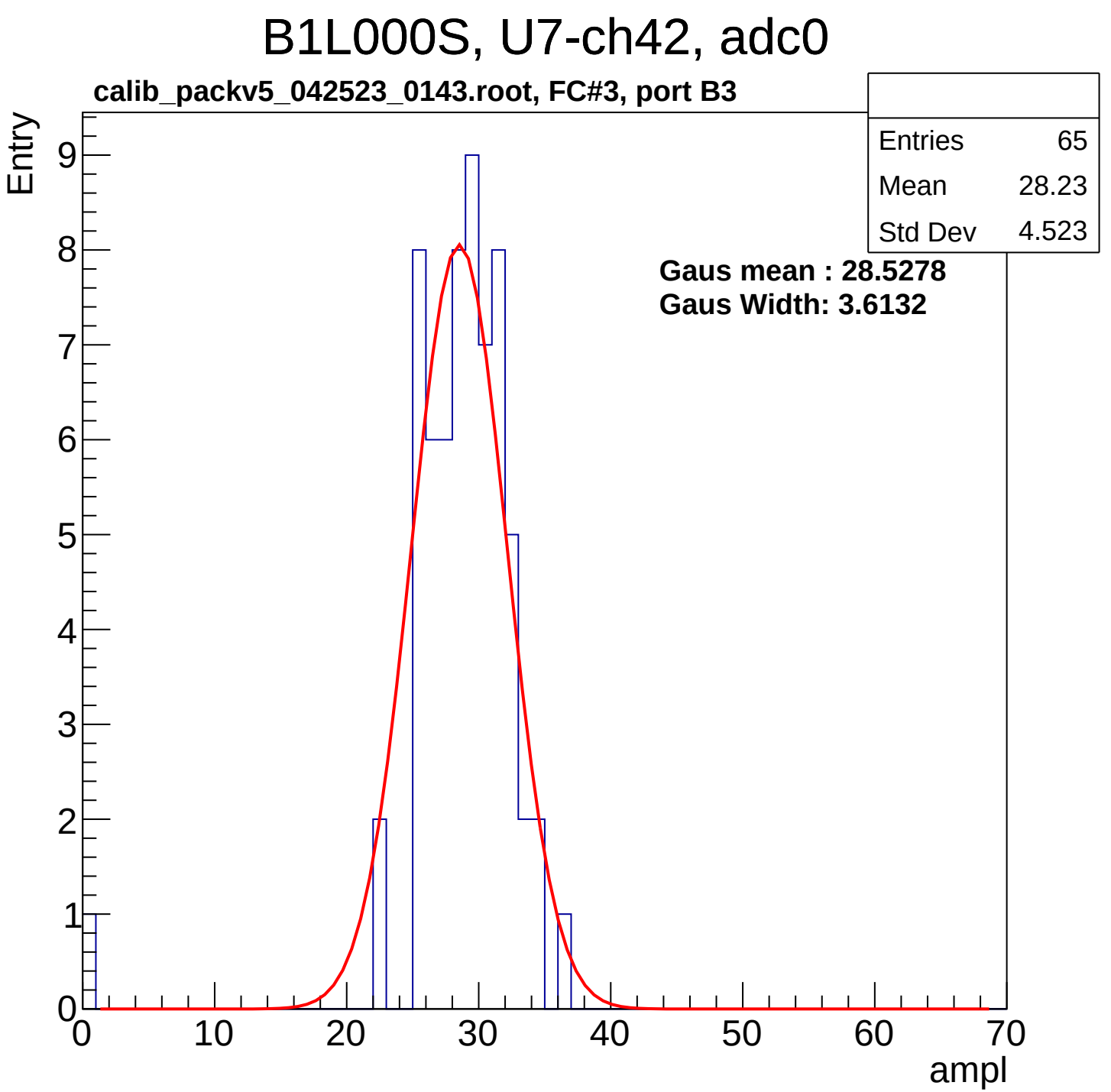
B1L000S, U7-ch42, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.23
Std Dev 4.523
Gaus mean : 28.5278
Gaus Width: 3.6132
Entry
ampl
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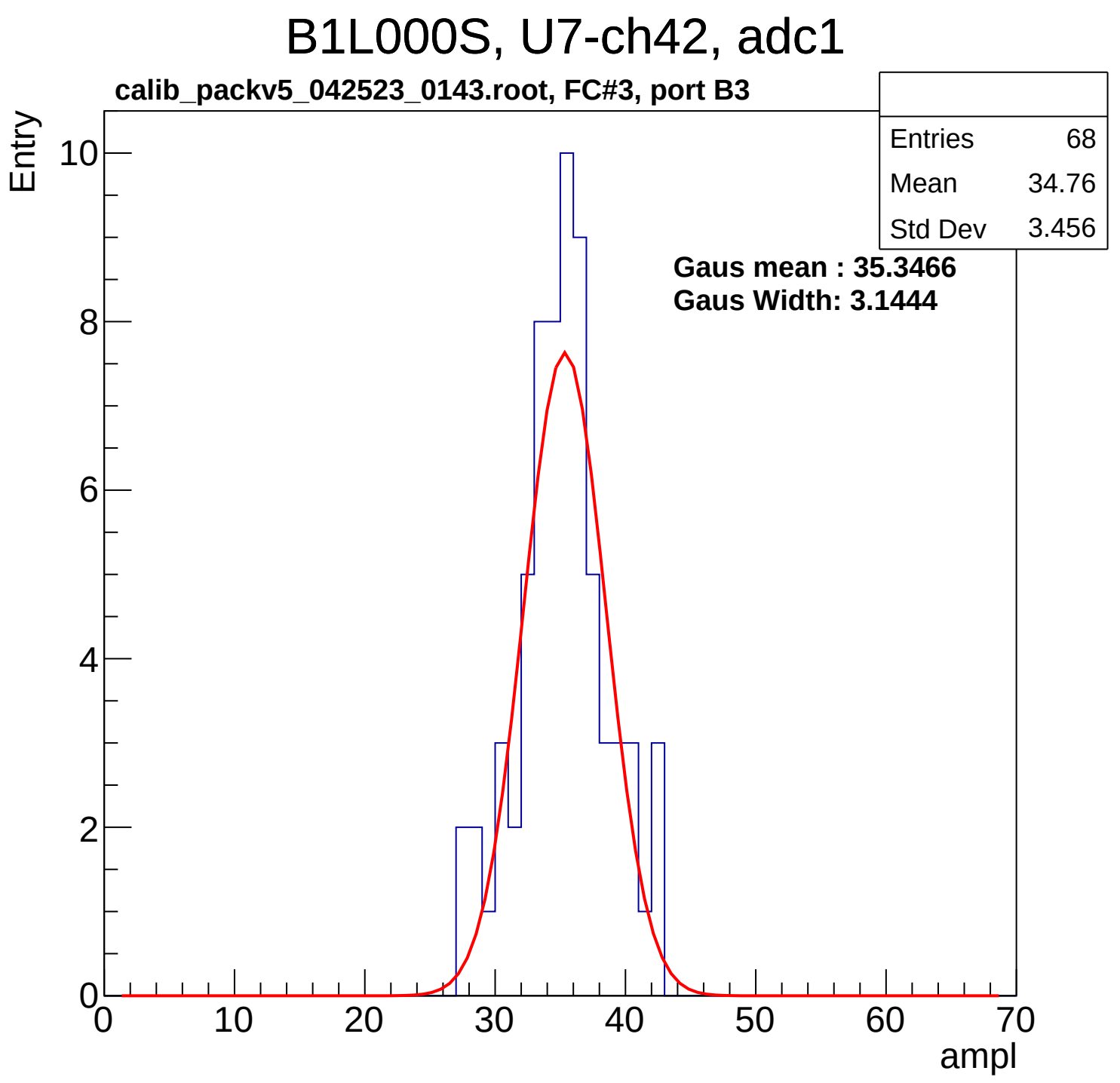
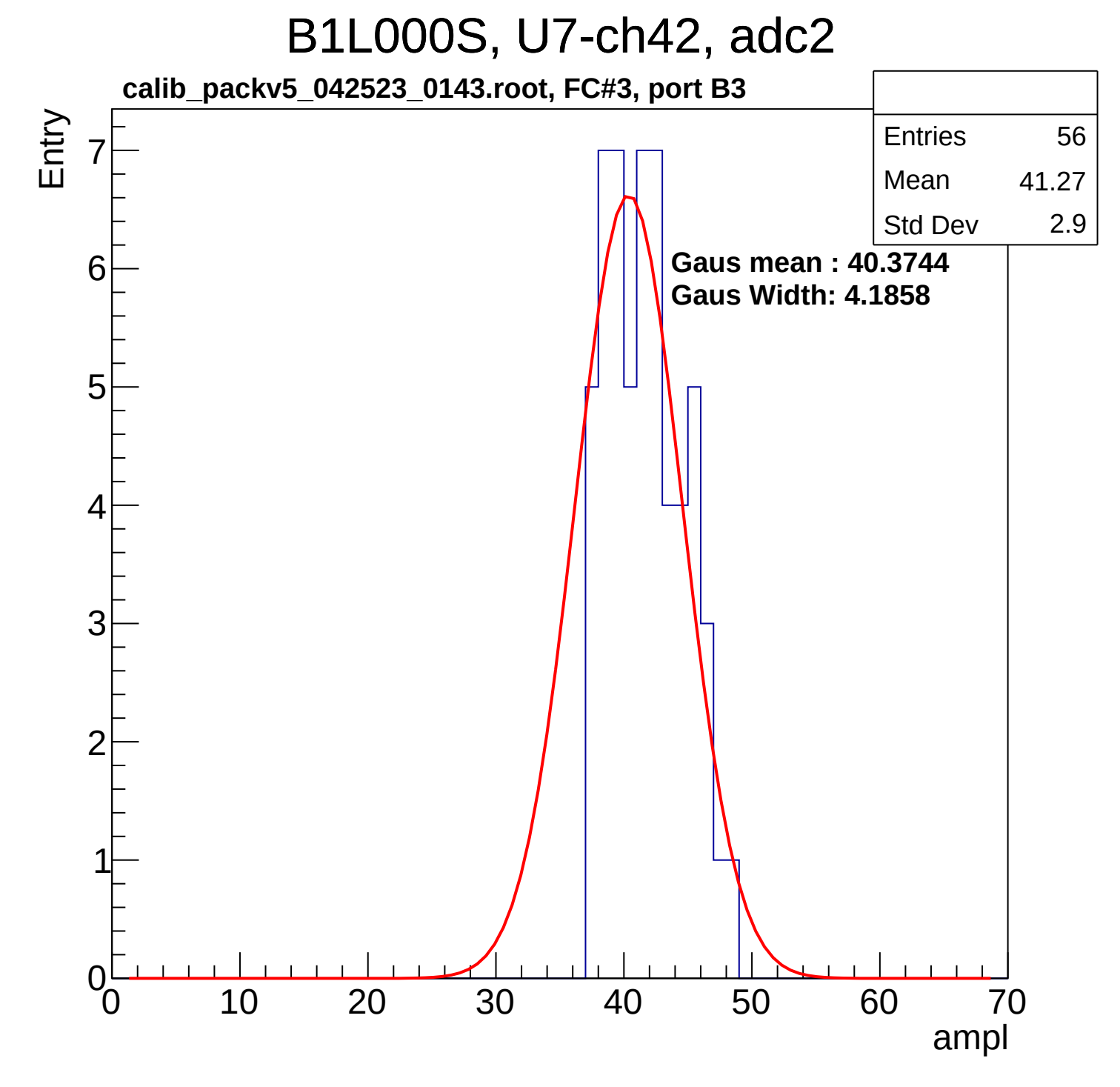
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch42, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 34.76
Std Dev 3.456
Gaus mean : 35.3466
Gaus Width: 3.1444
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch42, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41.27
Std Dev 2.9
Gaus mean : 40.3744
Gaus Width: 4.1858
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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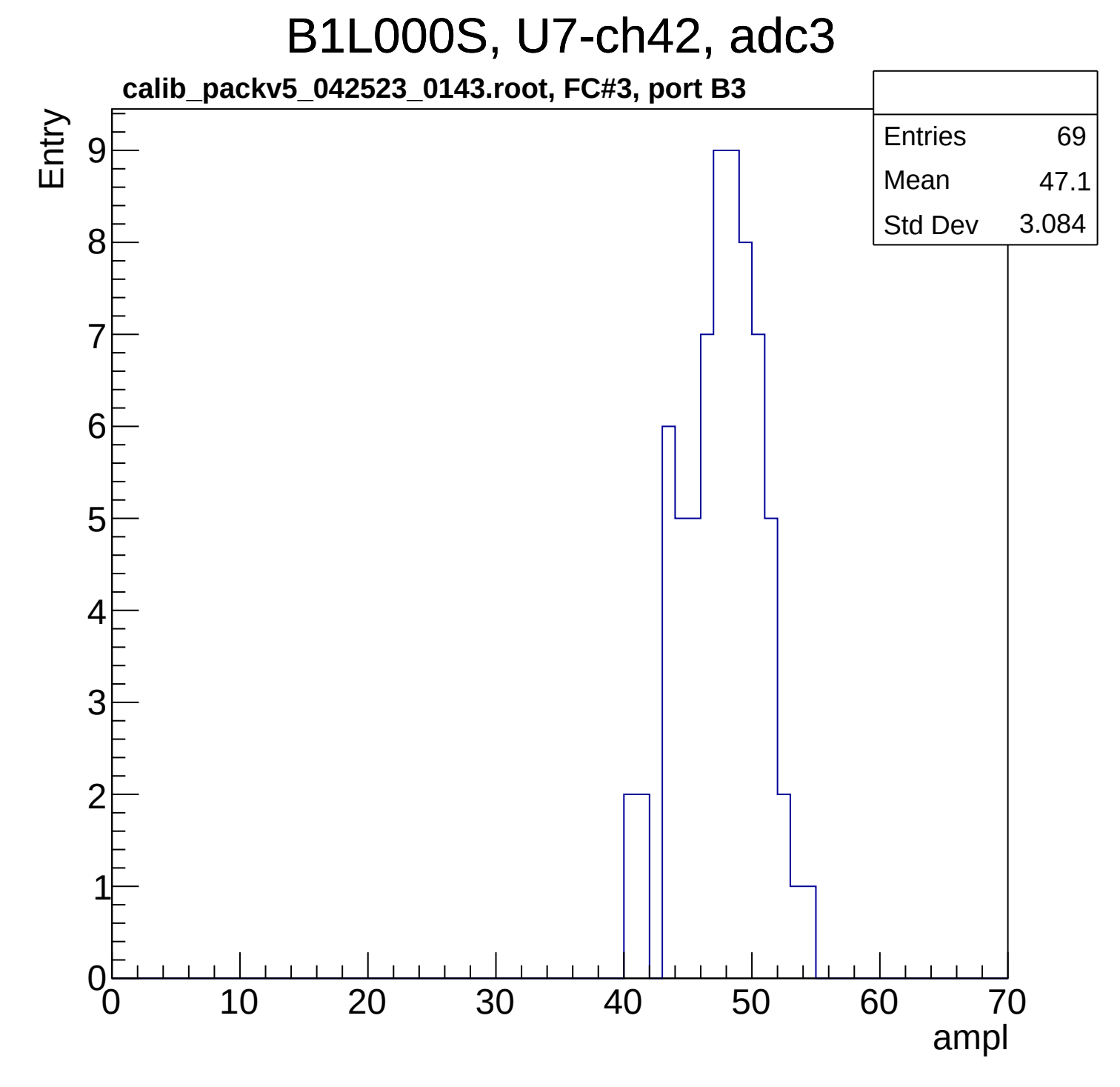
B1L000S, U7-ch42, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 47.1
Std Dev 3.084
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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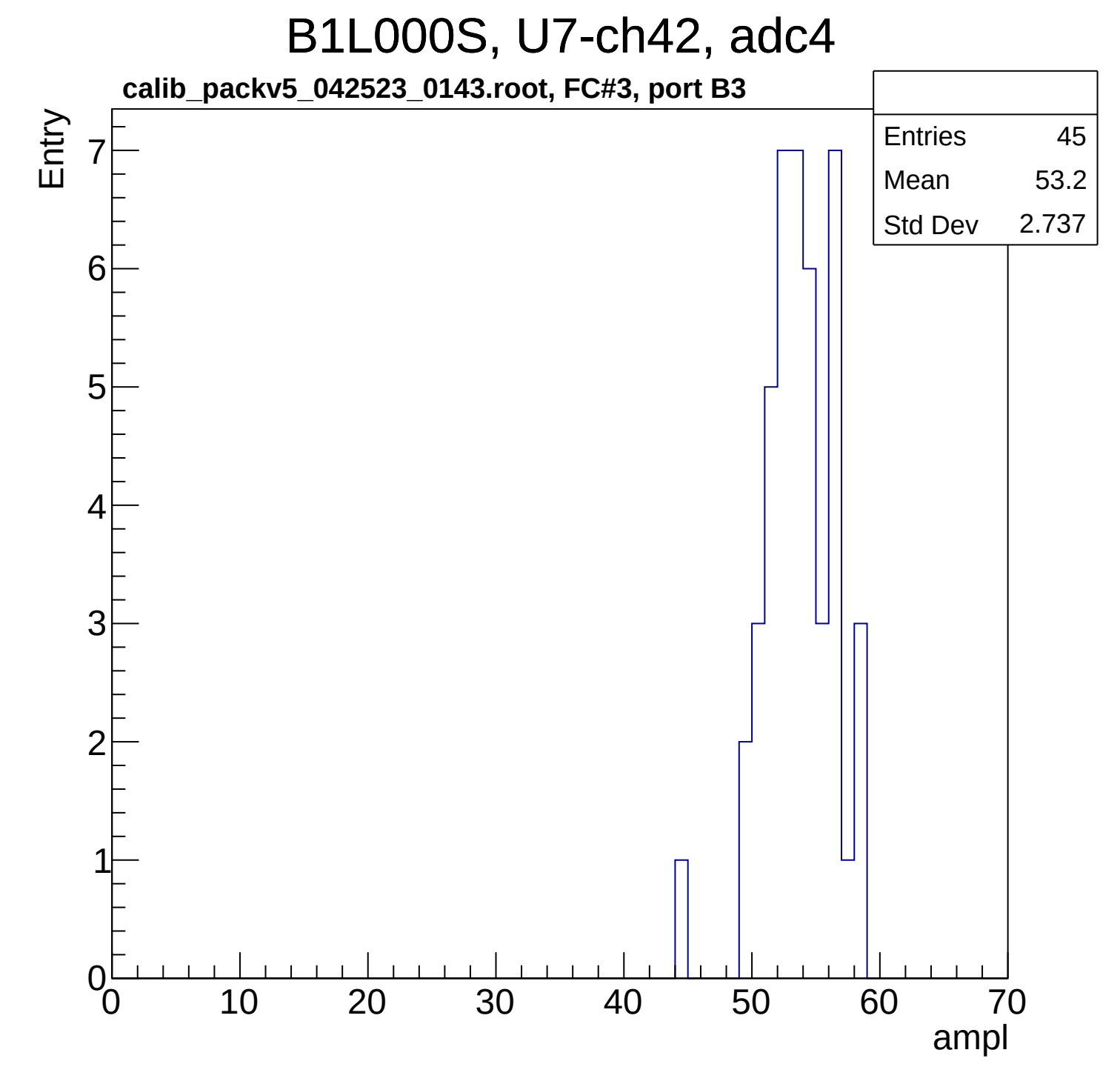
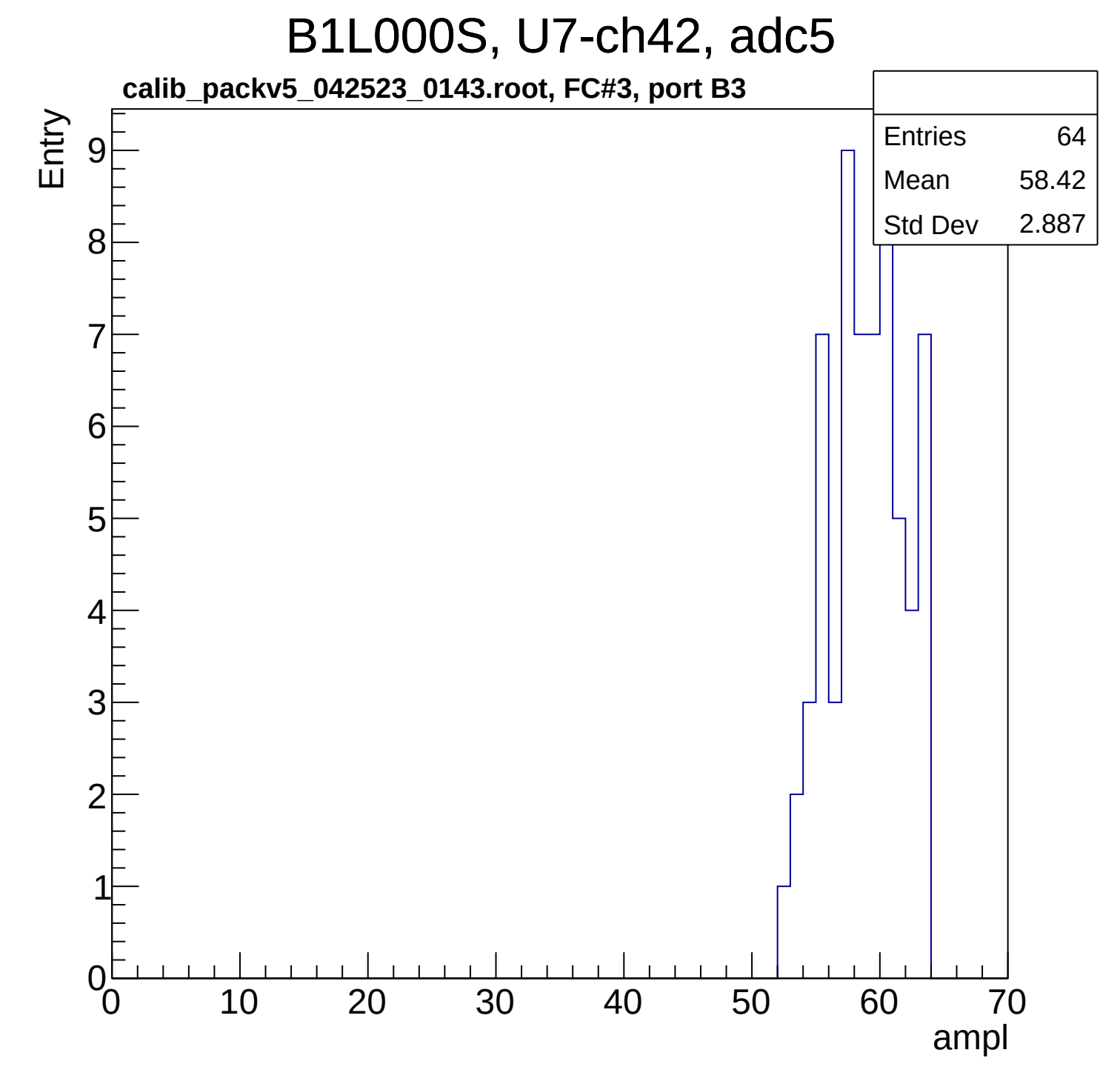
60
70


B1L000S, U7-ch42, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.2
Std Dev 2.737
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch42, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 58.42
Std Dev 2.887
Entry
ampl
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2
3
4
5
6
7
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0
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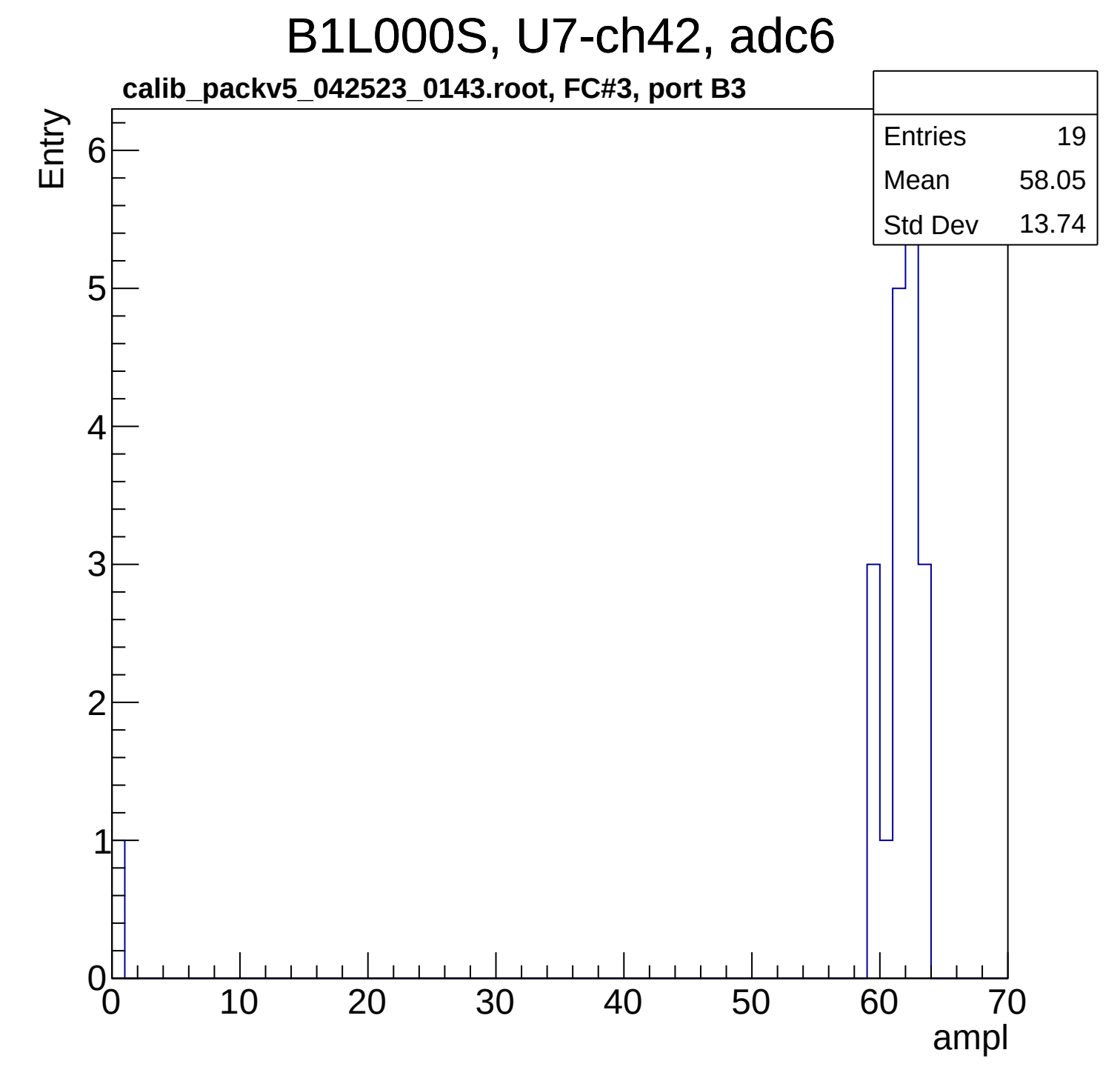
B1L000S, U7-ch42, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.05
Std Dev 13.74
Entry
ampl
0
1
2
3
4
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0
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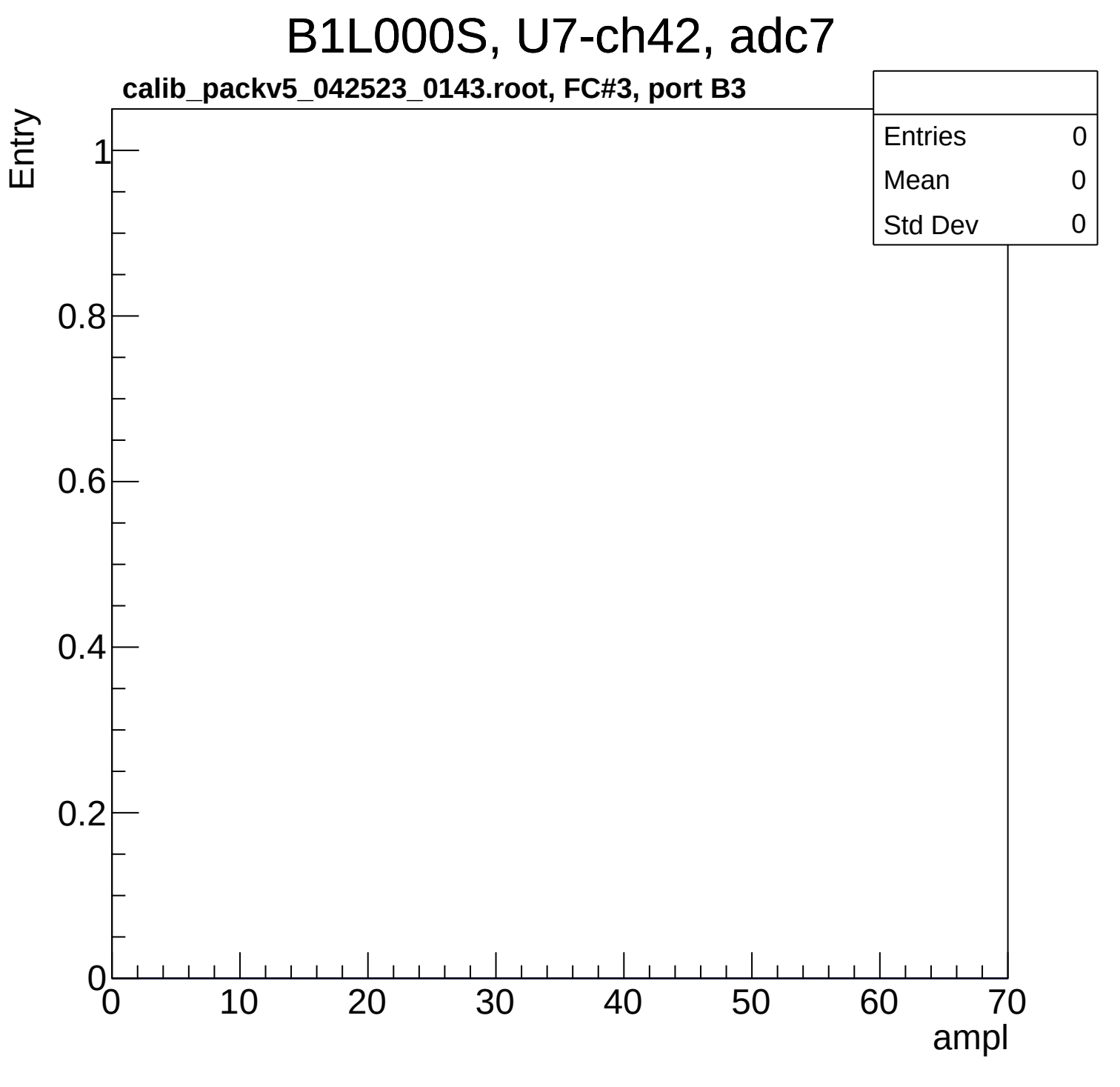
B1L000S, U7-ch42, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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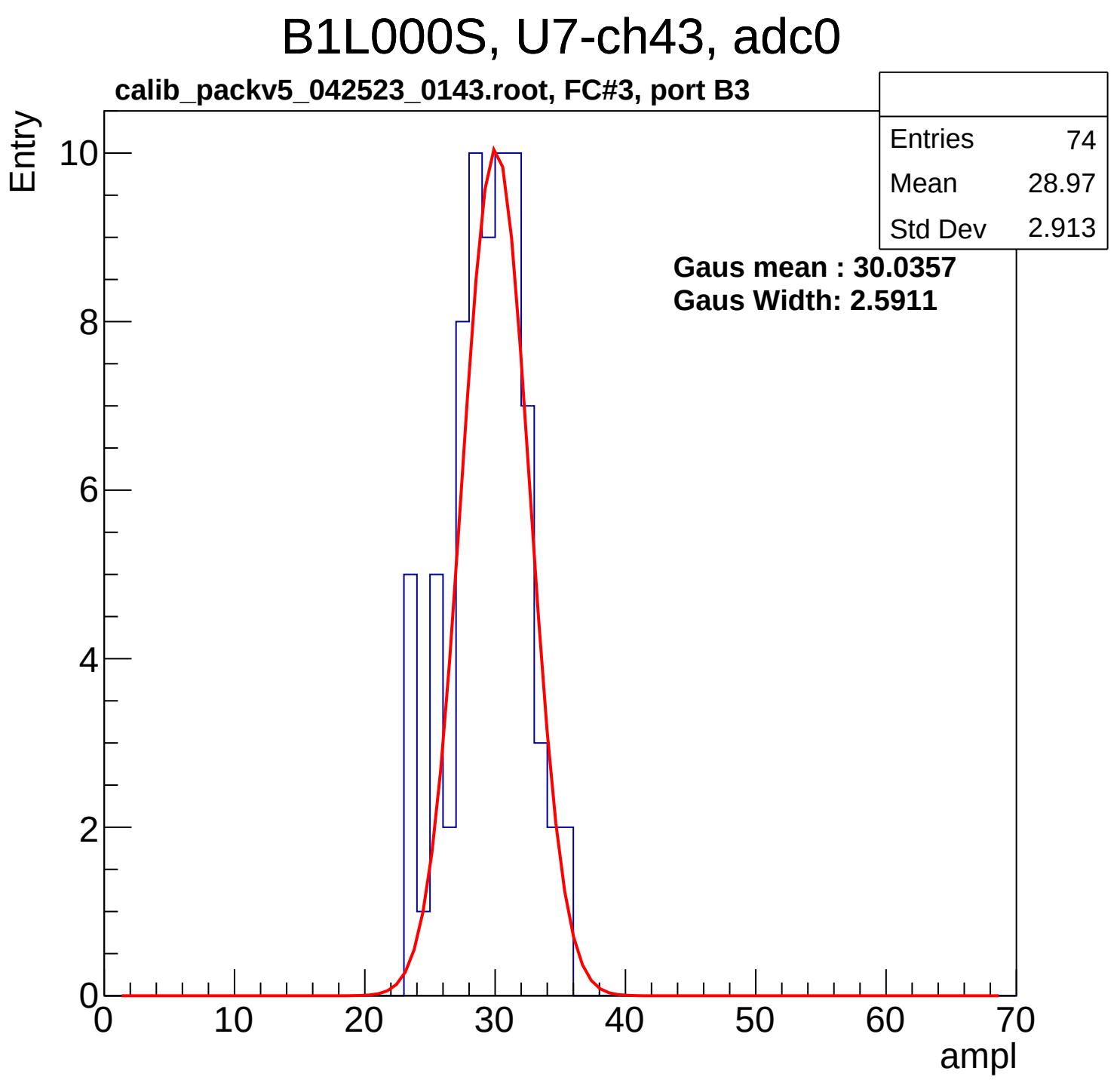
30
40
50
60
70
ampl


B1L000S, U7-ch43, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 28.97
Std Dev 2.913
Gaus mean : 30.0357
Gaus Width: 2.5911
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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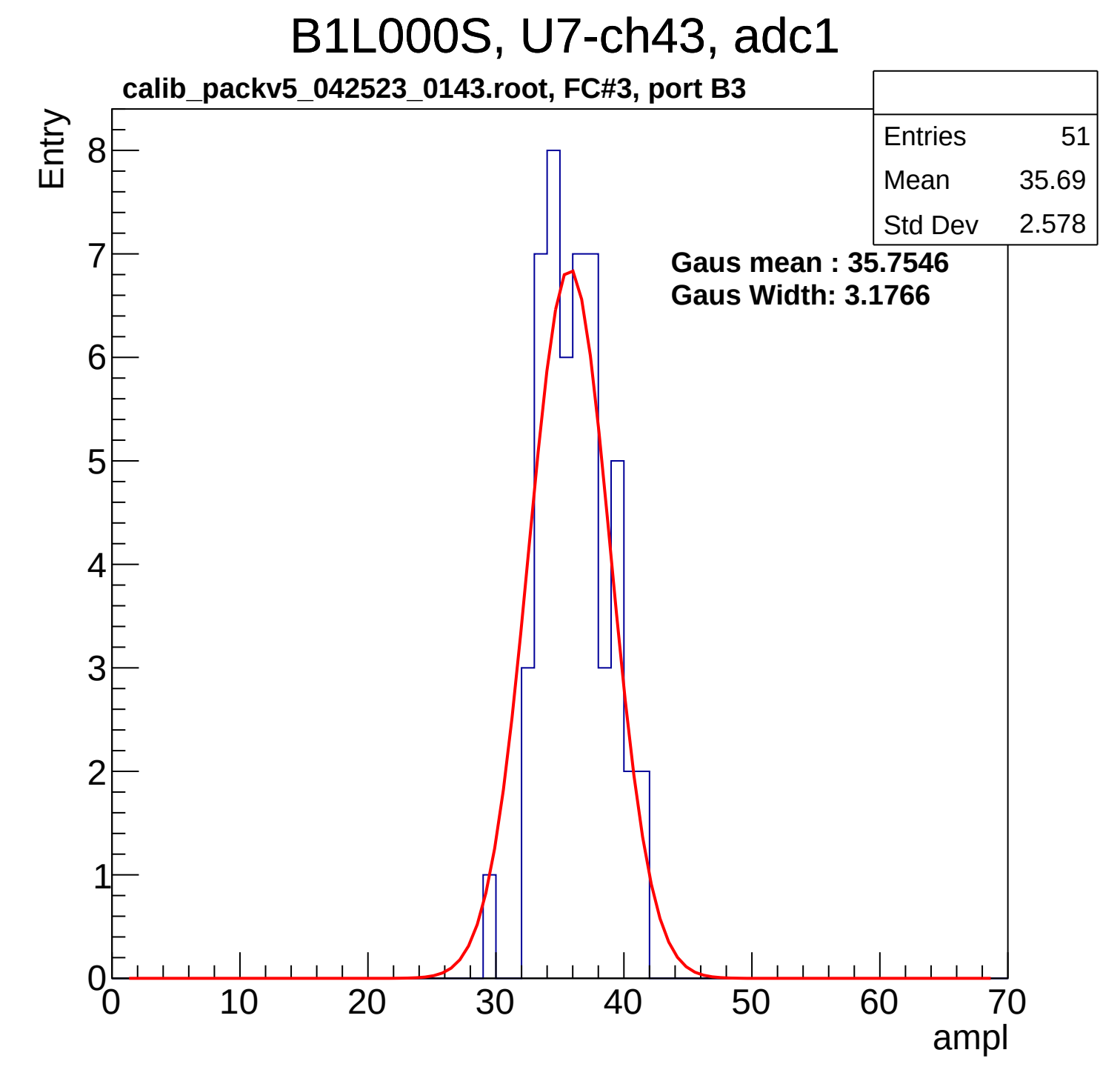
B1L000S, U7-ch43, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 35.69
Std Dev 2.578
Gaus mean : 35.7546
Gaus Width: 3.1766
Entry
ampl
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1
2
3
4
5
6
7
8
0
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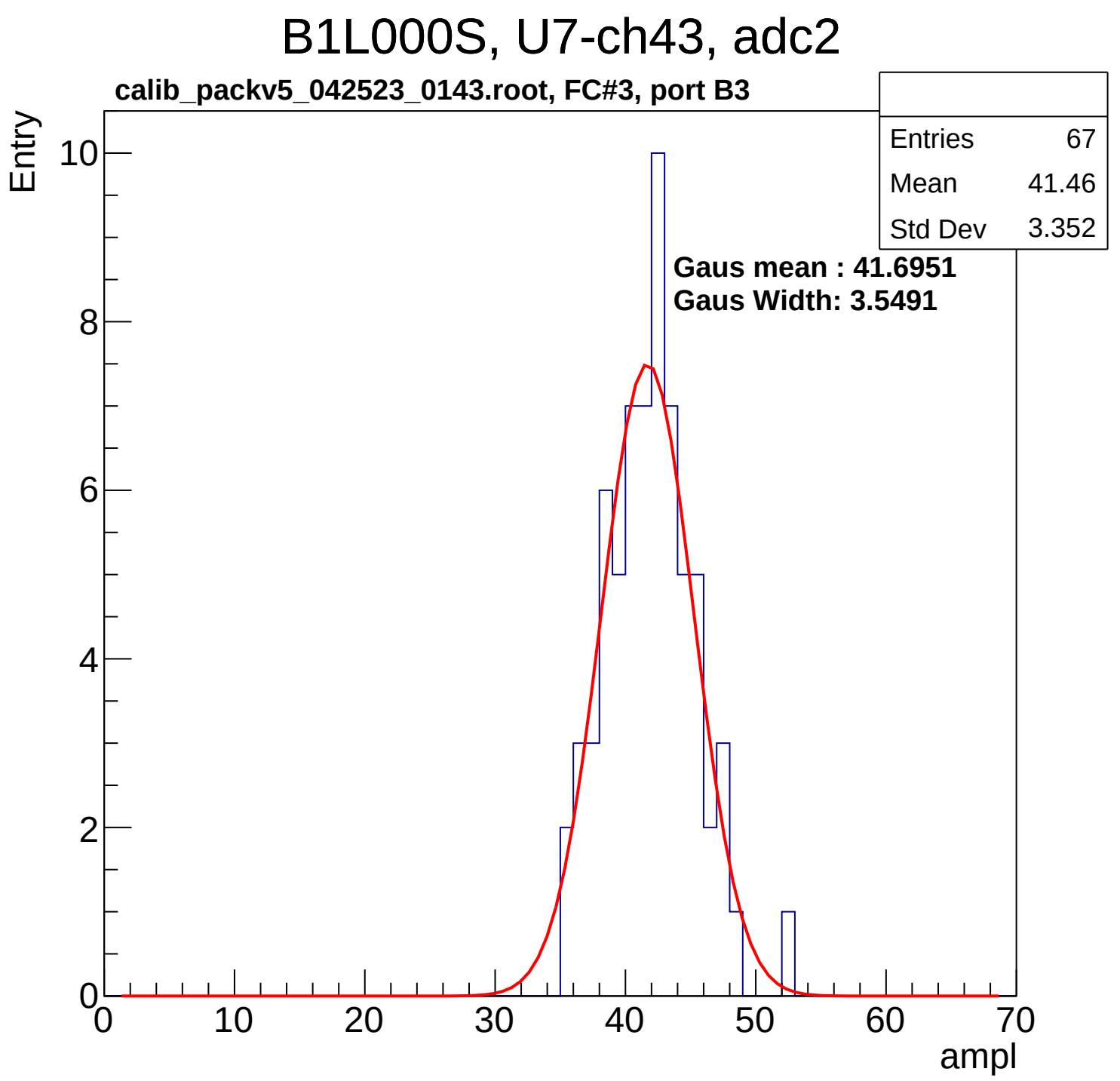
B1L000S, U7-ch43, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.46
Std Dev 3.352
Gaus mean : 41.6951
Gaus Width: 3.5491
Entry
ampl
0
2
4
6
8
10
0
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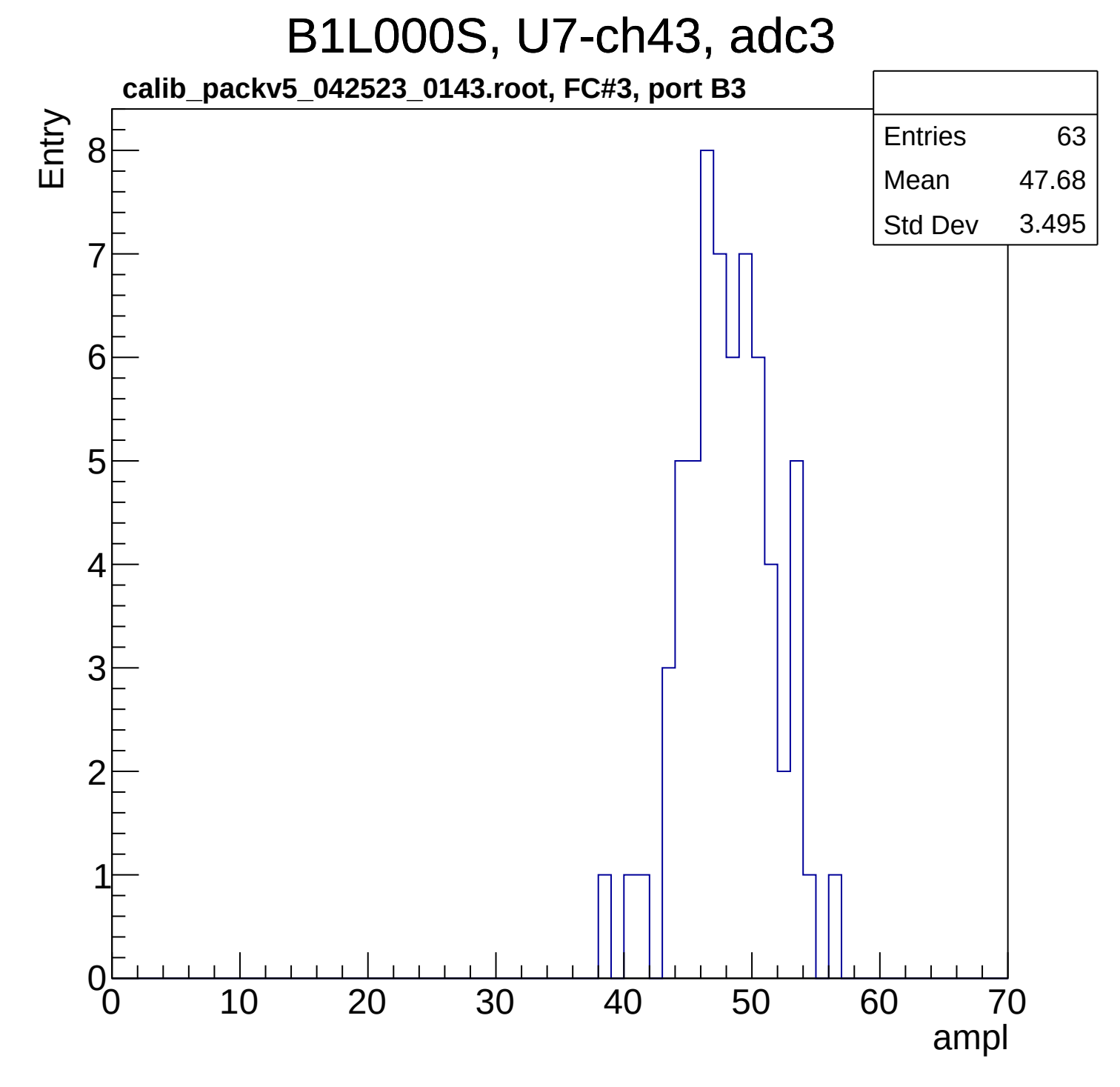
B1L000S, U7-ch43, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 47.68
Std Dev 3.495
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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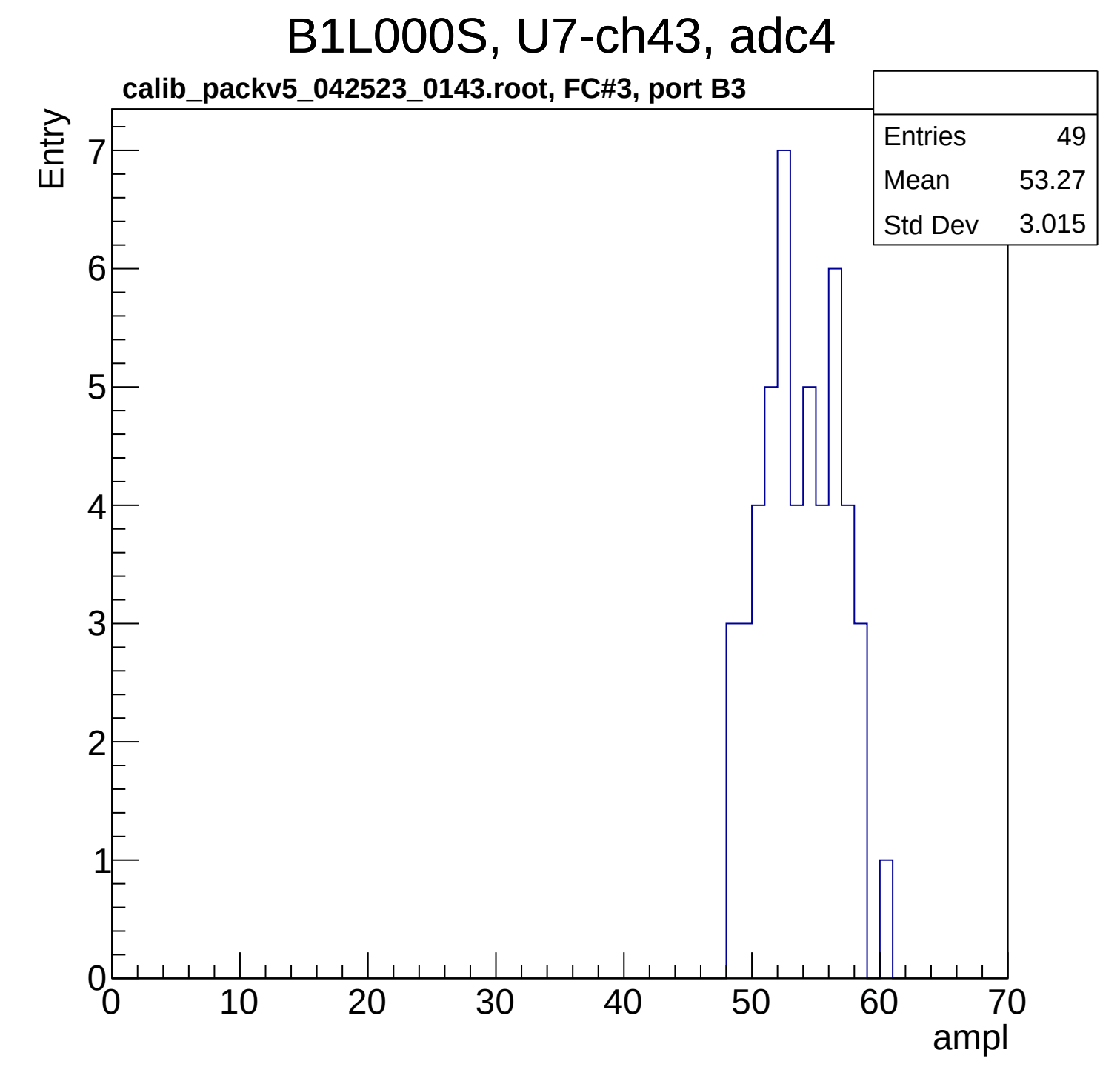
B1L000S, U7-ch43, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.27
Std Dev 3.015
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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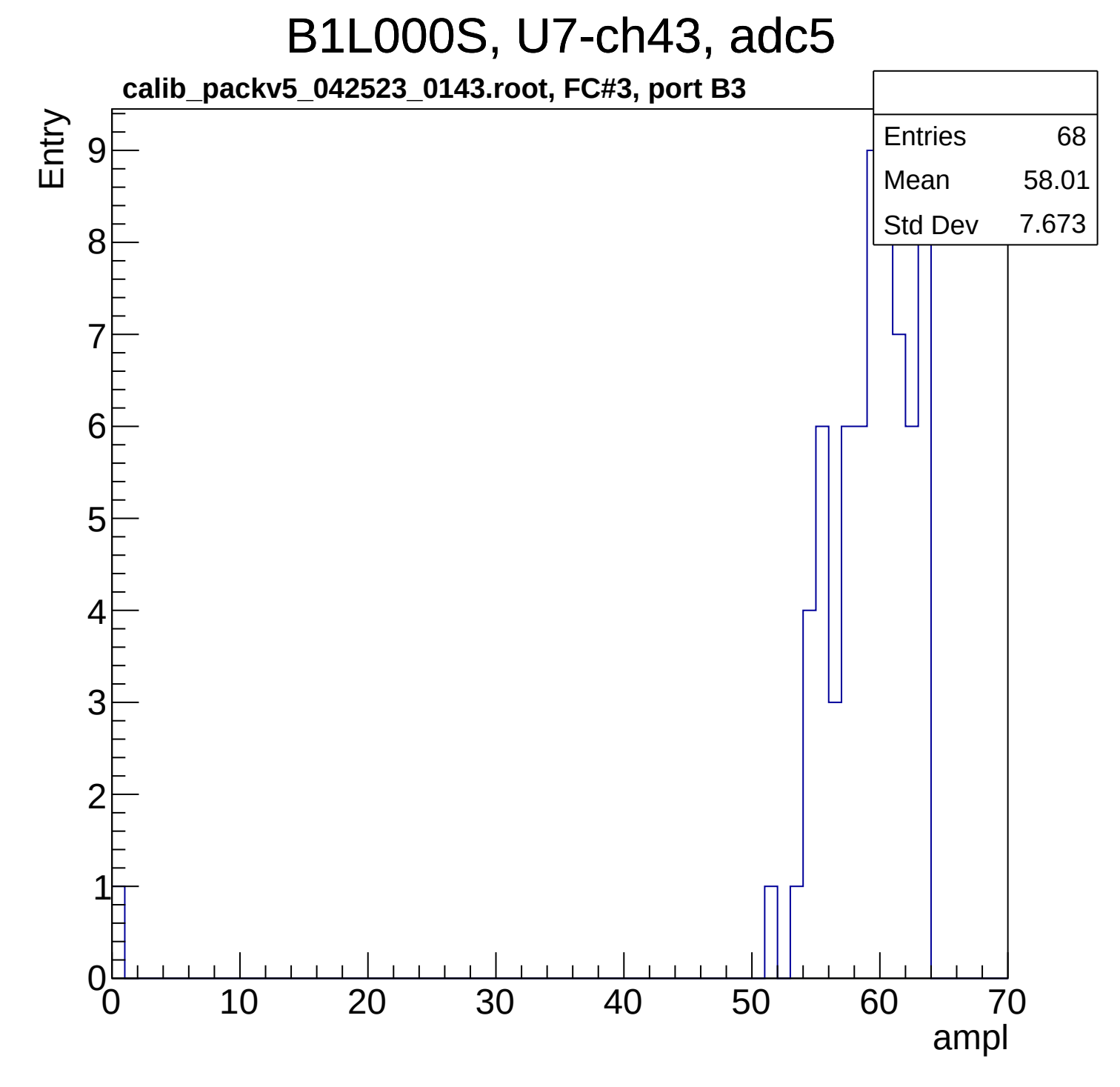
B1L000S, U7-ch43, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 58.01
Std Dev 7.673
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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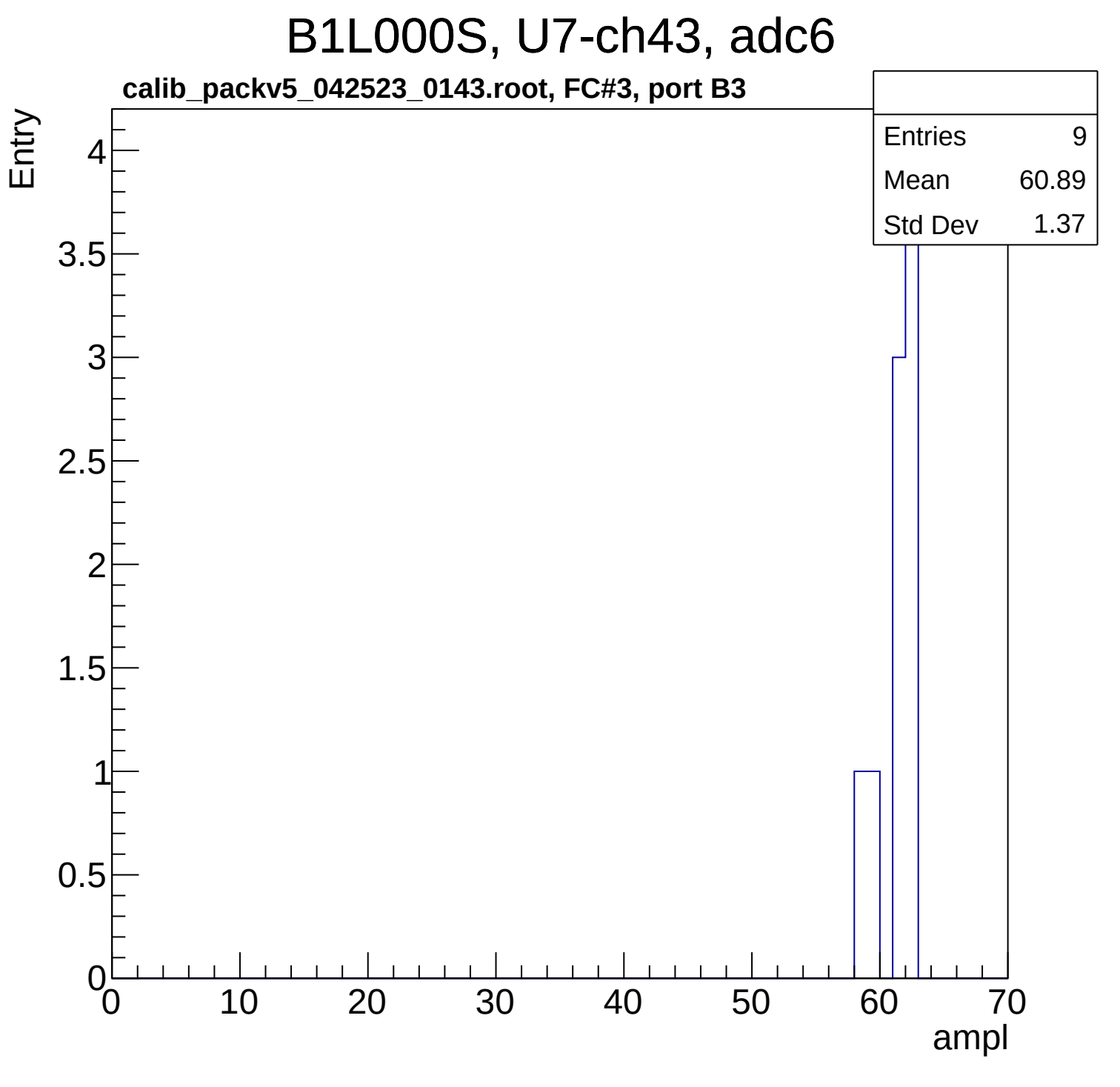
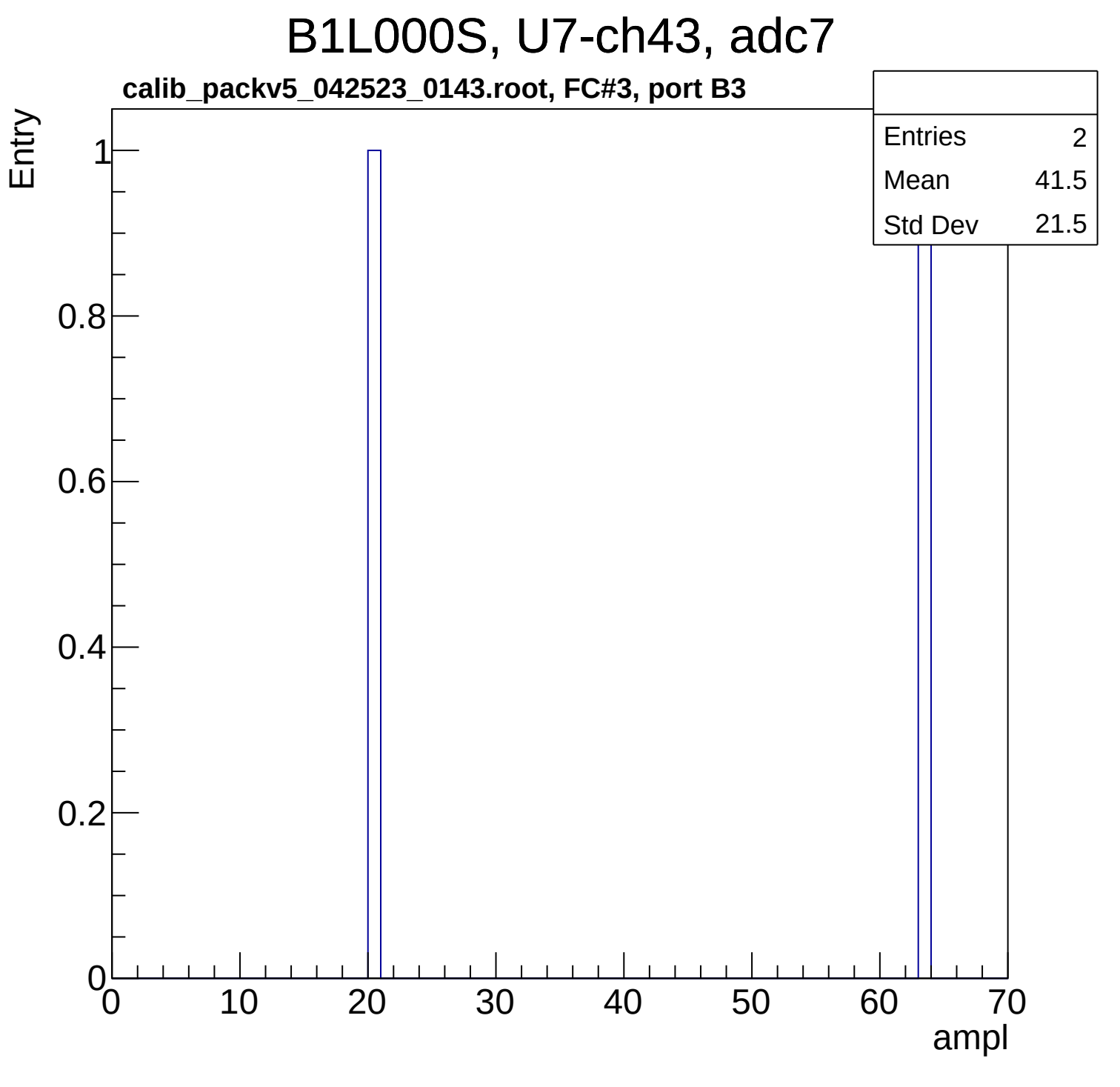

B1L000S, U7-ch43, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 60.89
Std Dev 1.37
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch43, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
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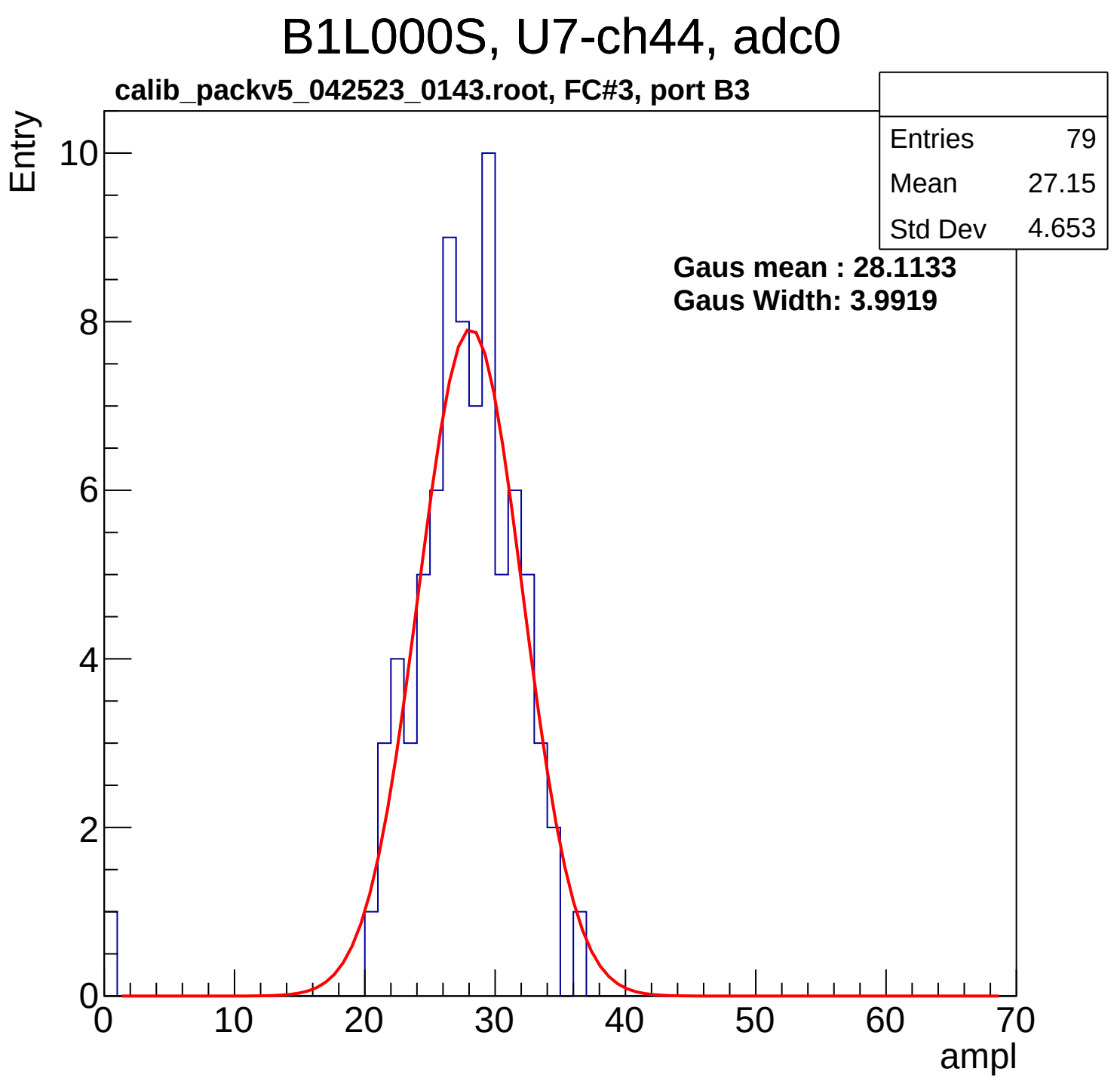
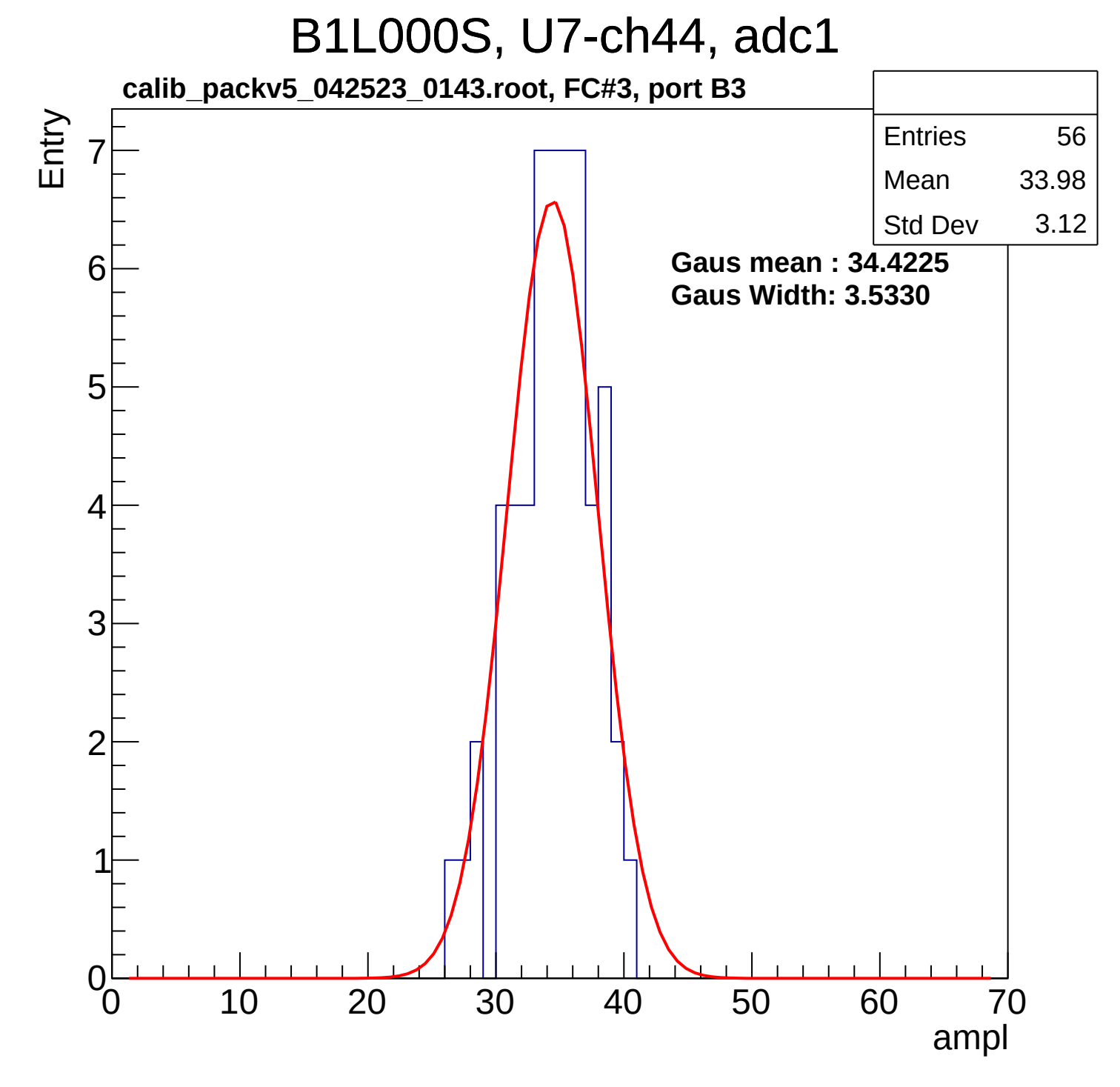
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch44, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 27.15
Std Dev 4.653
Gaus mean : 28.1133
Gaus Width: 3.9919
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch44, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 33.98
Std Dev 3.12
Gaus mean : 34.4225
Gaus Width: 3.5330
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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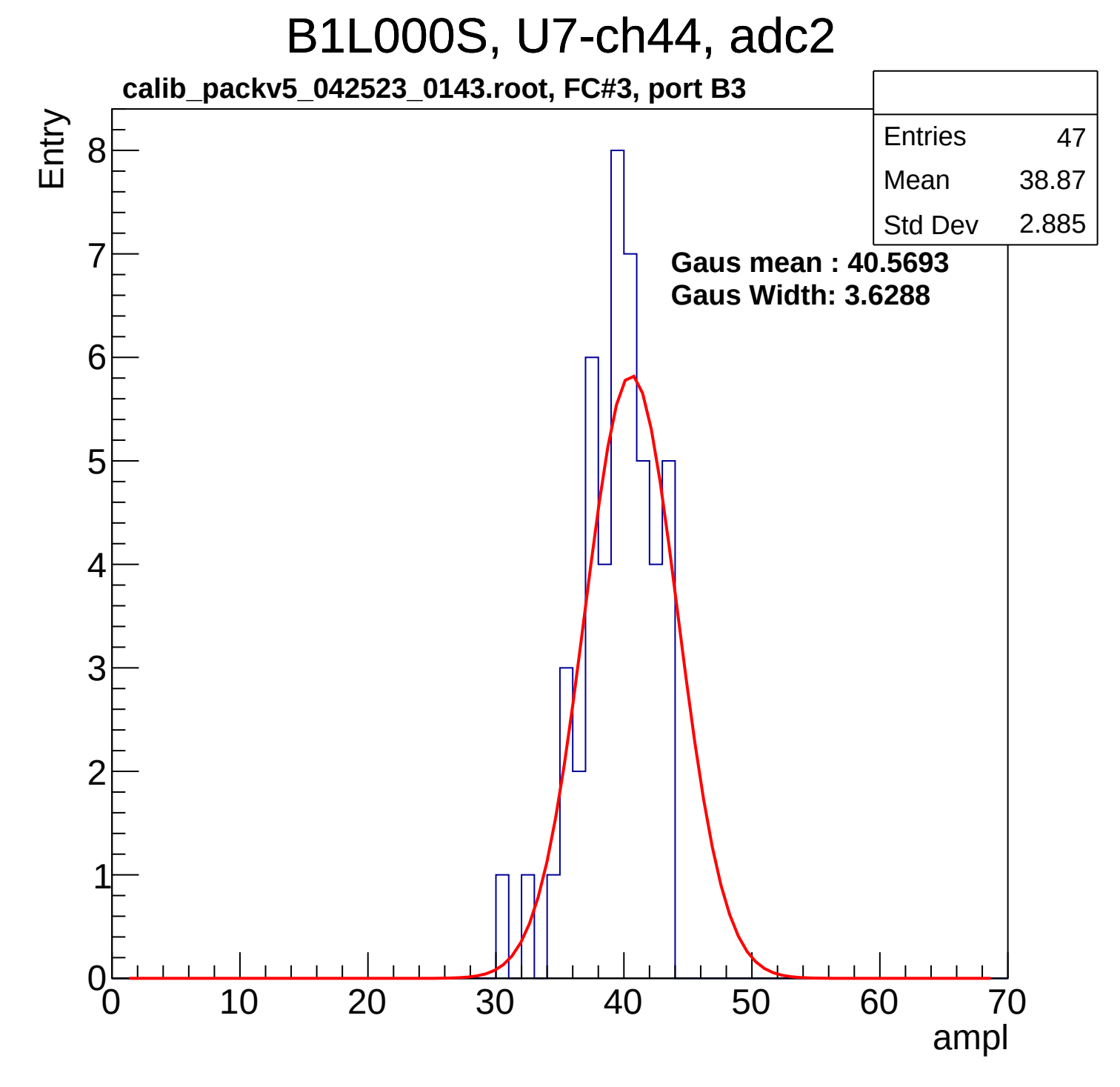
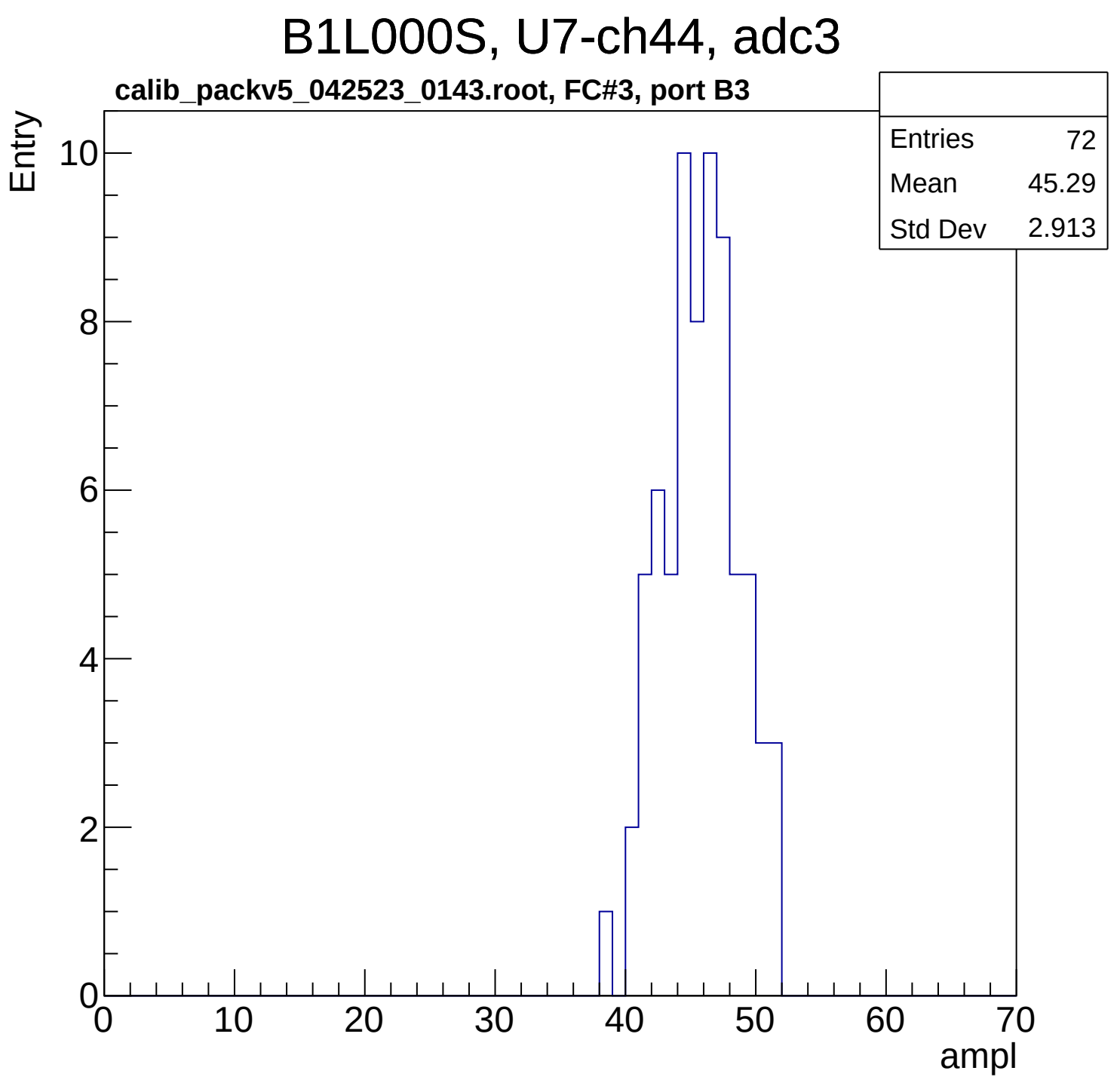
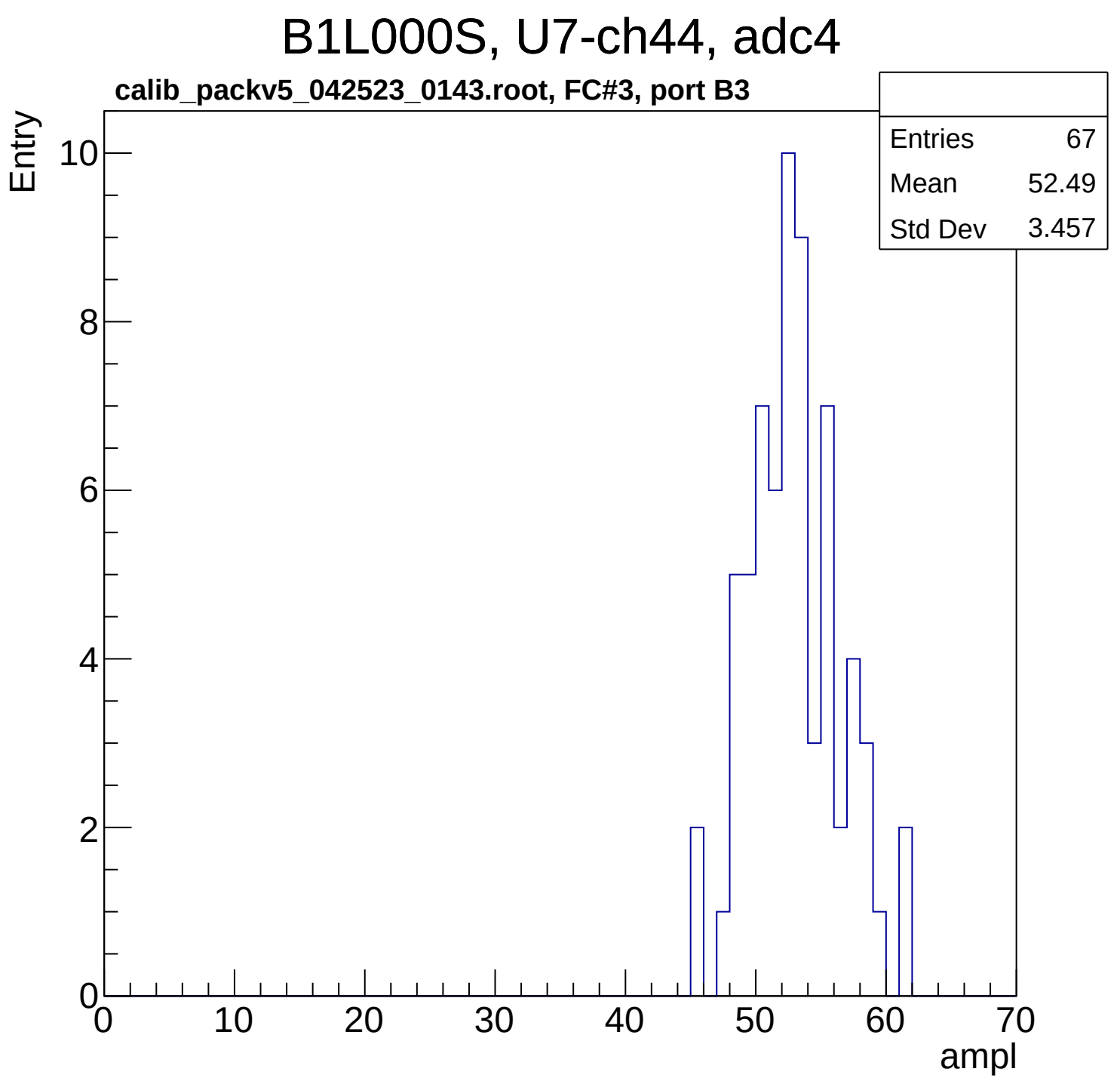
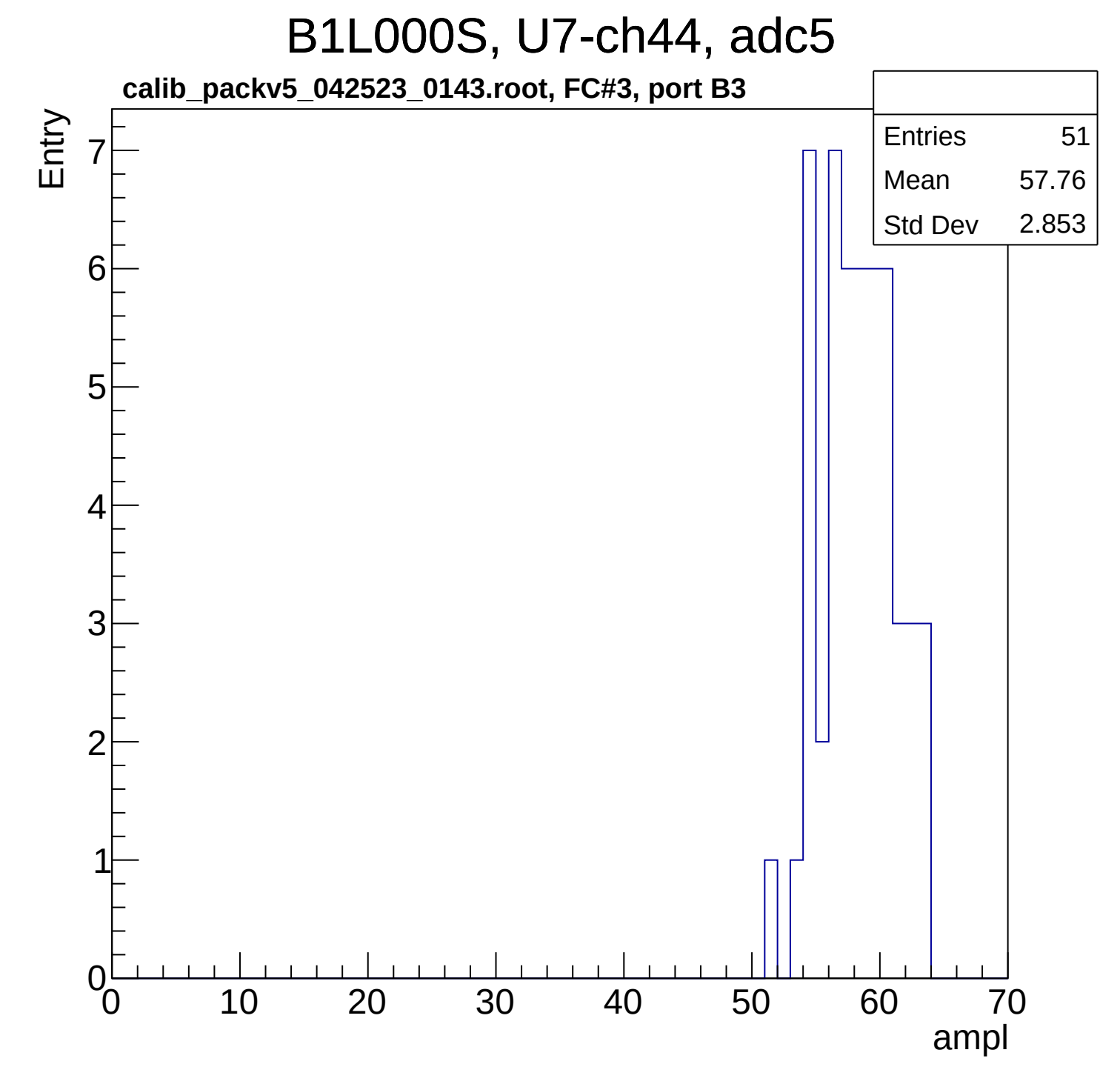
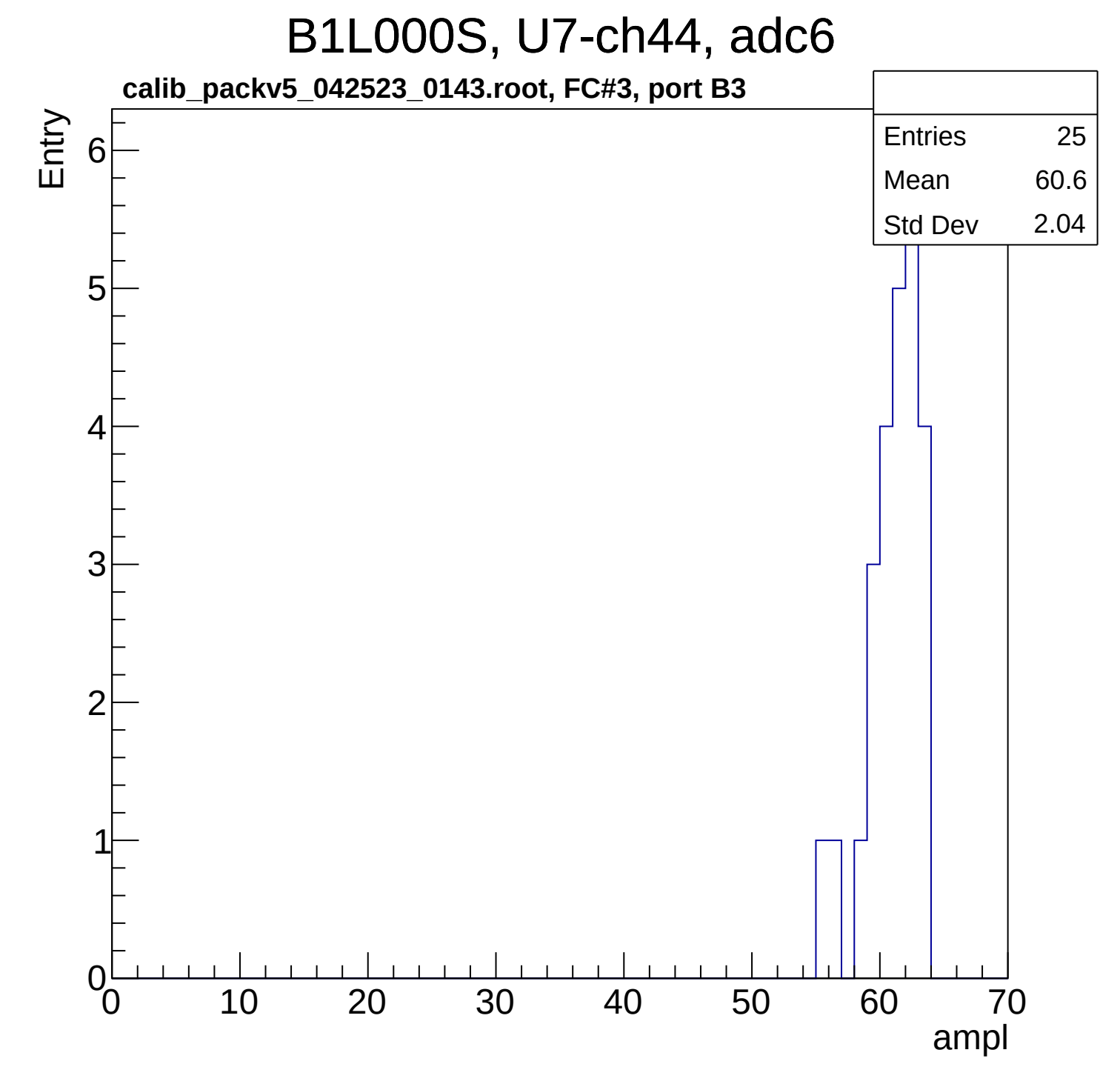
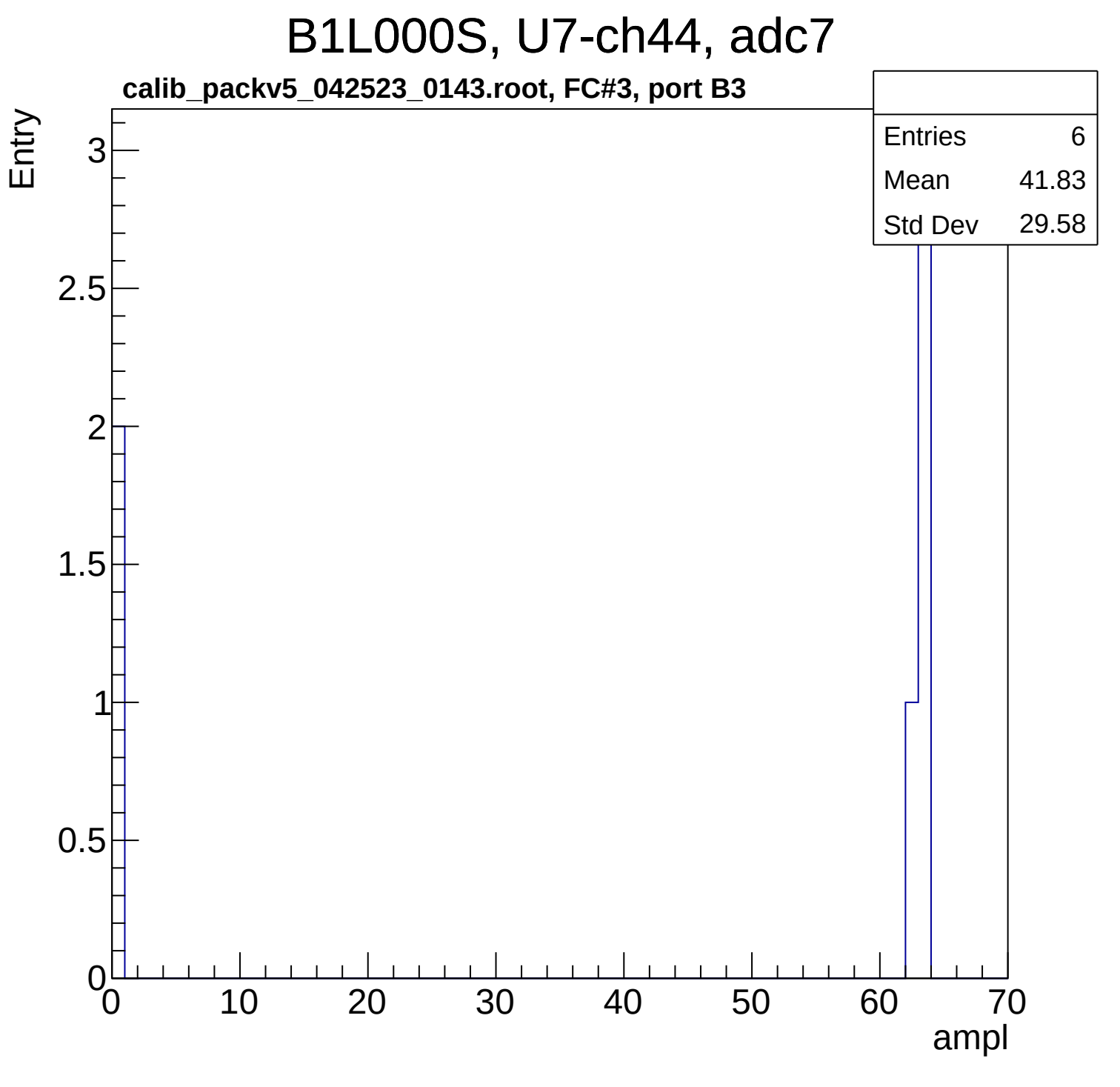

B1L000S, U7-ch44, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 38.87
Std Dev 2.885
Gaus mean : 40.5693
Gaus Width: 3.6288
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch44, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 45.29
Std Dev 2.913
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch44, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 52.49
Std Dev 3.457
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch44, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 57.76
Std Dev 2.853
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch44, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 60.6
Std Dev 2.04
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch44, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 41.83
Std Dev 29.58
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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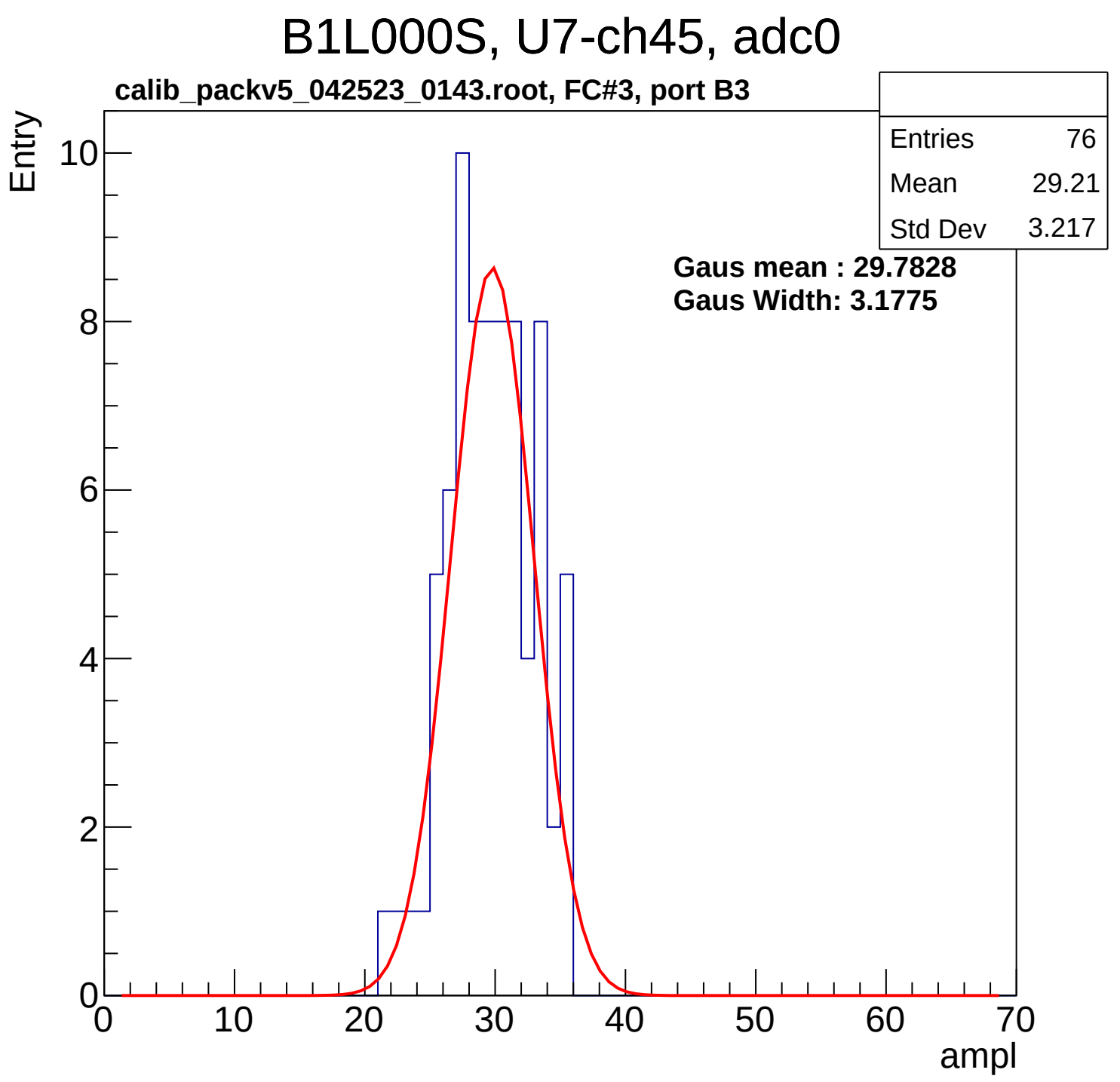
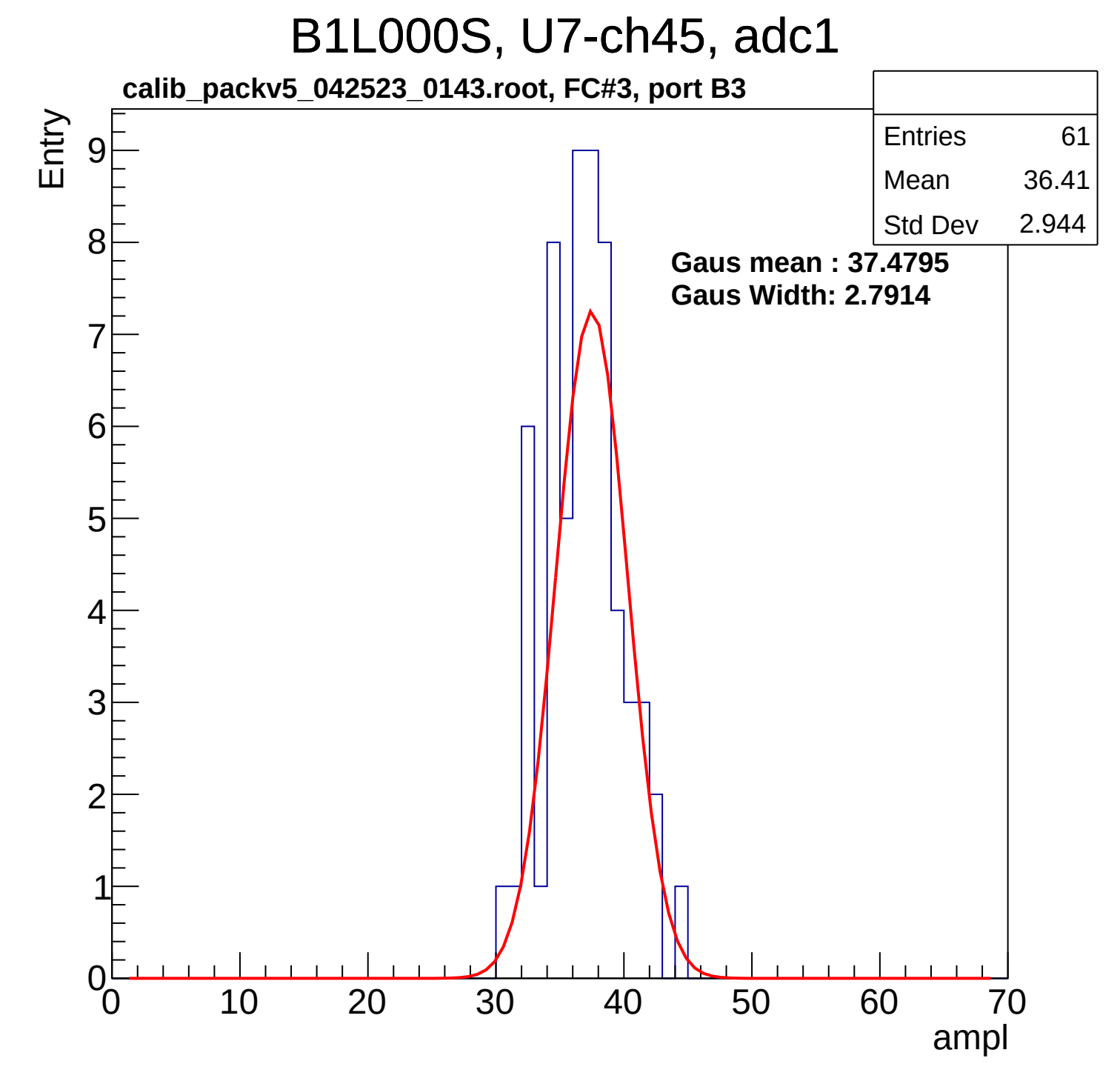
60
70


B1L000S, U7-ch45, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 29.21
Std Dev 3.217
Gaus mean : 29.7828
Gaus Width: 3.1775
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch45, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 36.41
Std Dev 2.944
Gaus mean : 37.4795
Gaus Width: 2.7914
Entry
ampl
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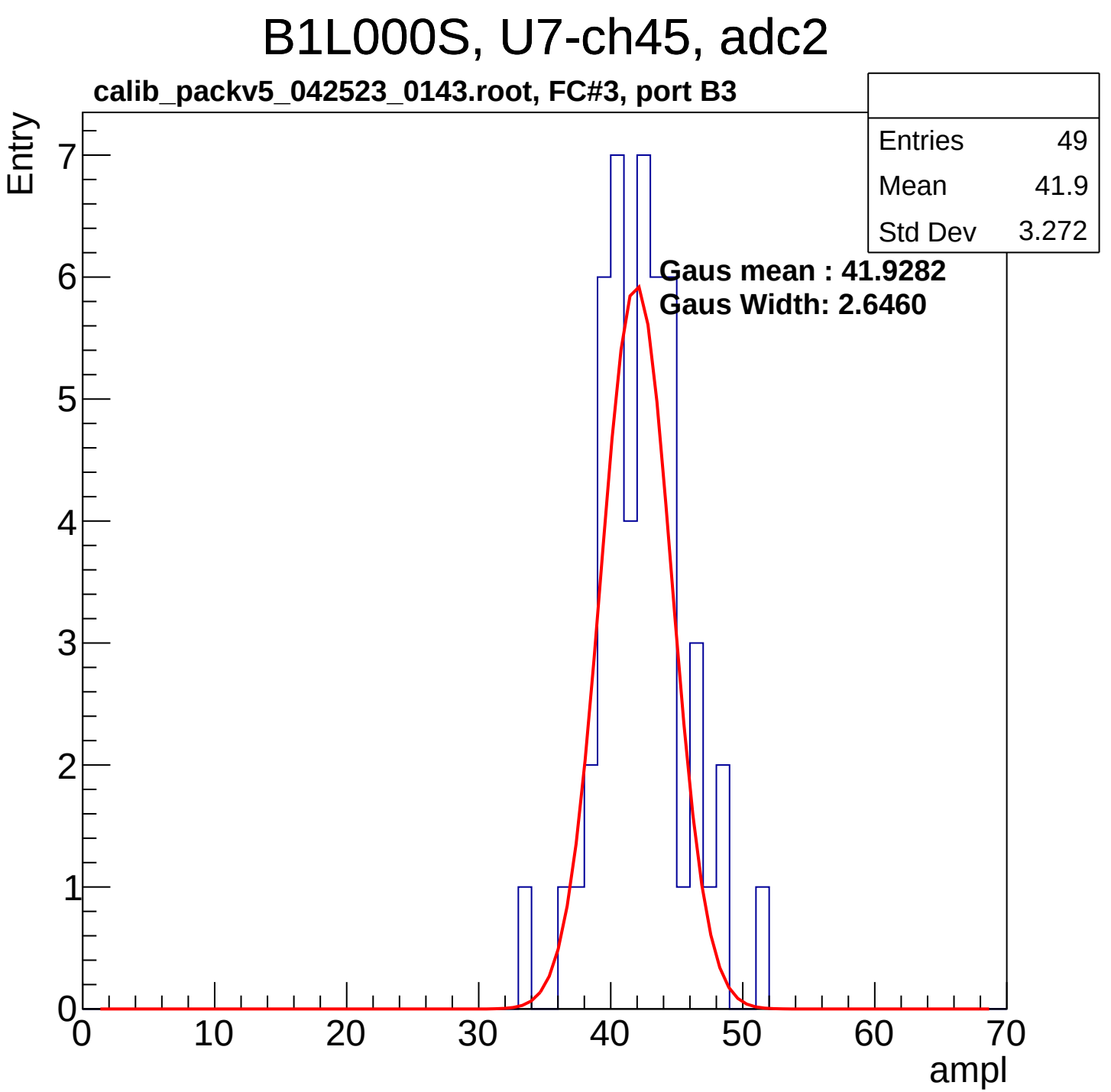
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch45, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 41.9
Std Dev 3.272
Gaus mean : 41.9282
Gaus Width: 2.6460
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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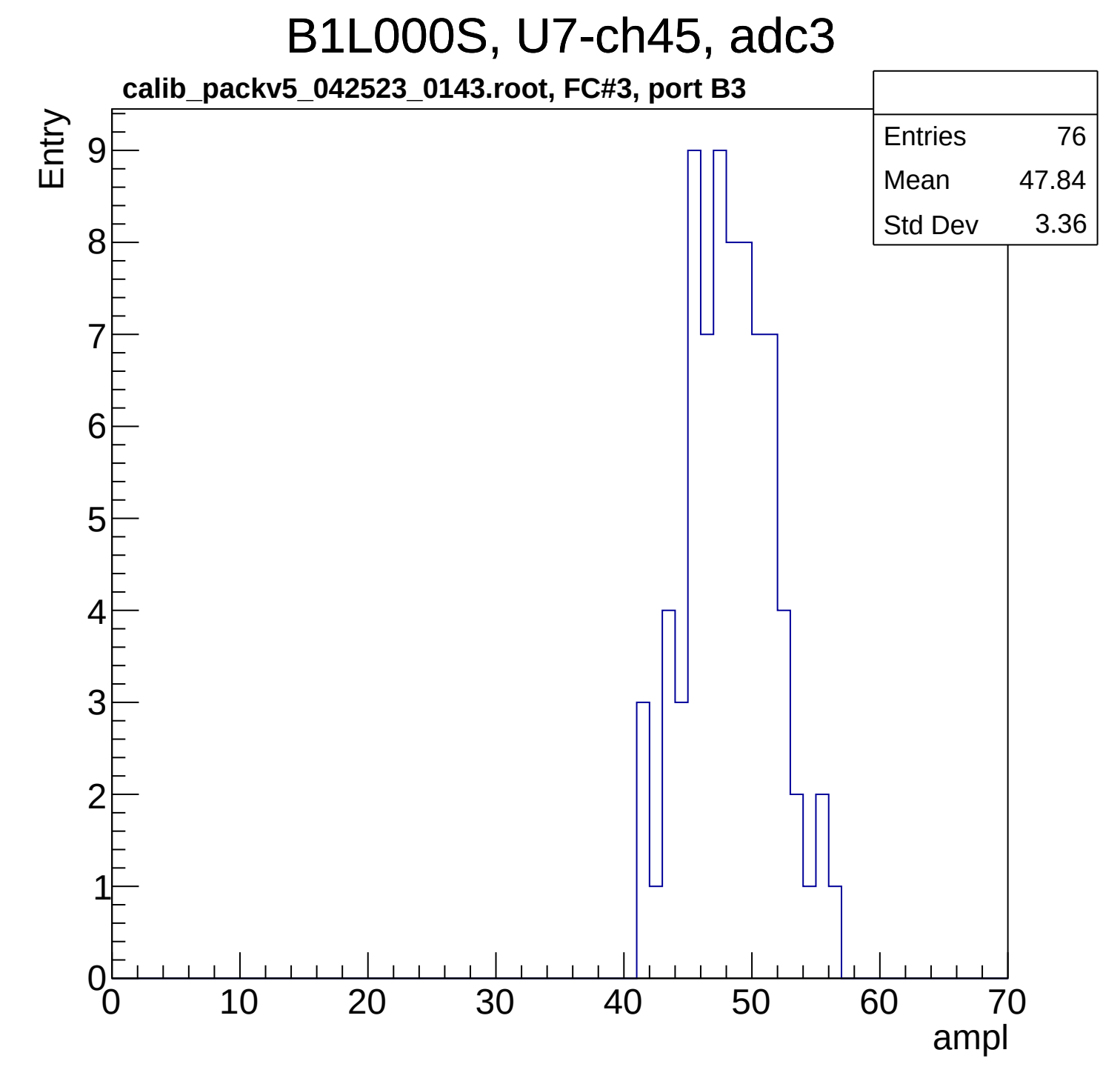
B1L000S, U7-ch45, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 47.84
Std Dev 3.36
Entry
ampl
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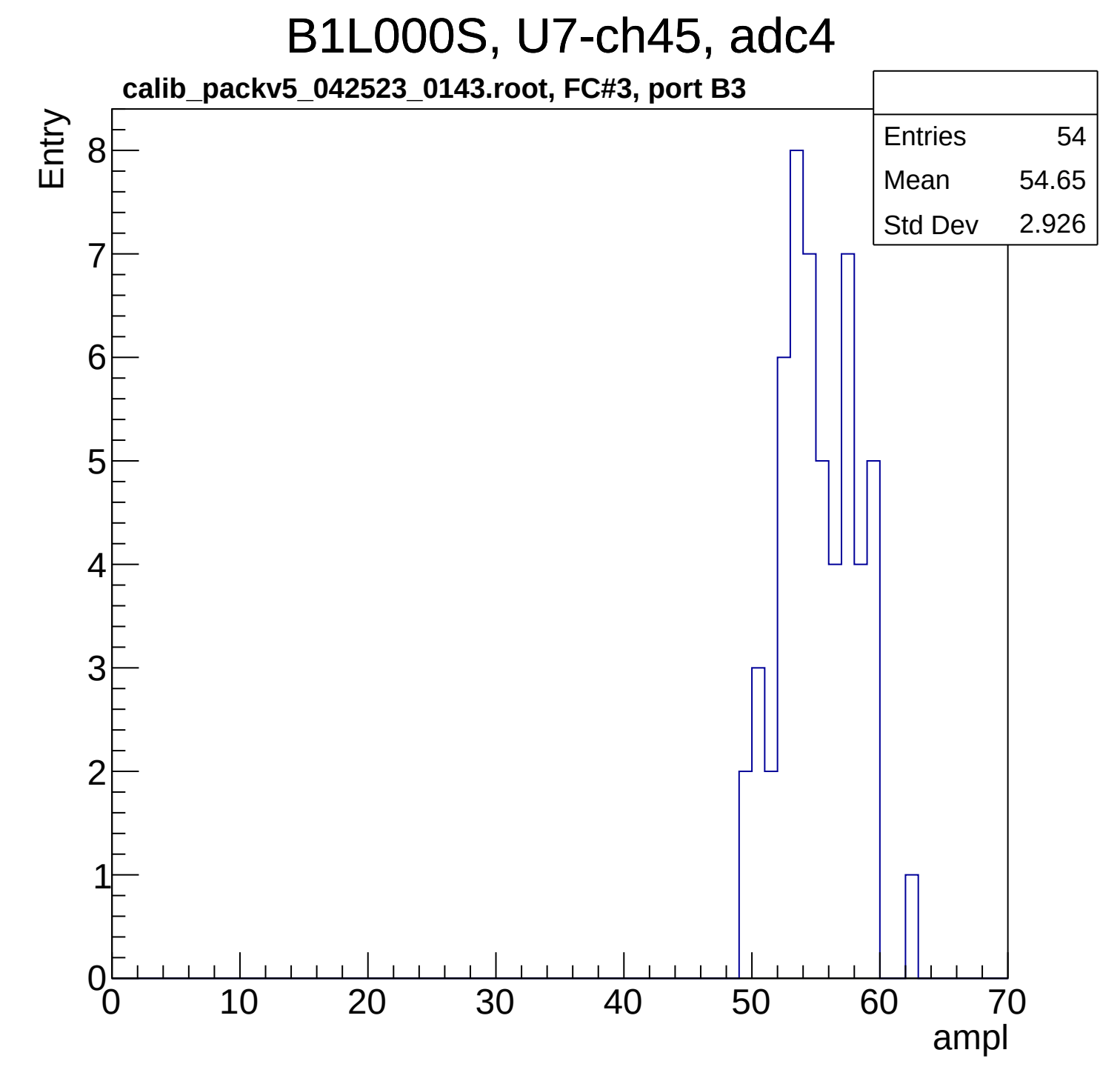
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch45, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.65
Std Dev 2.926
Entry
ampl
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5
6
7
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0
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40
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60
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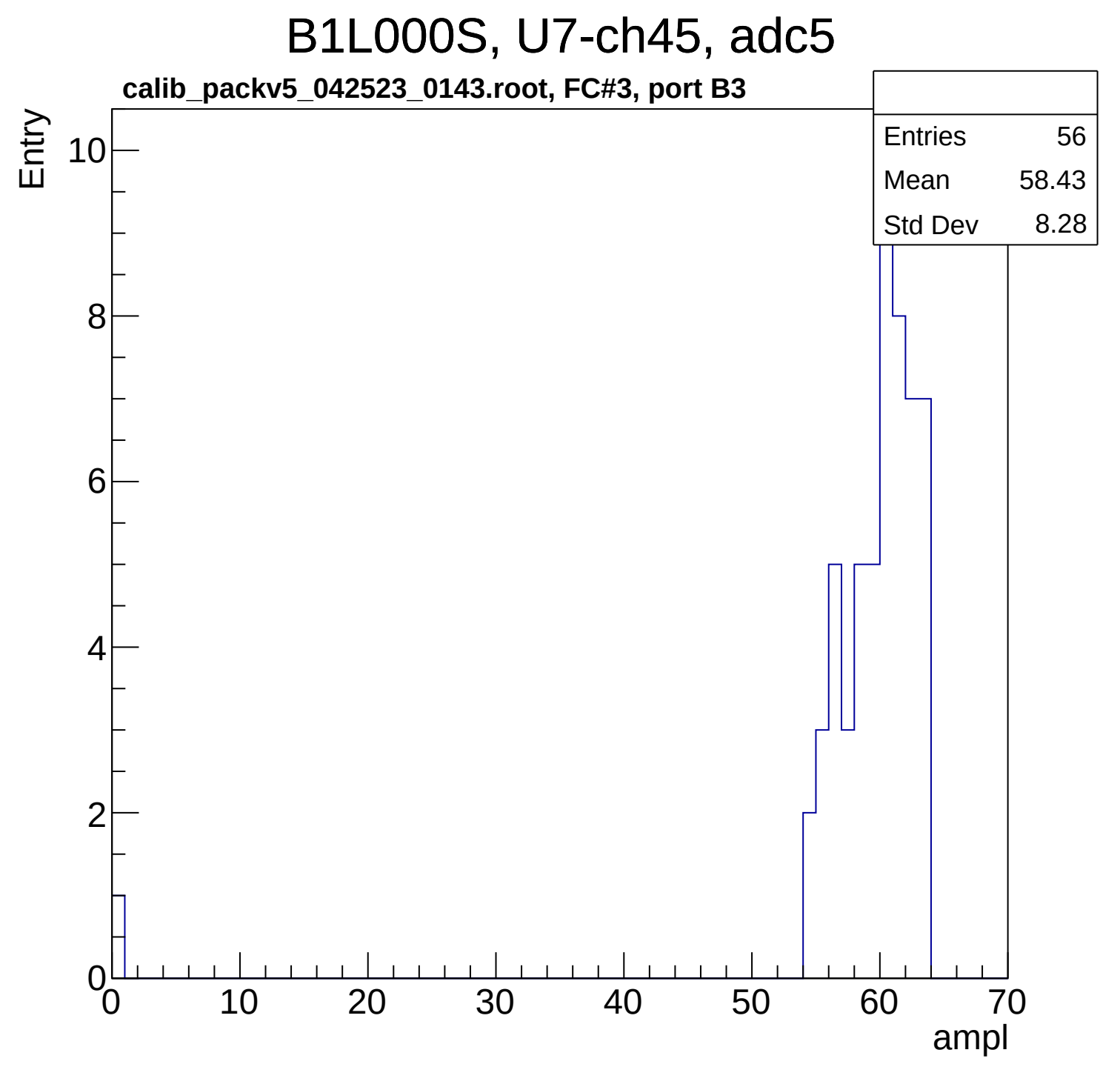
B1L000S, U7-ch45, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 58.43
Std Dev 8.28
Entry
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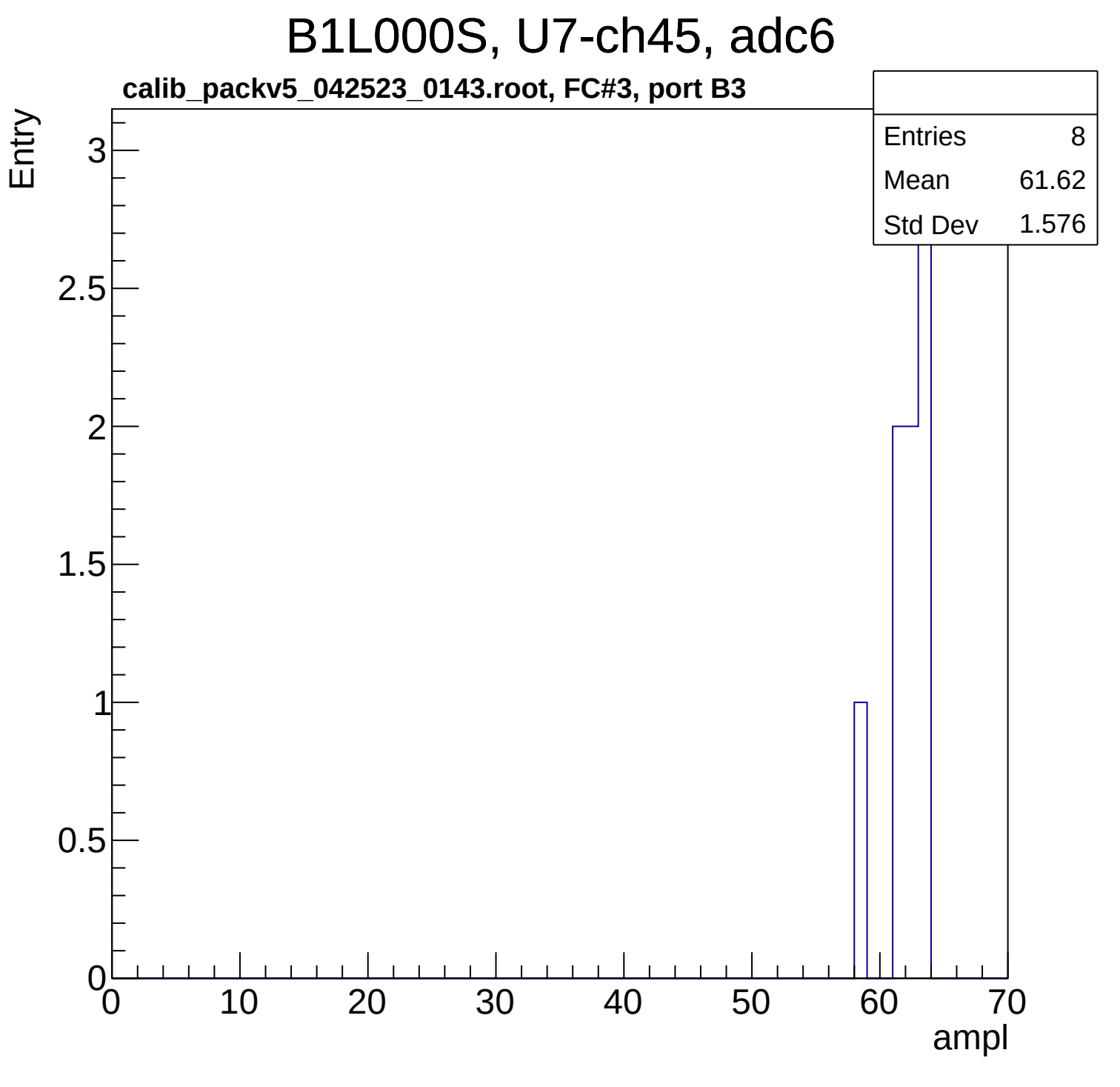
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch45, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.62
Std Dev 1.576
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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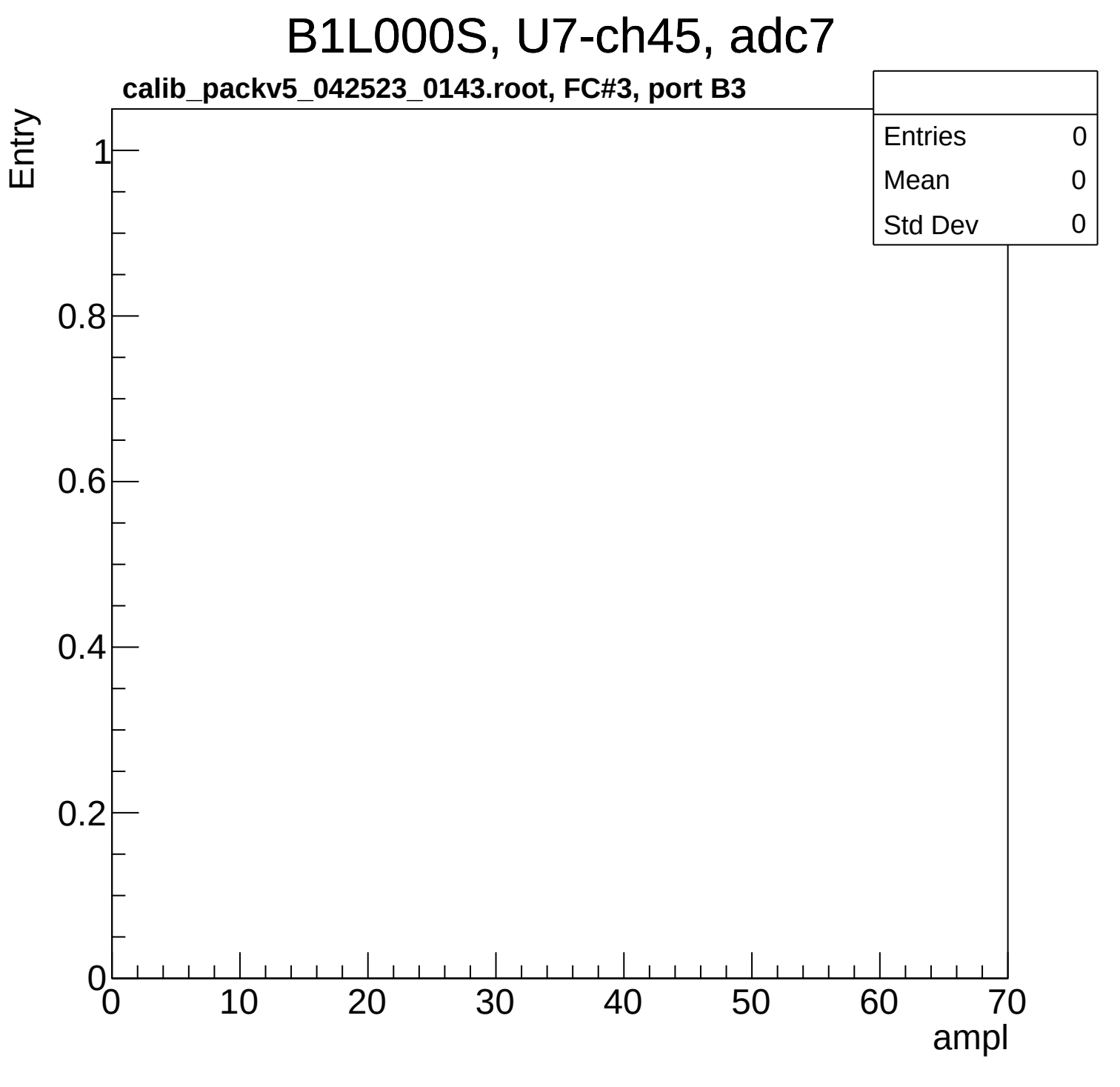
20
30
40
50
60
70


B1L000S, U7-ch45, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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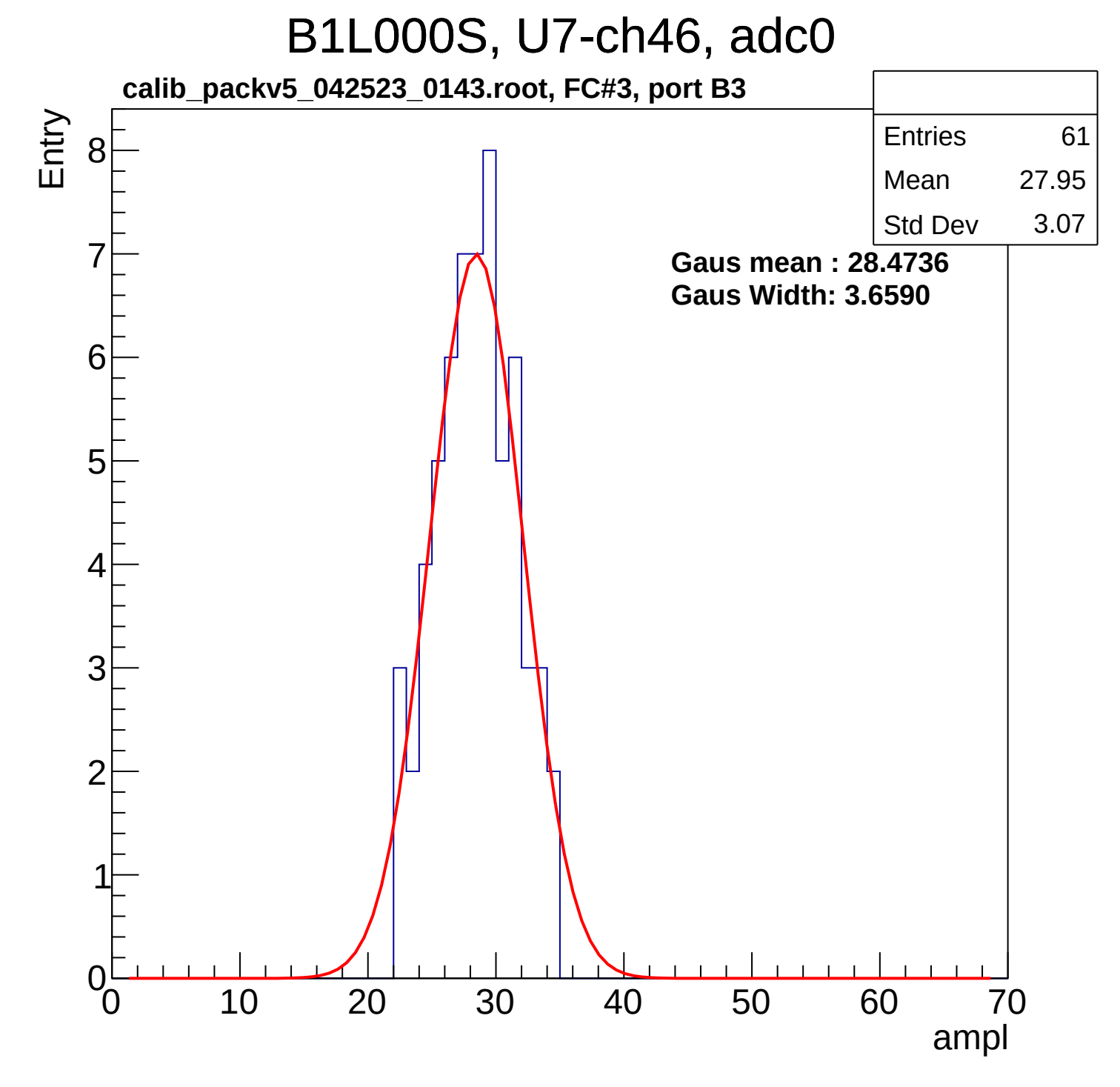
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch46, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 27.95
Std Dev 3.07
Gaus mean : 28.4736
Gaus Width: 3.6590
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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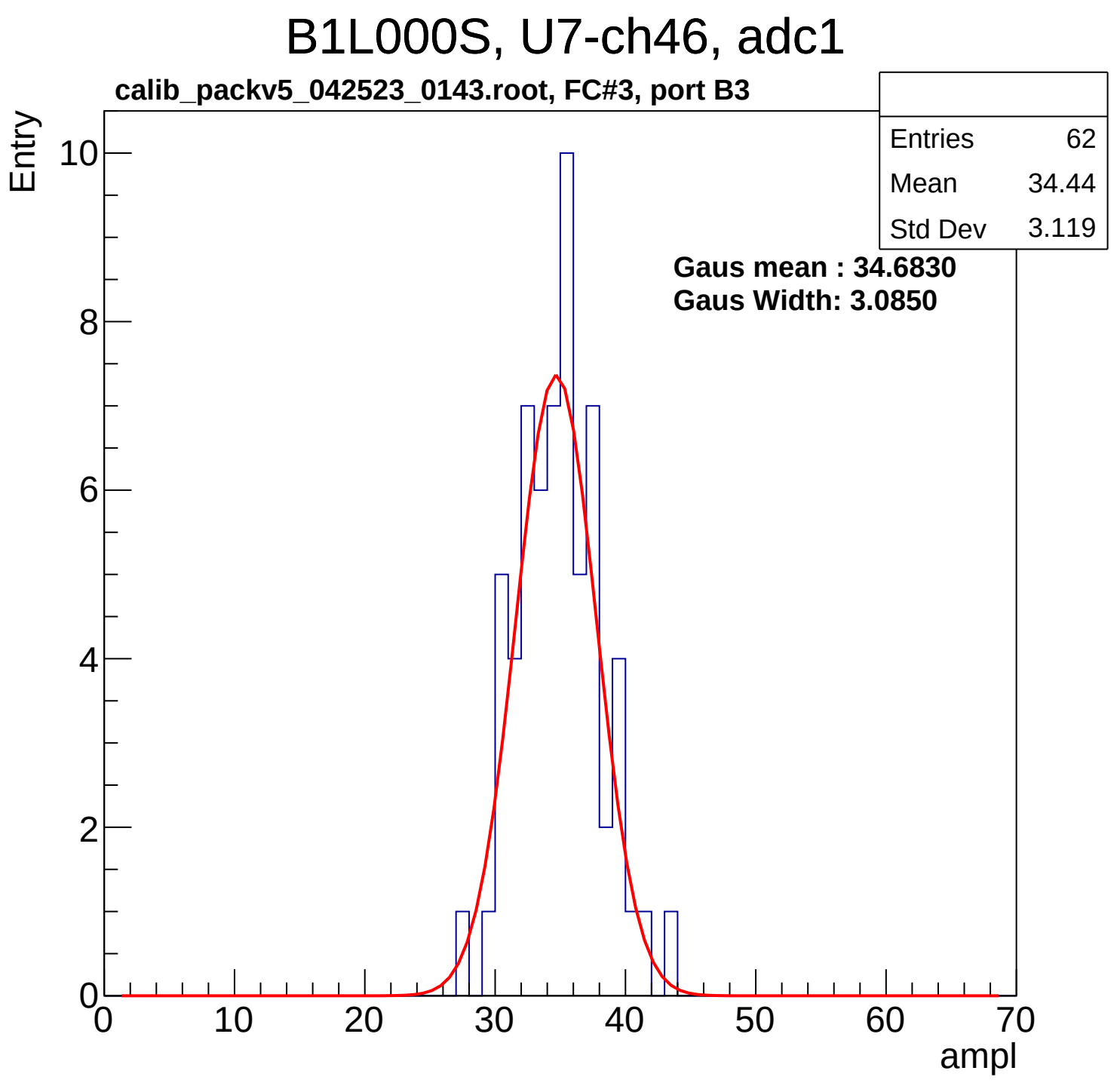
B1L000S, U7-ch46, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 34.44
Std Dev 3.119
Gaus mean : 34.6830
Gaus Width: 3.0850
Entry
ampl
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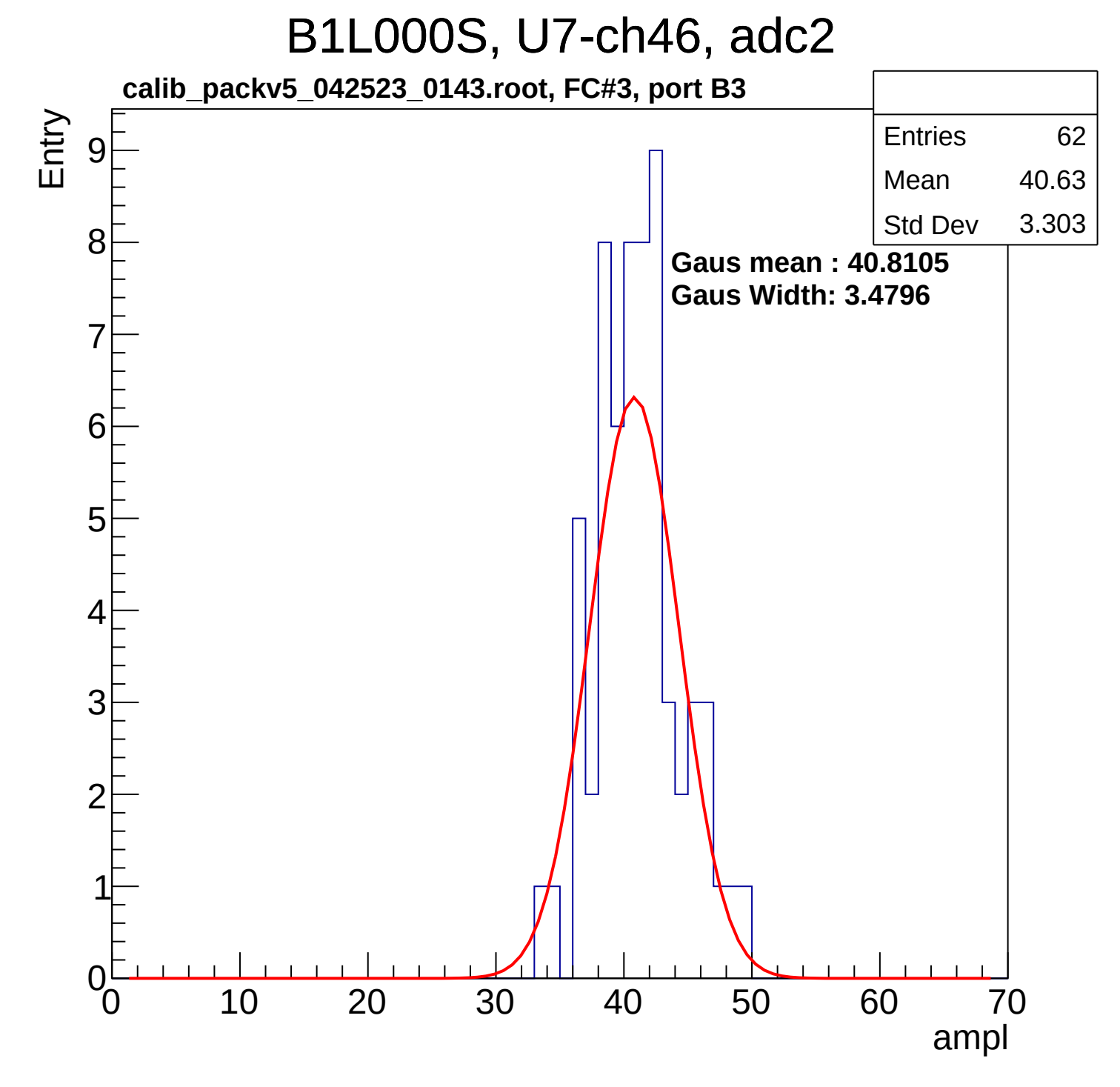
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch46, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 40.63
Std Dev 3.303
Gaus mean : 40.8105
Gaus Width: 3.4796
Entry
ampl
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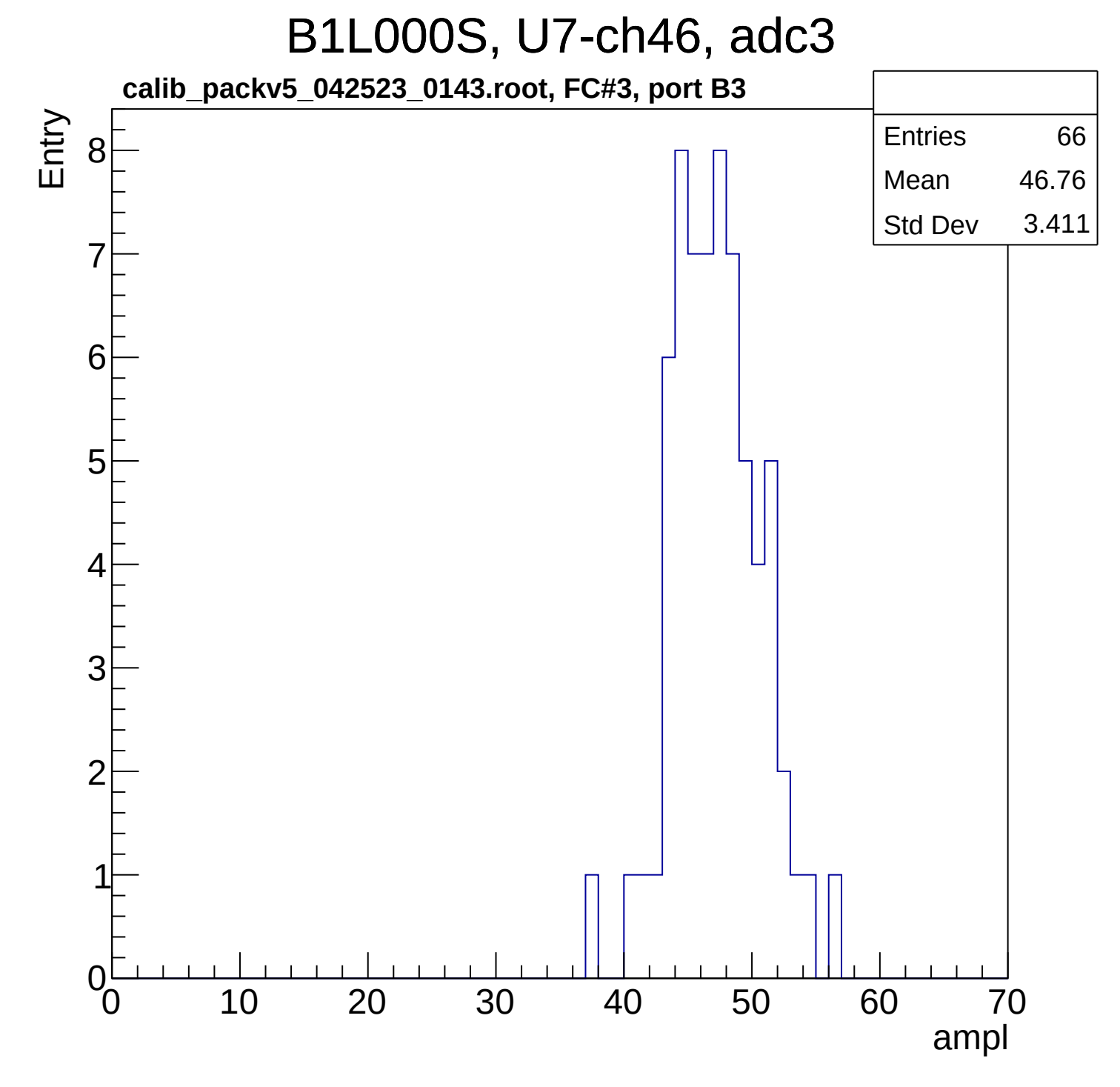
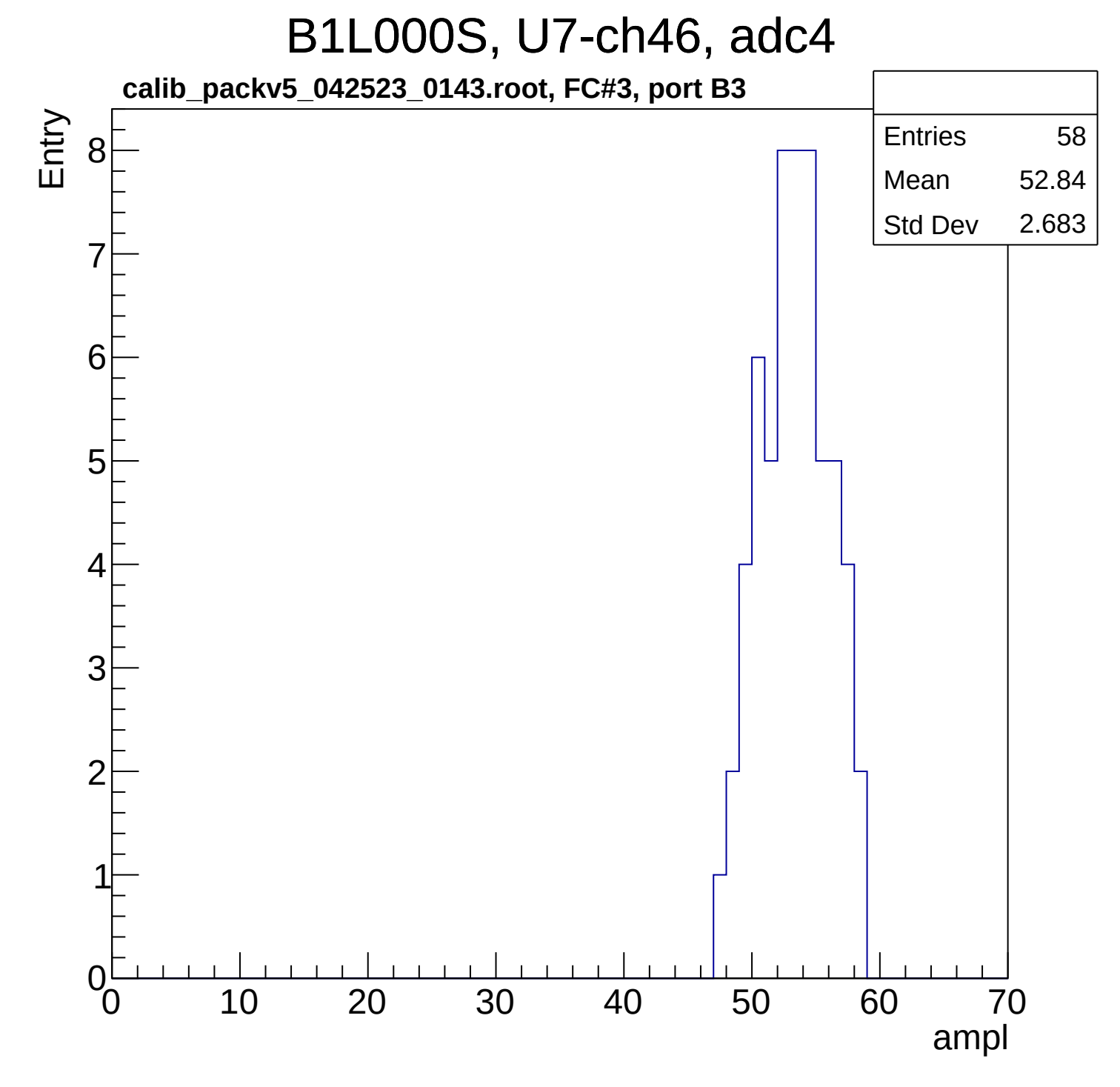
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch46, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 46.76
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch46, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 52.84
Std Dev 2.683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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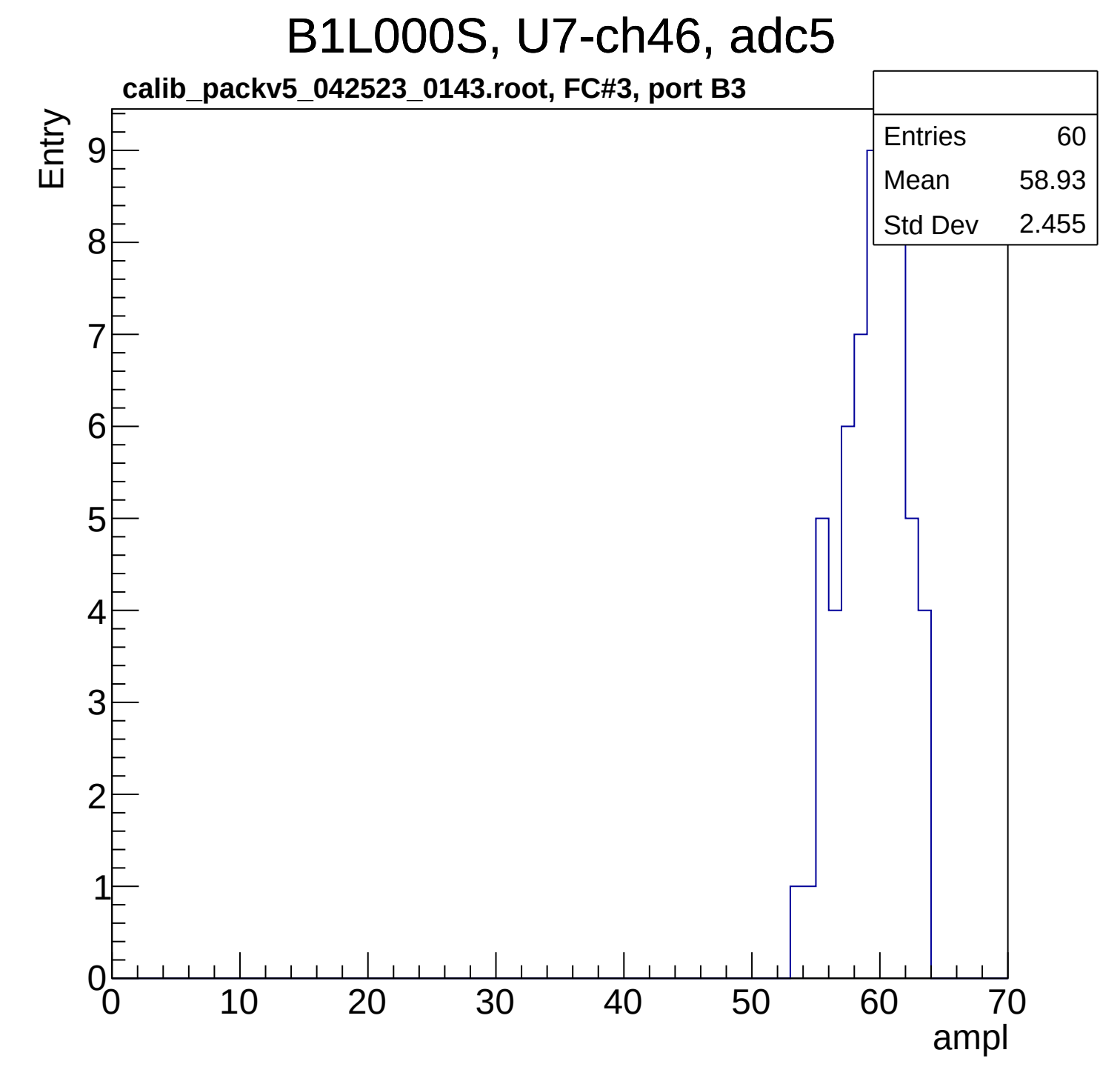
B1L000S, U7-ch46, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.93
Std Dev 2.455
Entry
ampl
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3
4
5
6
7
8
9
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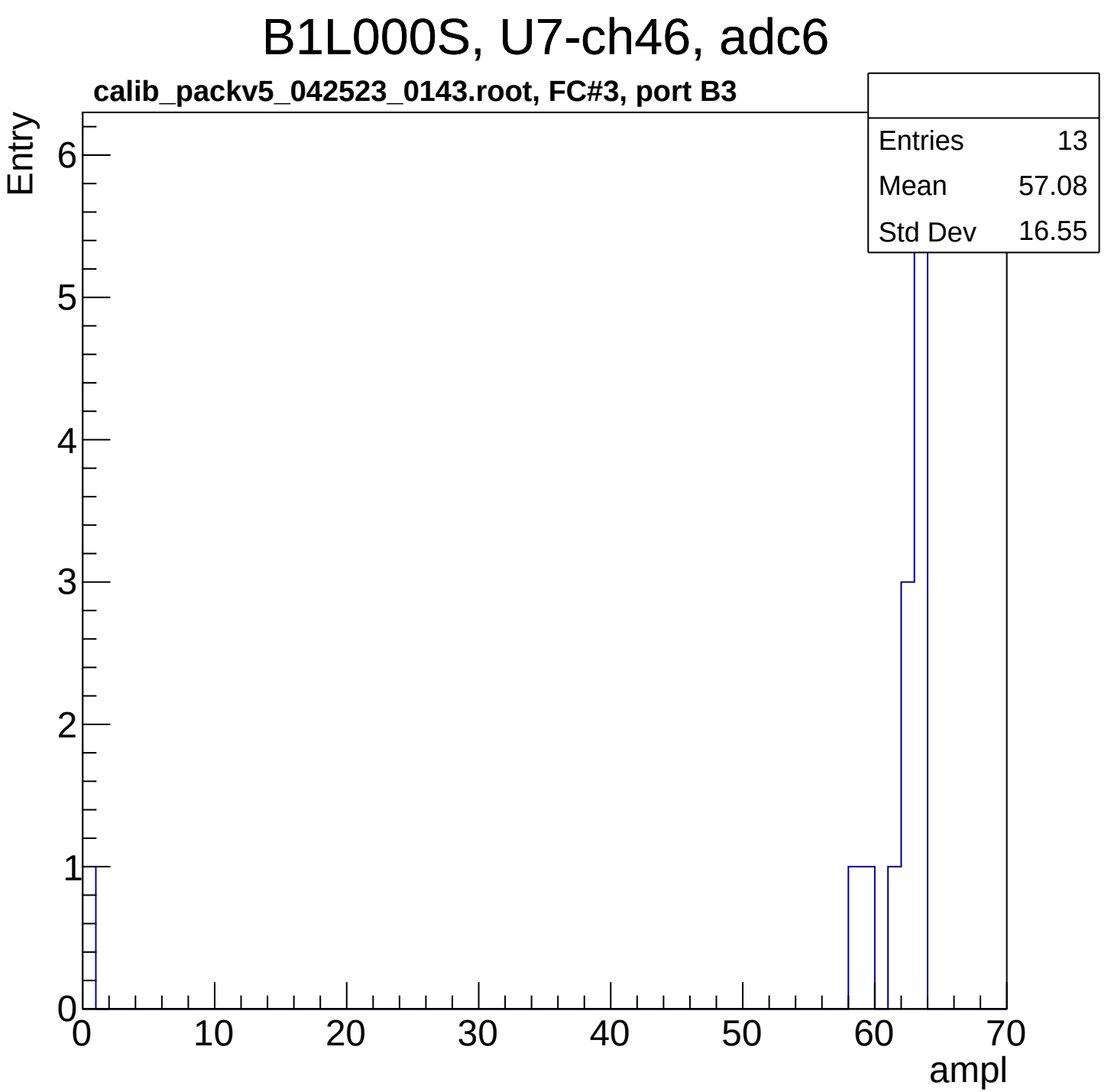
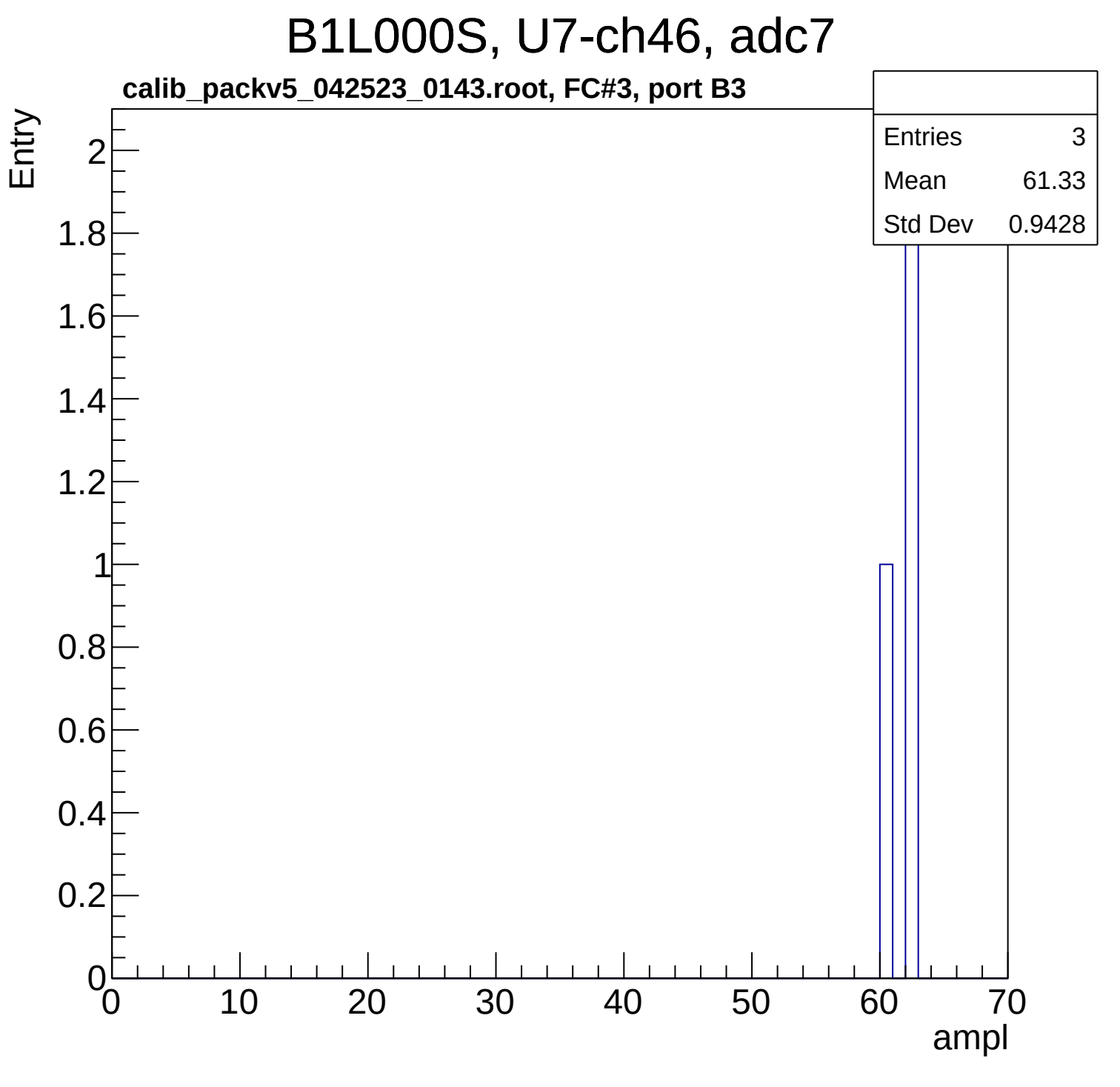

B1L000S, U7-ch46, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.08
Std Dev 16.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch46, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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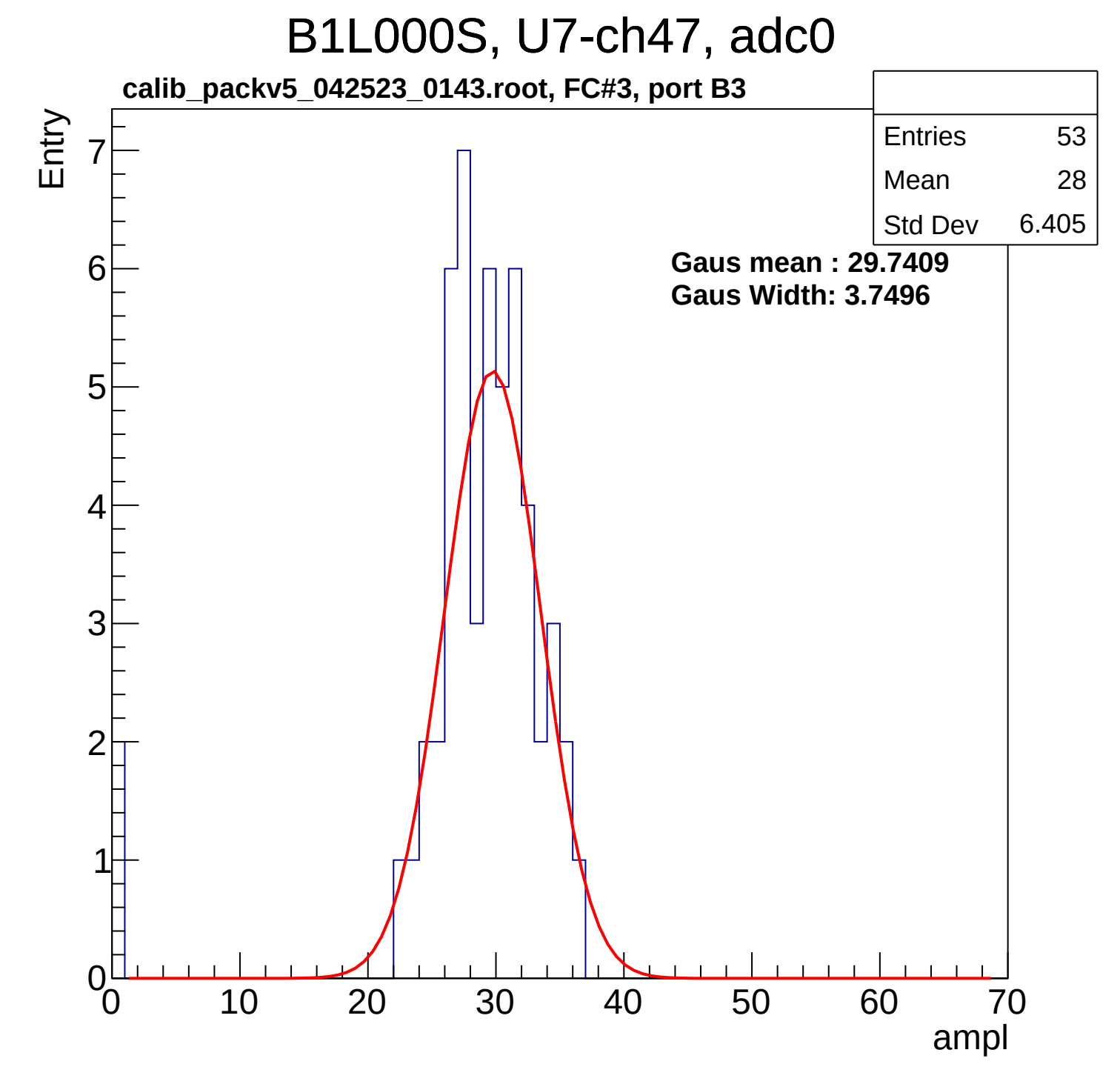
B1L000S, U7-ch47, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 28
Std Dev 6.405
Gaus mean : 29.7409
Gaus Width: 3.7496
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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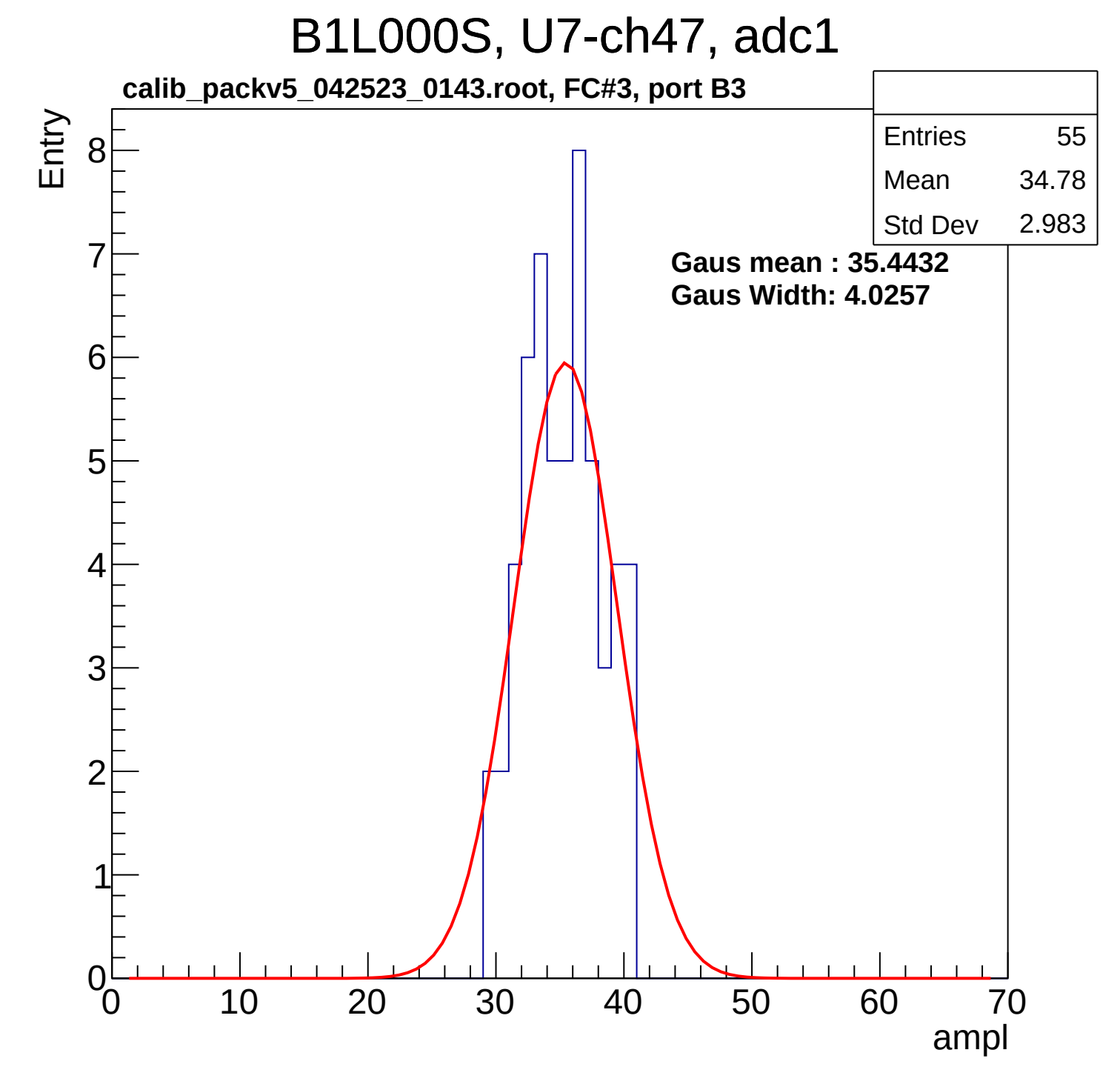
B1L000S, U7-ch47, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 34.78
Std Dev 2.983
Gaus mean : 35.4432
Gaus Width: 4.0257
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
50
60
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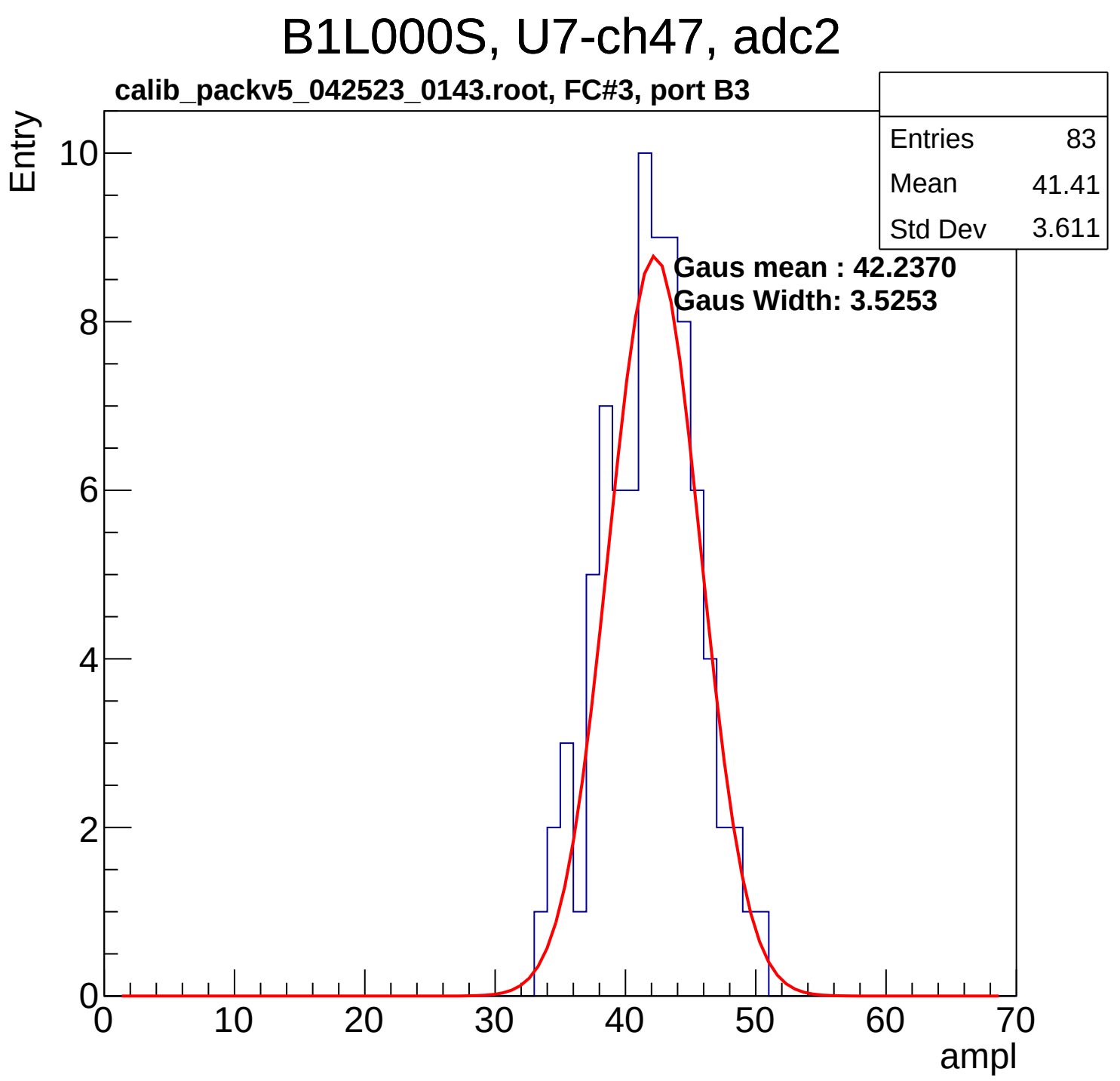
B1L000S, U7-ch47, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 41.41
Std Dev 3.611
Gaus mean : 42.2370
Gaus Width: 3.5253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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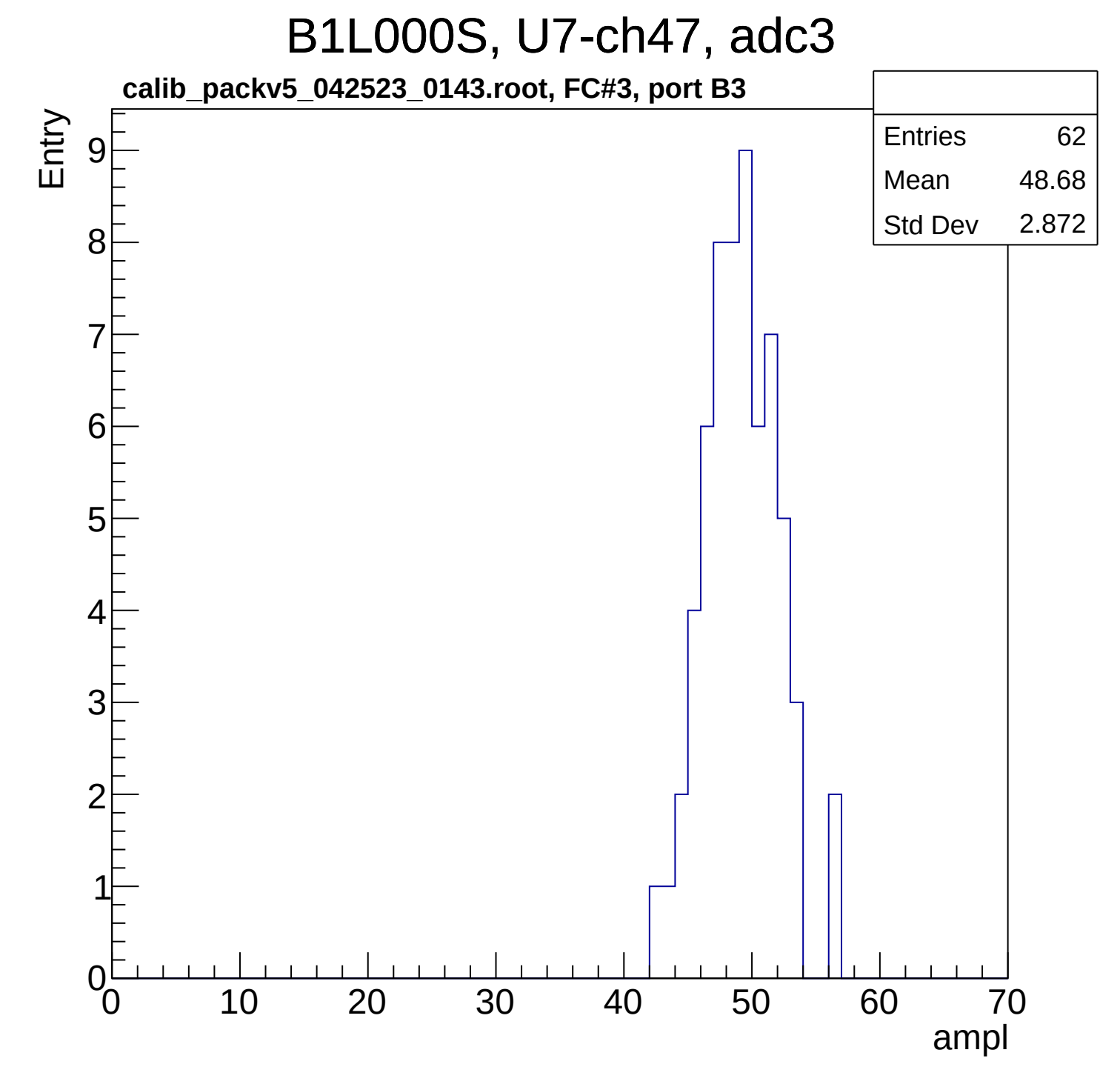
B1L000S, U7-ch47, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 48.68
Std Dev 2.872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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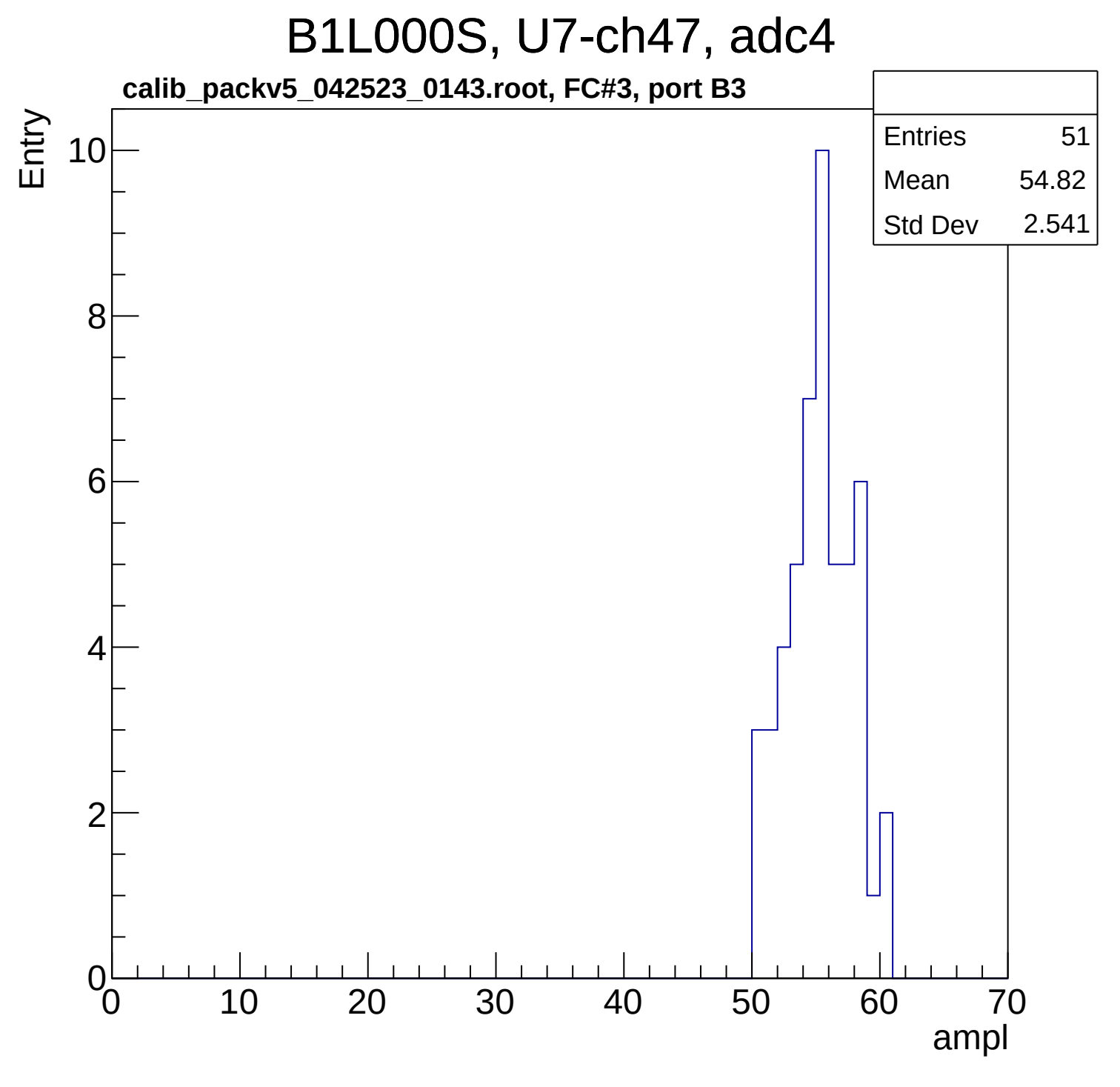
B1L000S, U7-ch47, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.82
Std Dev 2.541
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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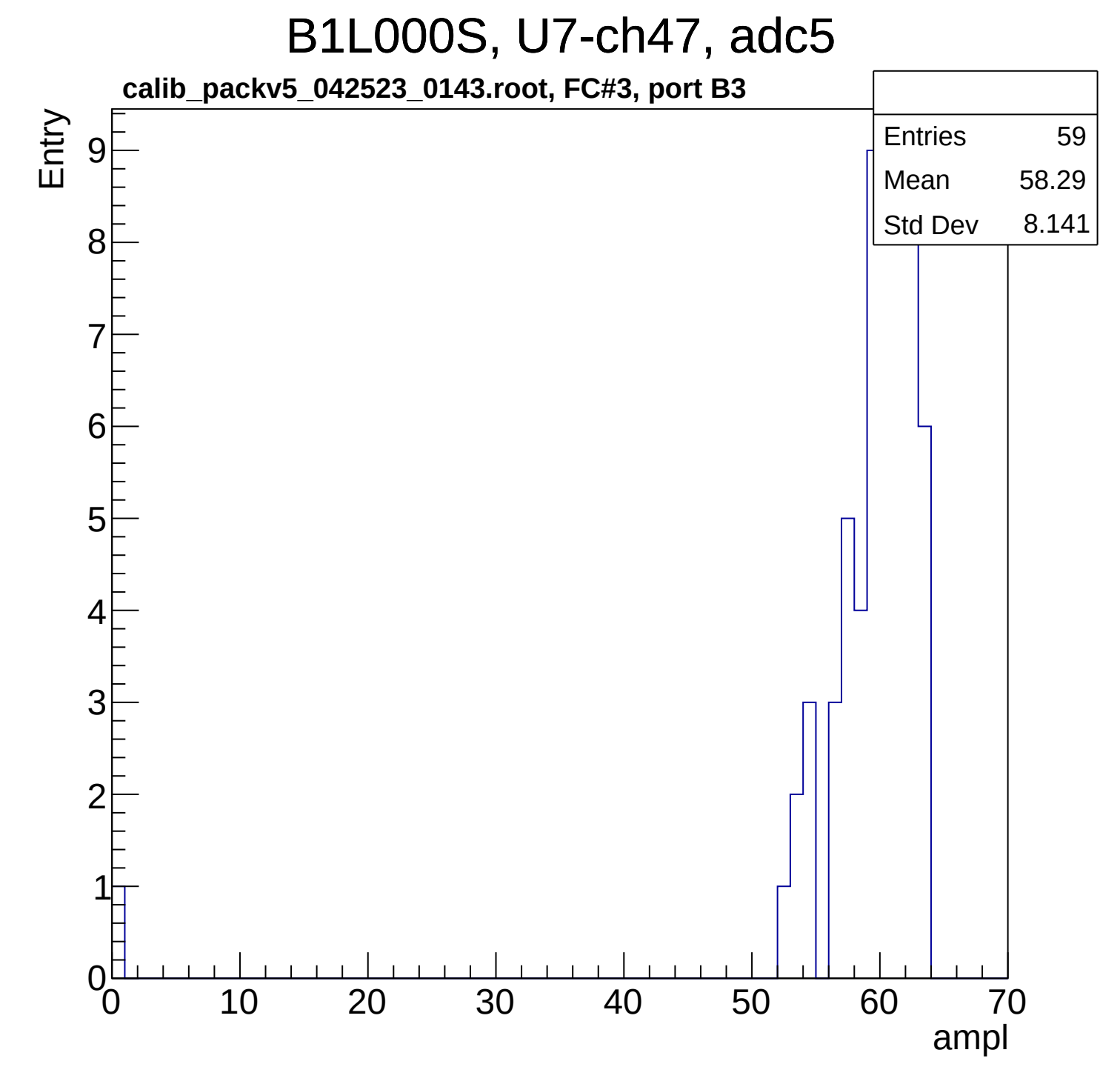
B1L000S, U7-ch47, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.29
Std Dev 8.141
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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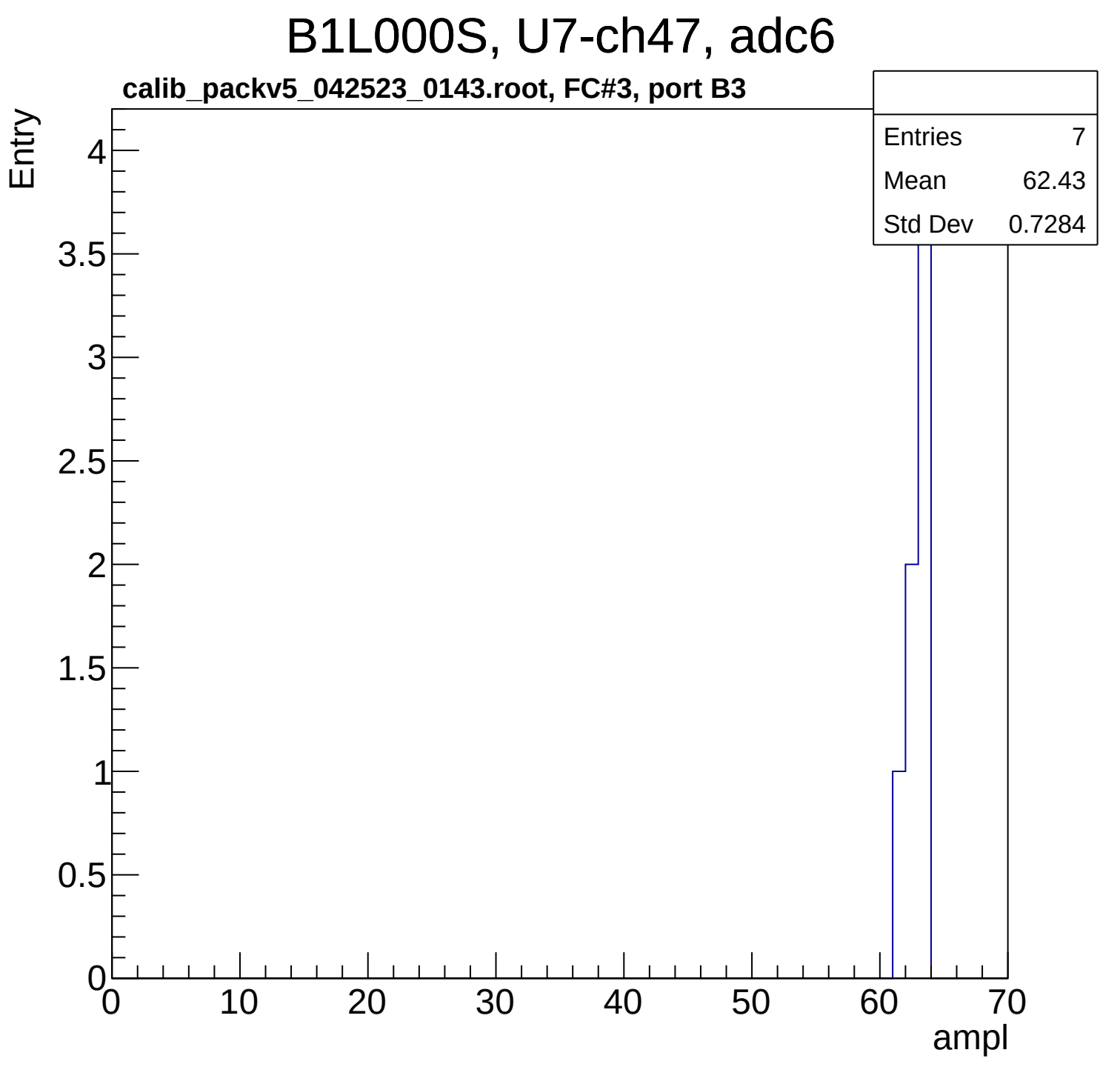
B1L000S, U7-ch47, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.43
Std Dev 0.7284
Entry
ampl
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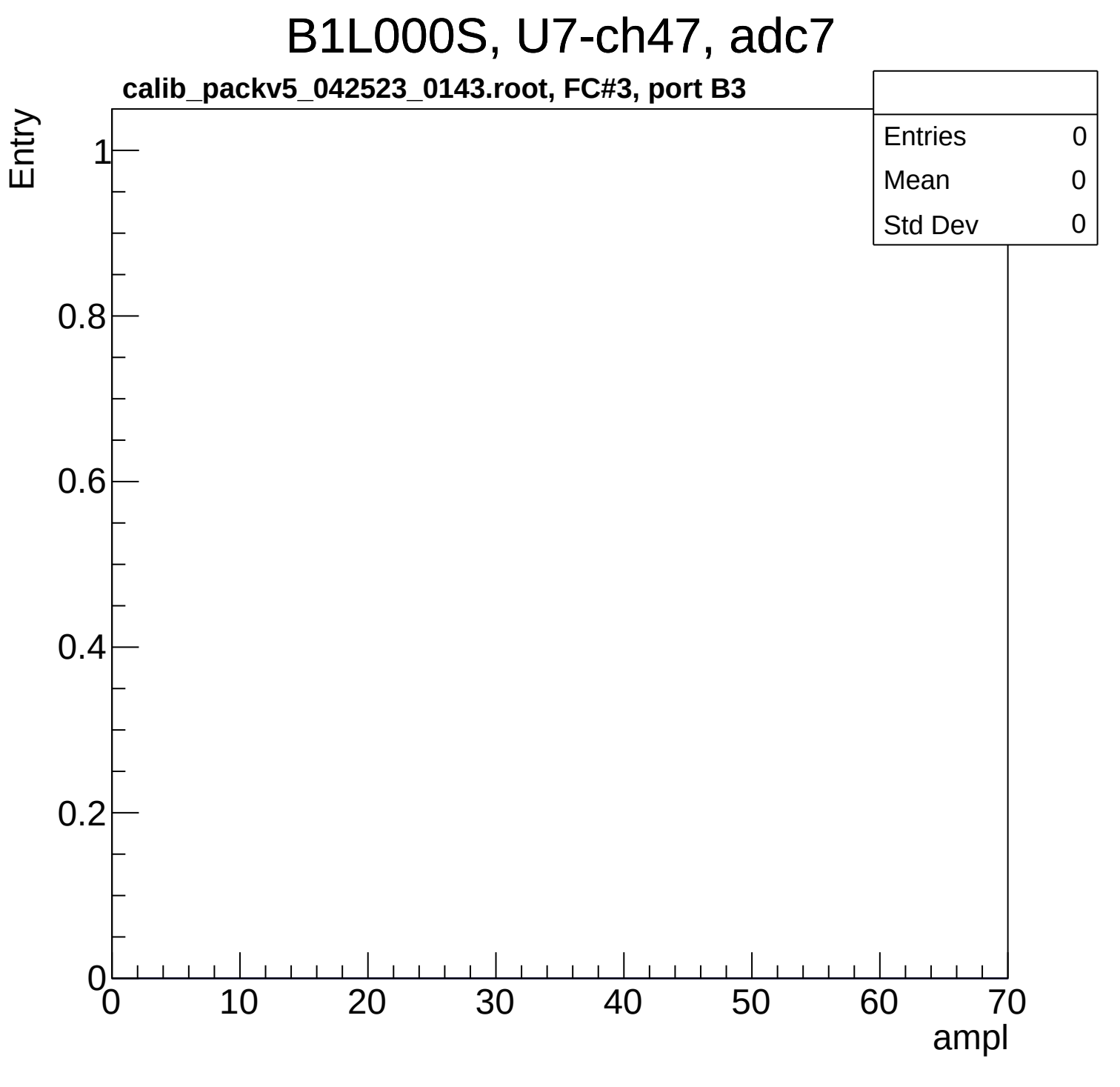
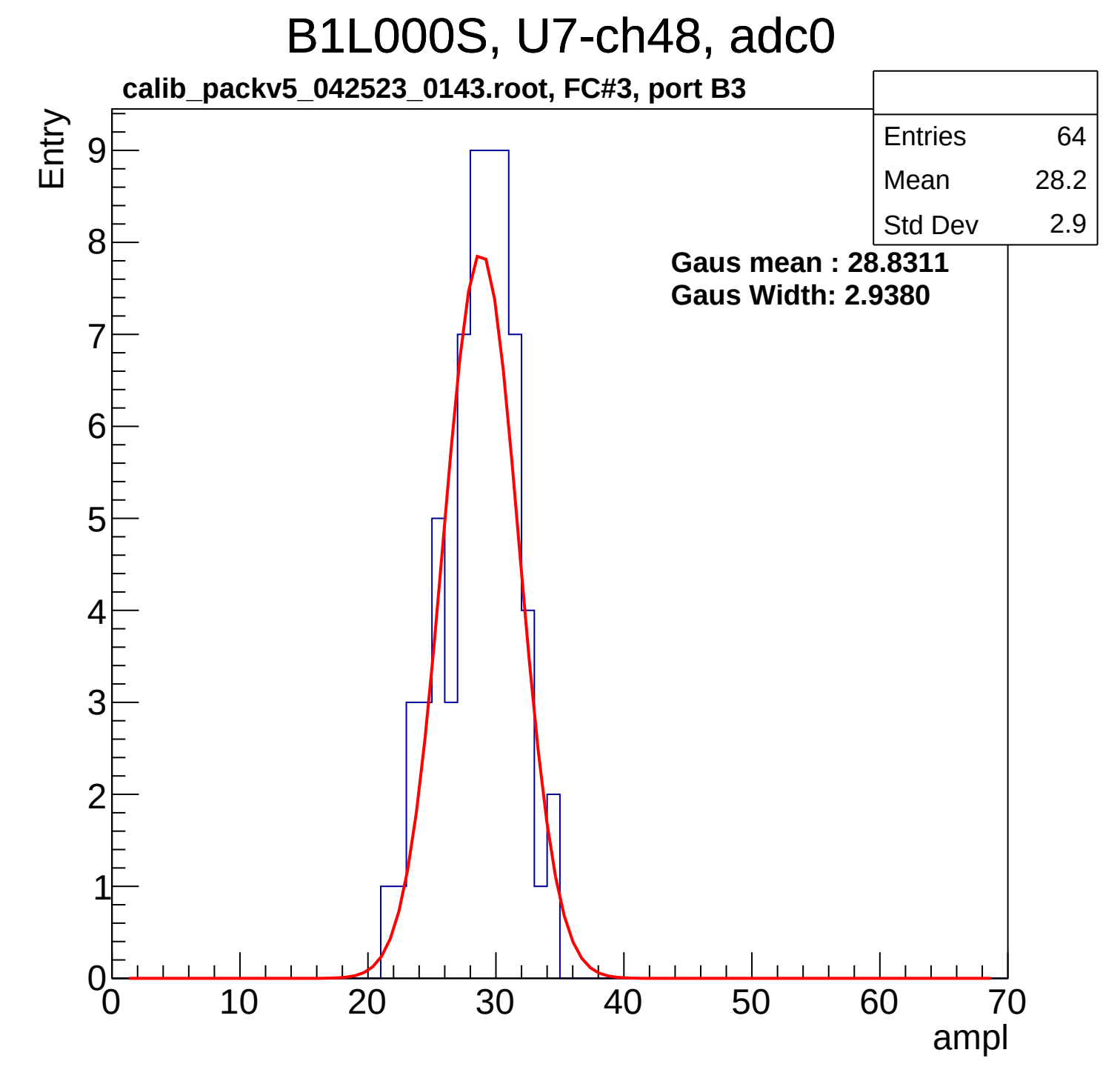
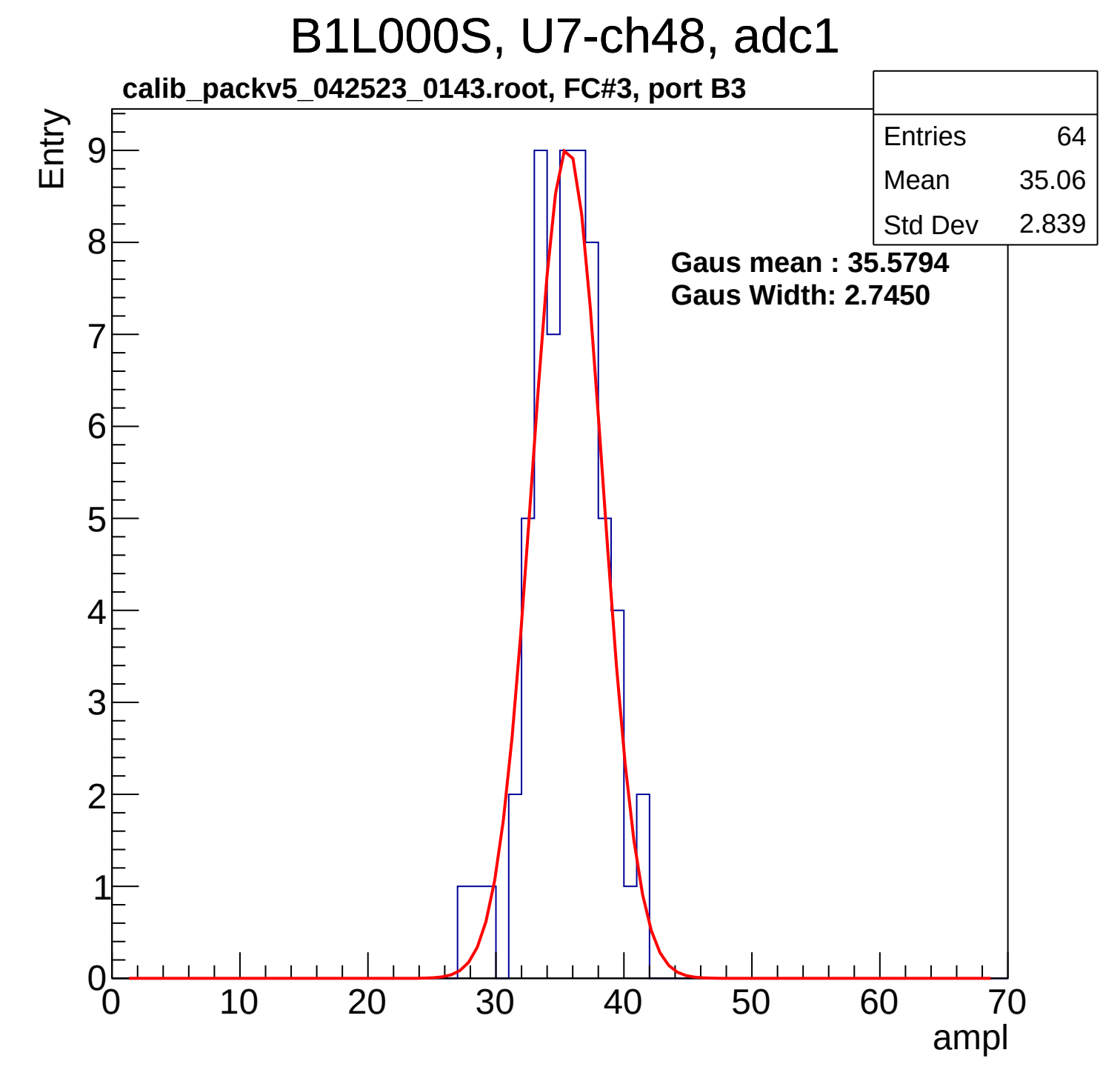
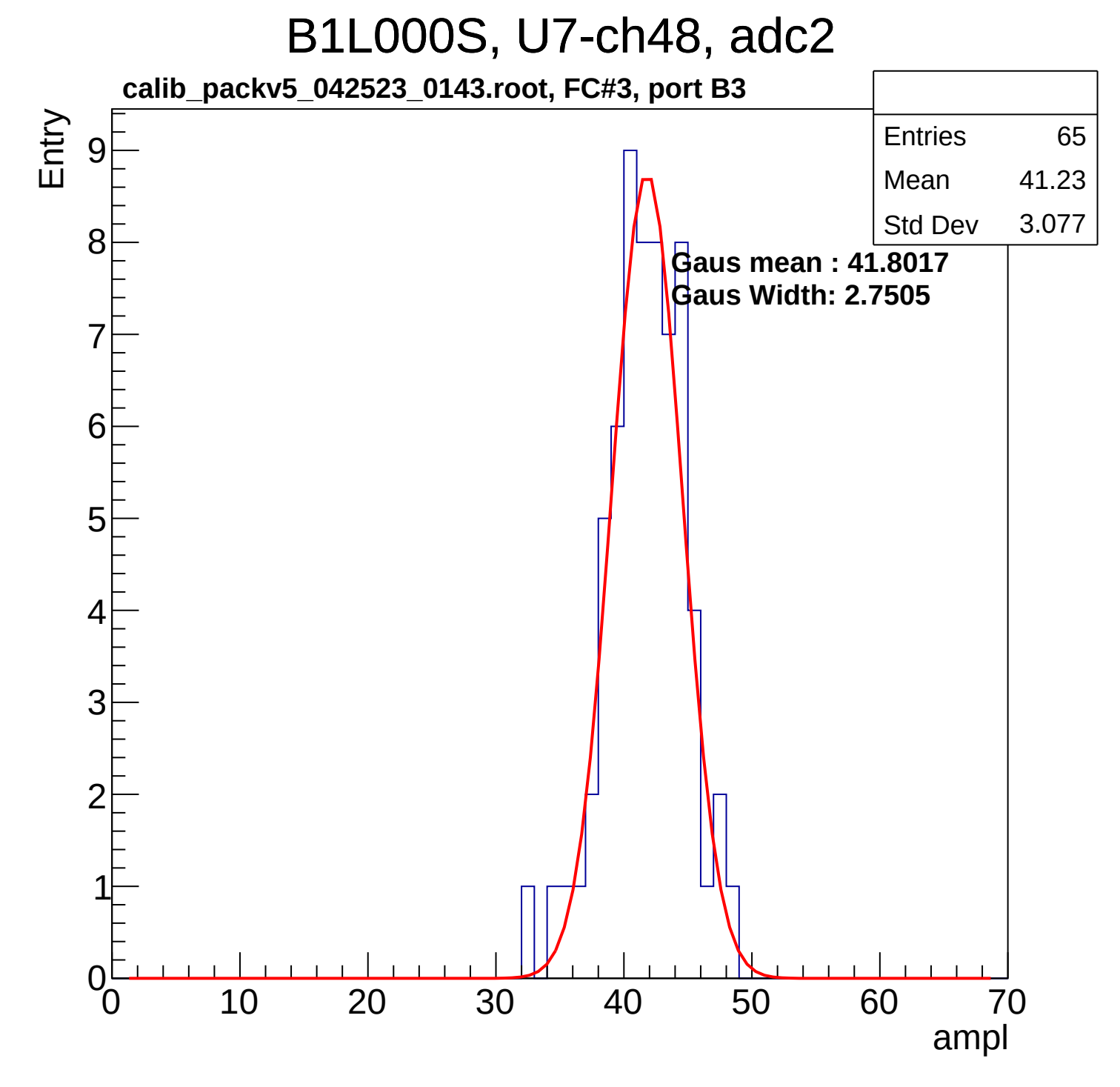
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch47, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch48, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.2
Std Dev 2.9
Gaus mean : 28.8311
Gaus Width: 2.9380
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch48, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.06
Std Dev 2.839
Gaus mean : 35.5794
Gaus Width: 2.7450
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch48, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 41.23
Std Dev 3.077
Gaus mean : 41.8017
Gaus Width: 2.7505
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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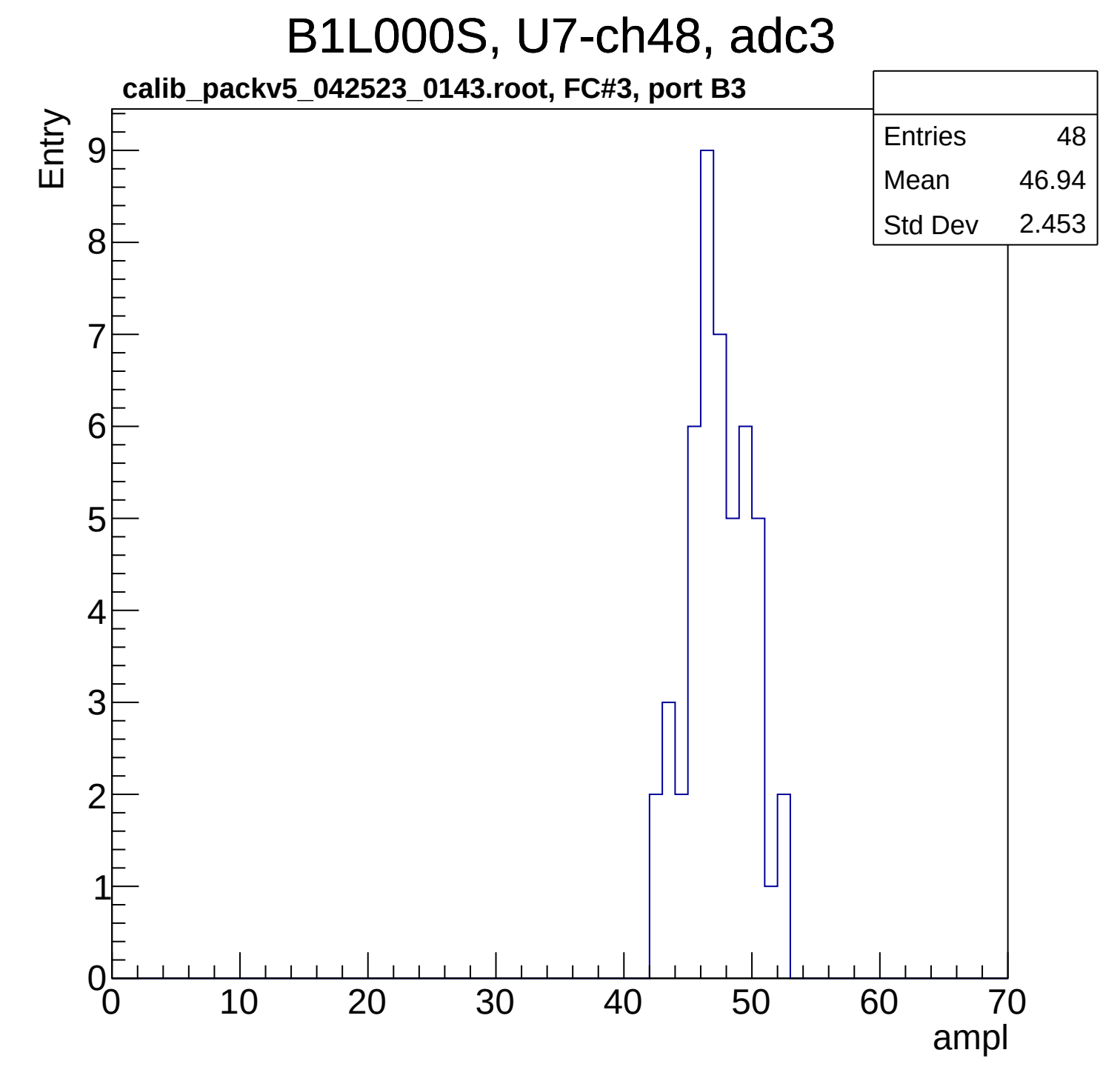
B1L000S, U7-ch48, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 46.94
Std Dev 2.453
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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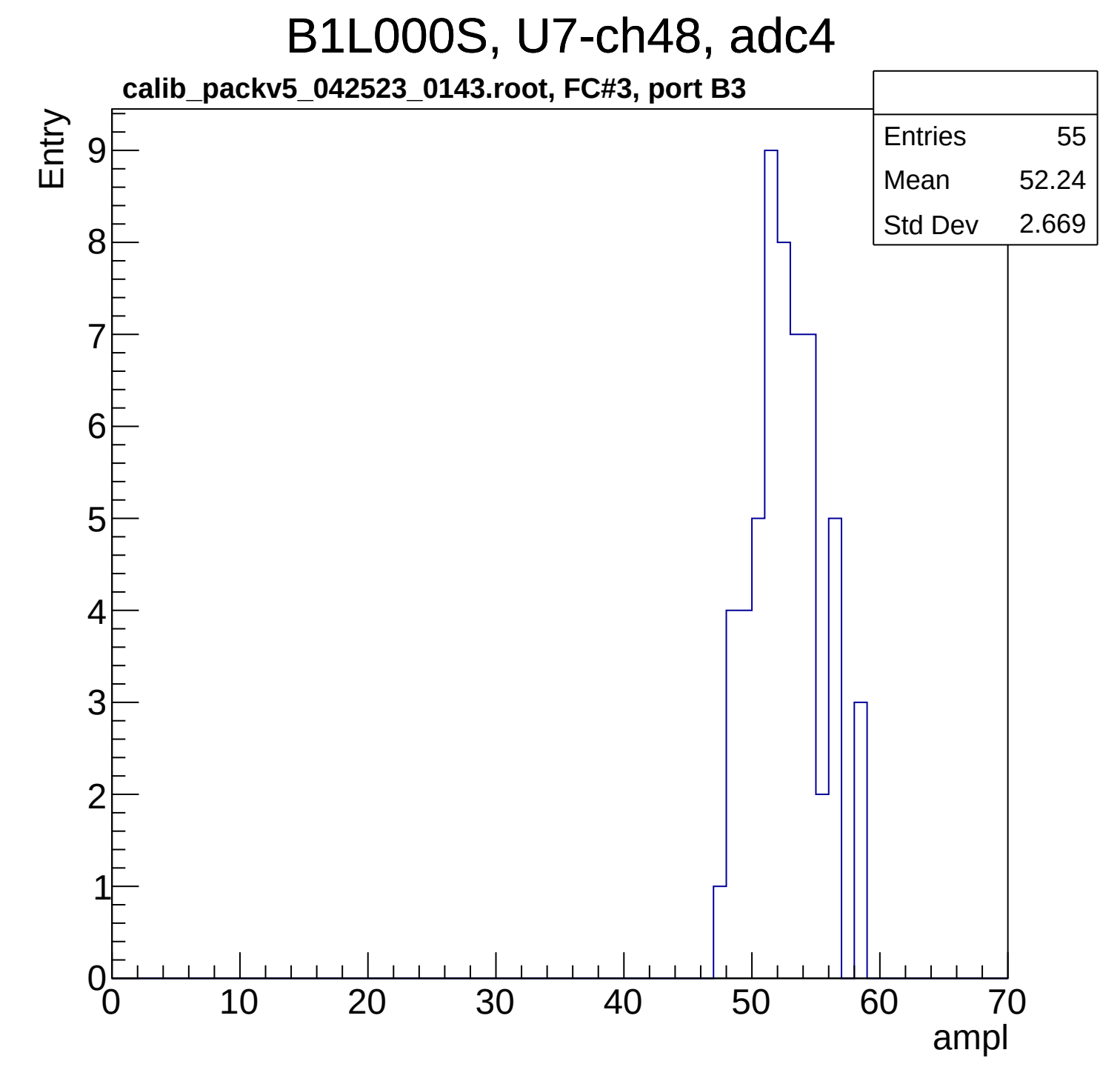
B1L000S, U7-ch48, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.24
Std Dev 2.669
Entry
ampl
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2
3
4
5
6
7
8
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0
10
20
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40
50
60
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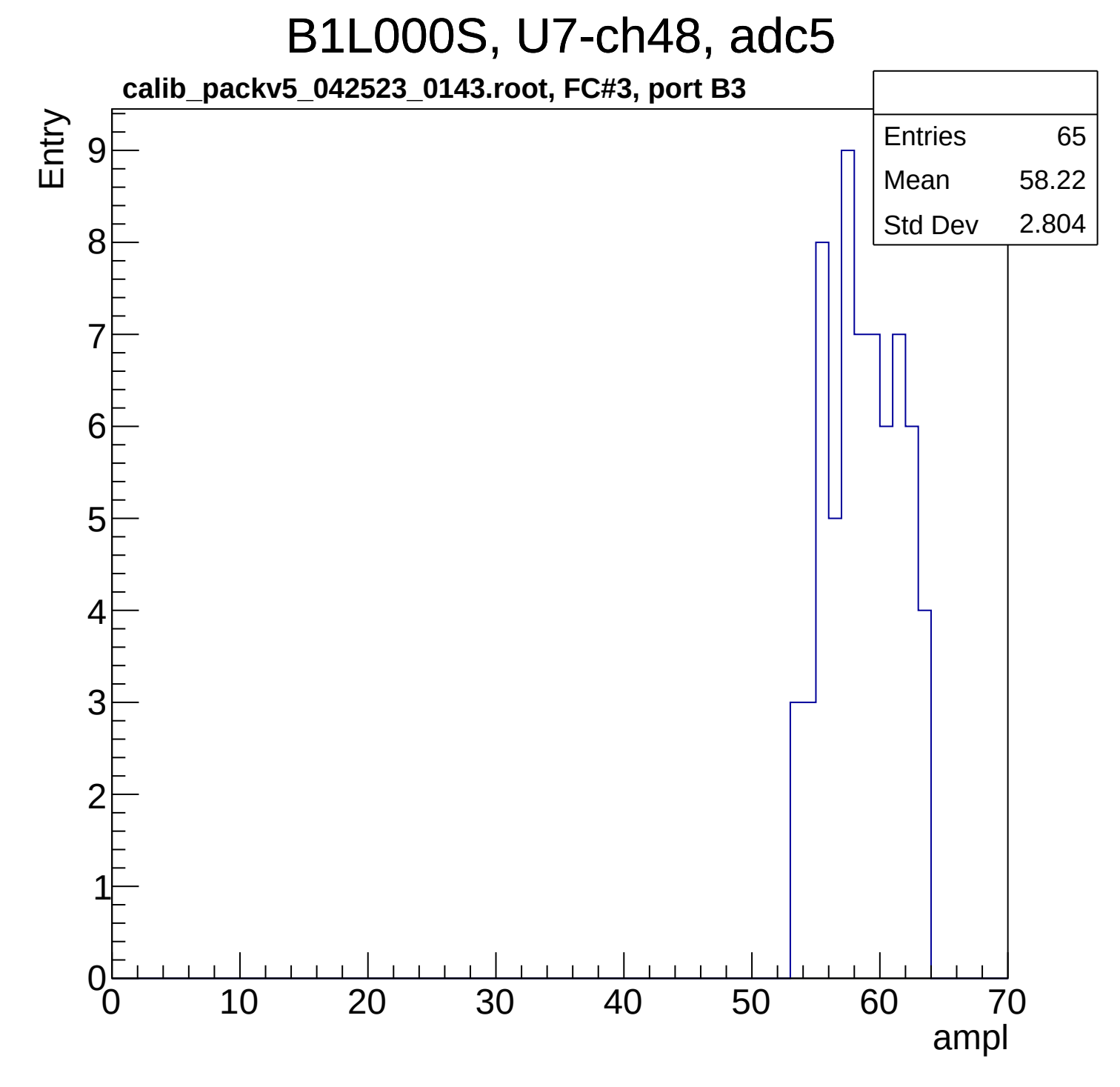
B1L000S, U7-ch48, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 58.22
Std Dev 2.804
Entry
ampl
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3
4
5
6
7
8
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0
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40
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60
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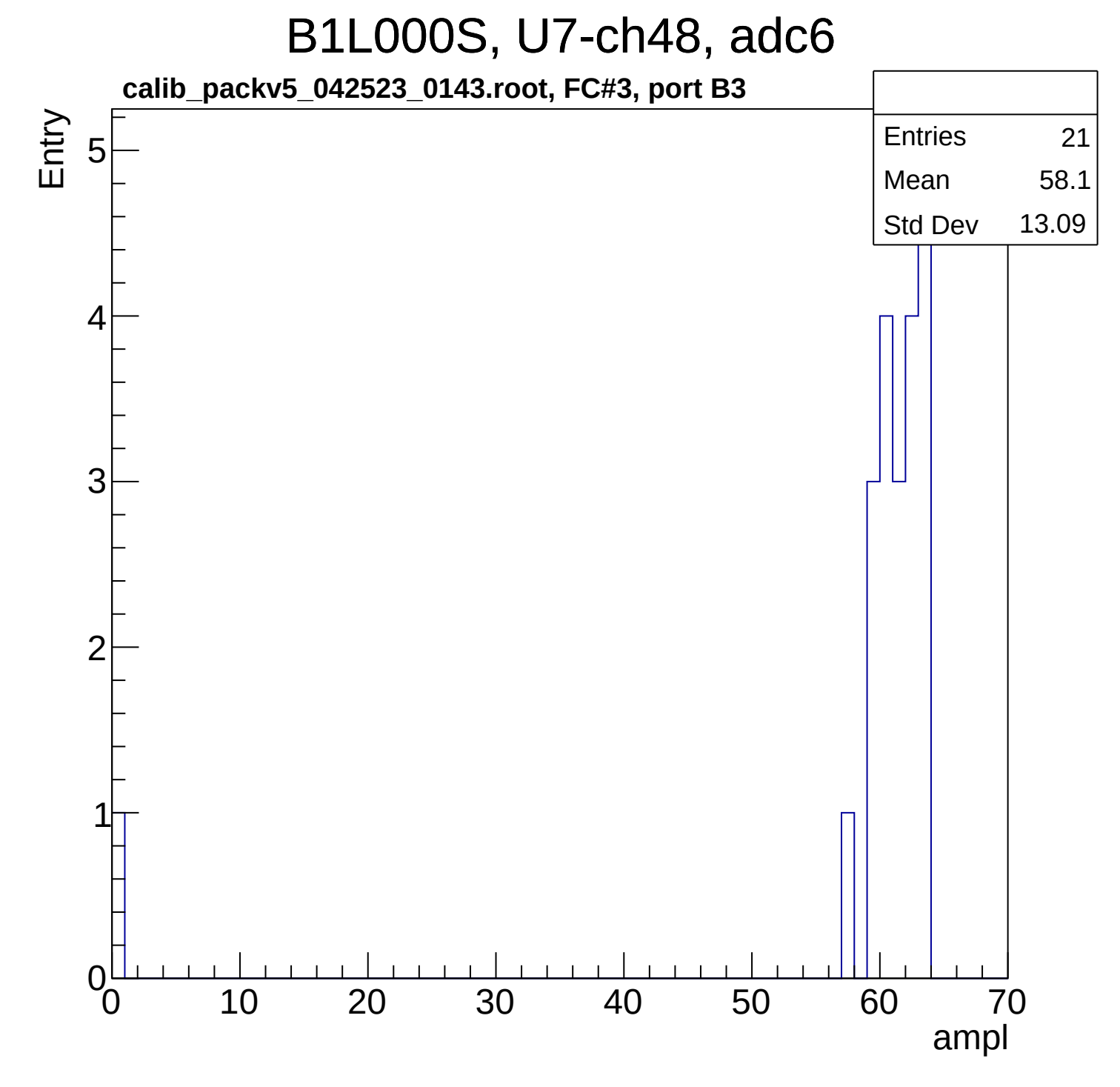
B1L000S, U7-ch48, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 58.1
Std Dev 13.09
Entry
0
1
2
3
4
5
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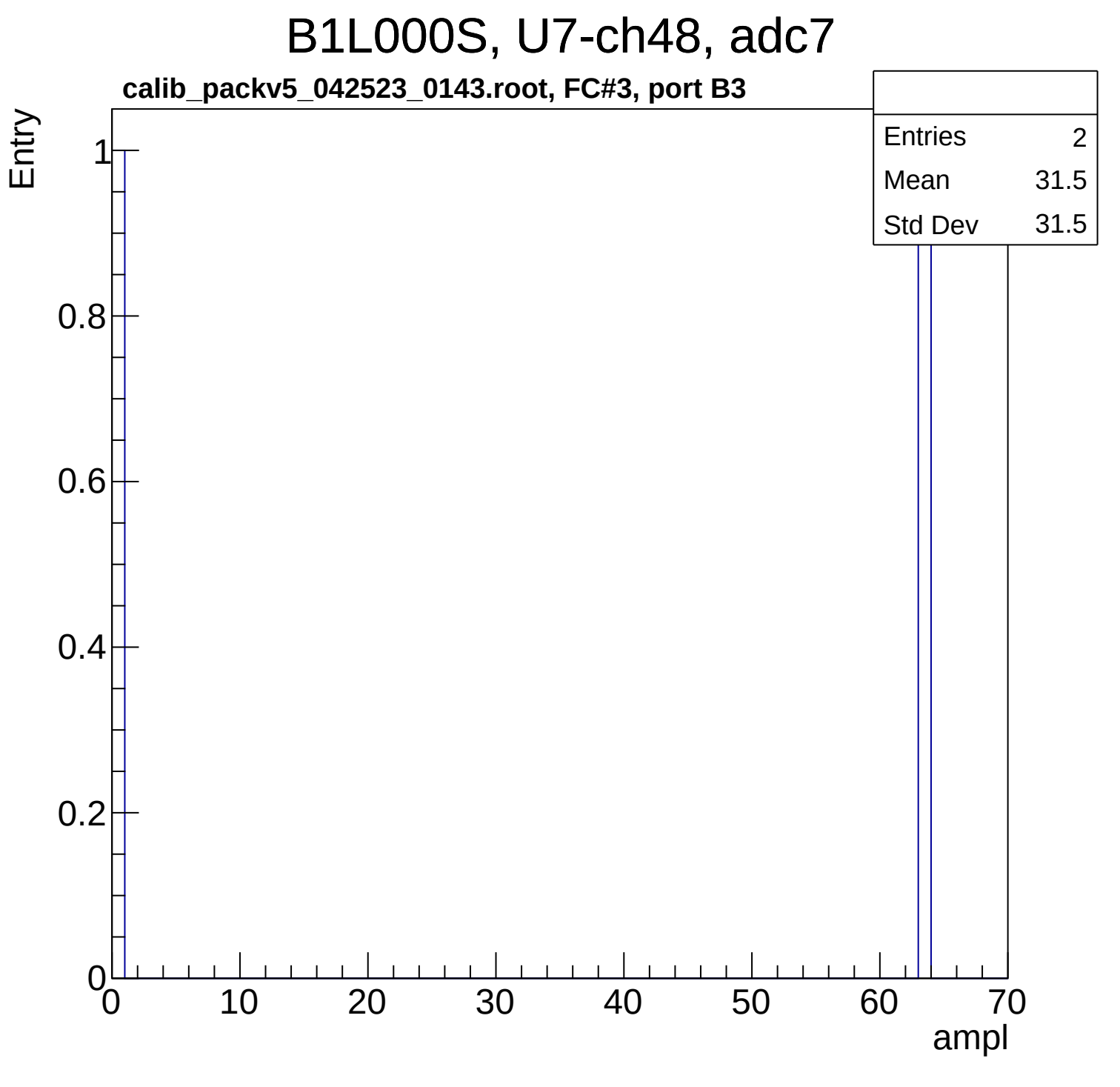
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch48, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
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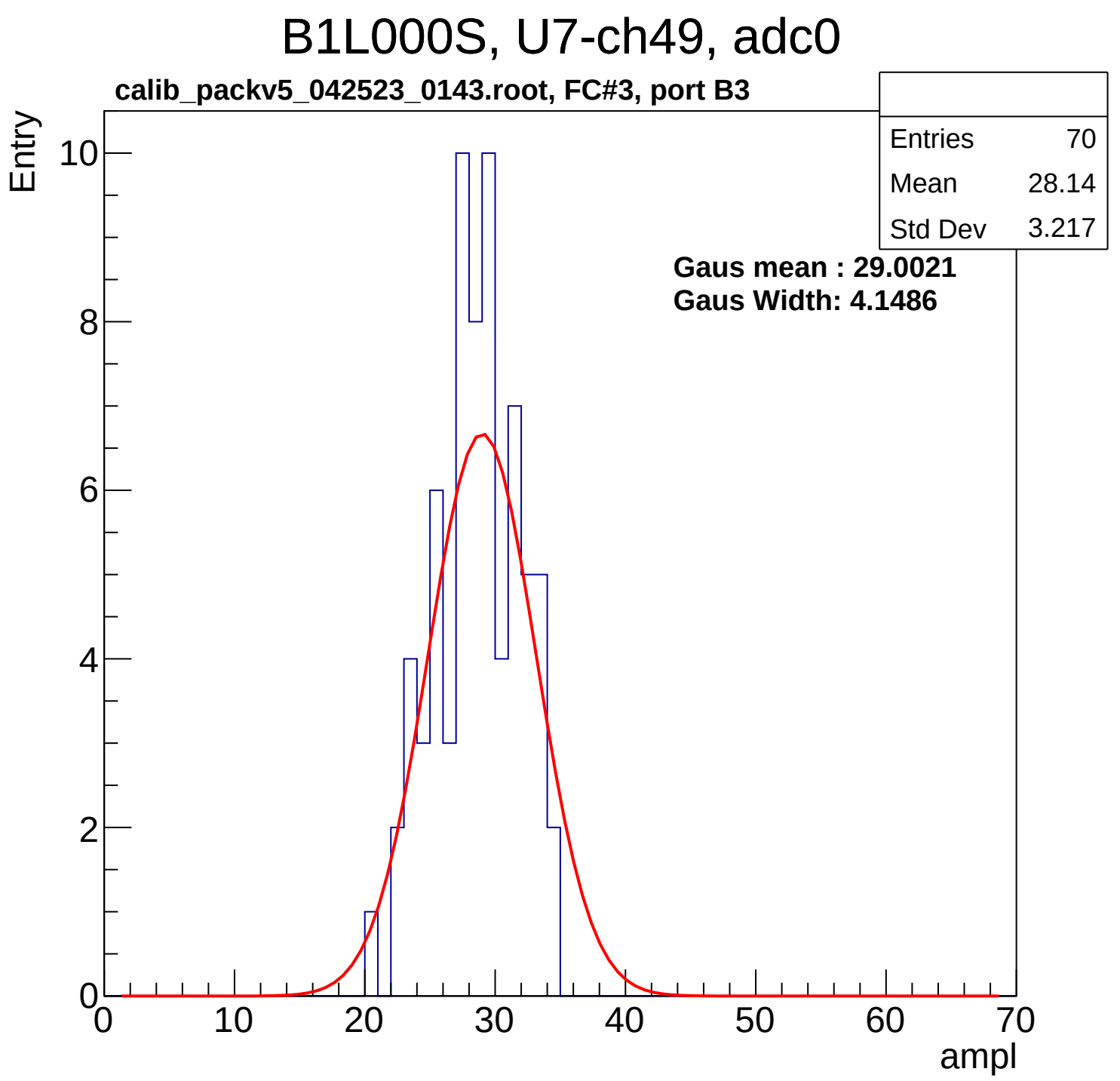
40
50
60
70
ampl


B1L000S, U7-ch49, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 28.14
Std Dev 3.217
Gaus mean : 29.0021
Gaus Width: 4.1486
Entry
ampl
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2
4
6
8
10
0
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20
30
40
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60
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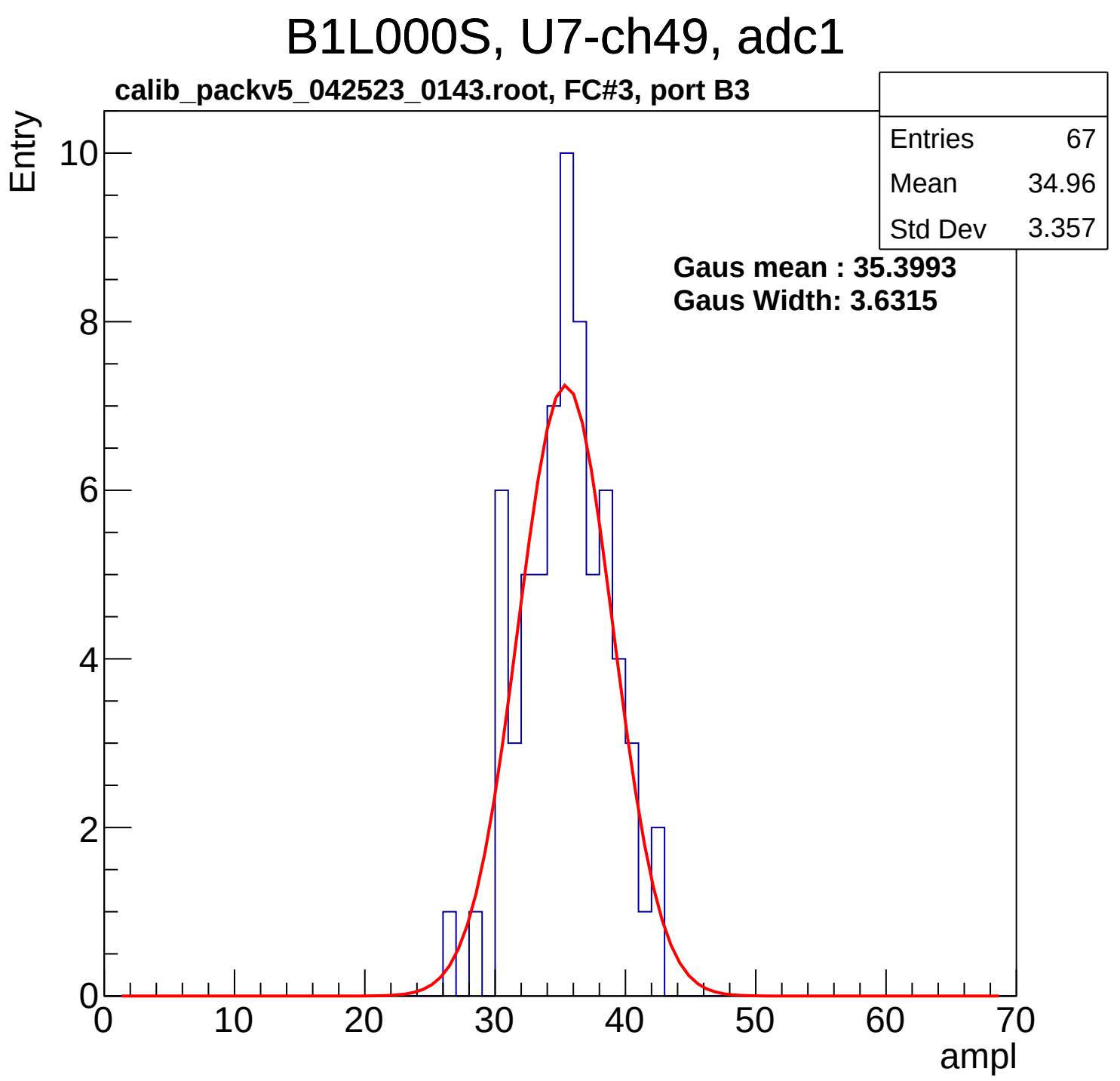
B1L000S, U7-ch49, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 34.96
Std Dev 3.357
Gaus mean : 35.3993
Gaus Width: 3.6315
Entry
ampl
0
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4
6
8
10
0
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20
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40
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60
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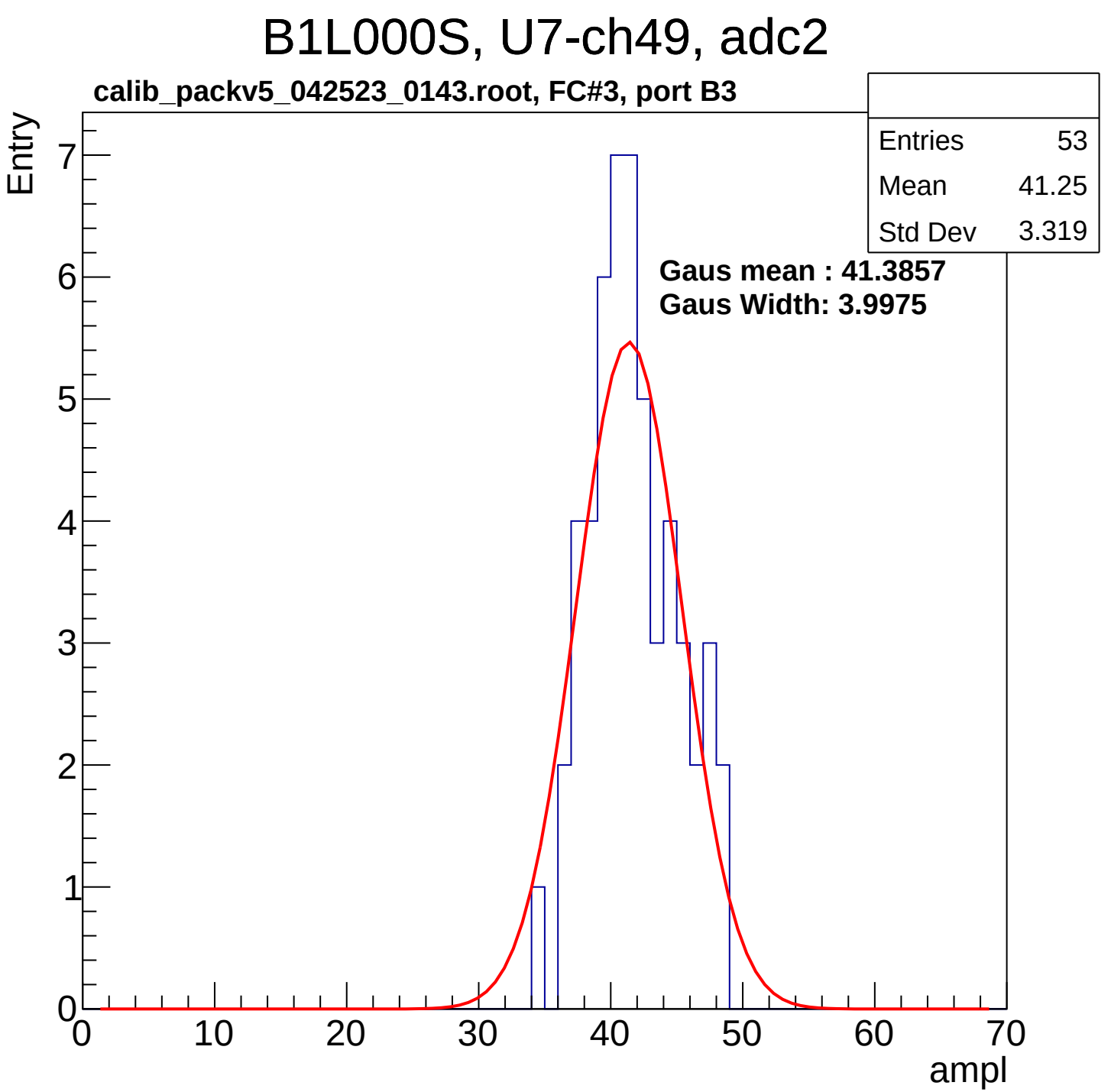
B1L000S, U7-ch49, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 41.25
Std Dev 3.319
Gaus mean : 41.3857
Gaus Width: 3.9975
Entry
ampl
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3
4
5
6
7
0
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60
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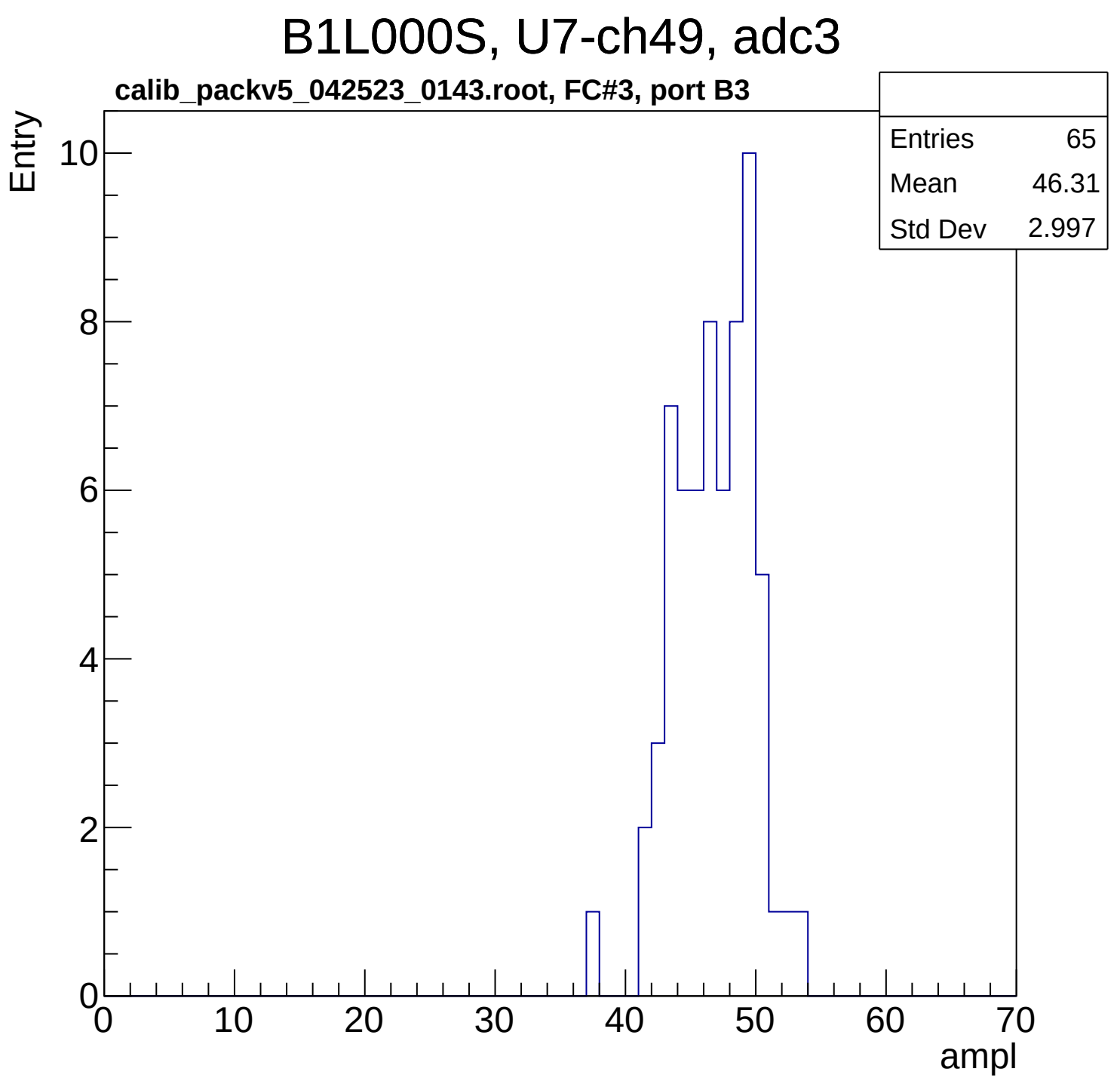
B1L000S, U7-ch49, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 46.31
Std Dev 2.997
Entry
ampl
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4
6
8
10
0
10
20
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40
50
60
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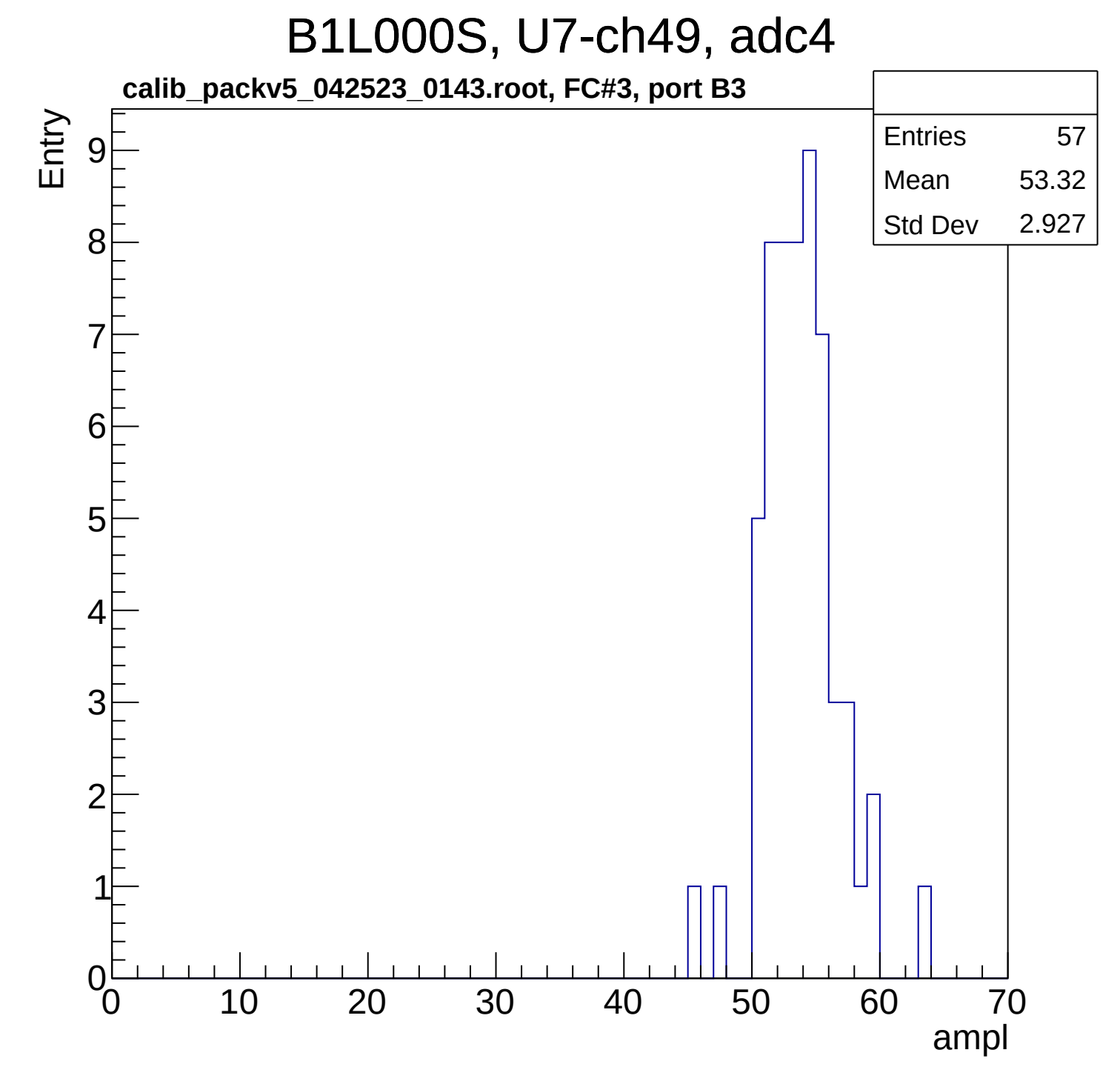
B1L000S, U7-ch49, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.32
Std Dev 2.927
Entry
ampl
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2
3
4
5
6
7
8
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0
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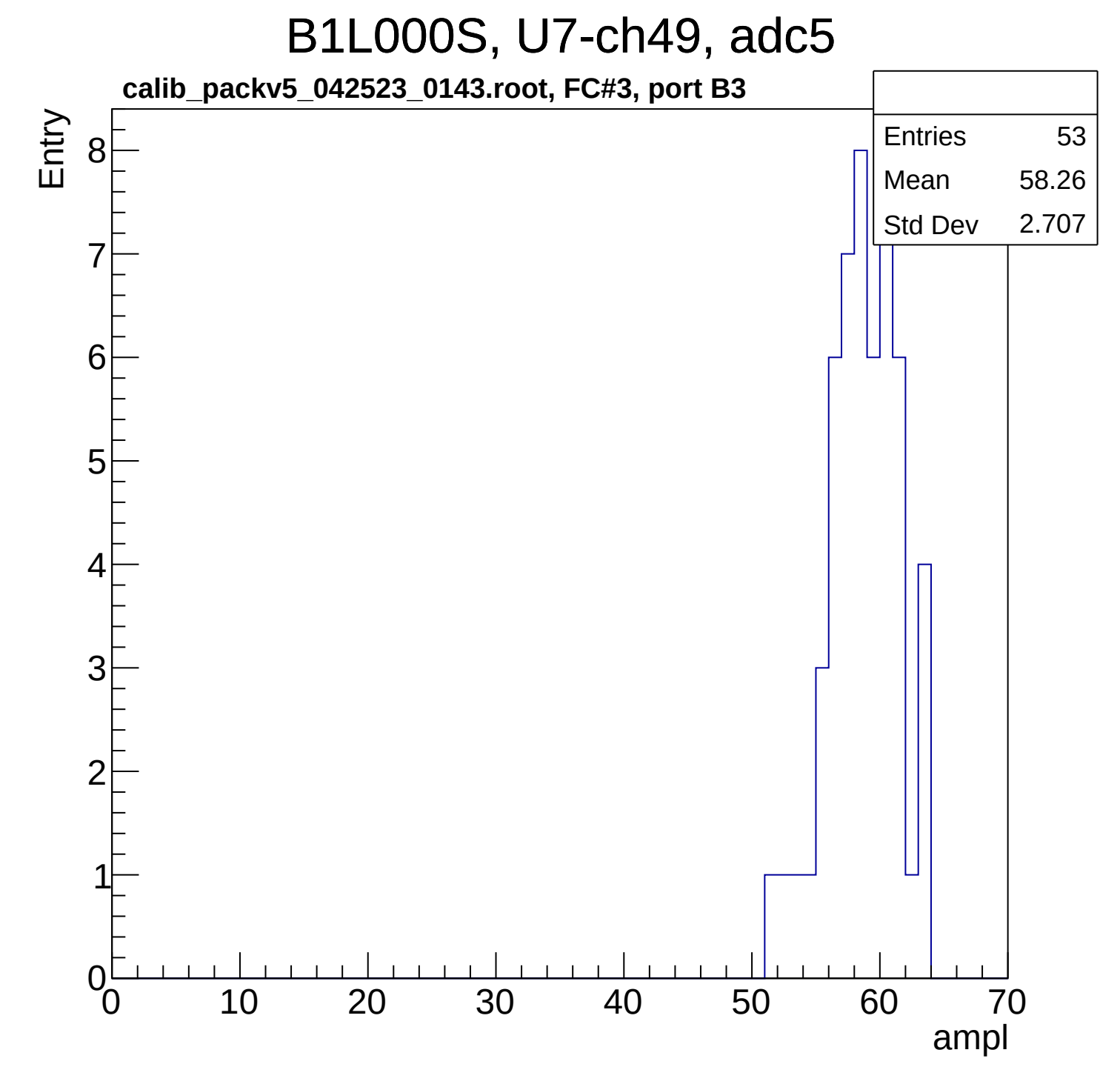
B1L000S, U7-ch49, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.26
Std Dev 2.707
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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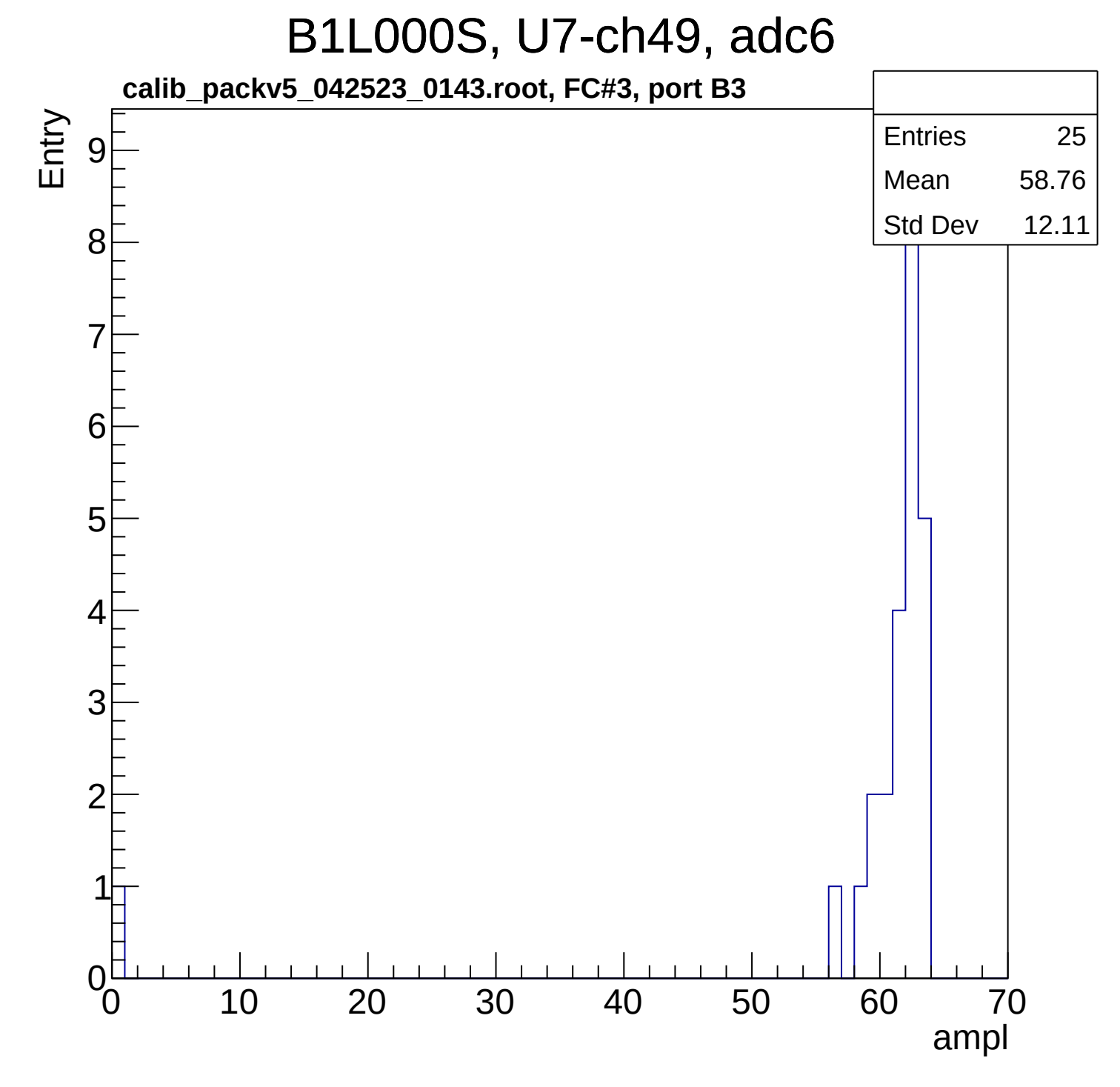
B1L000S, U7-ch49, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 58.76
Std Dev 12.11
Entry
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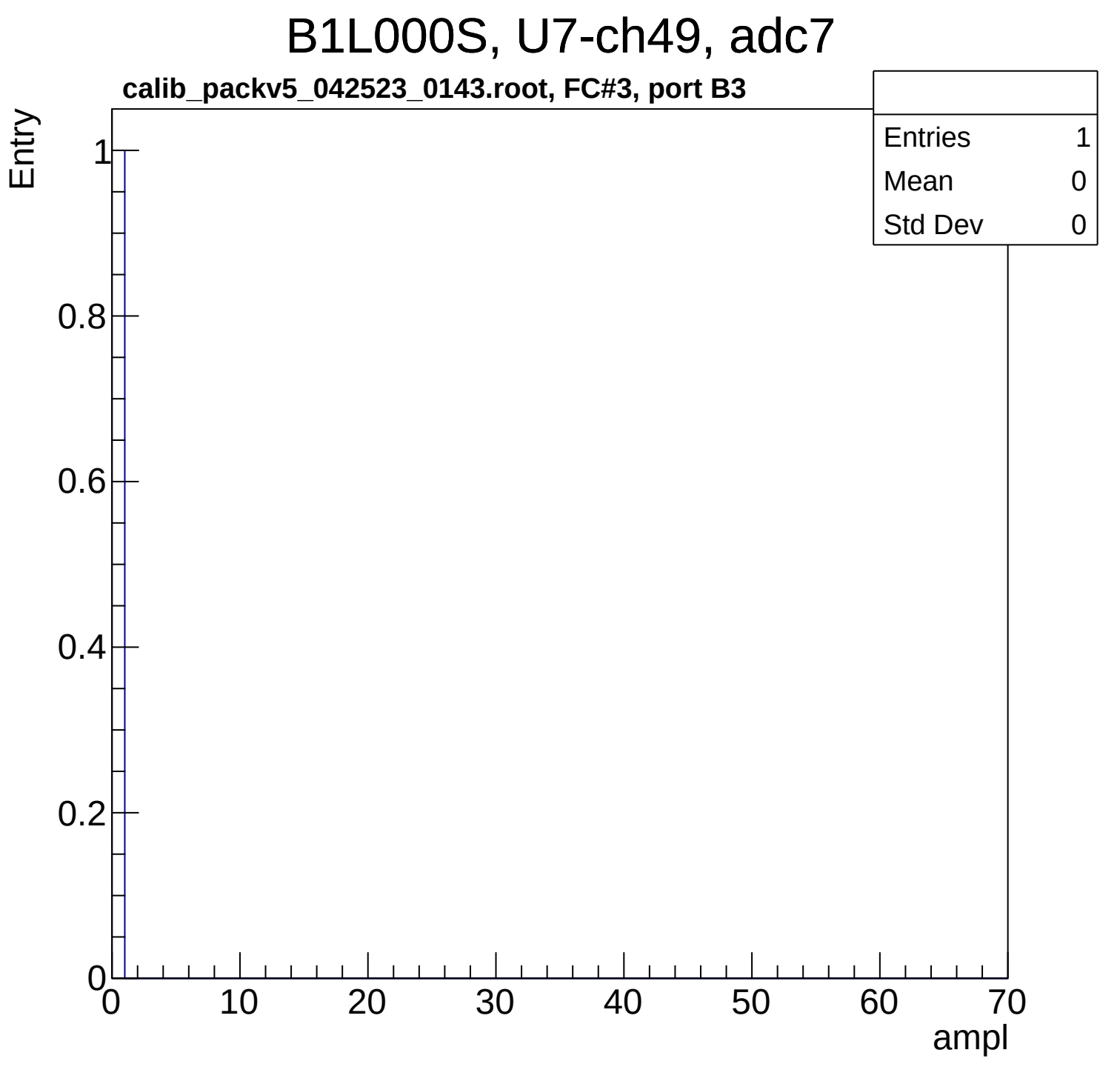
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch49, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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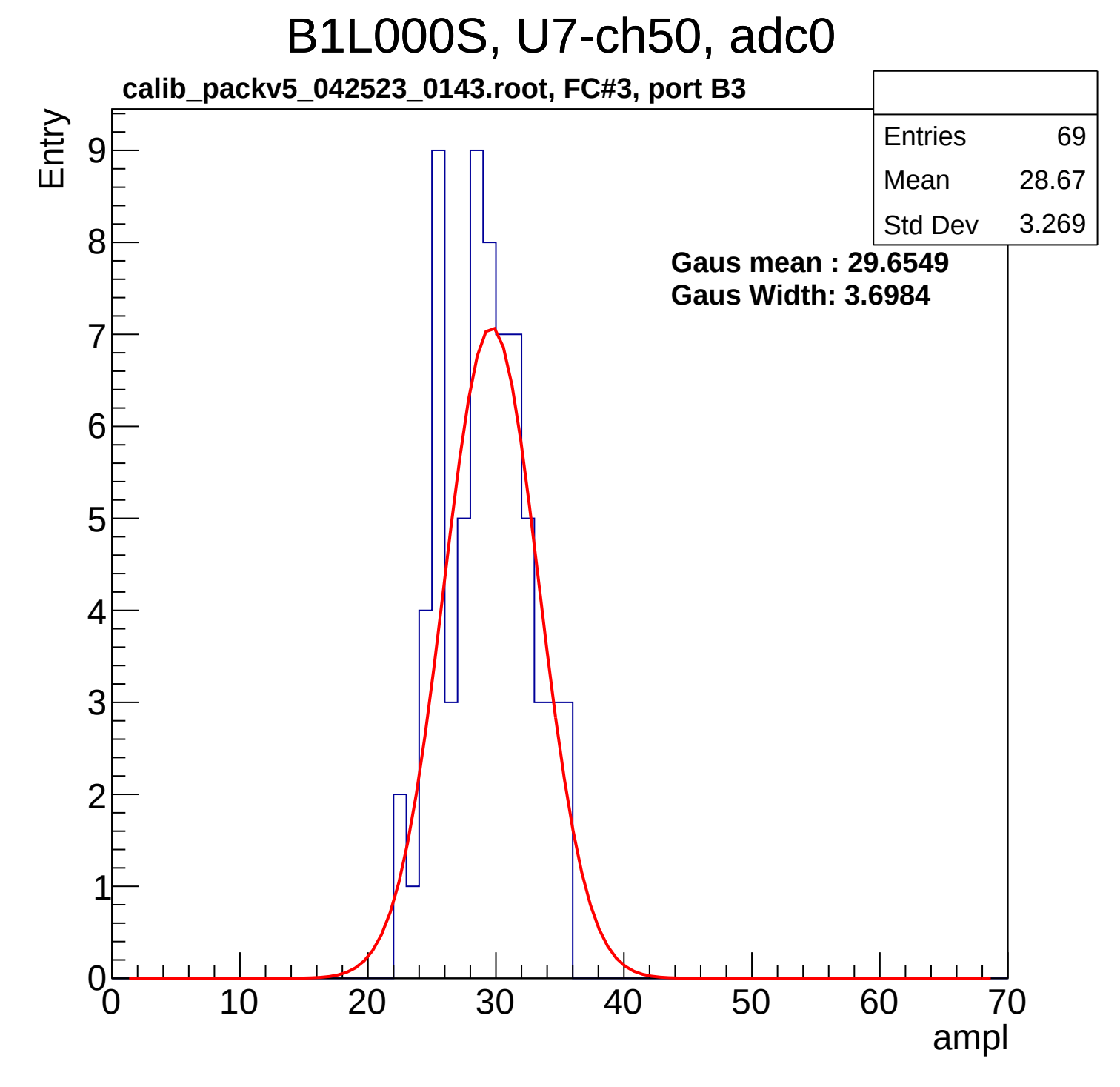
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch50, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 28.67
Std Dev 3.269
Gaus mean : 29.6549
Gaus Width: 3.6984
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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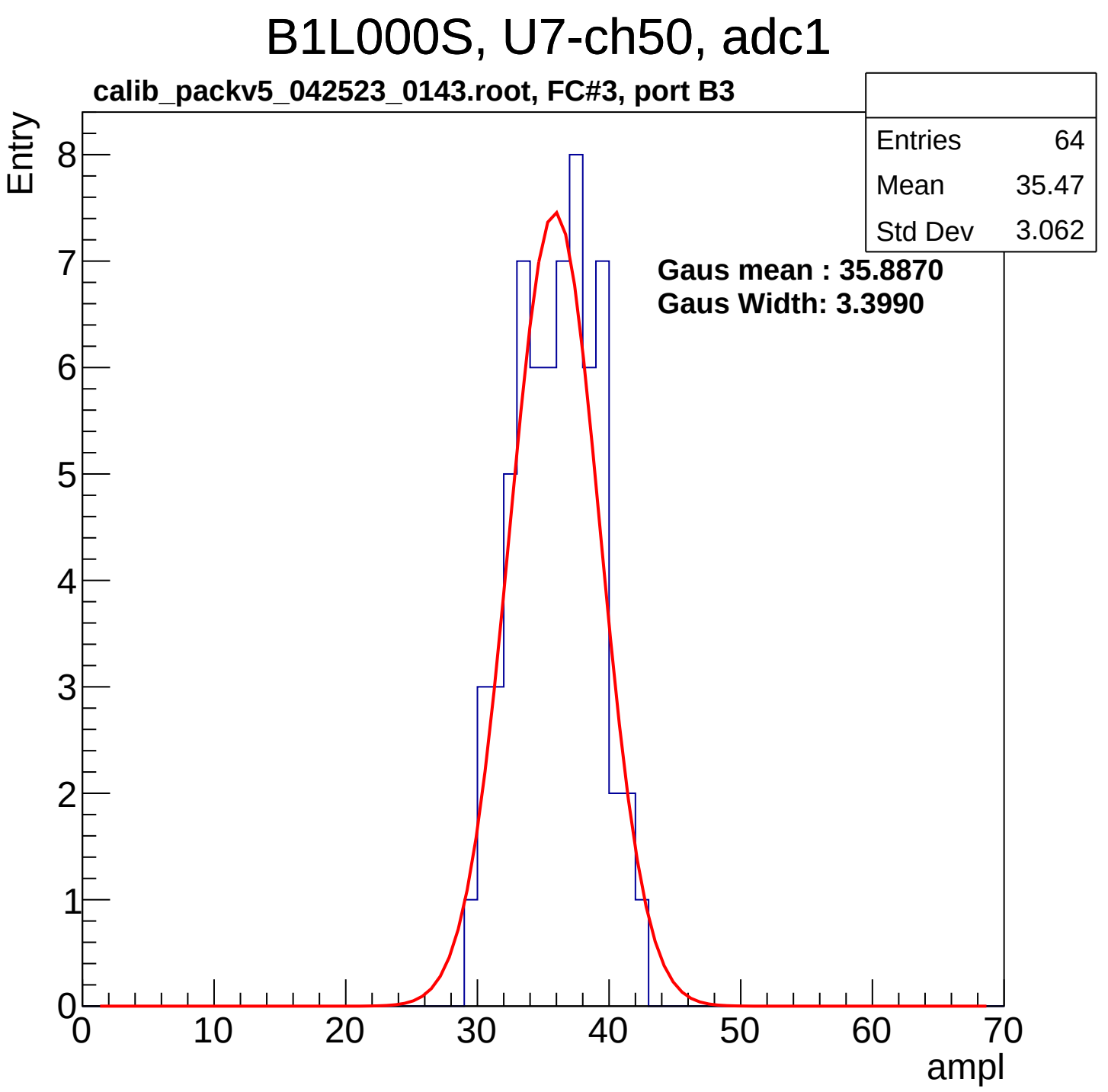
B1L000S, U7-ch50, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 35.47
Std Dev 3.062
Gaus mean : 35.8870
Gaus Width: 3.3990
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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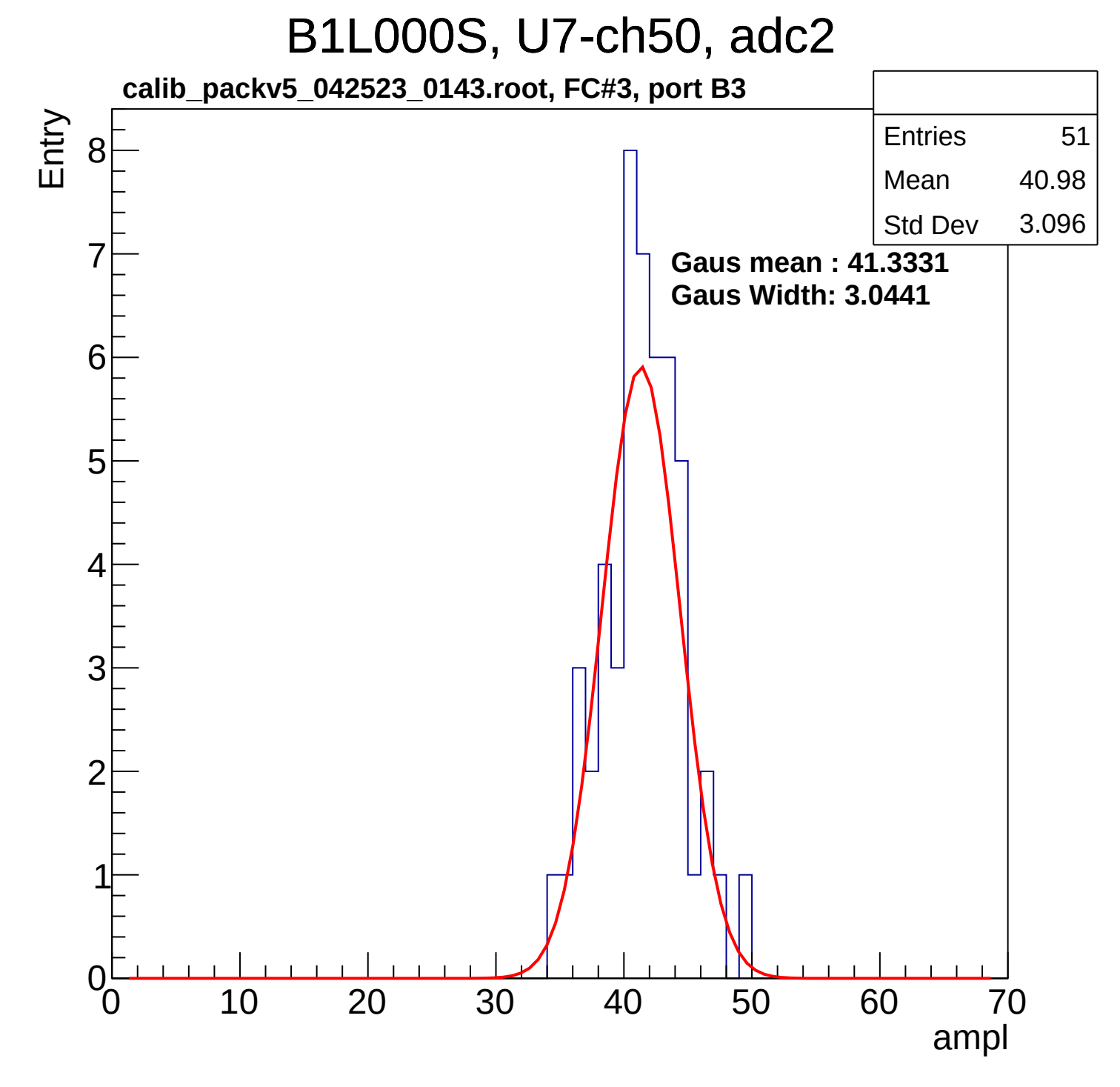
B1L000S, U7-ch50, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 40.98
Std Dev 3.096
Gaus mean : 41.3331
Gaus Width: 3.0441
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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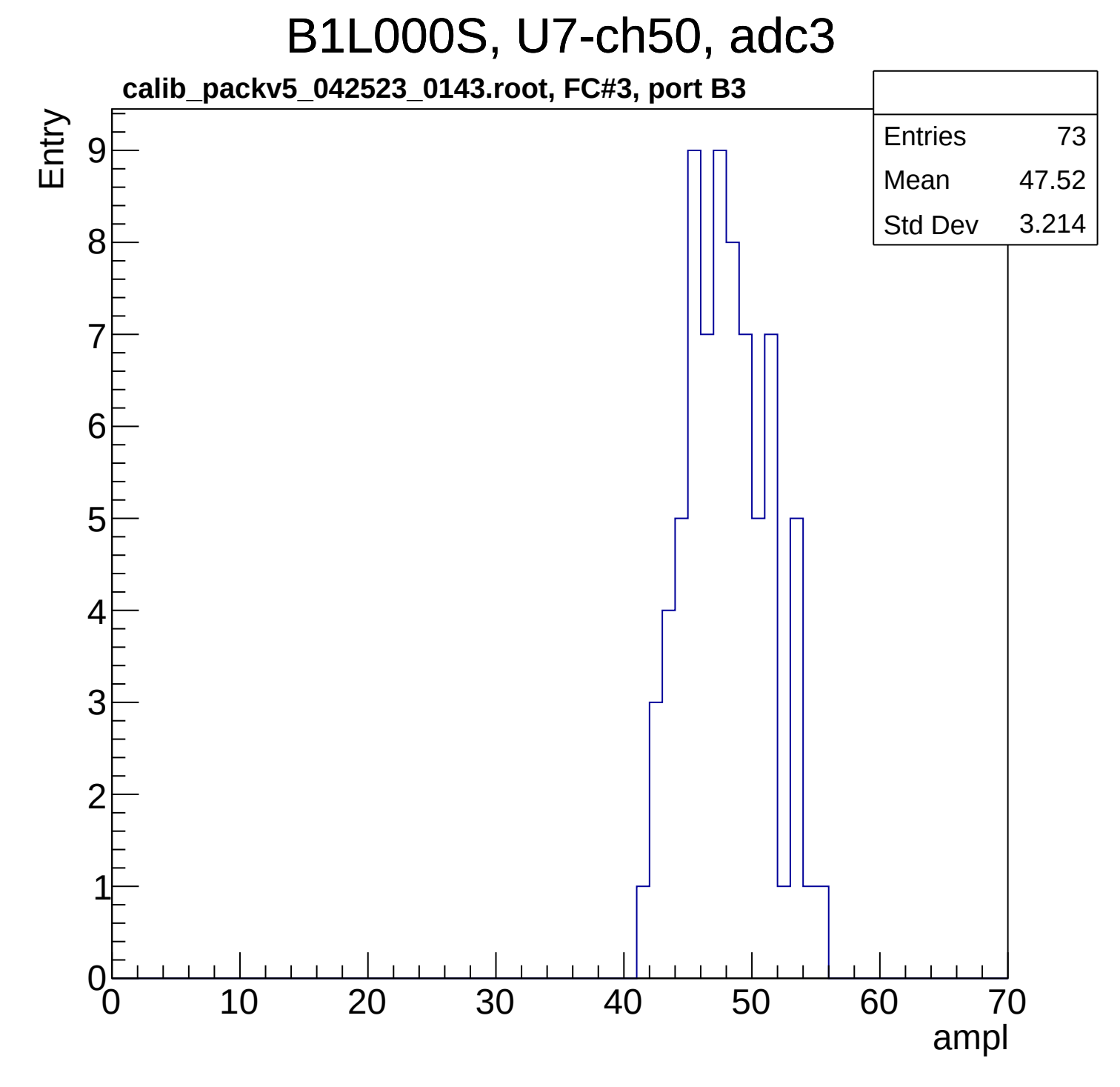
B1L000S, U7-ch50, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 47.52
Std Dev 3.214
Entry
ampl
0
1
2
3
4
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6
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8
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0
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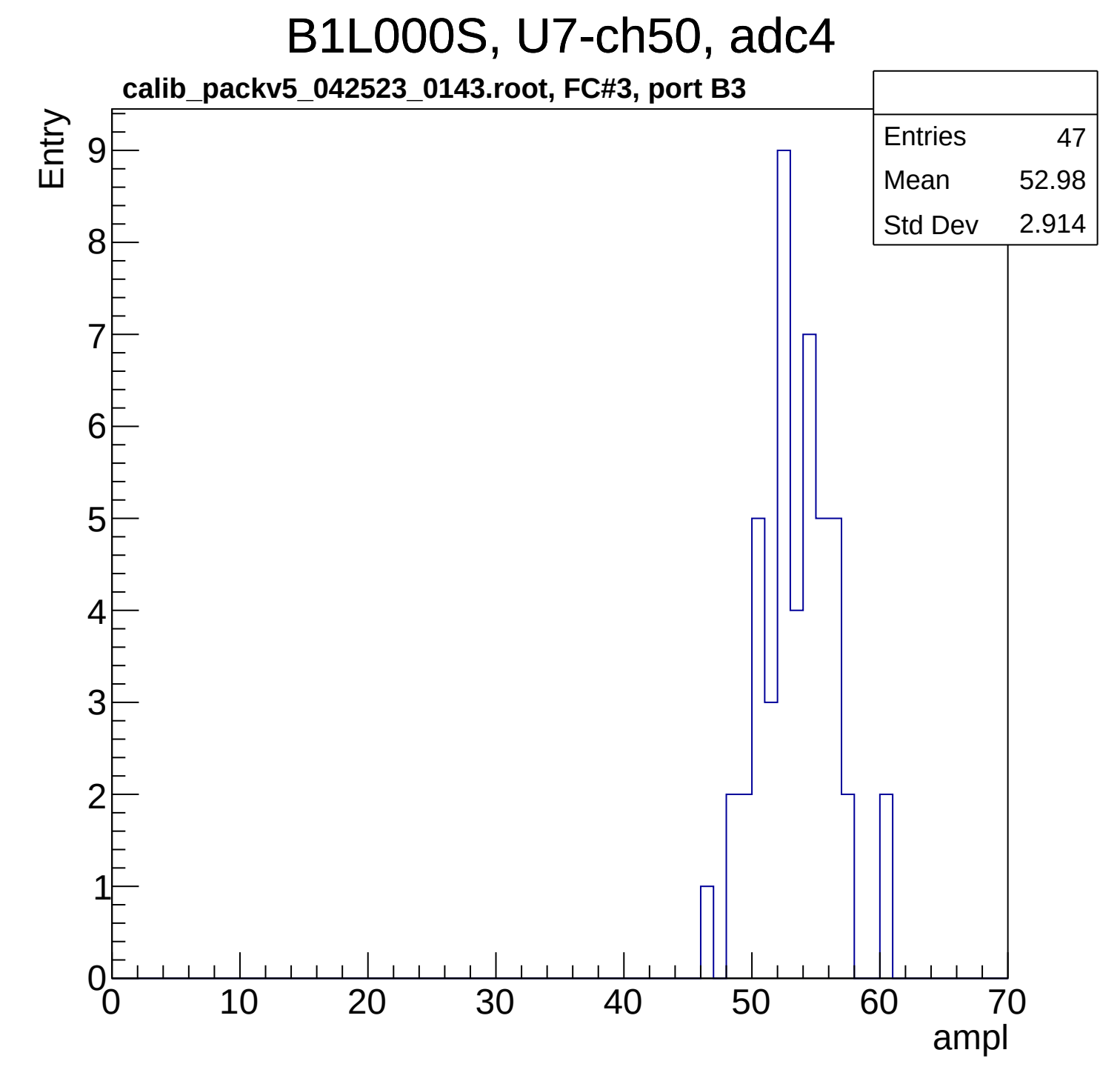
B1L000S, U7-ch50, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 52.98
Std Dev 2.914
Entry
ampl
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5
6
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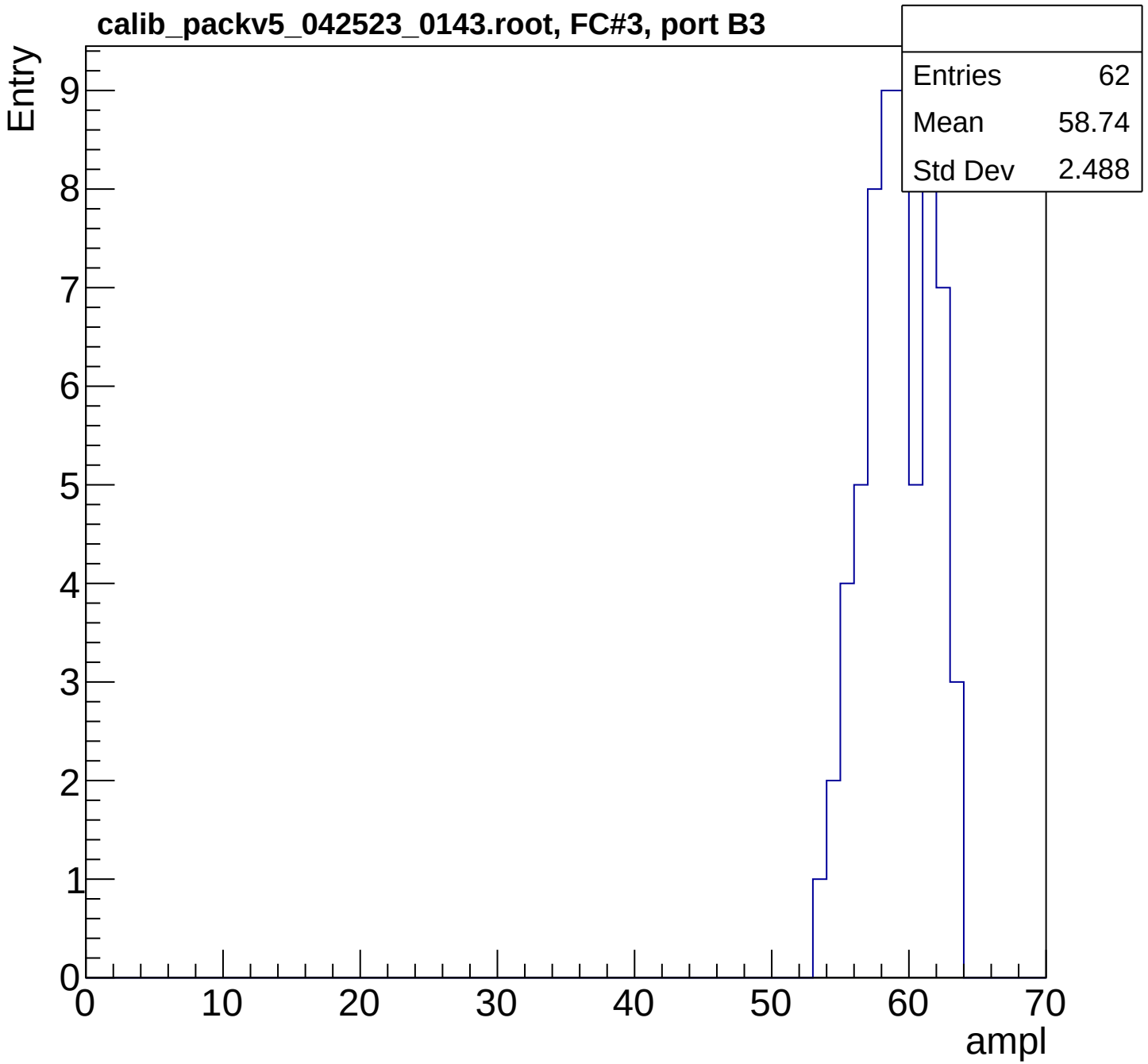
B1L000S, U7-ch50, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 58.74
Std Dev 2.488
Entry
ampl
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2
3
4
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6
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0
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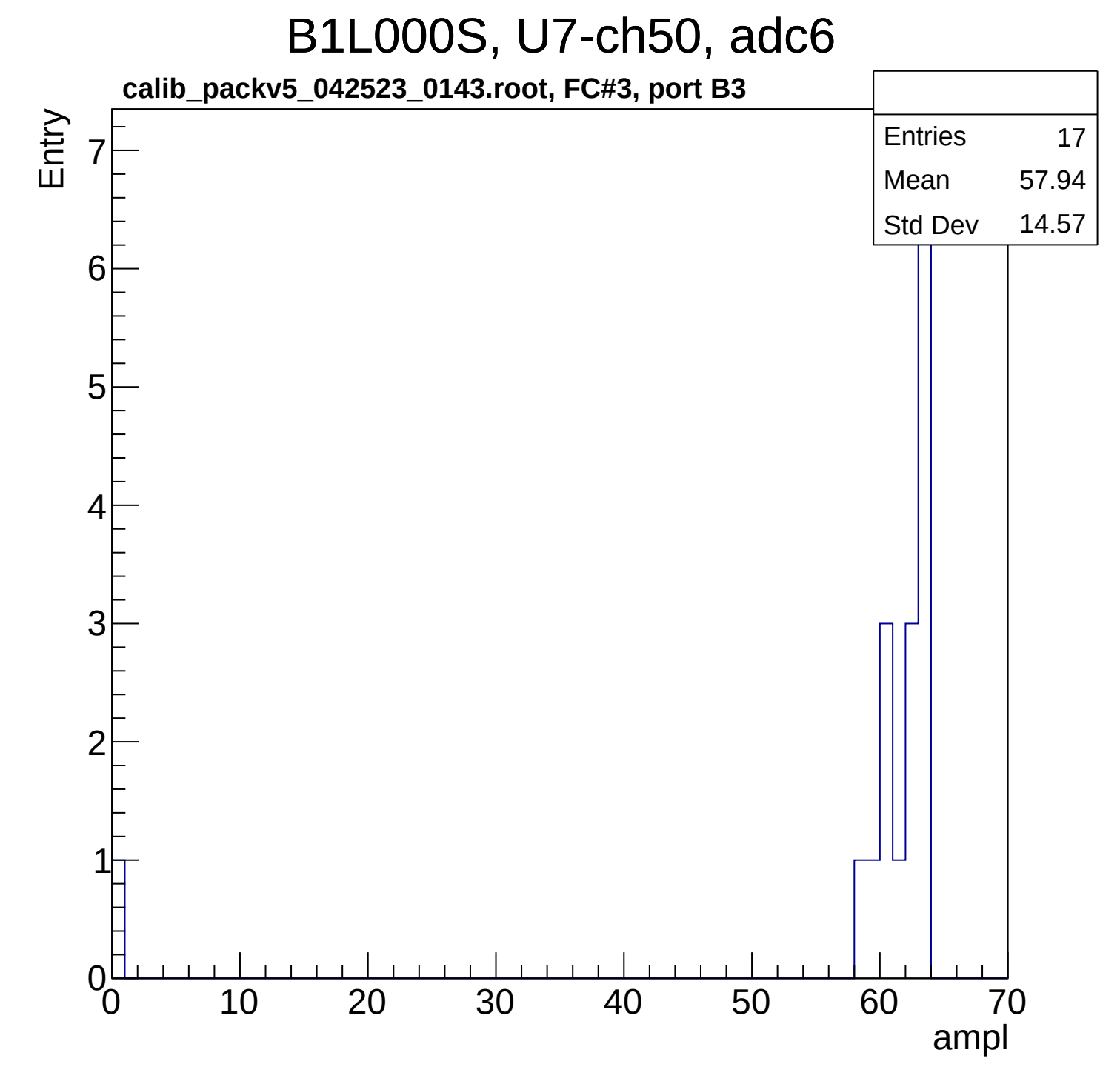
B1L000S, U7-ch50, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 57.94
Std Dev 14.57
Entry
ampl
0
1
2
3
4
5
6
7
0
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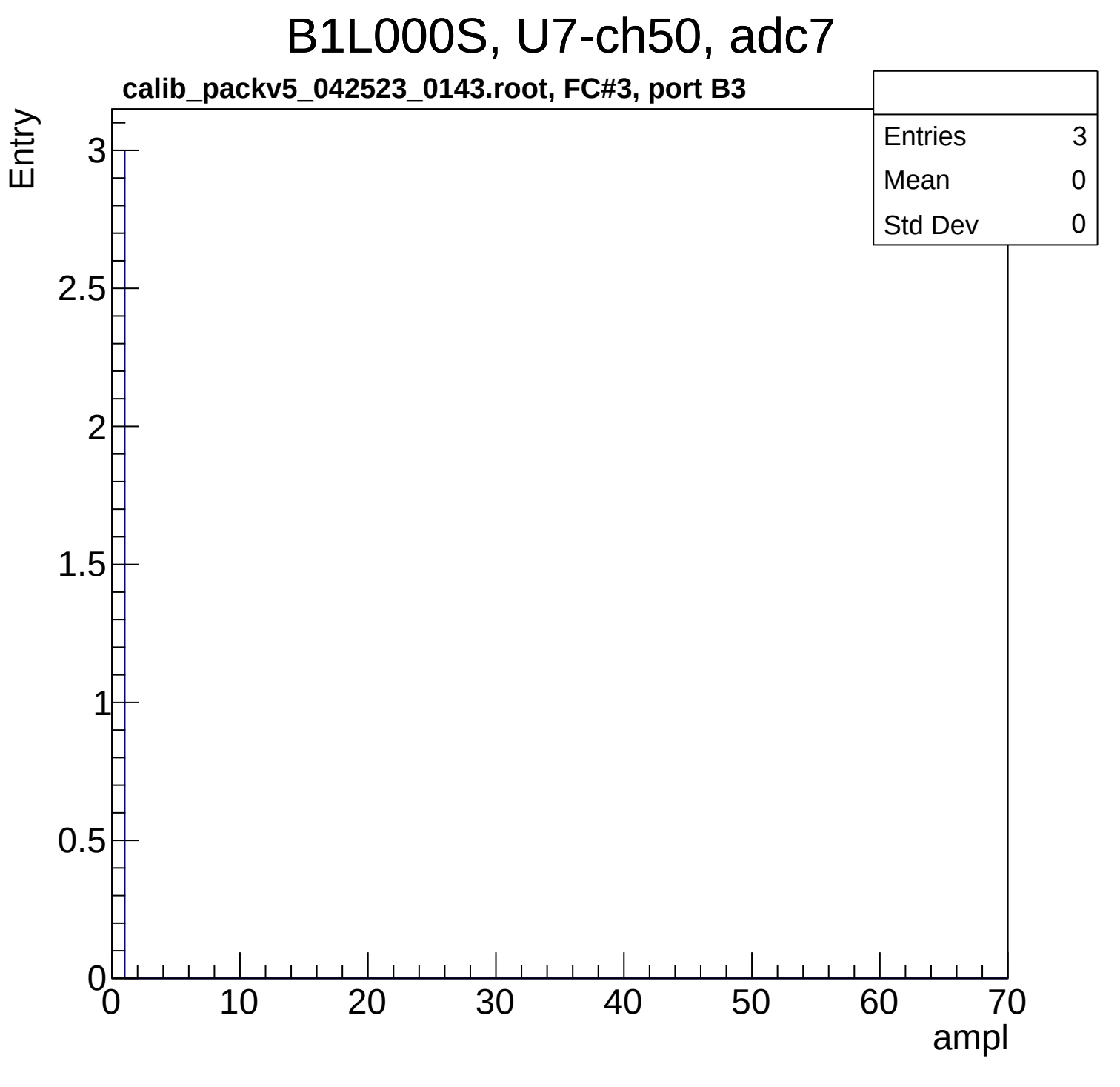
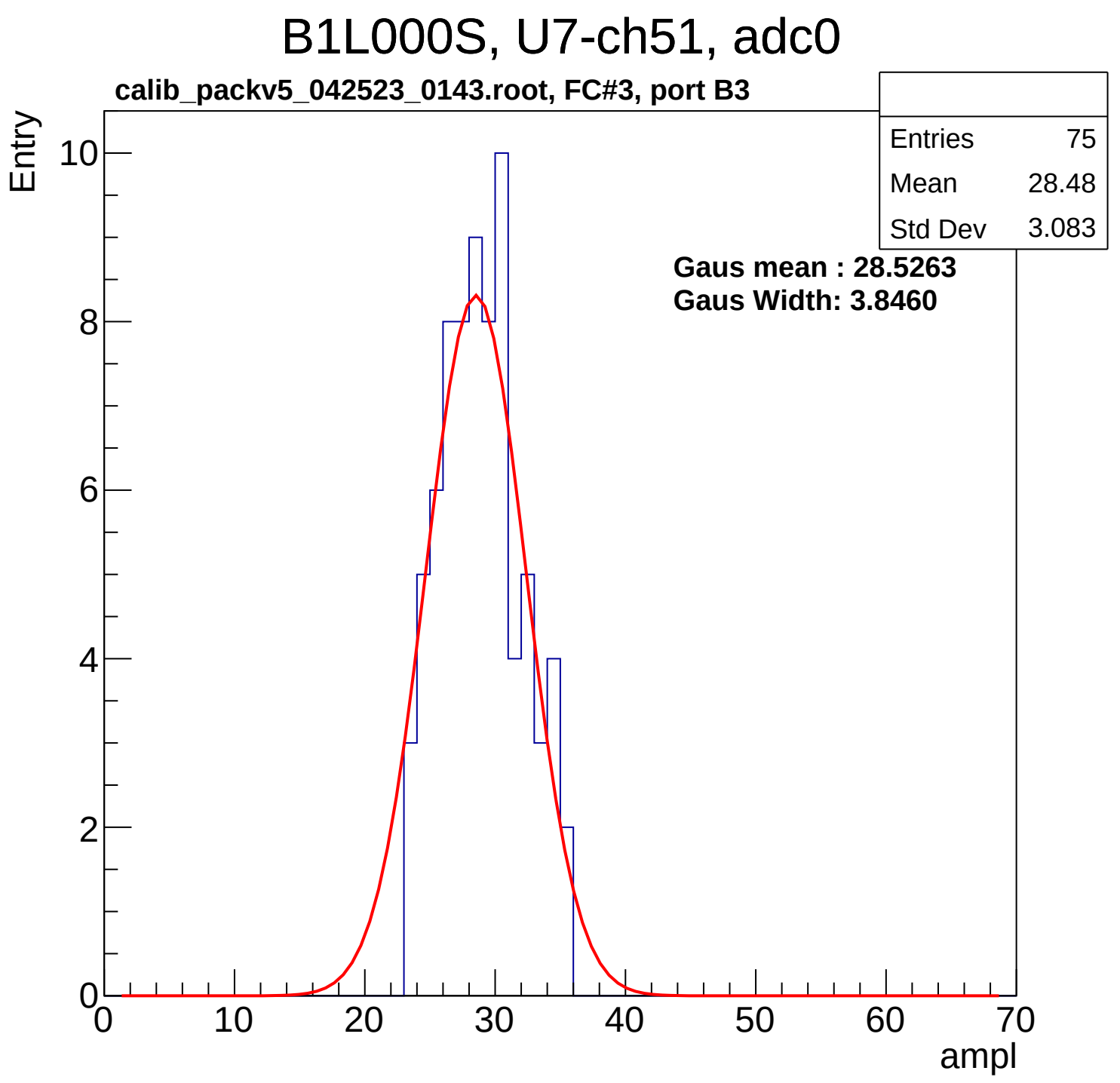

B1L000S, U7-ch50, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch51, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.48
Std Dev 3.083
Gaus mean : 28.5263
Gaus Width: 3.8460
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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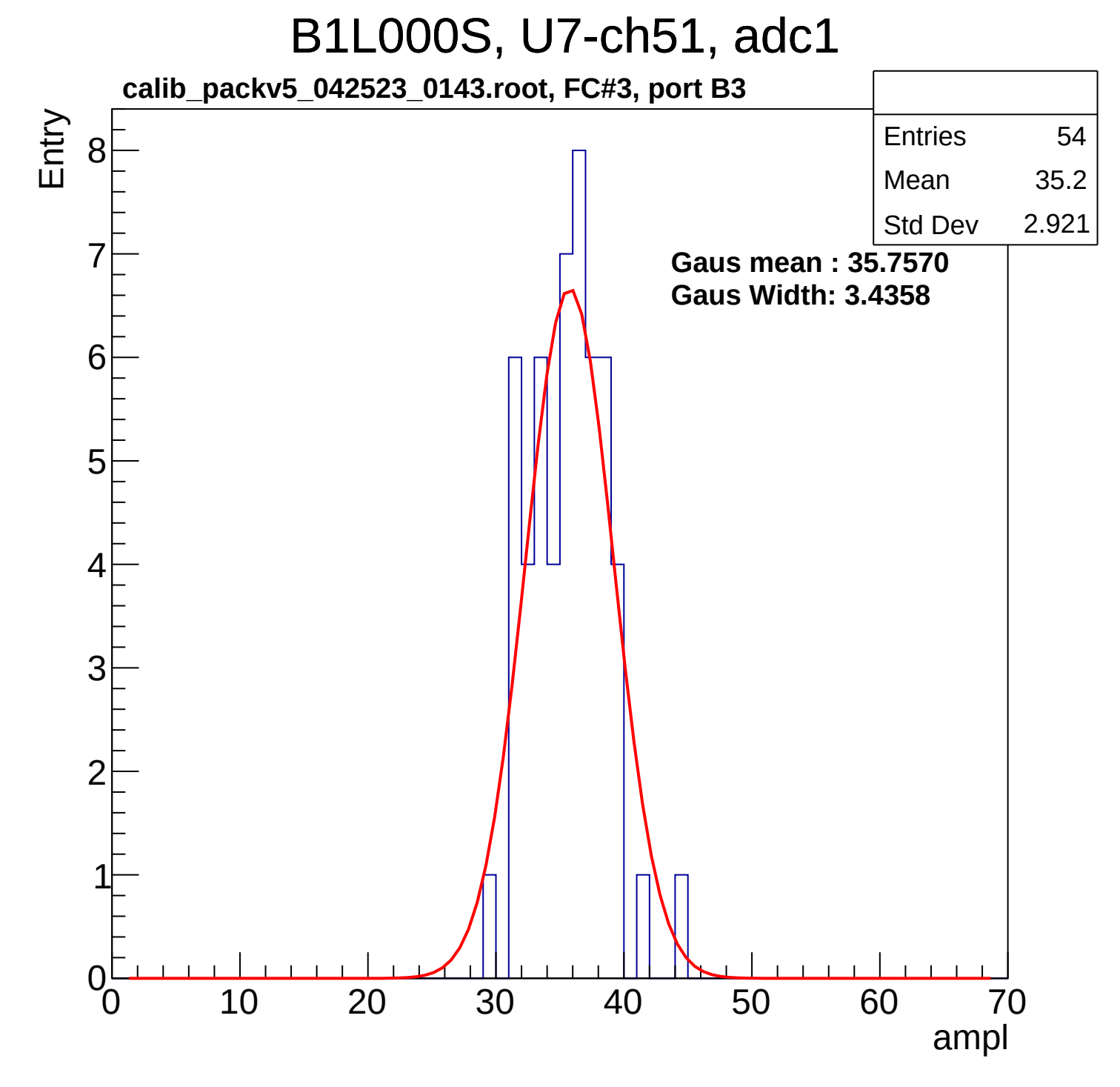
B1L000S, U7-ch51, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 35.2
Std Dev 2.921
Gaus mean : 35.7570
Gaus Width: 3.4358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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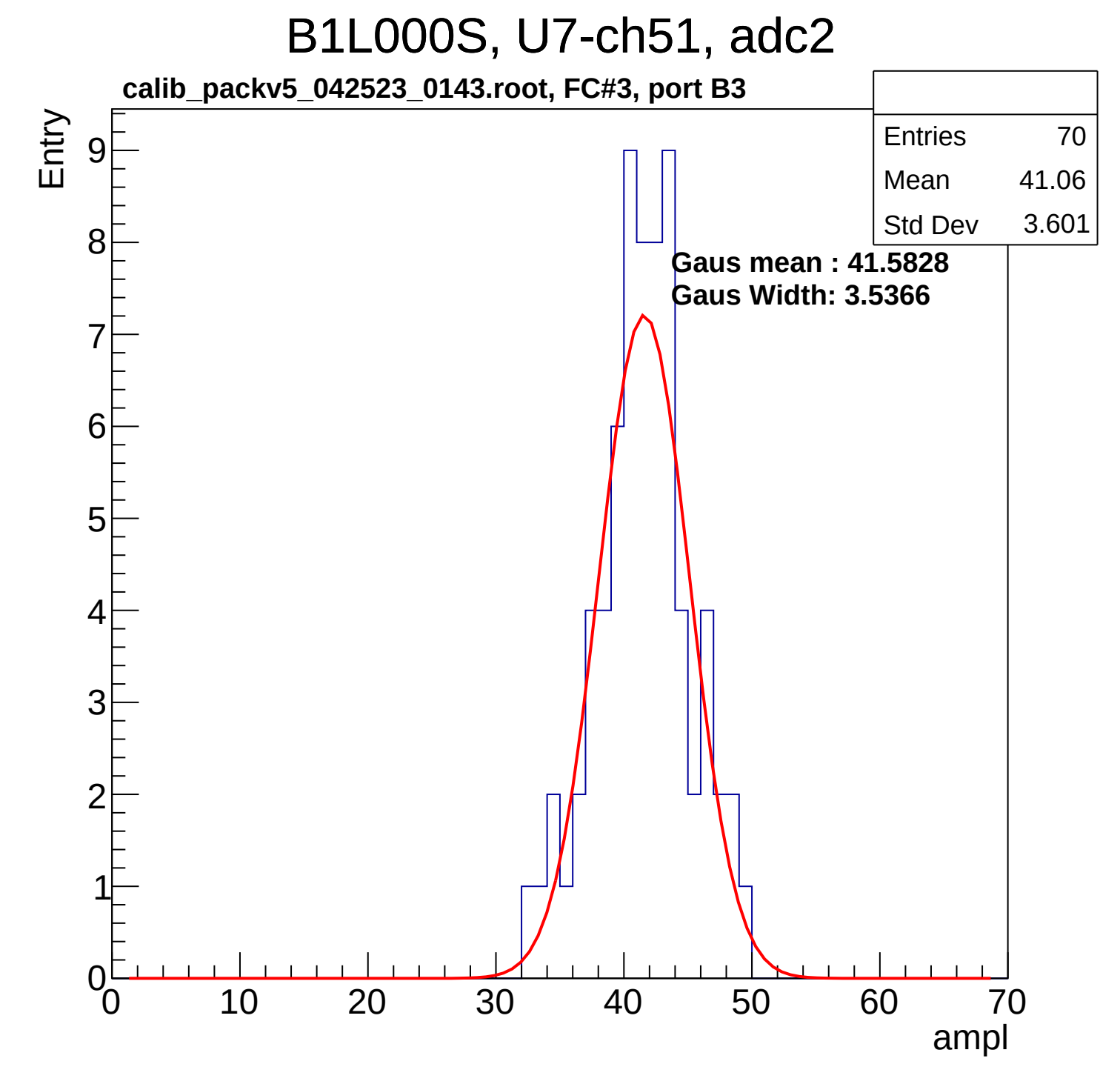
B1L000S, U7-ch51, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 41.06
Std Dev 3.601
Gaus mean : 41.5828
Gaus Width: 3.5366
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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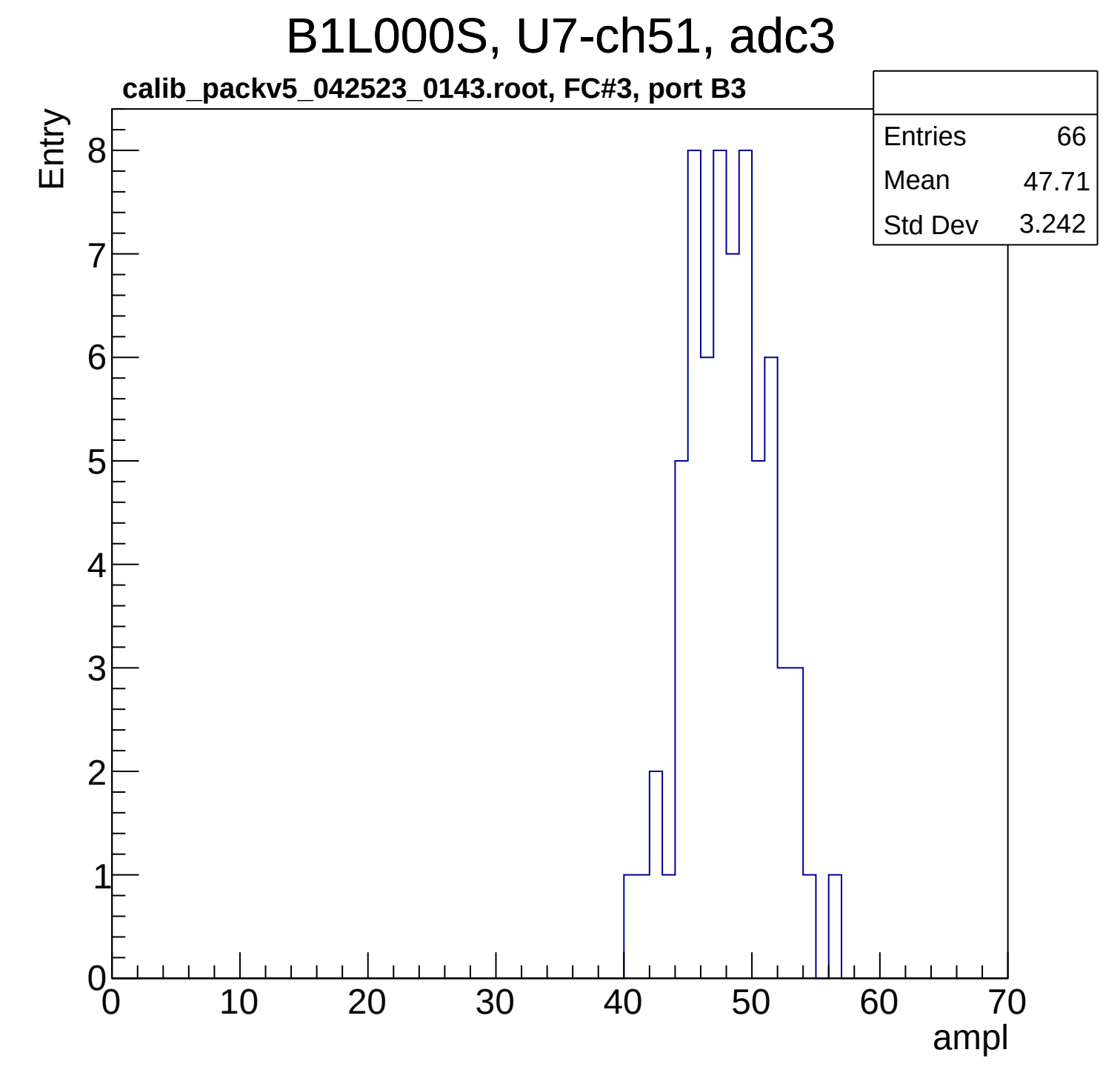
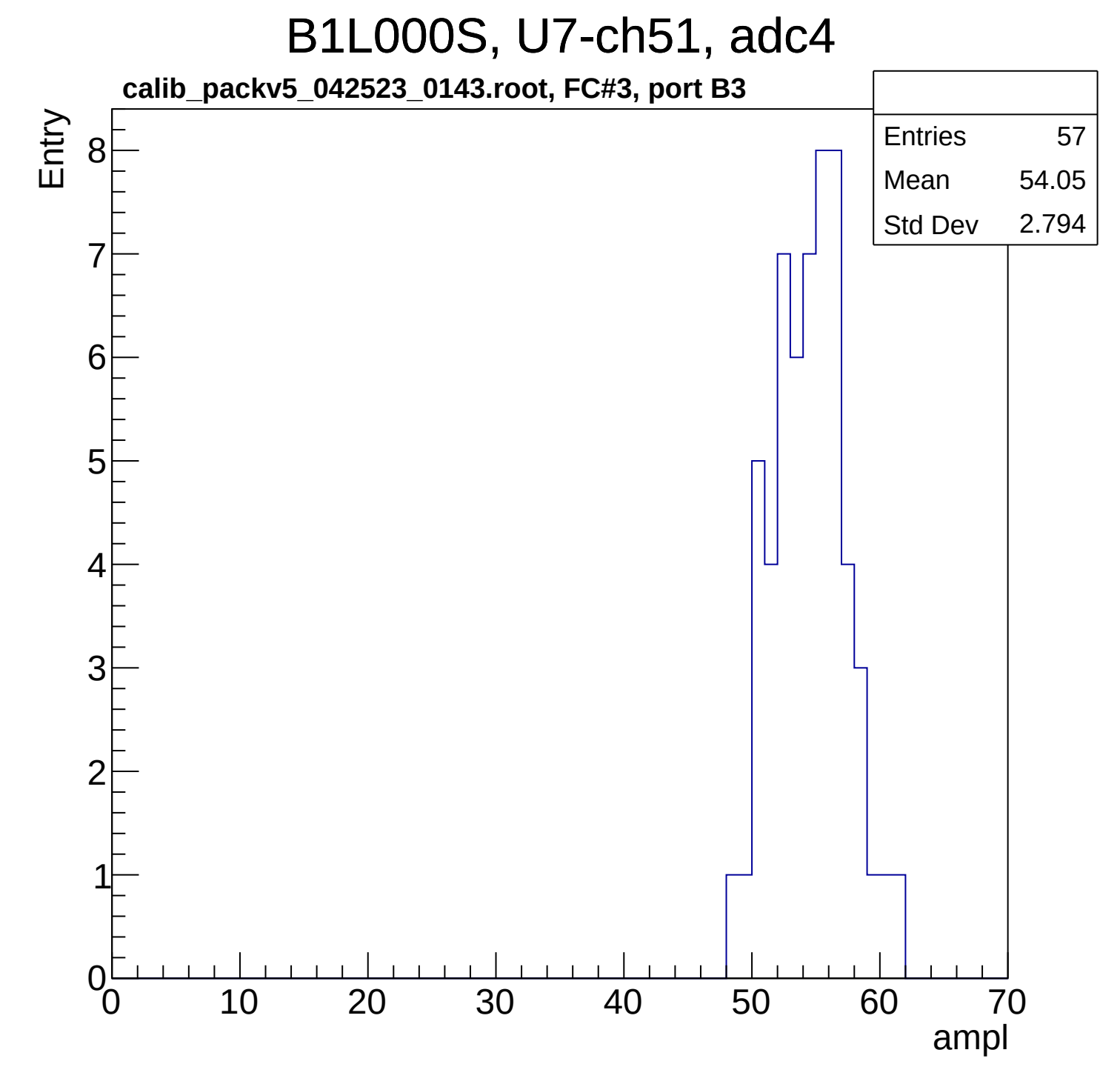
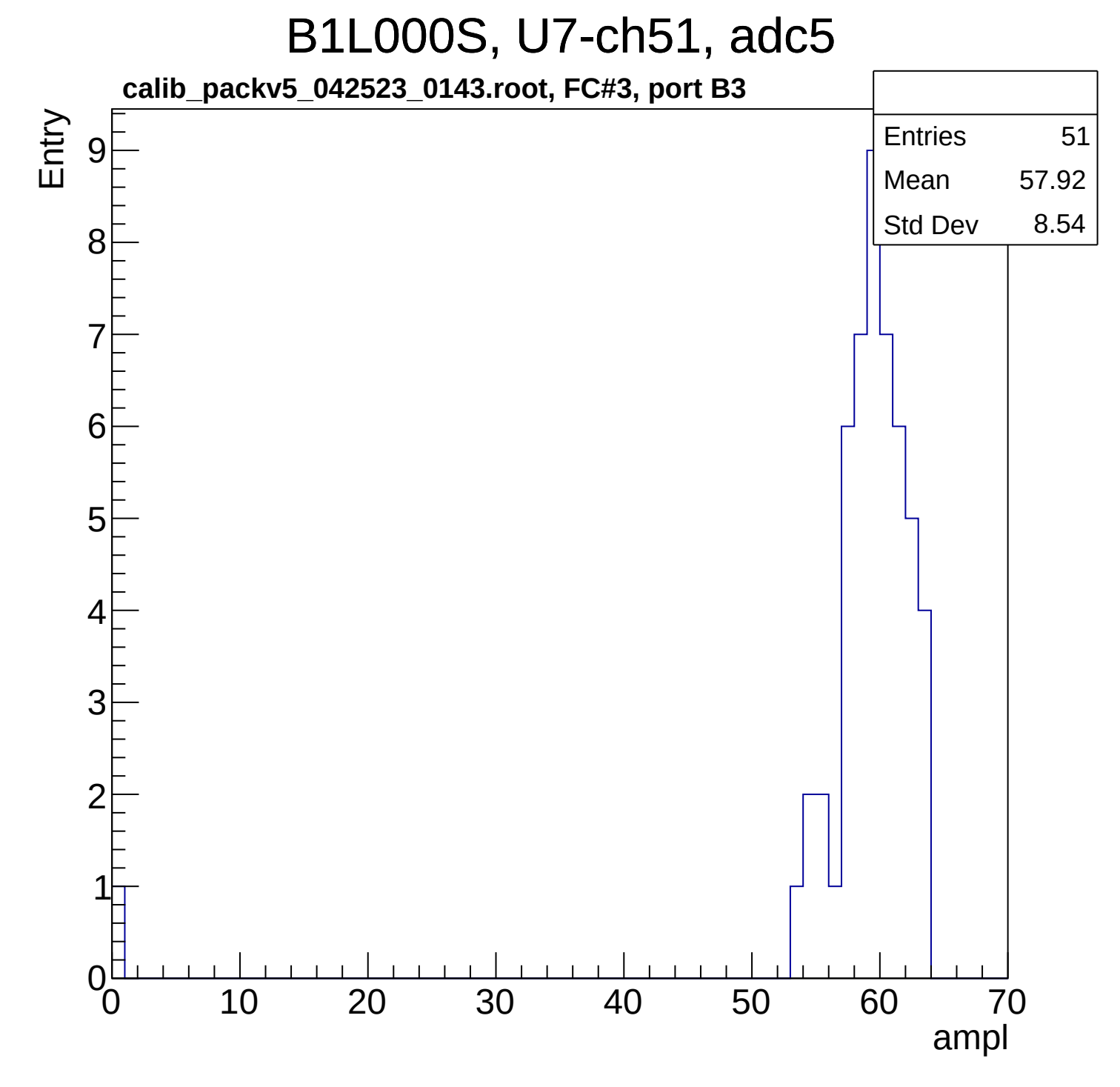

B1L000S, U7-ch51, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 47.71
Std Dev 3.242
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch51, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 54.05
Std Dev 2.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch51, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 57.92
Std Dev 8.54
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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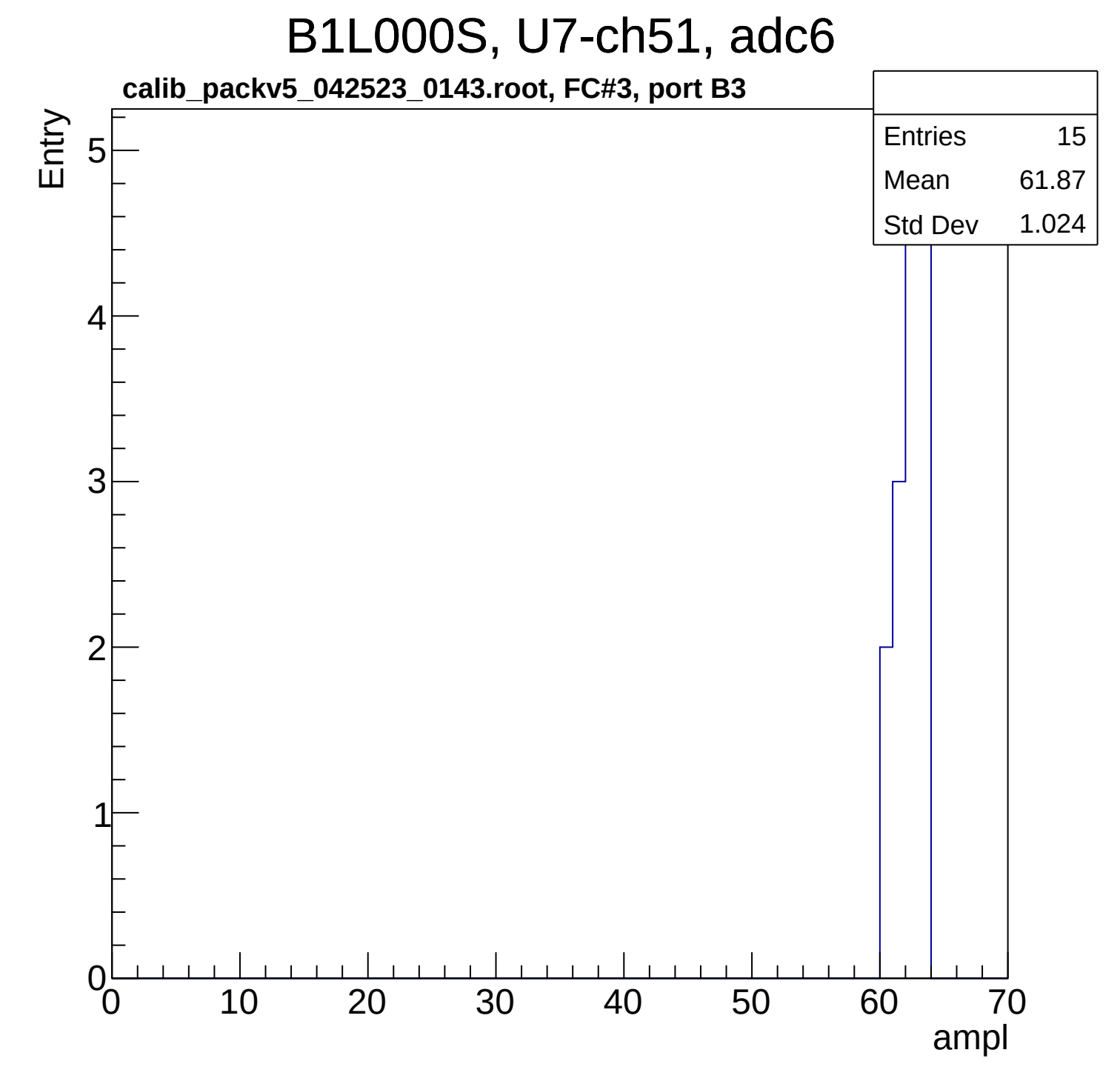
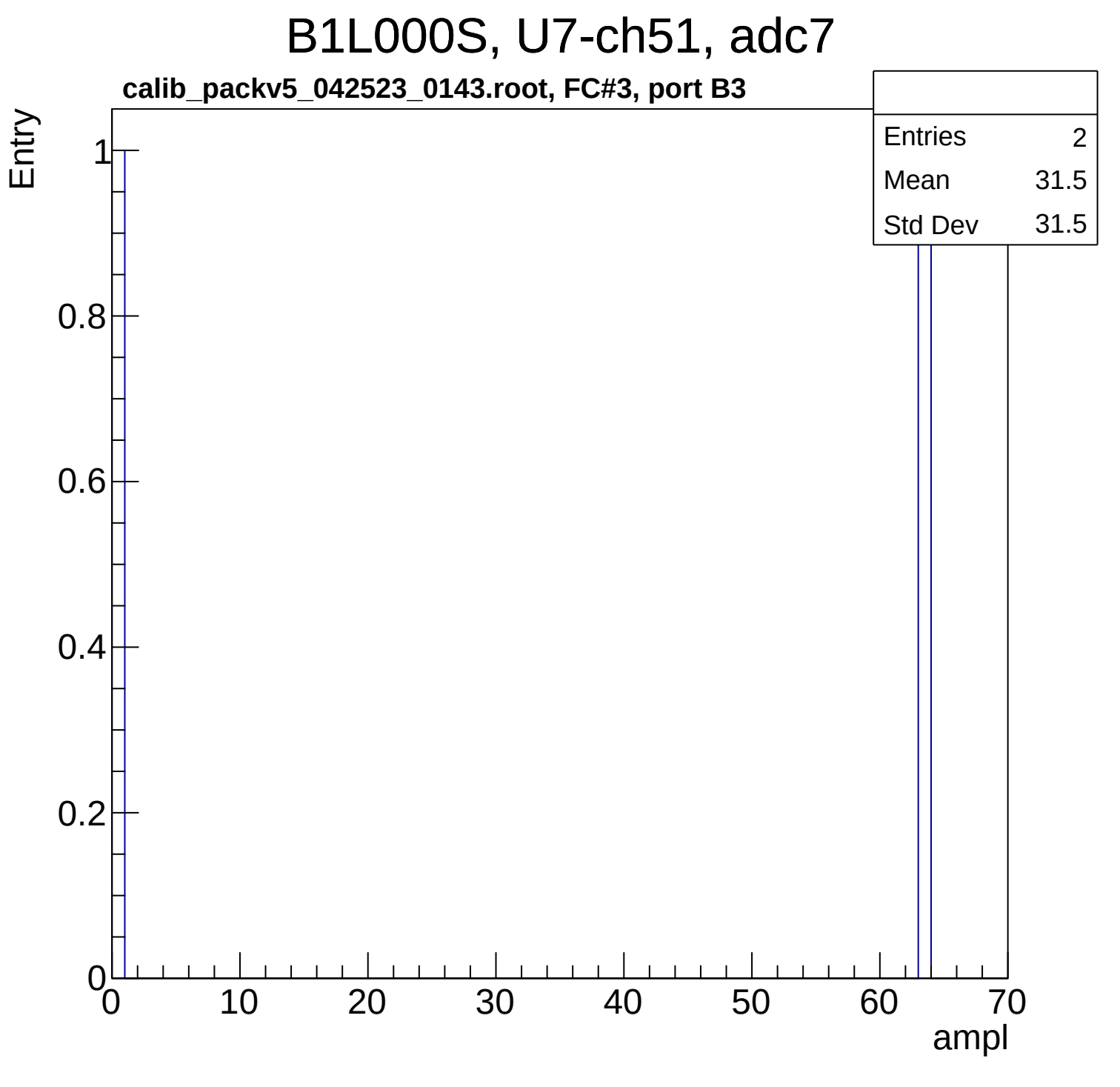
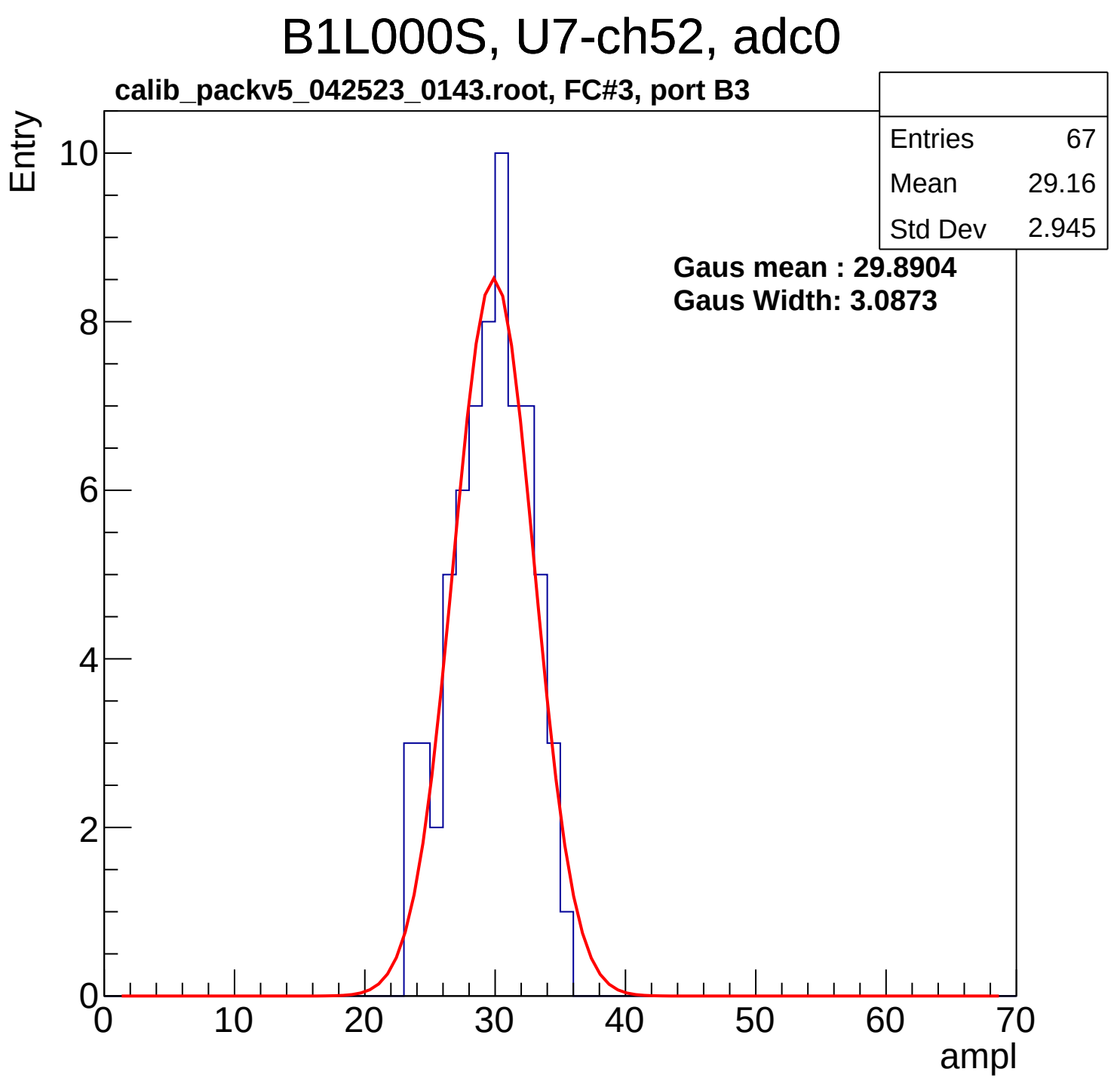
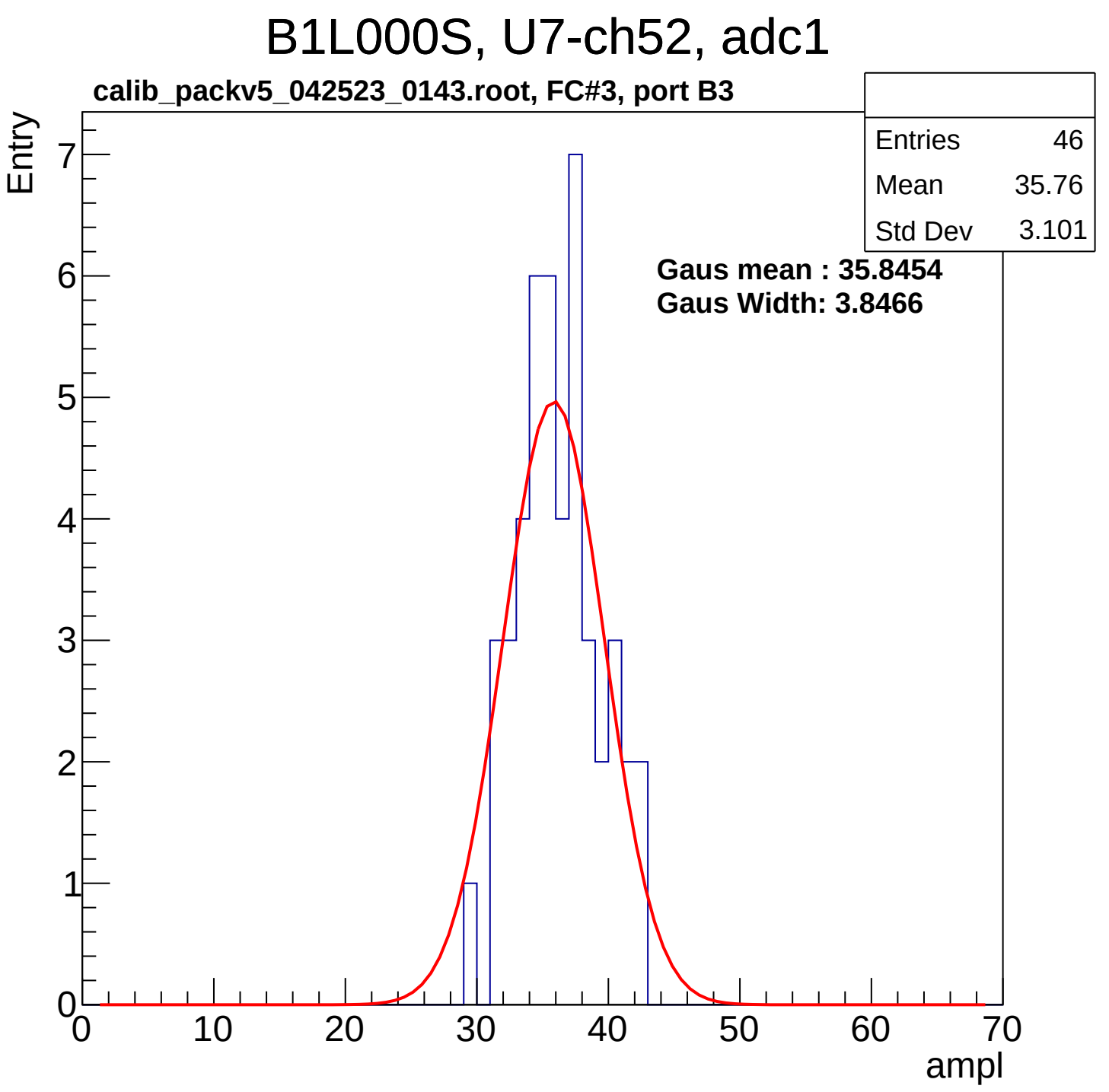
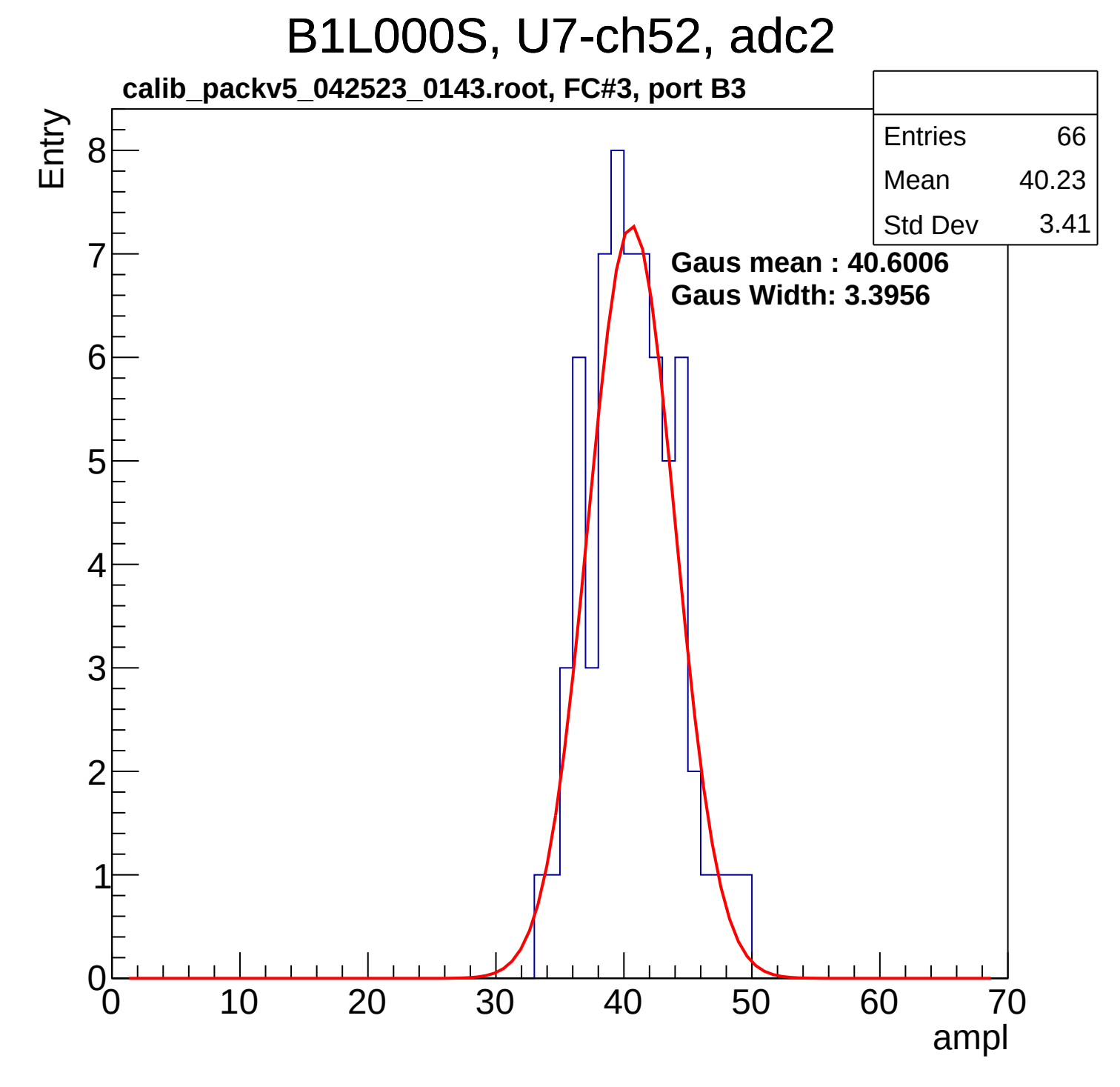
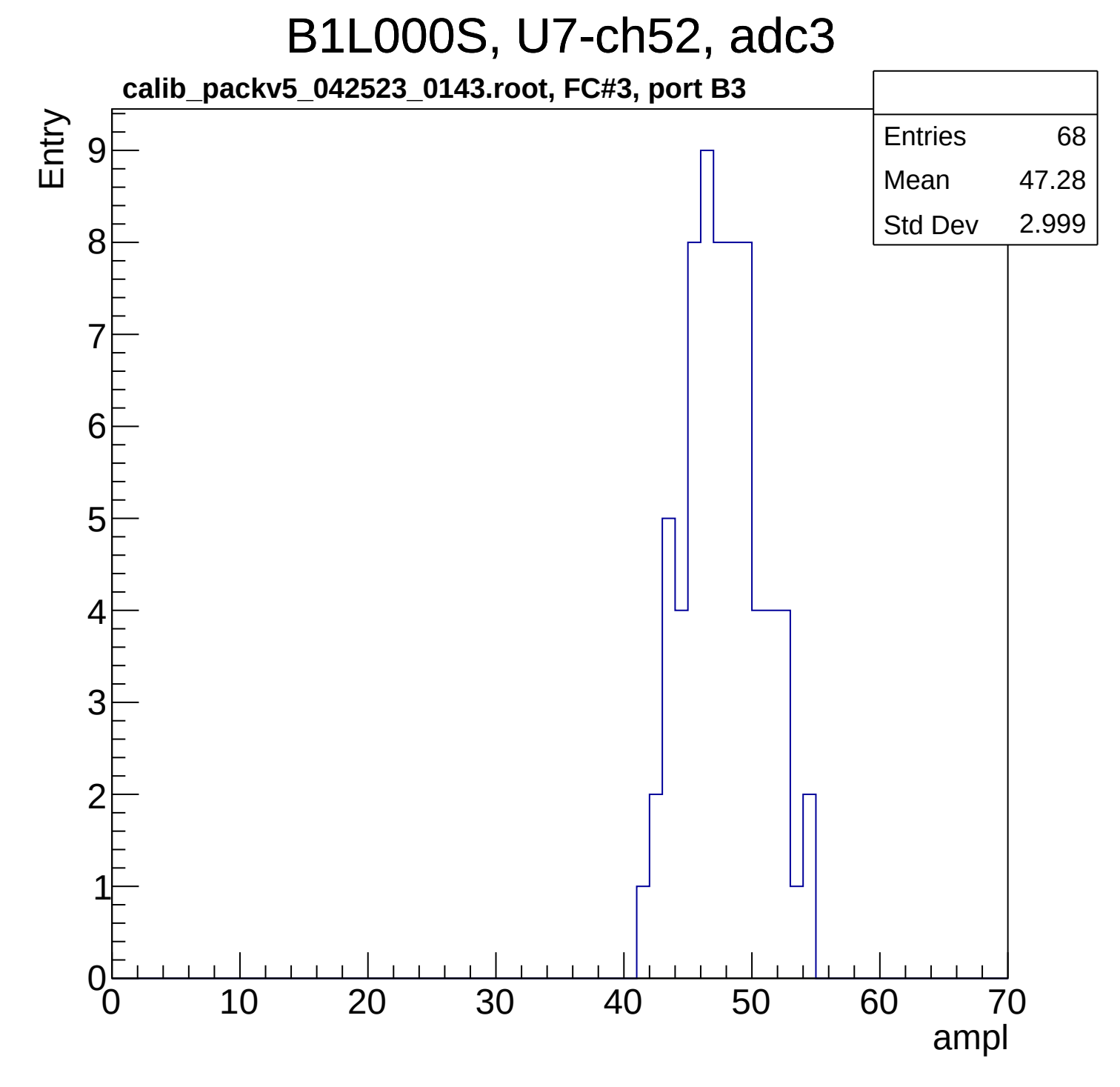

B1L000S, U7-ch51, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 61.87
Std Dev 1.024
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch51, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch52, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 29.16
Std Dev 2.945
Gaus mean : 29.8904
Gaus Width: 3.0873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch52, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 35.76
Std Dev 3.101
Gaus mean : 35.8454
Gaus Width: 3.8466
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch52, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 40.23
Std Dev 3.41
Gaus mean : 40.6006
Gaus Width: 3.3956
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch52, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 47.28
Std Dev 2.999
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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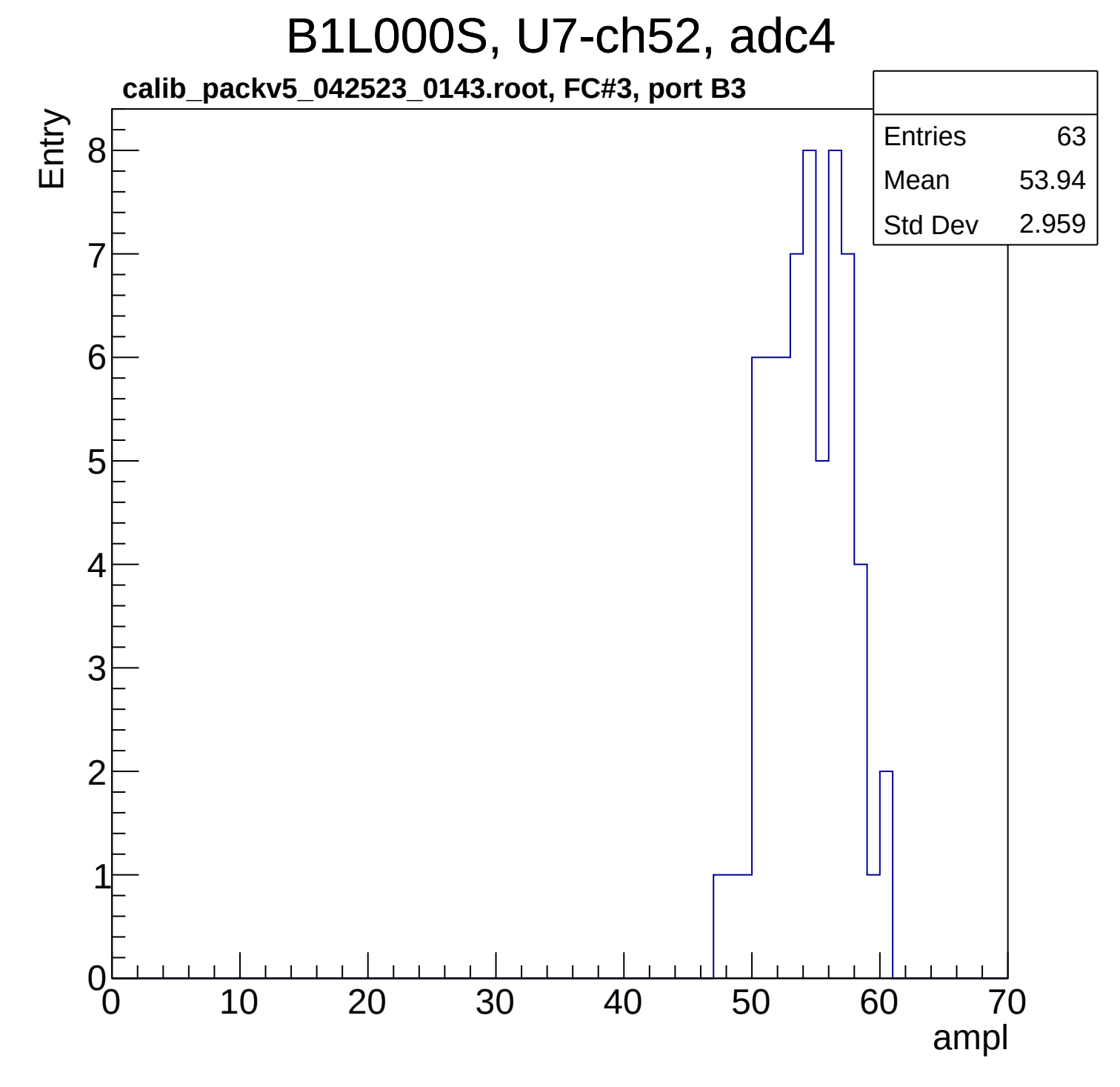
B1L000S, U7-ch52, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 53.94
Std Dev 2.959
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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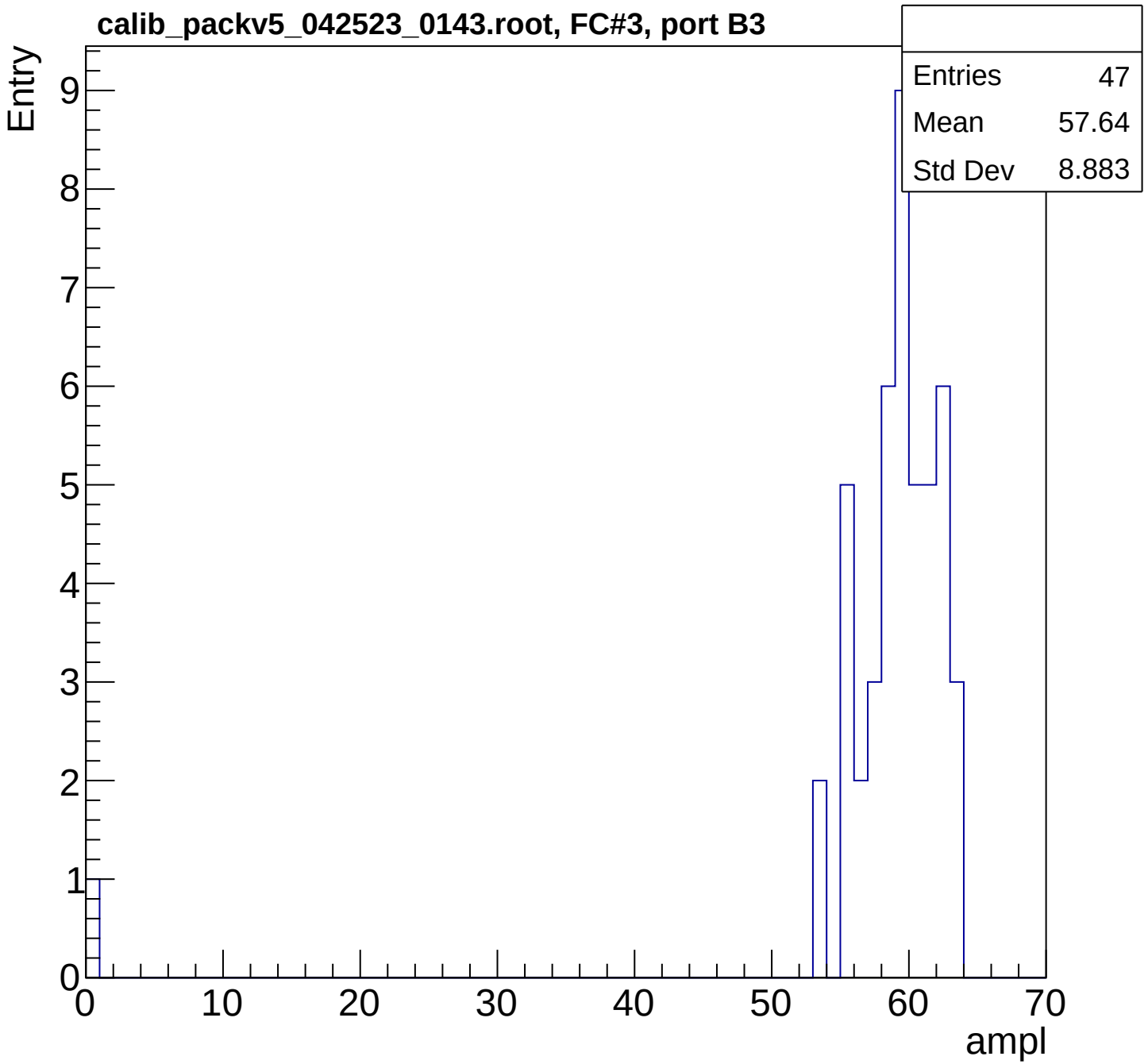
B1L000S, U7-ch52, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 57.64
Std Dev 8.883
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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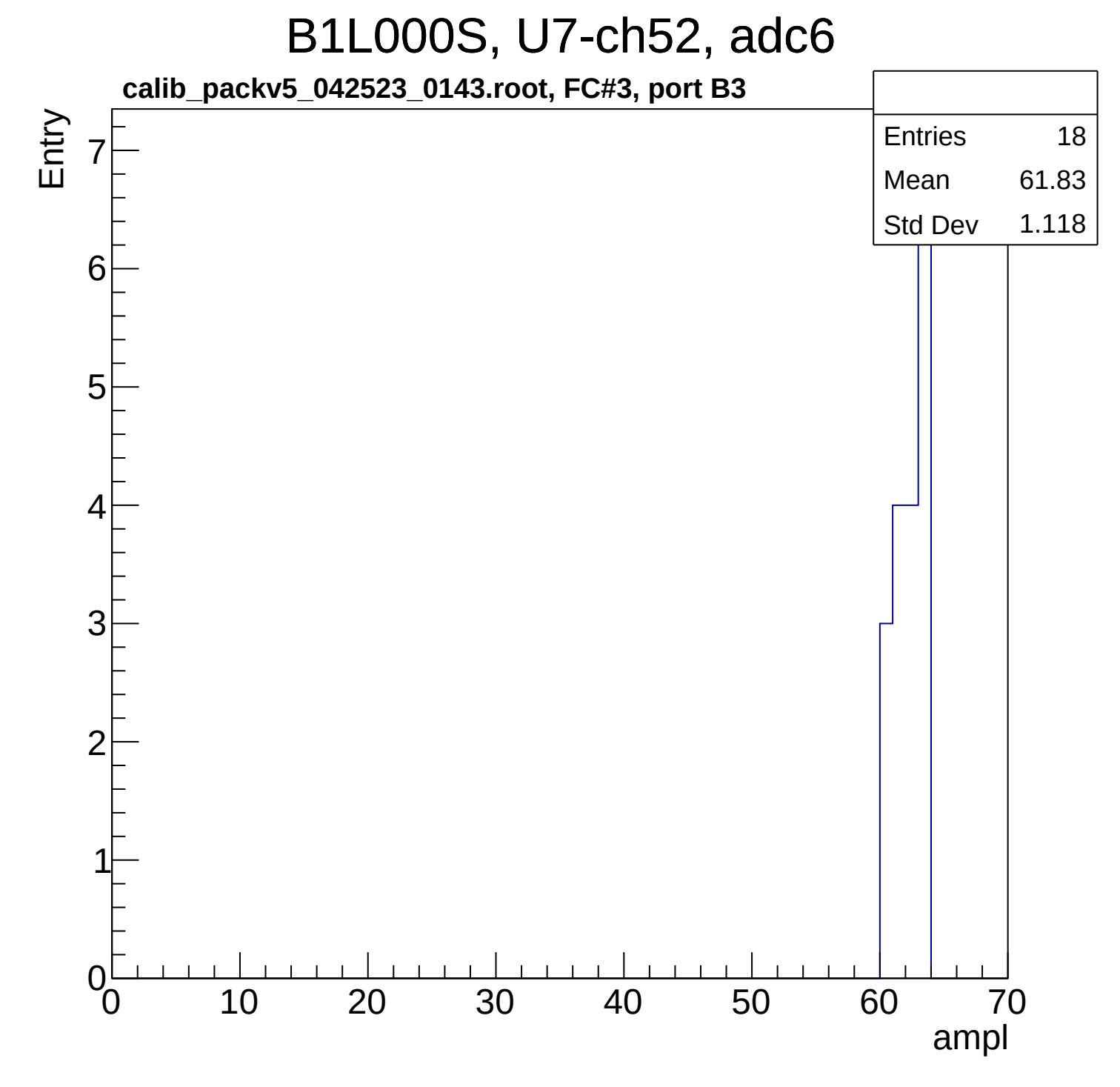
B1L000S, U7-ch52, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 61.83
Std Dev 1.118
Entry
ampl
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4
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6
7
0
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40
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60
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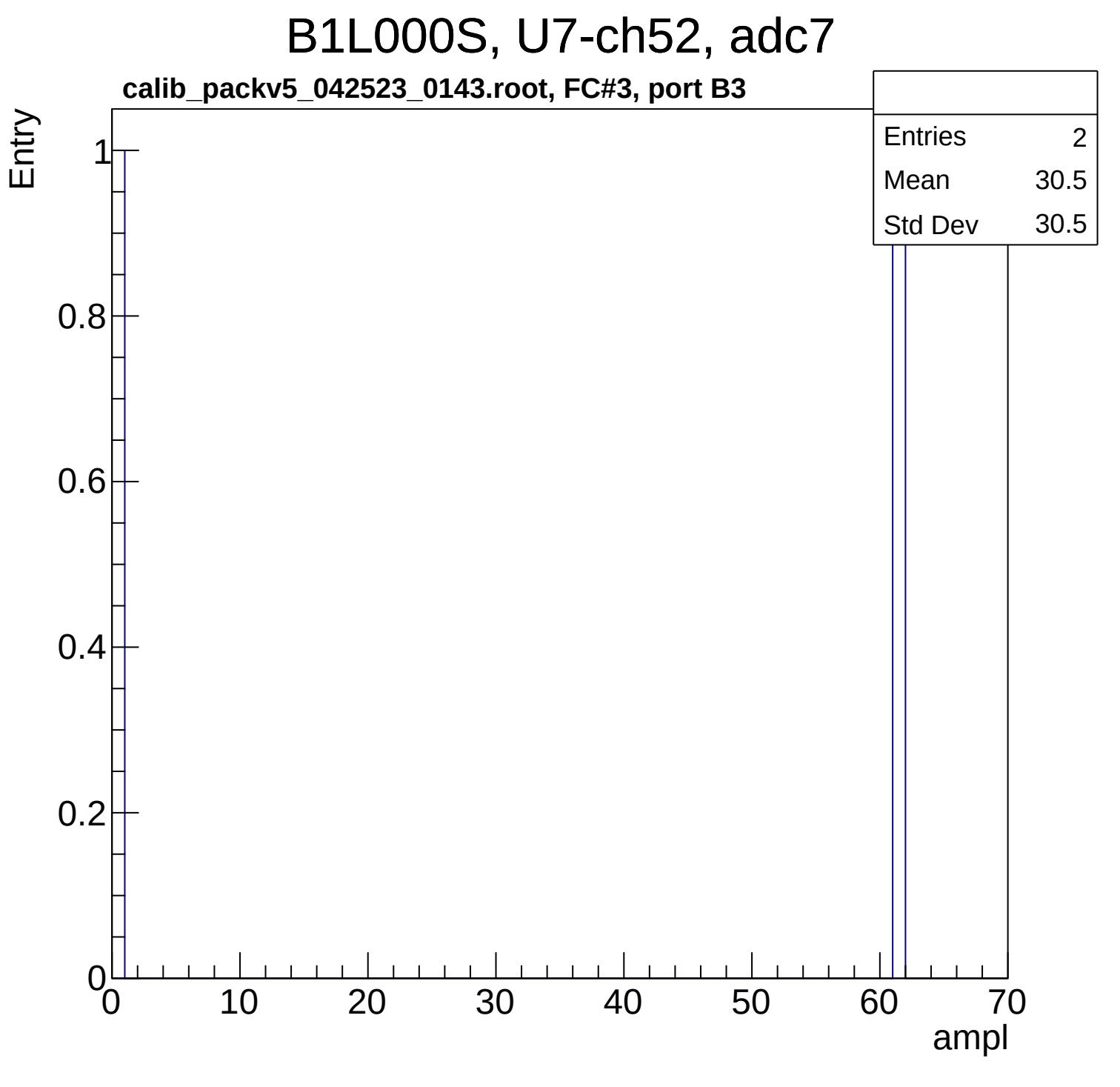
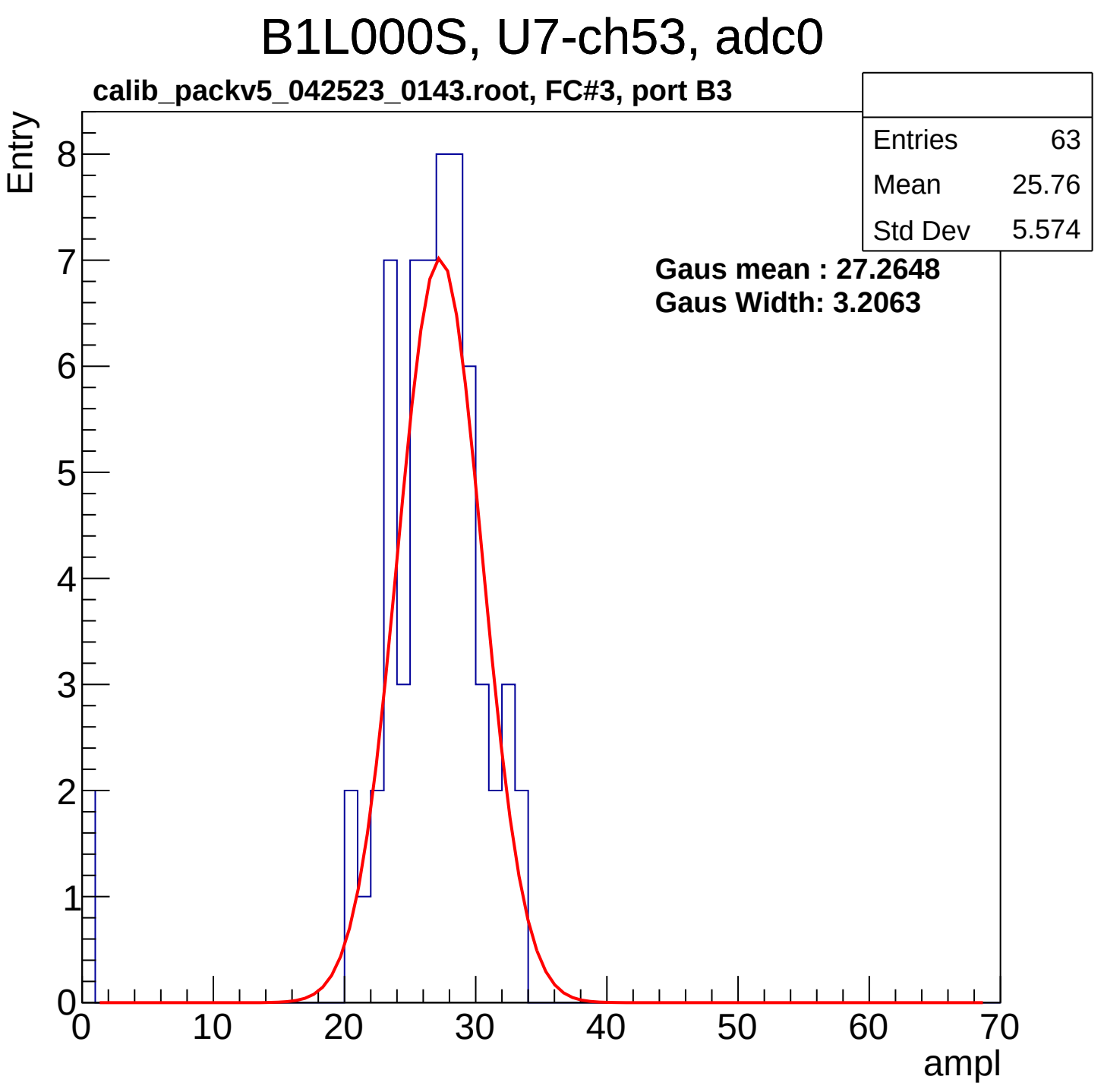

B1L000S, U7-ch52, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch53, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 25.76
Std Dev 5.574
Gaus mean : 27.2648
Gaus Width: 3.2063
Entry
ampl
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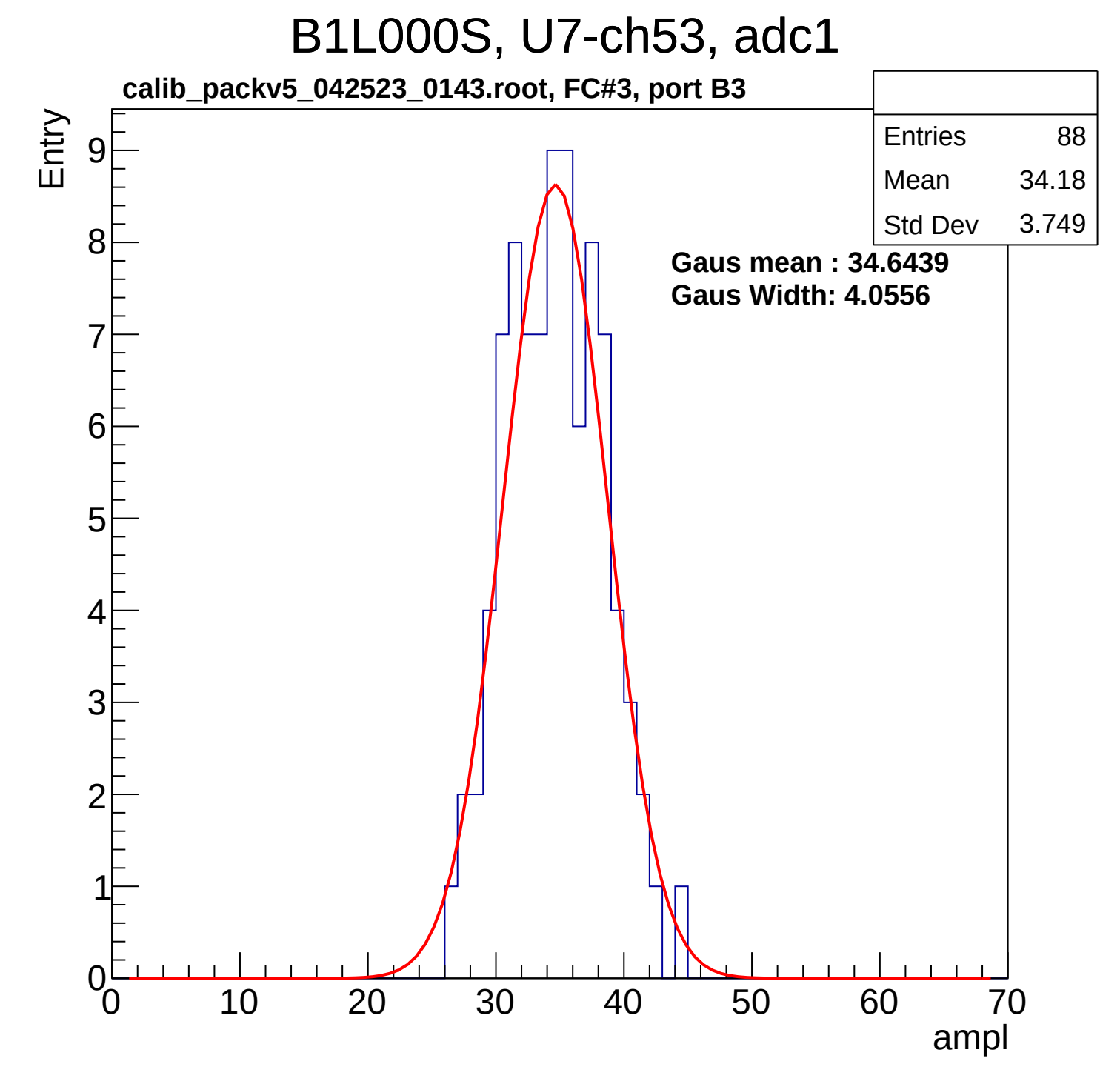
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch53, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 88
Mean 34.18
Std Dev 3.749
Gaus mean : 34.6439
Gaus Width: 4.0556
Entry
ampl
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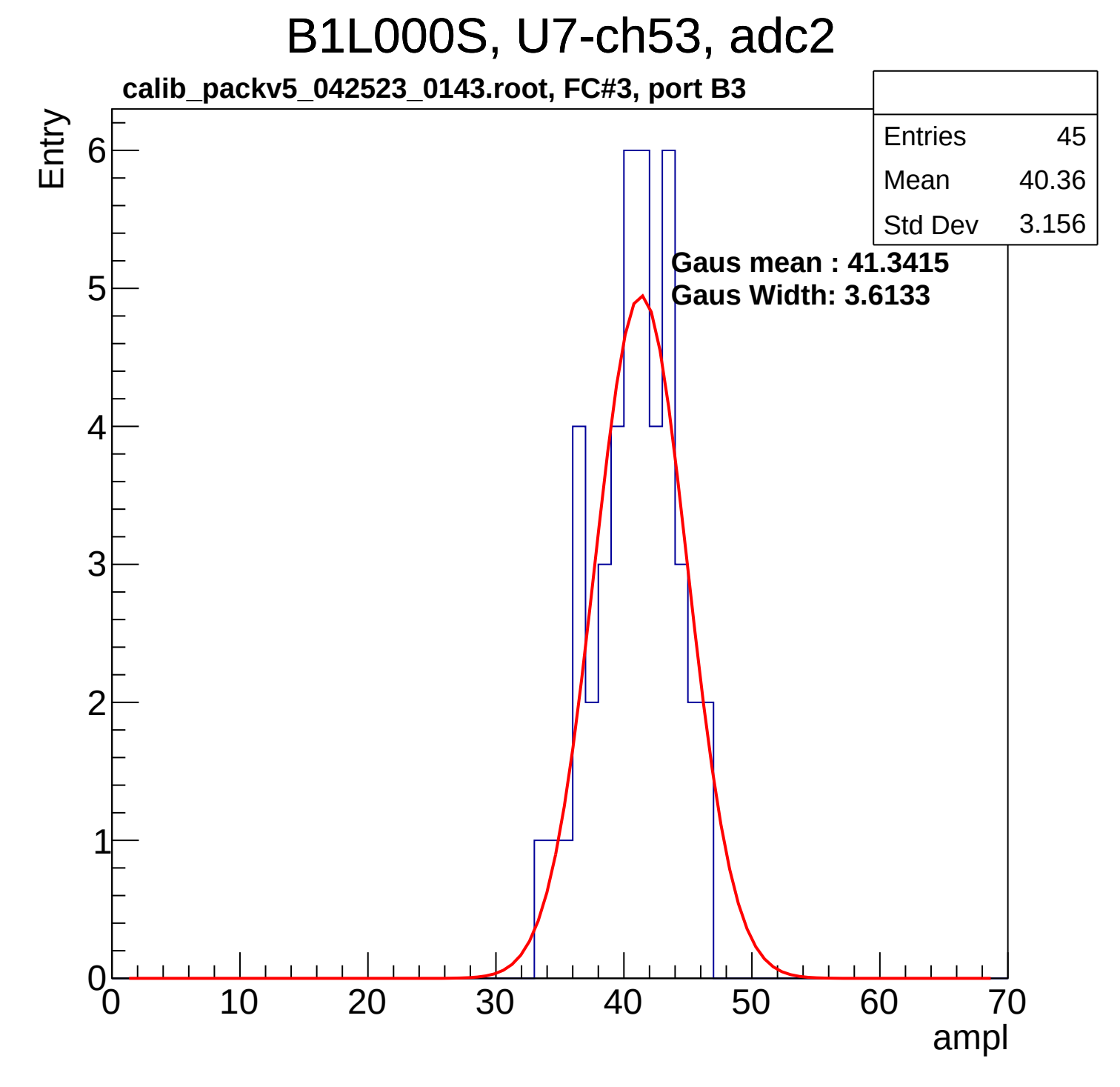
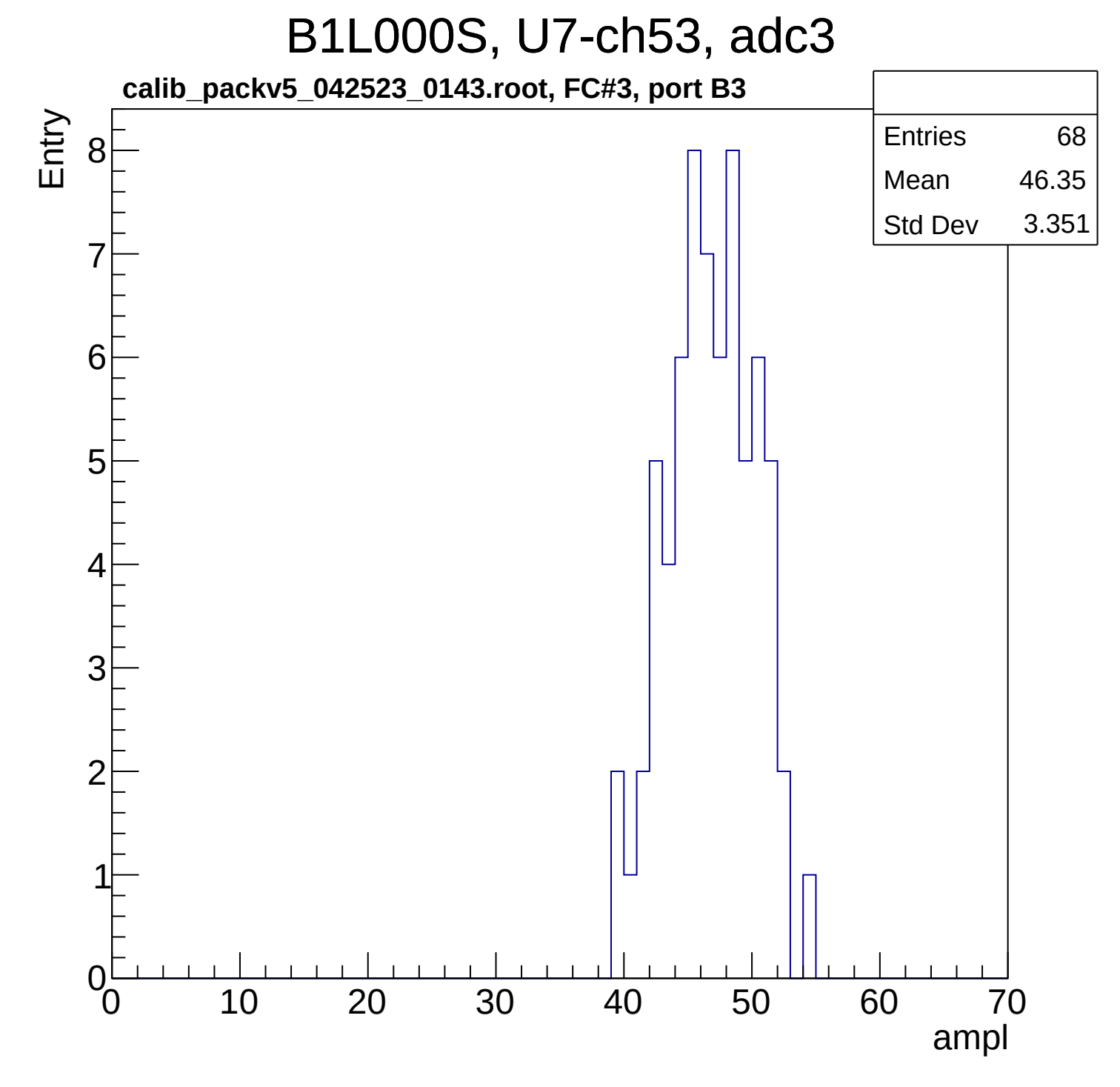
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch53, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 40.36
Std Dev 3.156
Gaus mean : 41.3415
Gaus Width: 3.6133
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U7-ch53, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 46.35
Std Dev 3.351
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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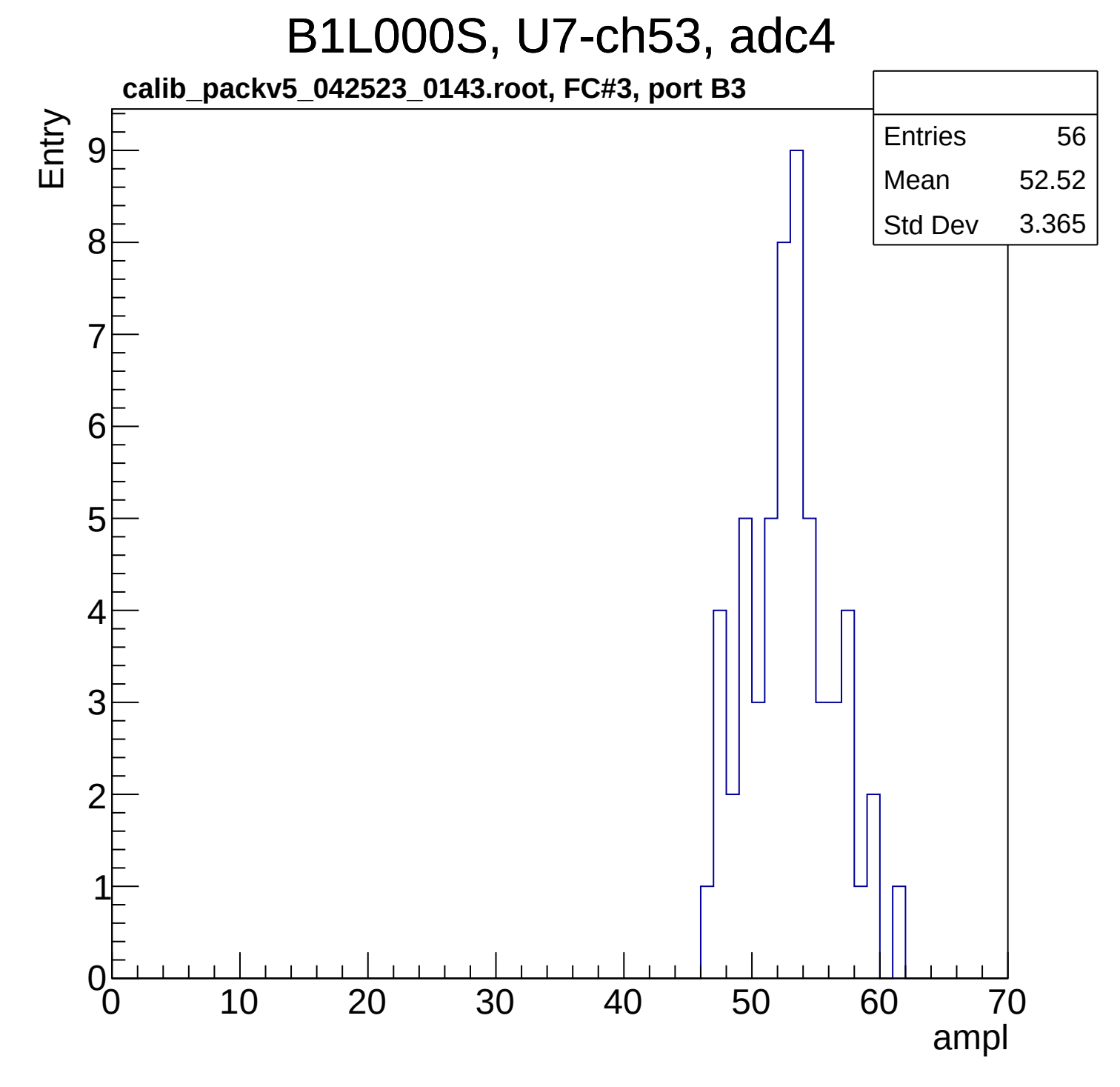
B1L000S, U7-ch53, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 52.52
Std Dev 3.365
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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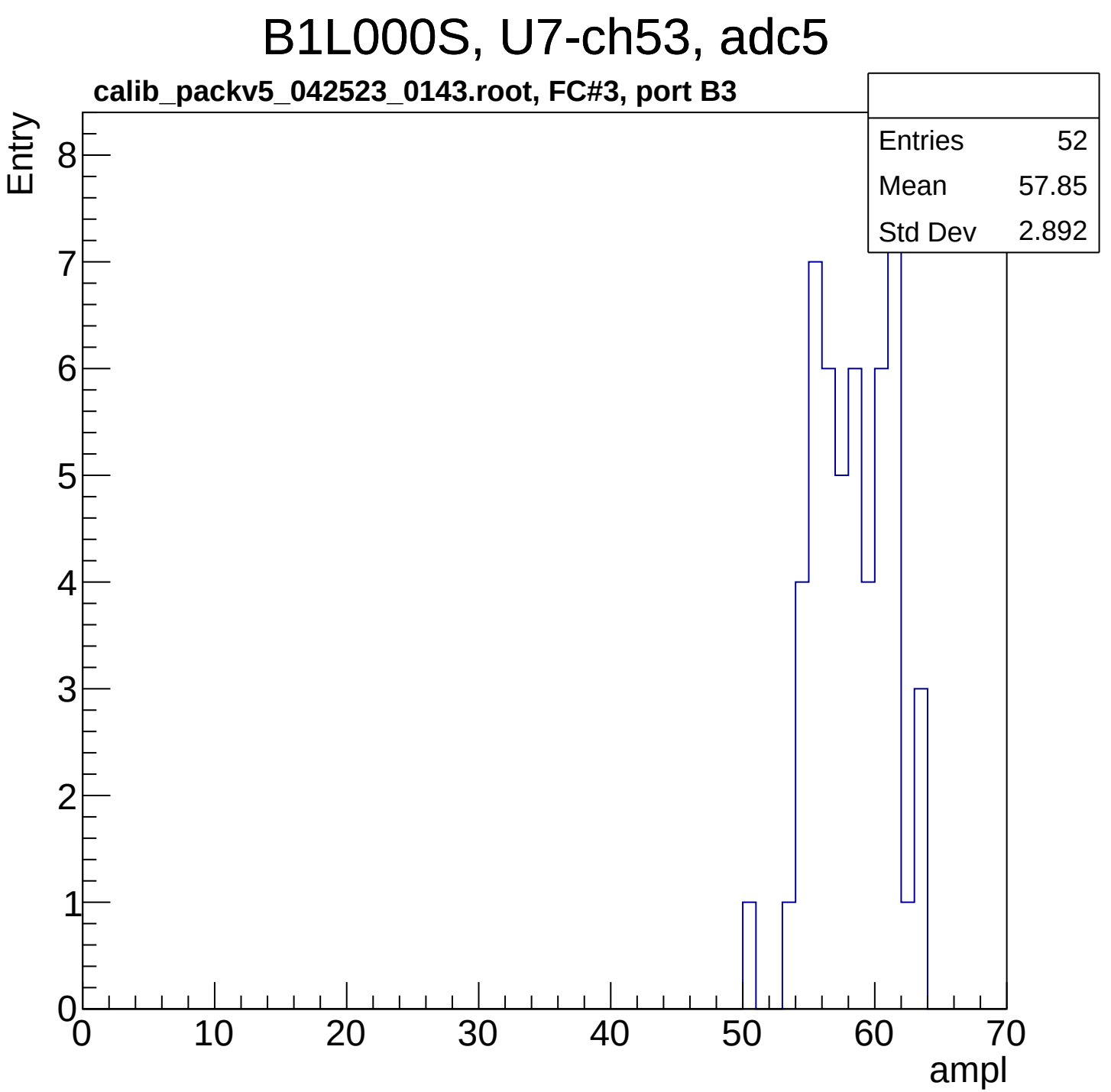
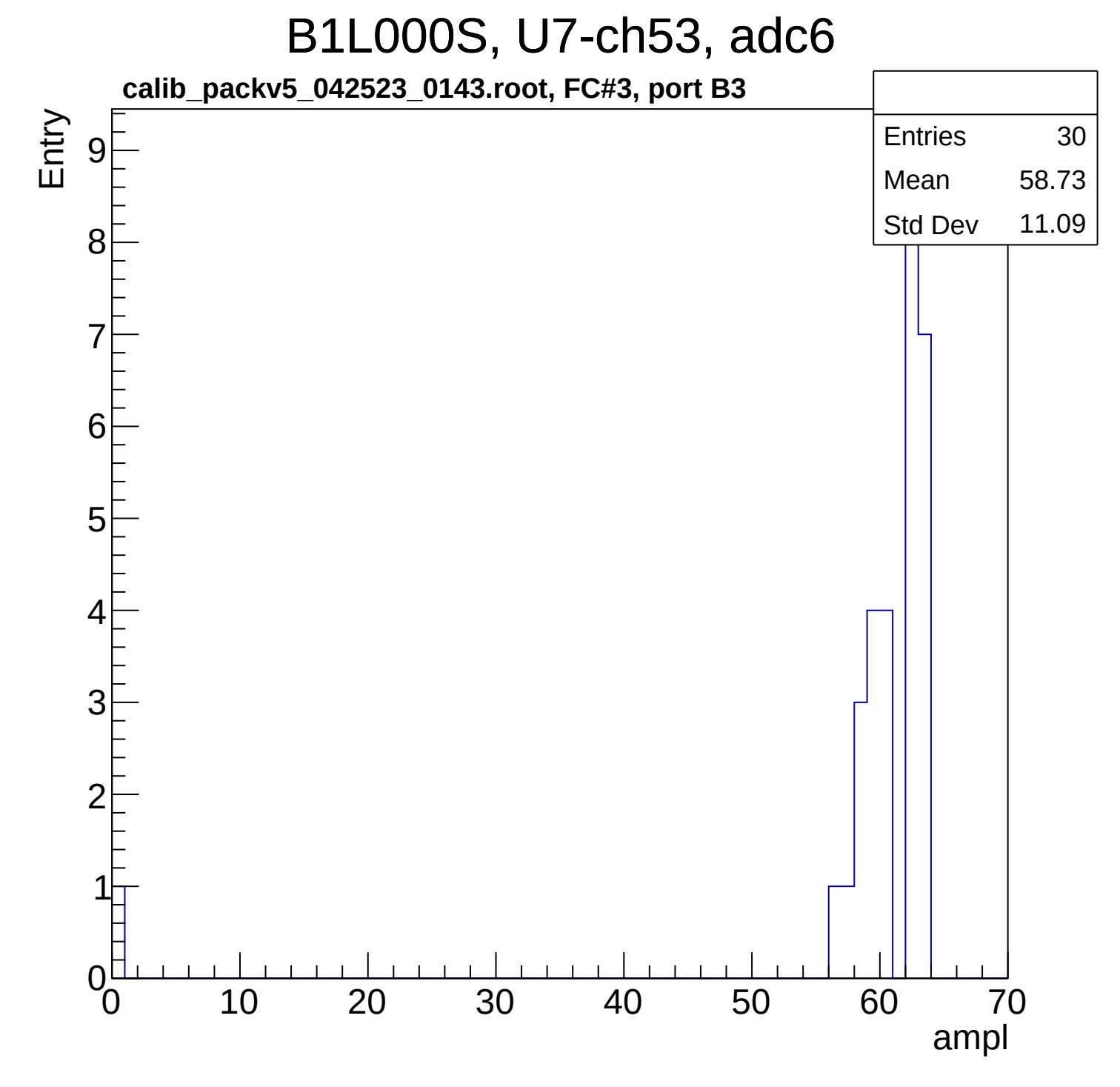

B1L000S, U7-ch53, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 57.85
Std Dev 2.892
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch53, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 58.73
Std Dev 11.09
Entry
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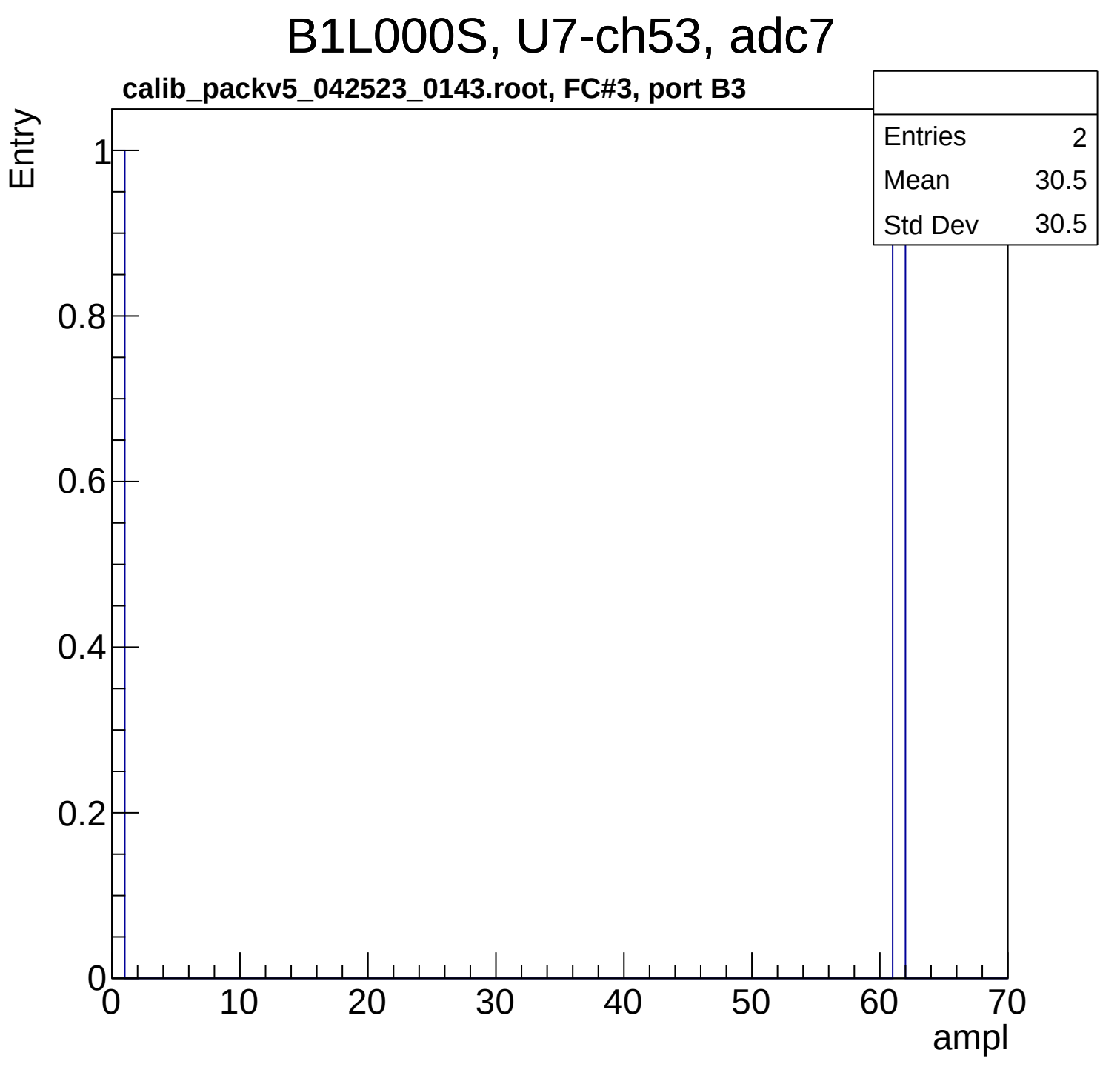
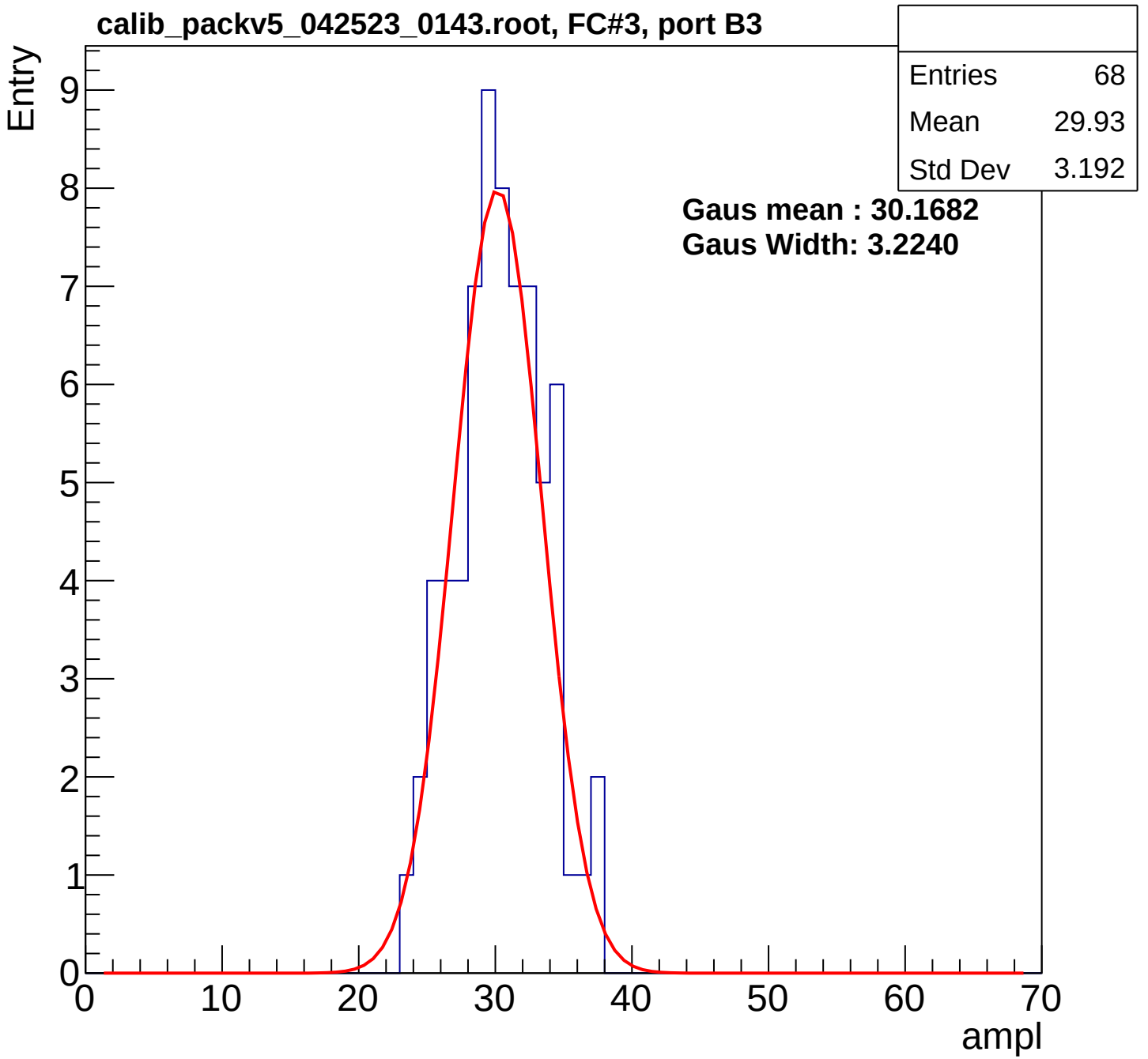
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch53, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch54, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 29.93
Std Dev 3.192
Gaus mean : 30.1682
Gaus Width: 3.2240
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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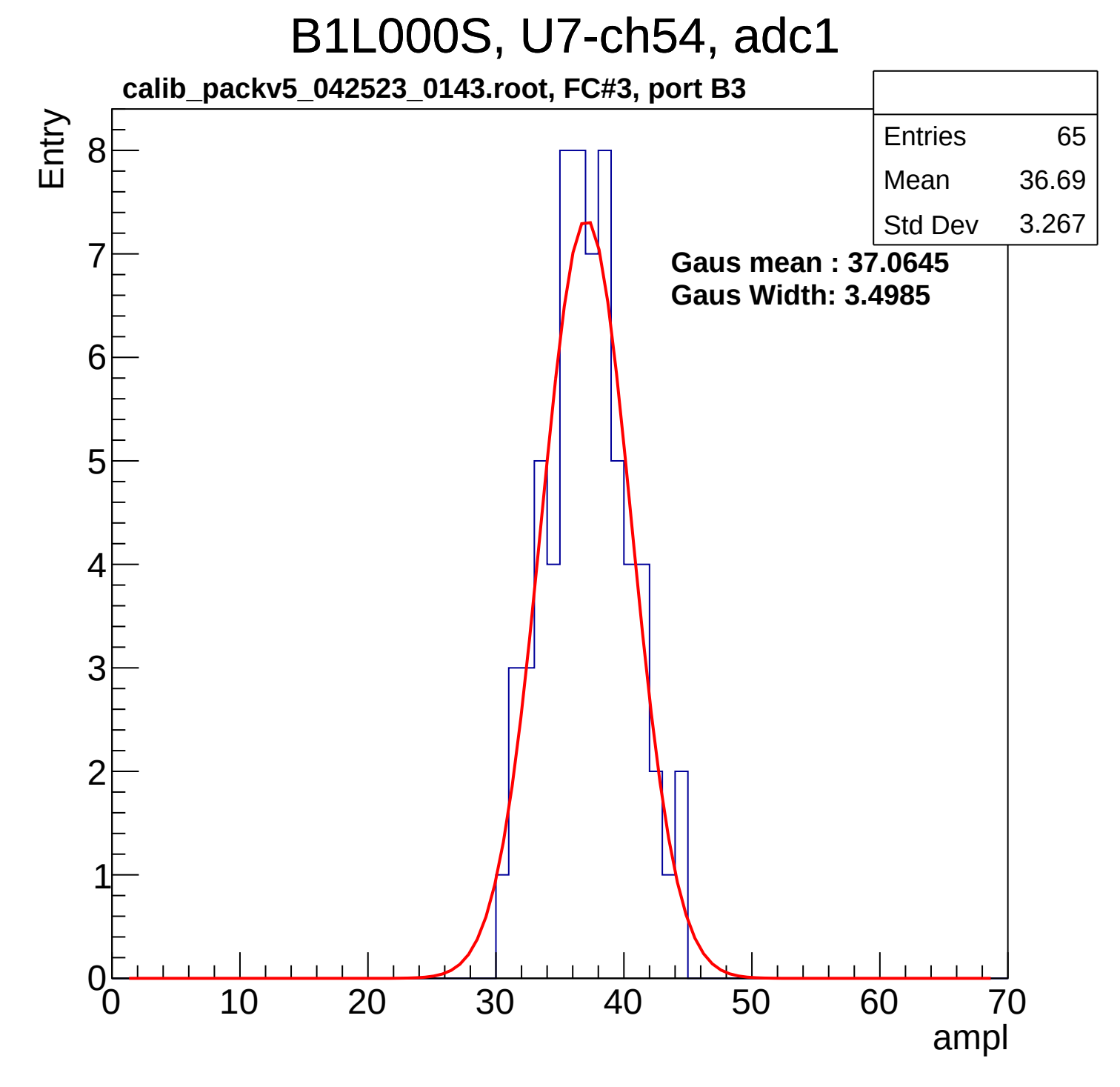
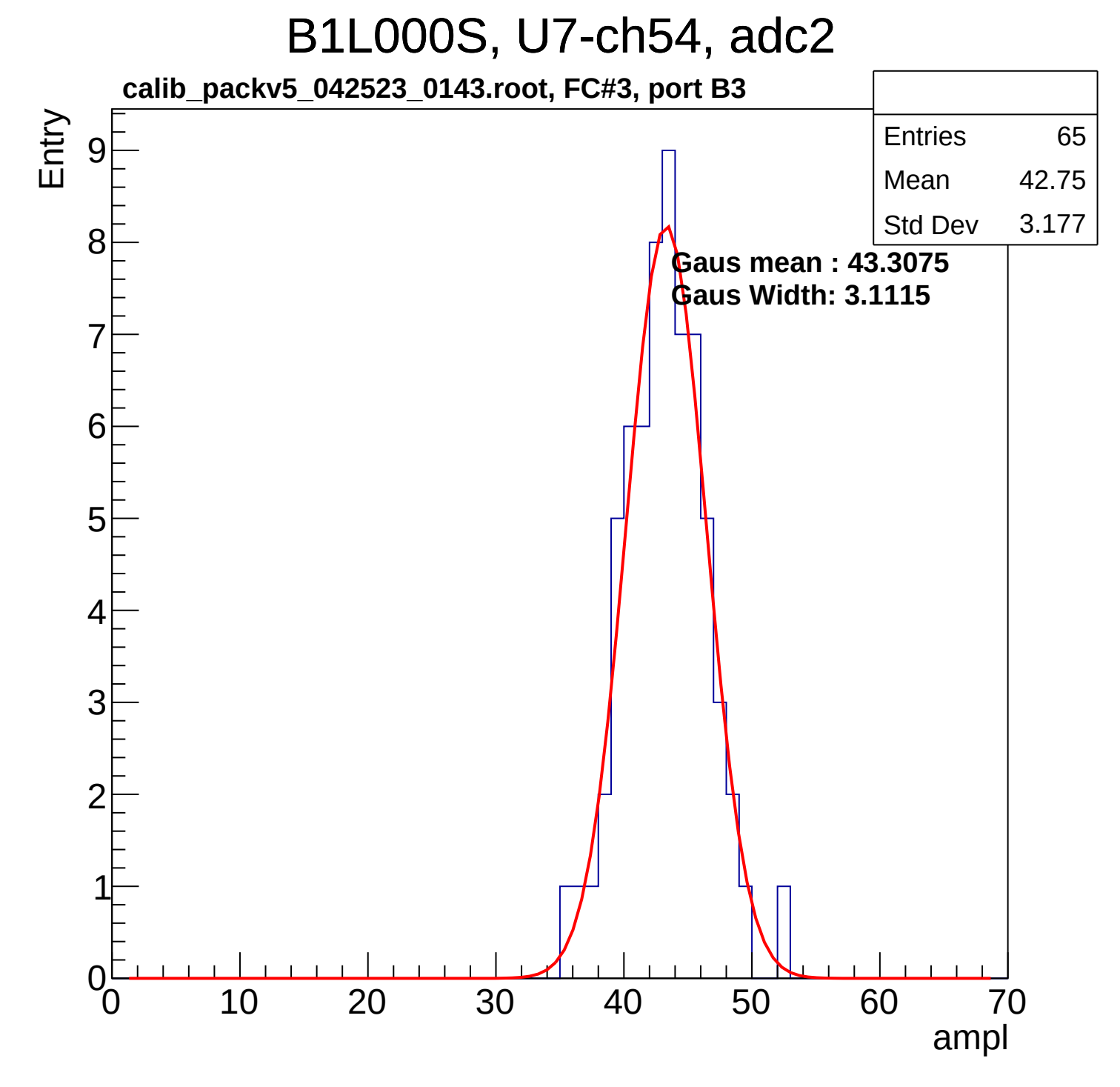

B1L000S, U7-ch54, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 36.69
Std Dev 3.267
Gaus mean : 37.0645
Gaus Width: 3.4985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch54, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 42.75
Std Dev 3.177
Gaus mean : 43.3075
Gaus Width: 3.1115
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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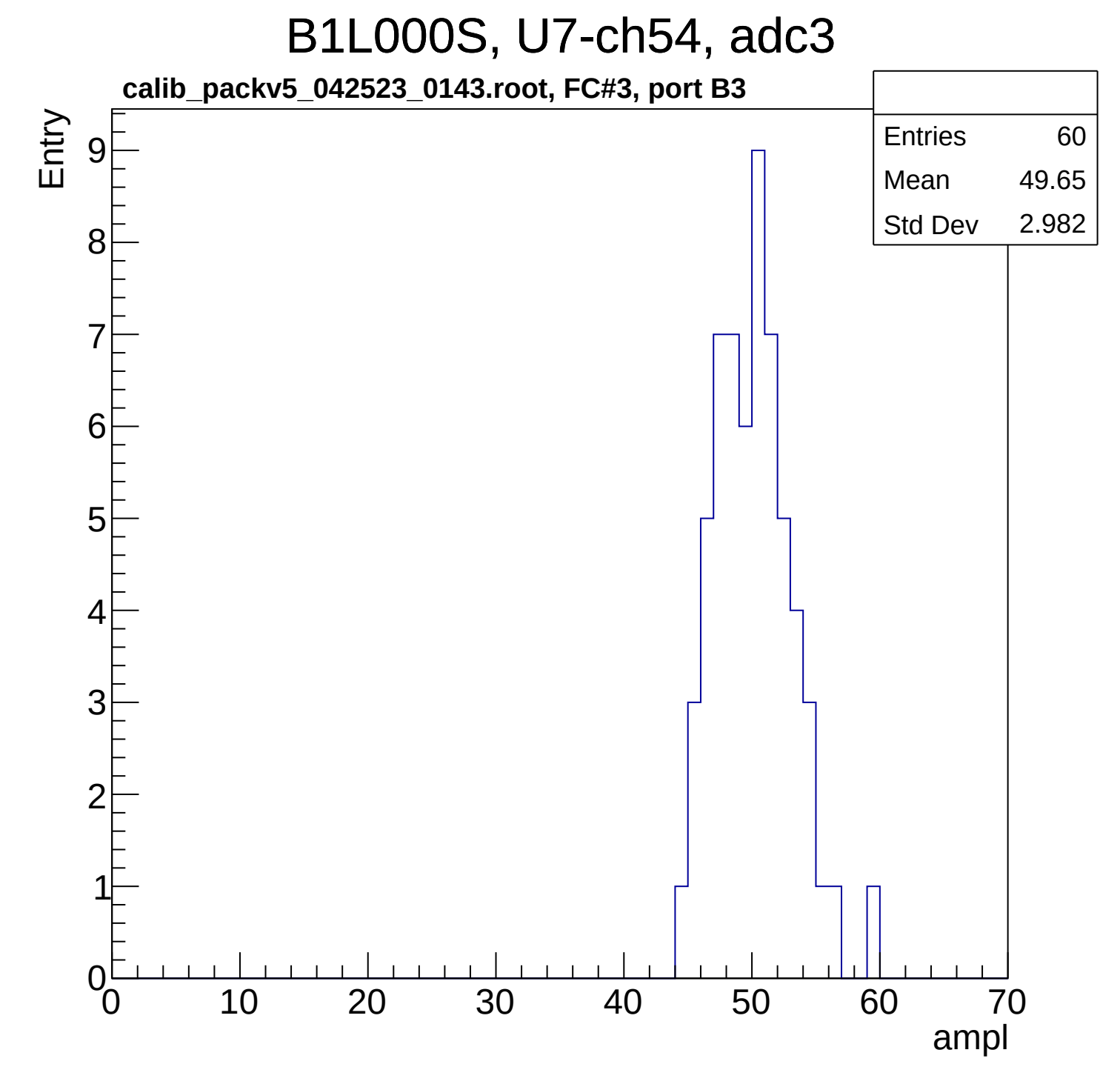
B1L000S, U7-ch54, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 49.65
Std Dev 2.982
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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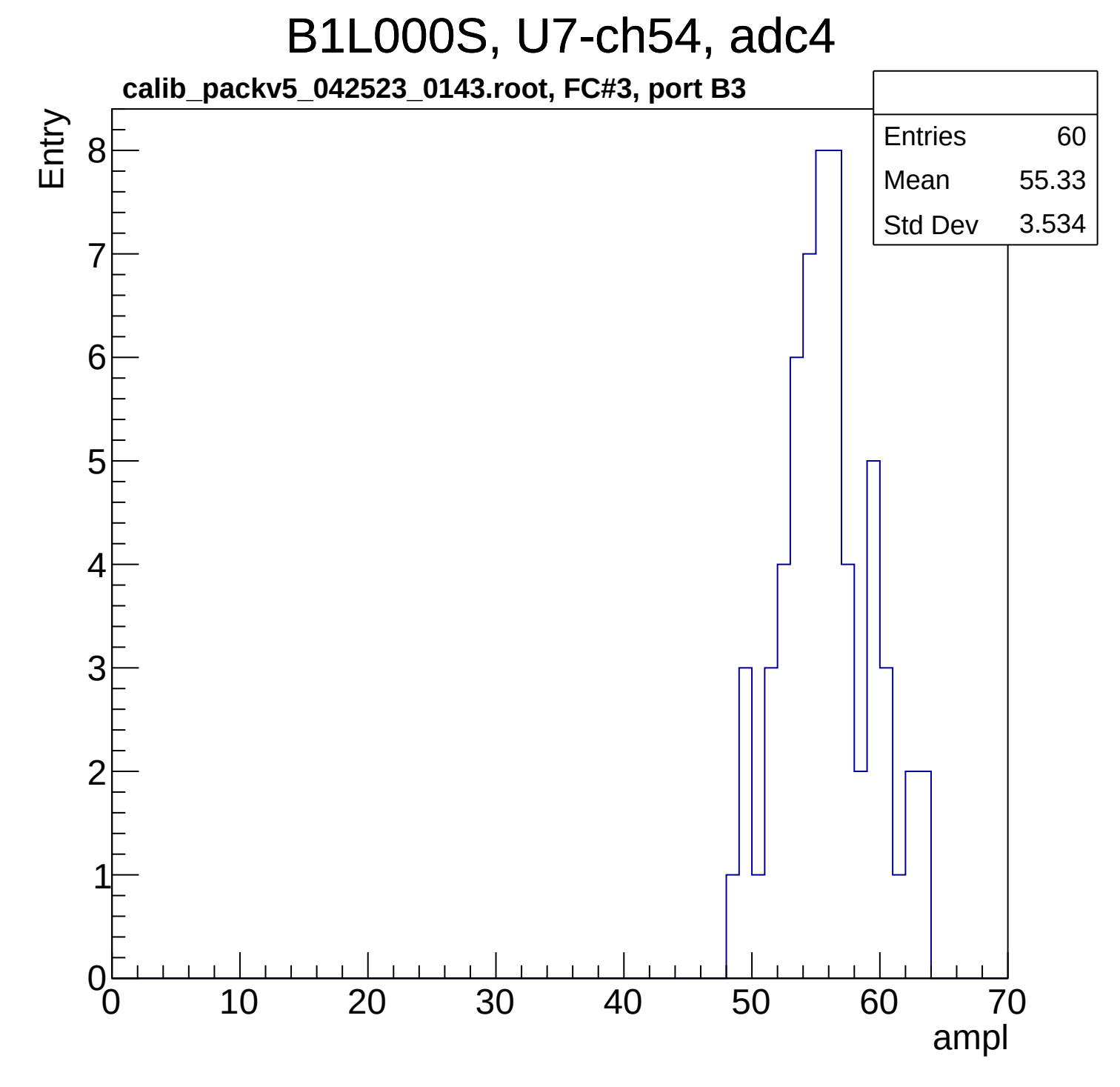
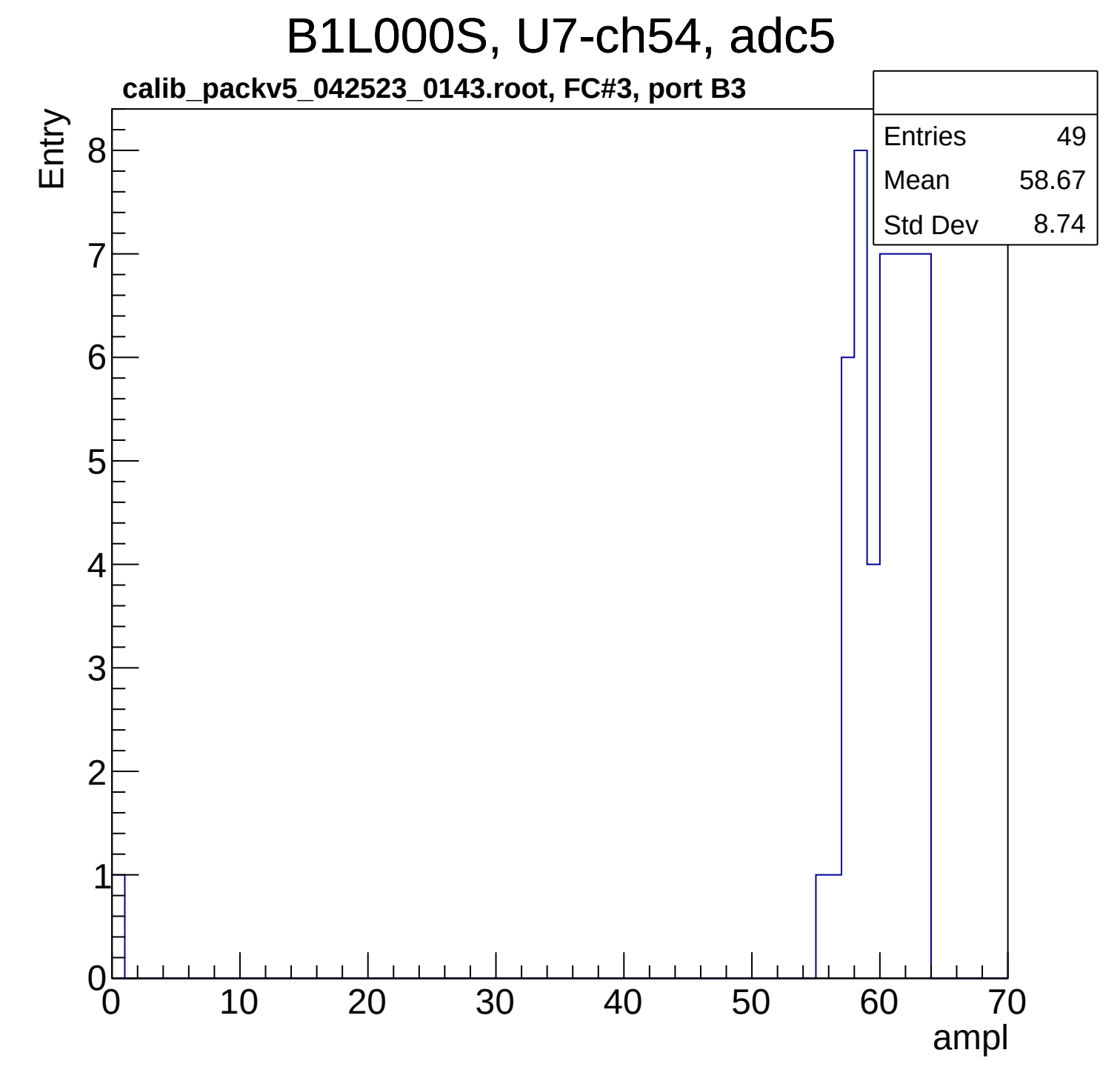

B1L000S, U7-ch54, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 55.33
Std Dev 3.534
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch54, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.67
Std Dev 8.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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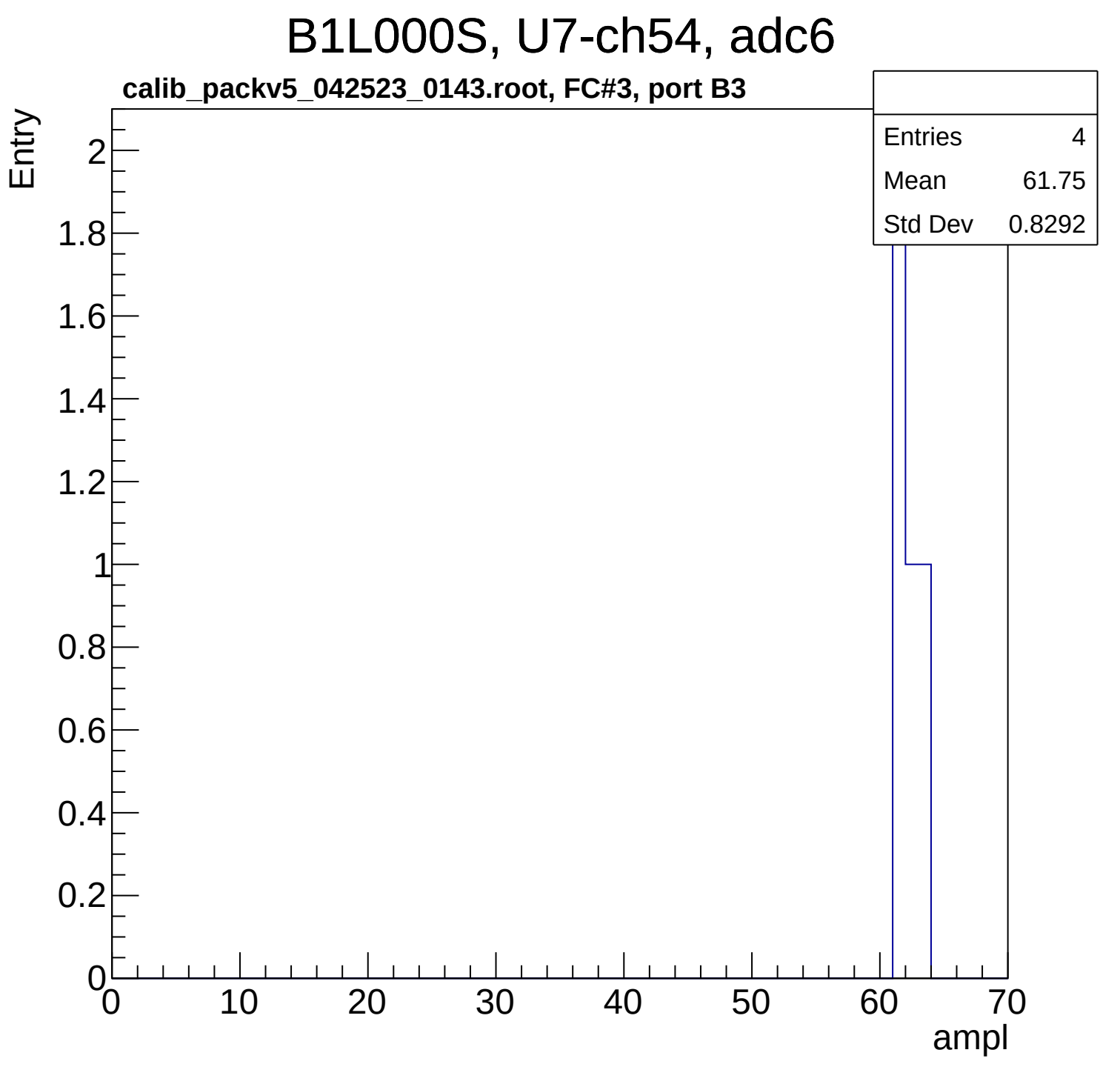
B1L000S, U7-ch54, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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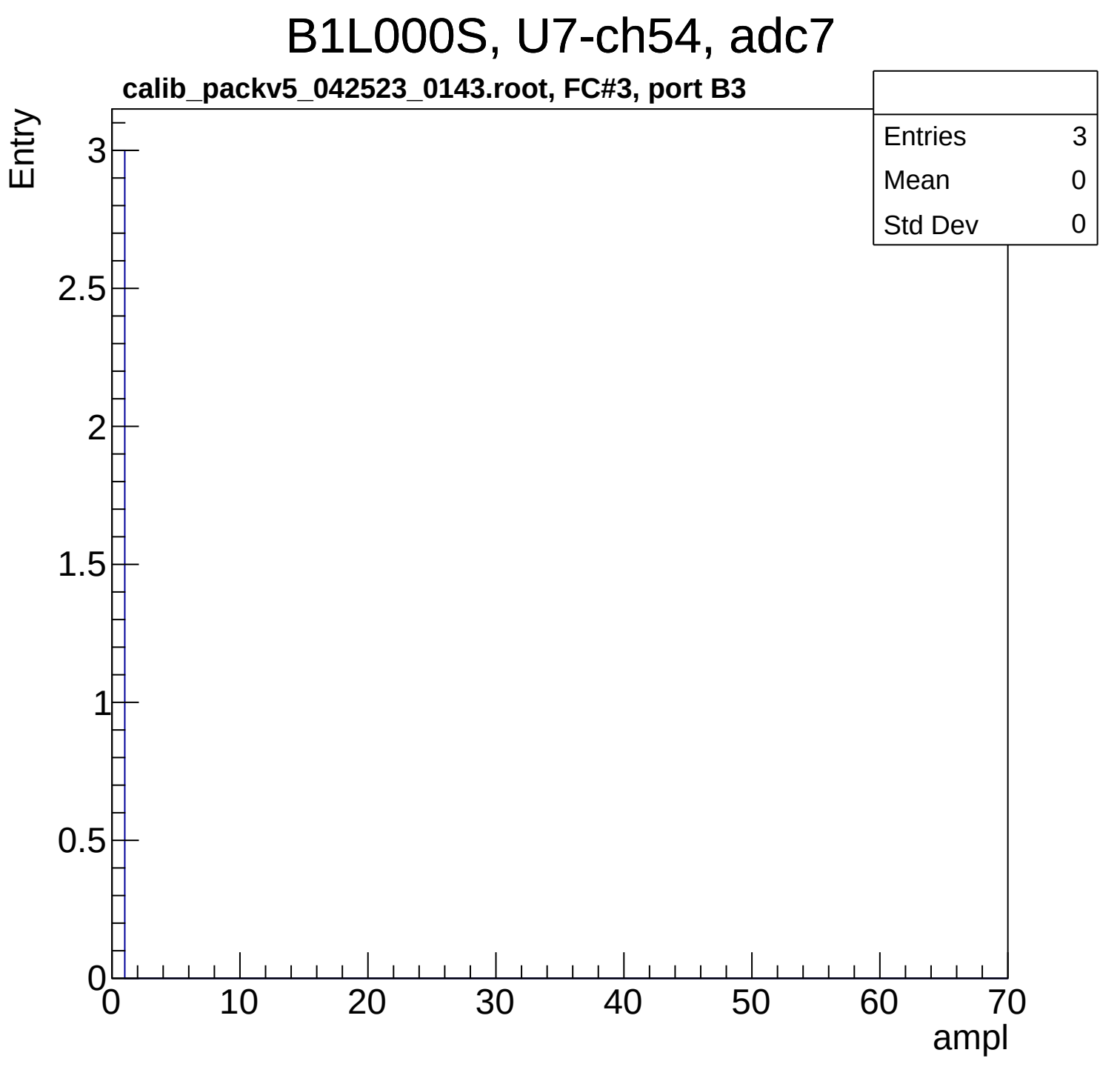
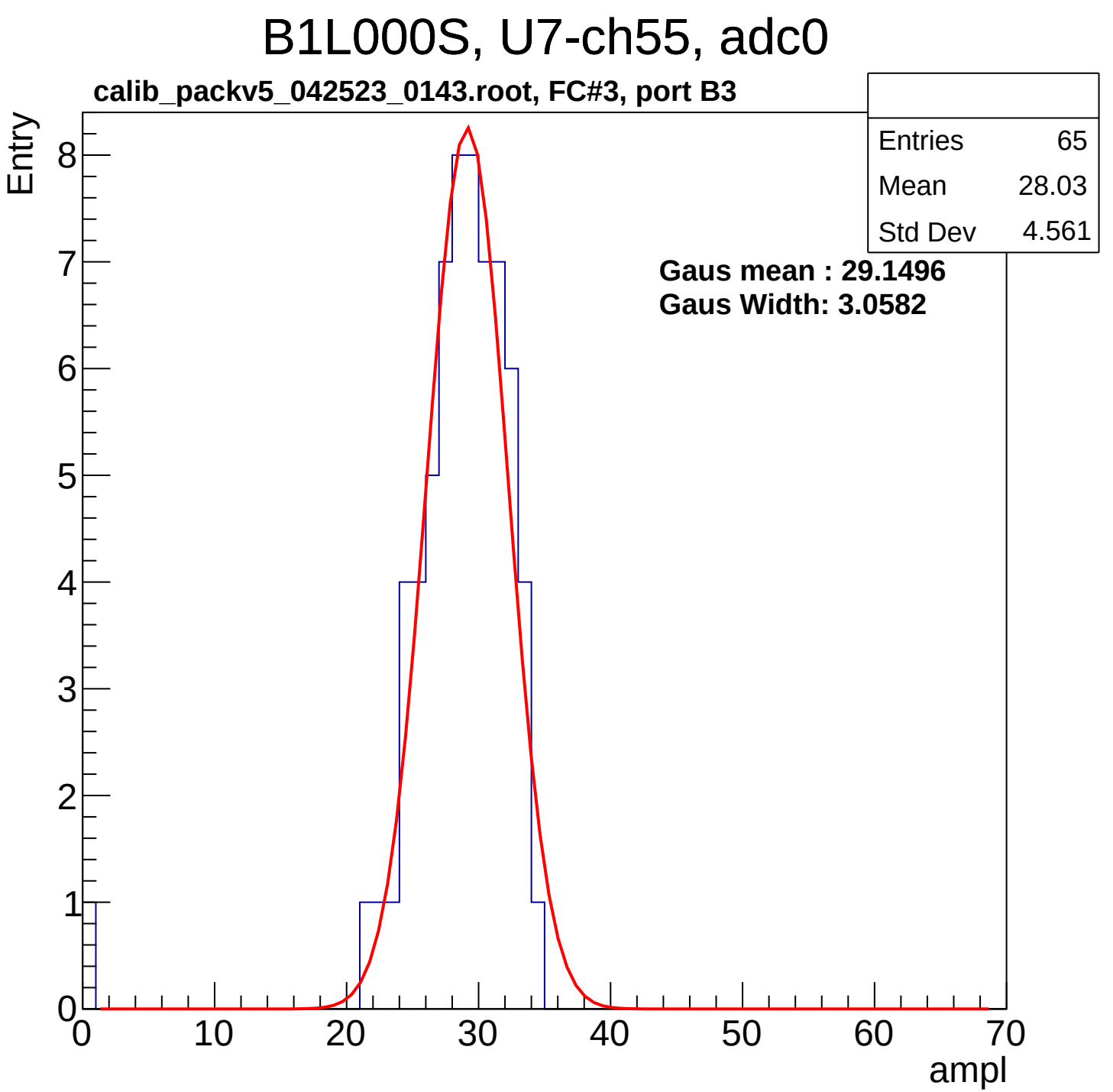
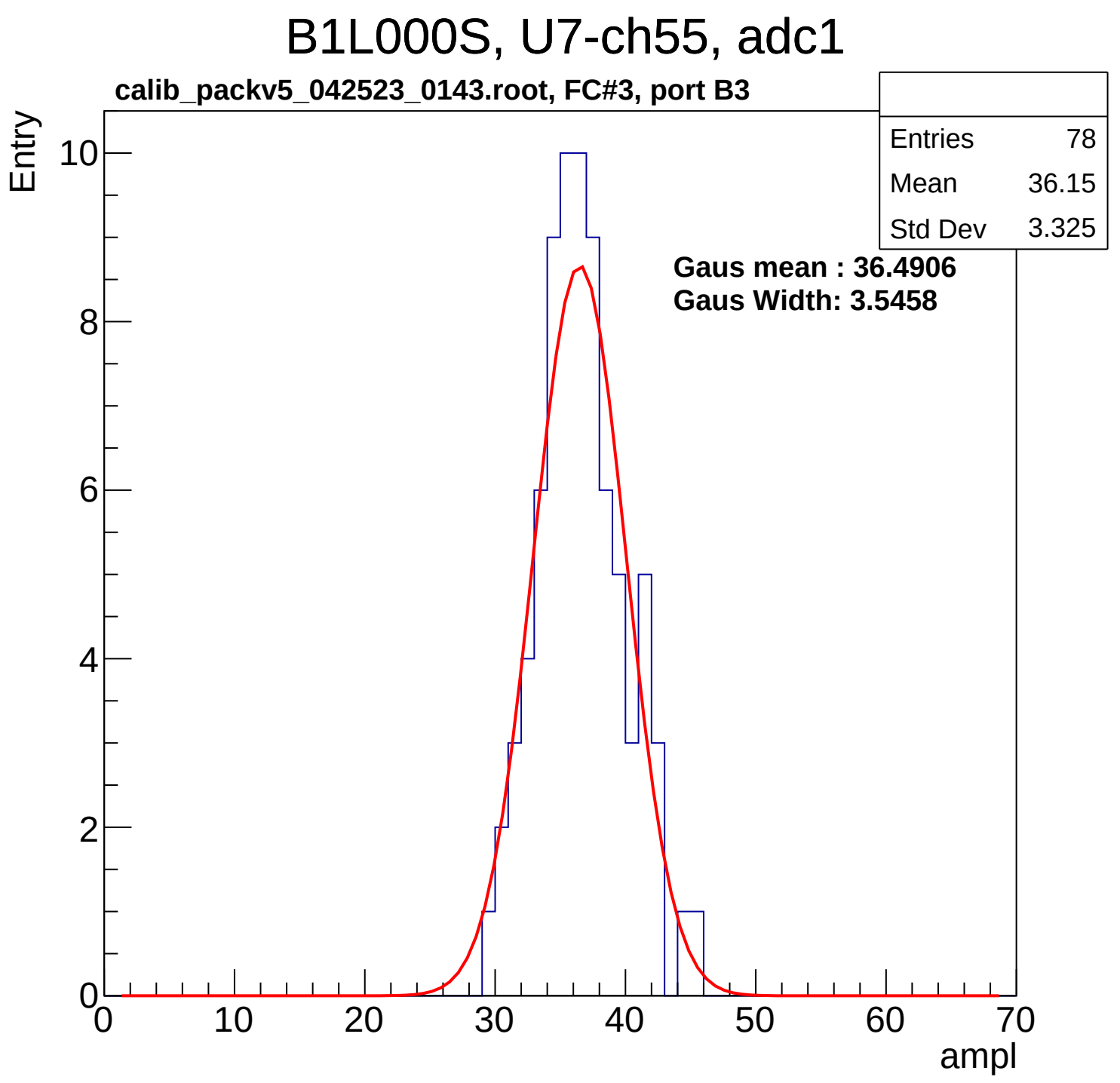
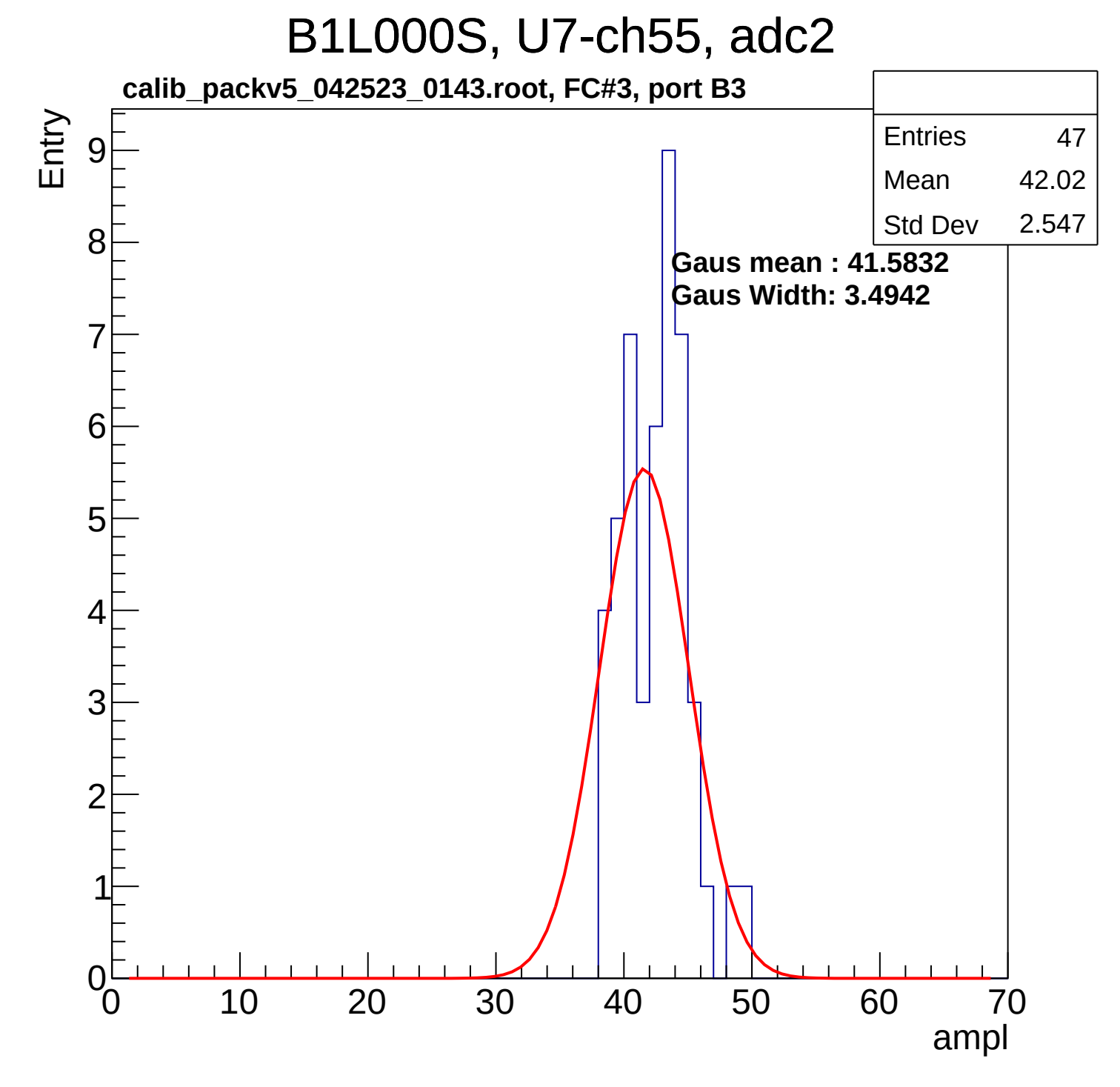
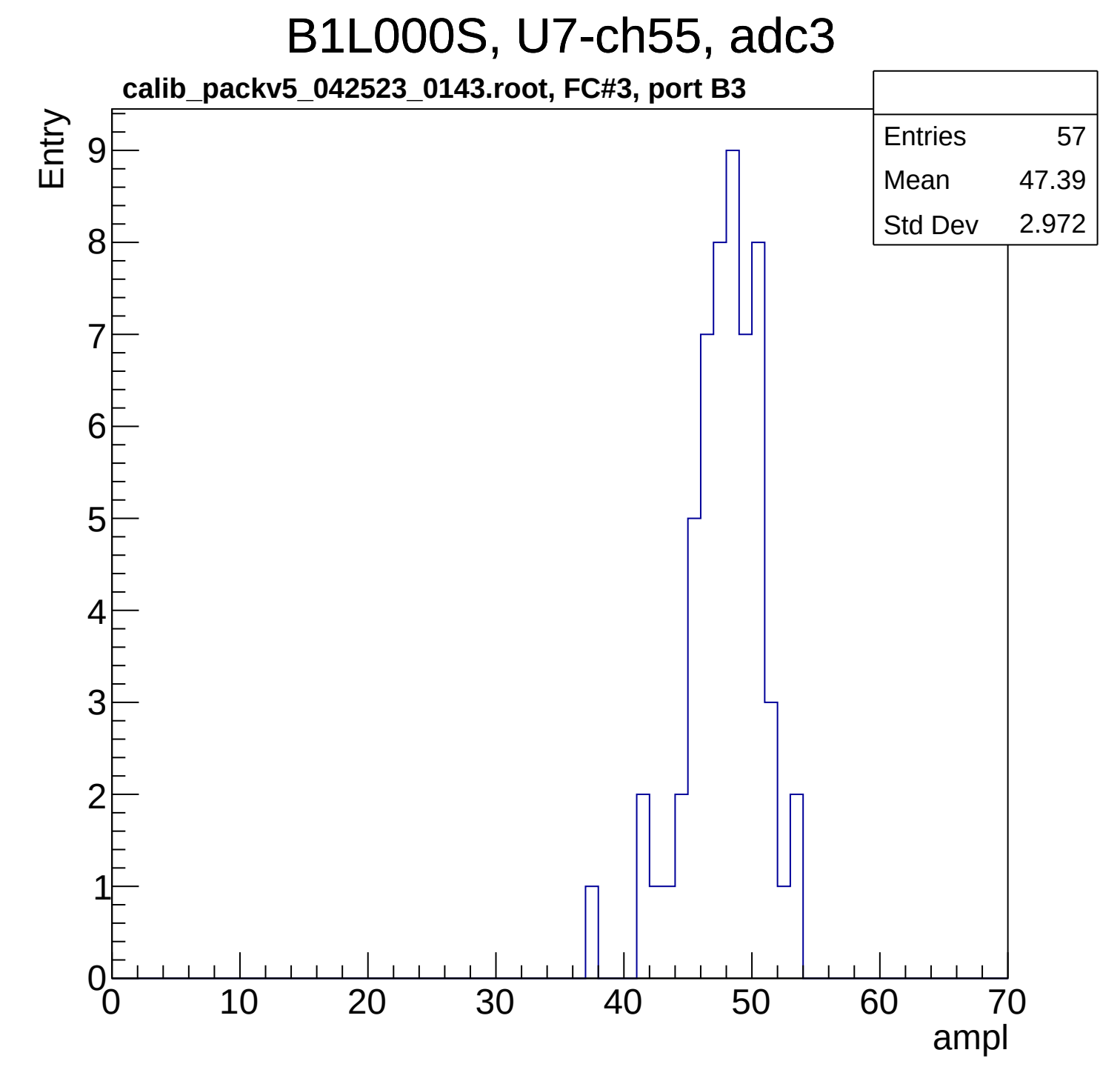
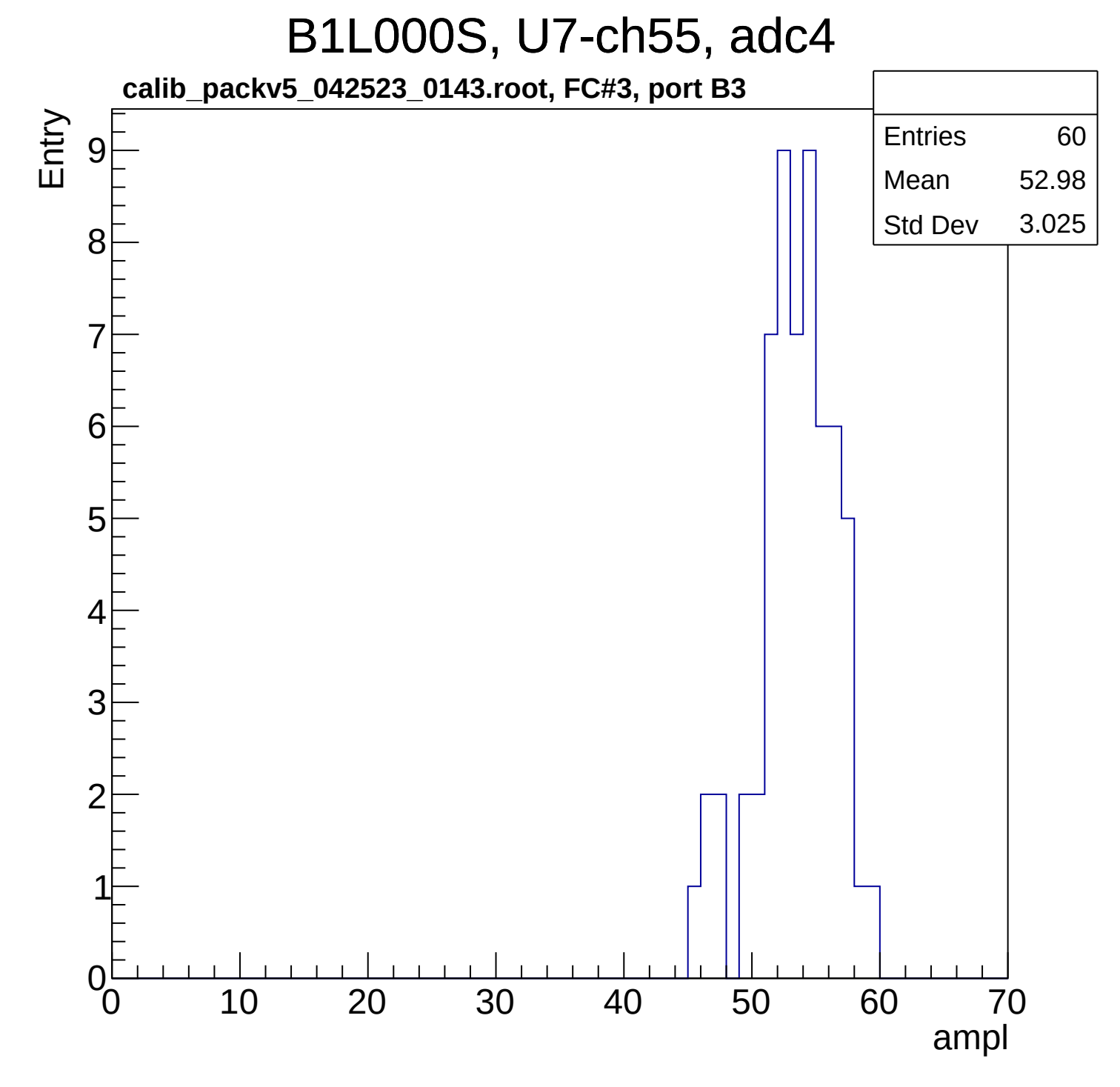

B1L000S, U7-ch54, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

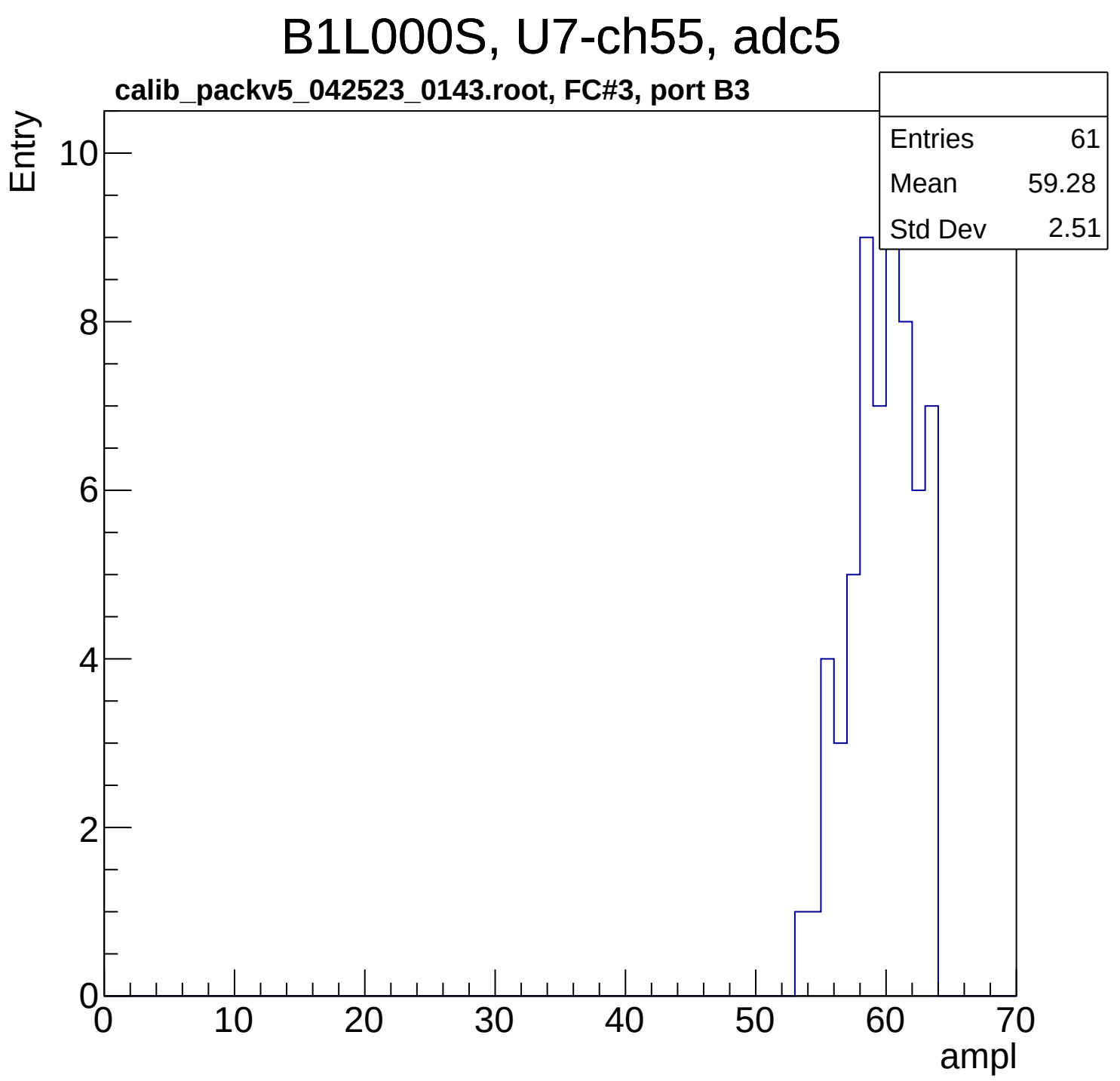

B1L000S, U7-ch55, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.03
Std Dev 4.561
Gaus mean : 29.1496
Gaus Width: 3.0582
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

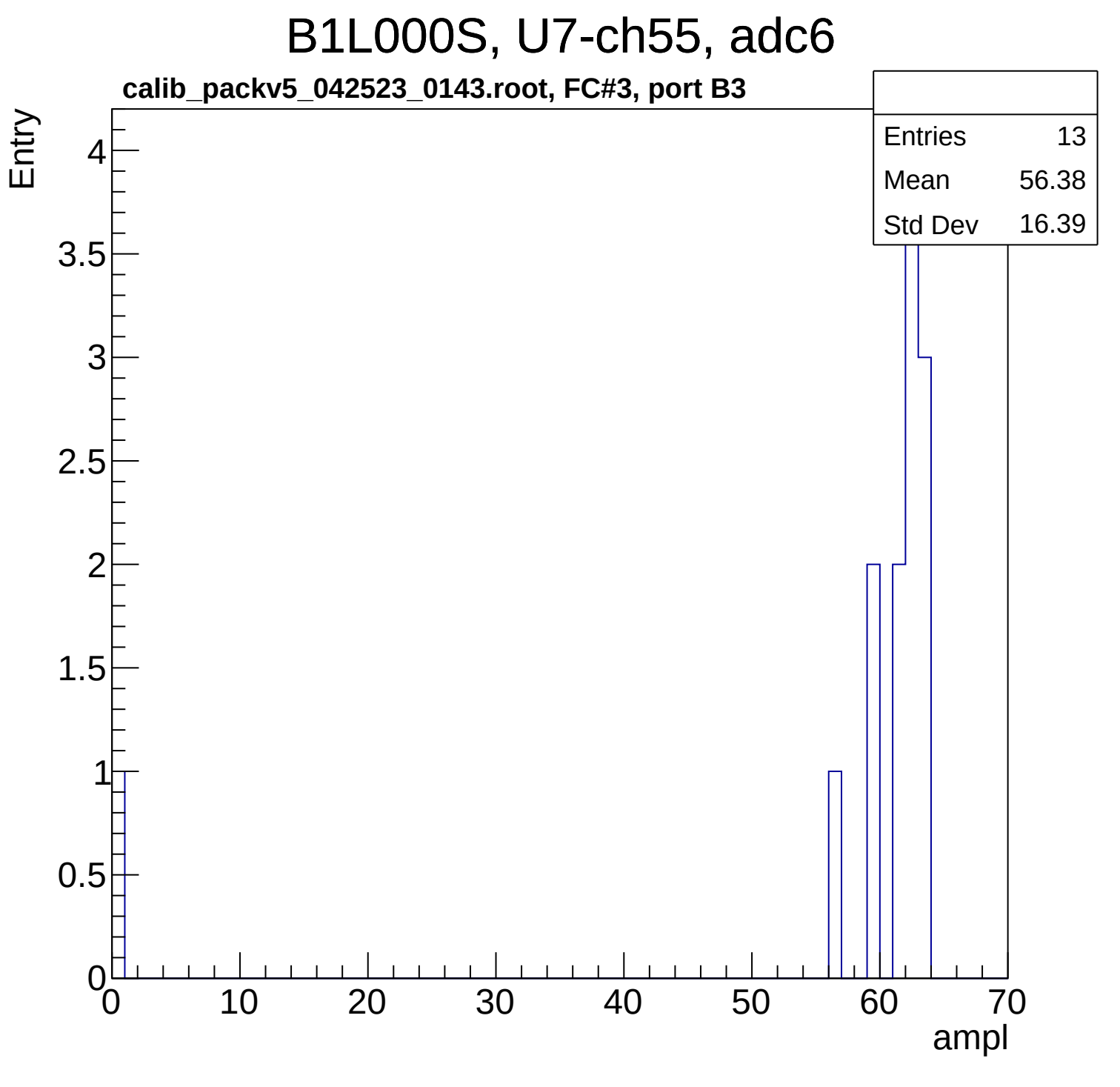

B1L000S, U7-ch55, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 36.15
Std Dev 3.325
Gaus mean : 36.4906
Gaus Width: 3.5458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

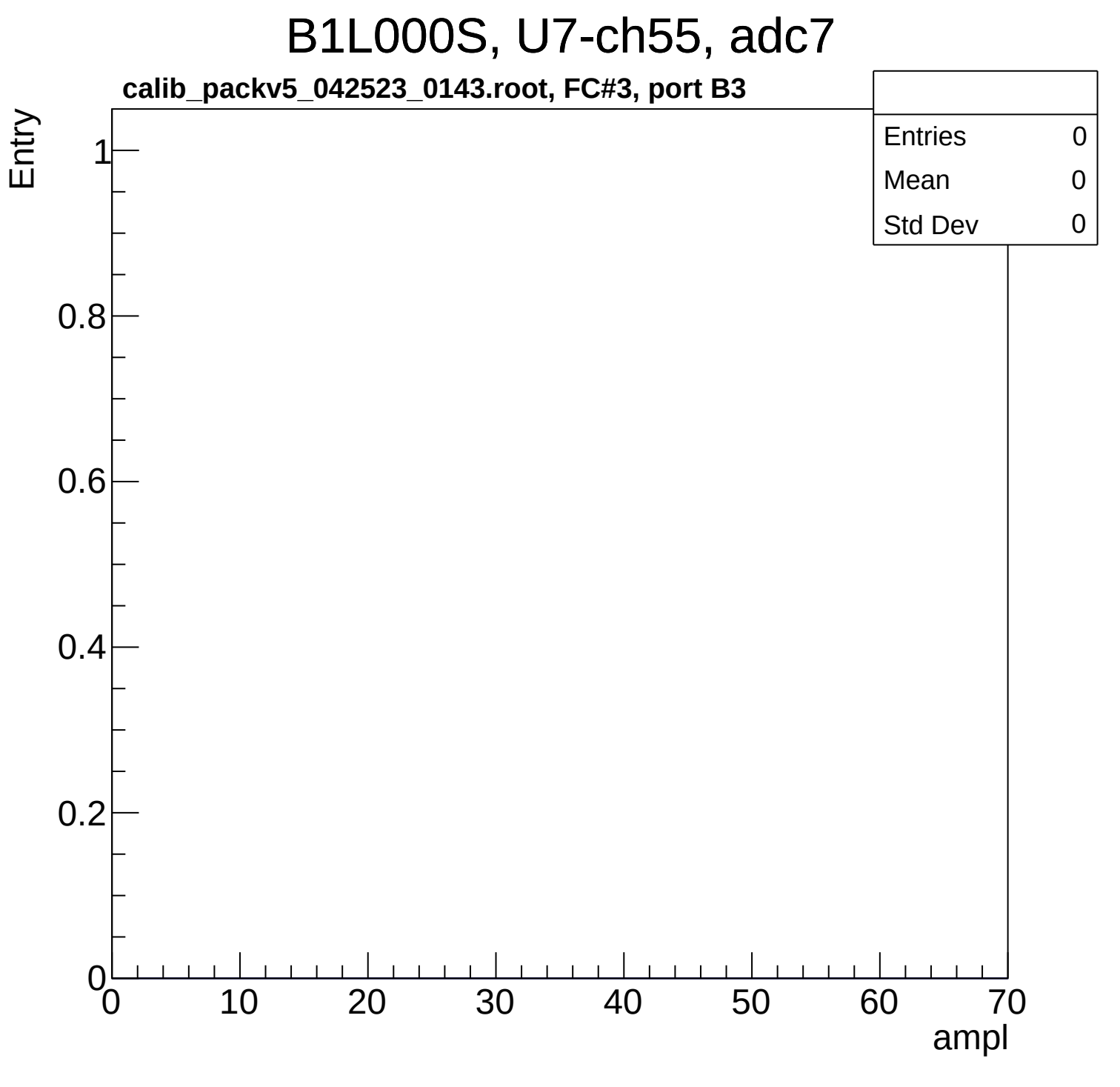

B1L000S, U7-ch55, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 42.02
Std Dev 2.547
Gaus mean : 41.5832
Gaus Width: 3.4942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

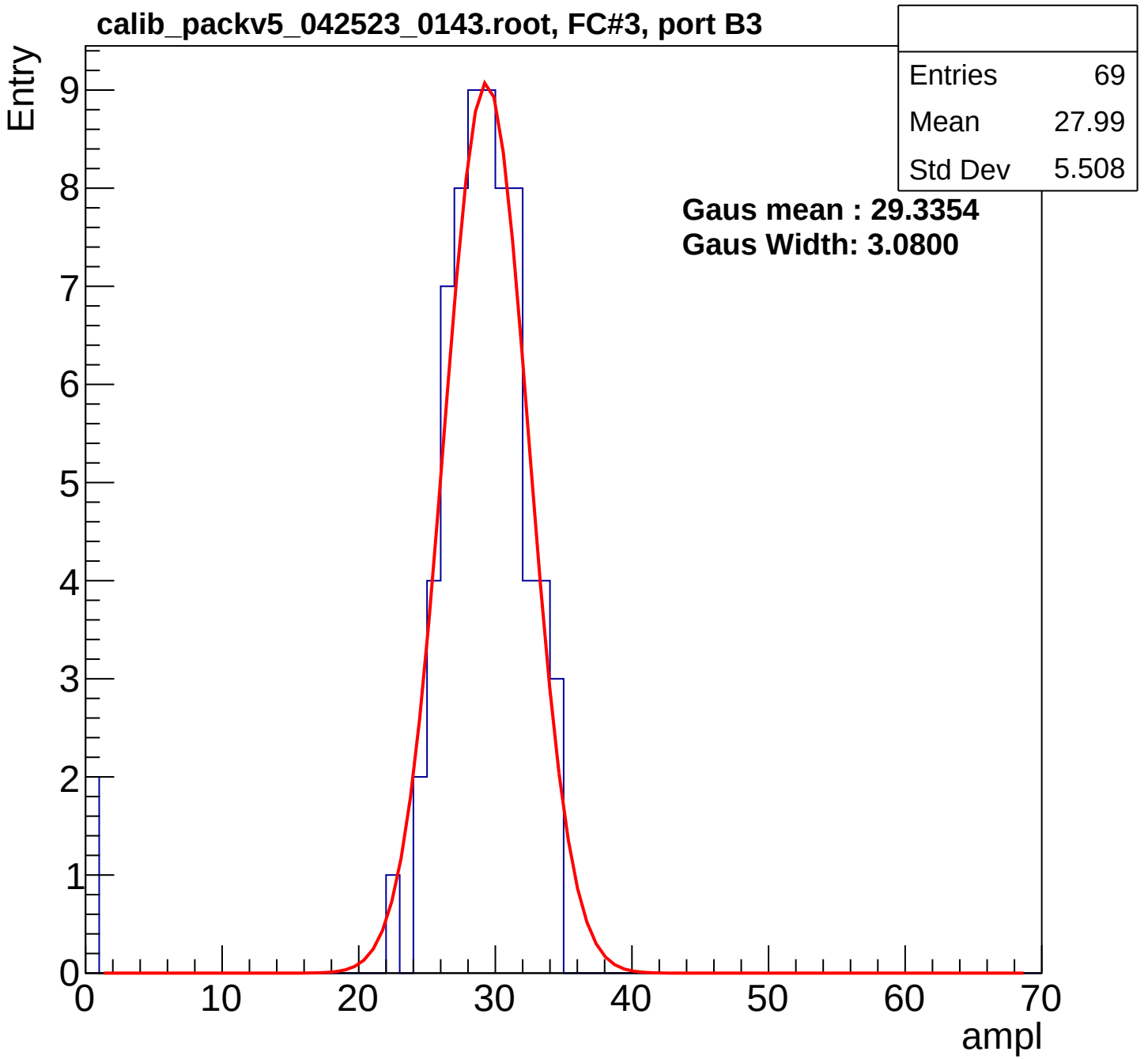

B1L000S, U7-ch55, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.39
Std Dev 2.972
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

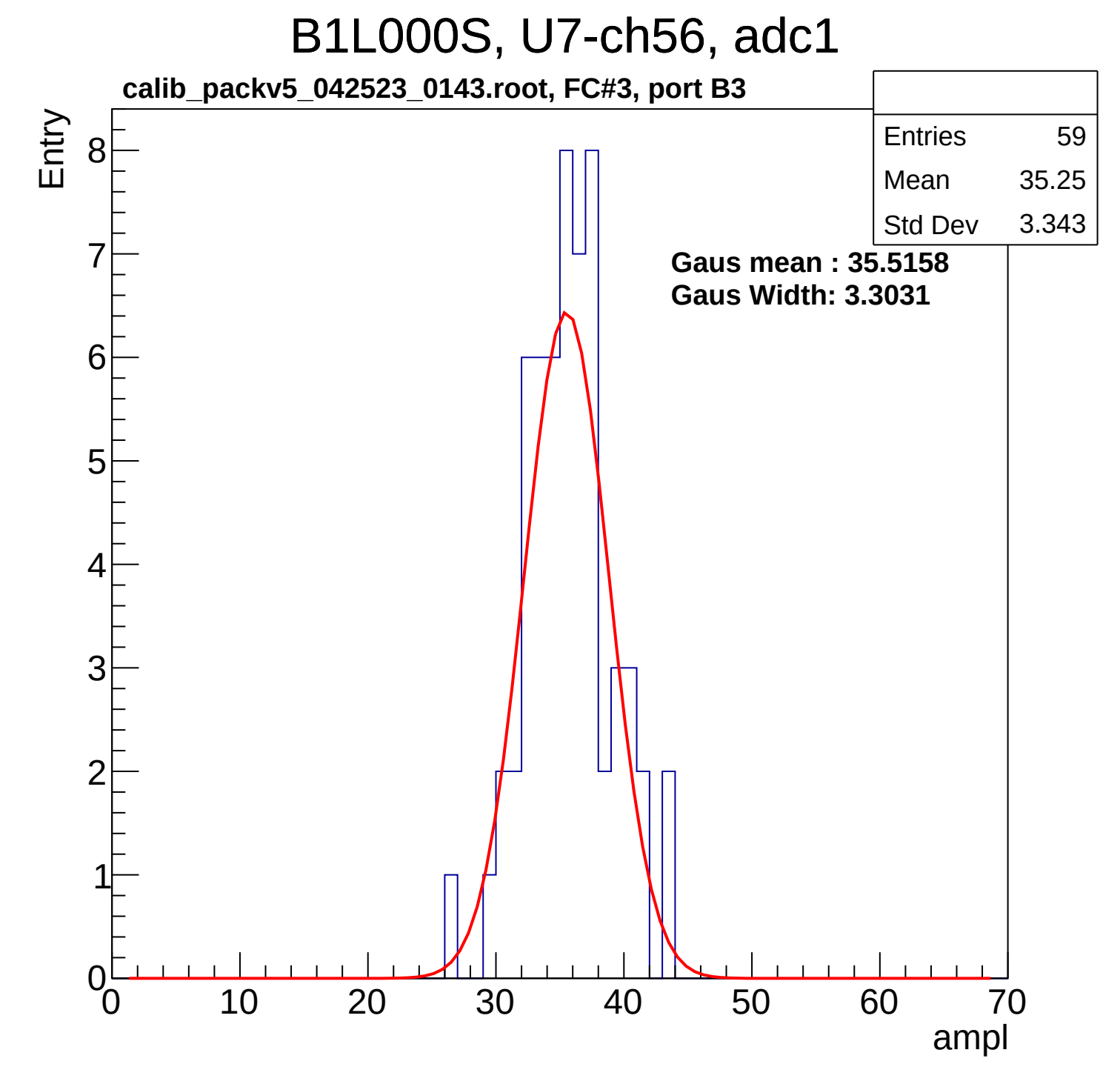

B1L000S, U7-ch55, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 52.98
Std Dev 3.025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

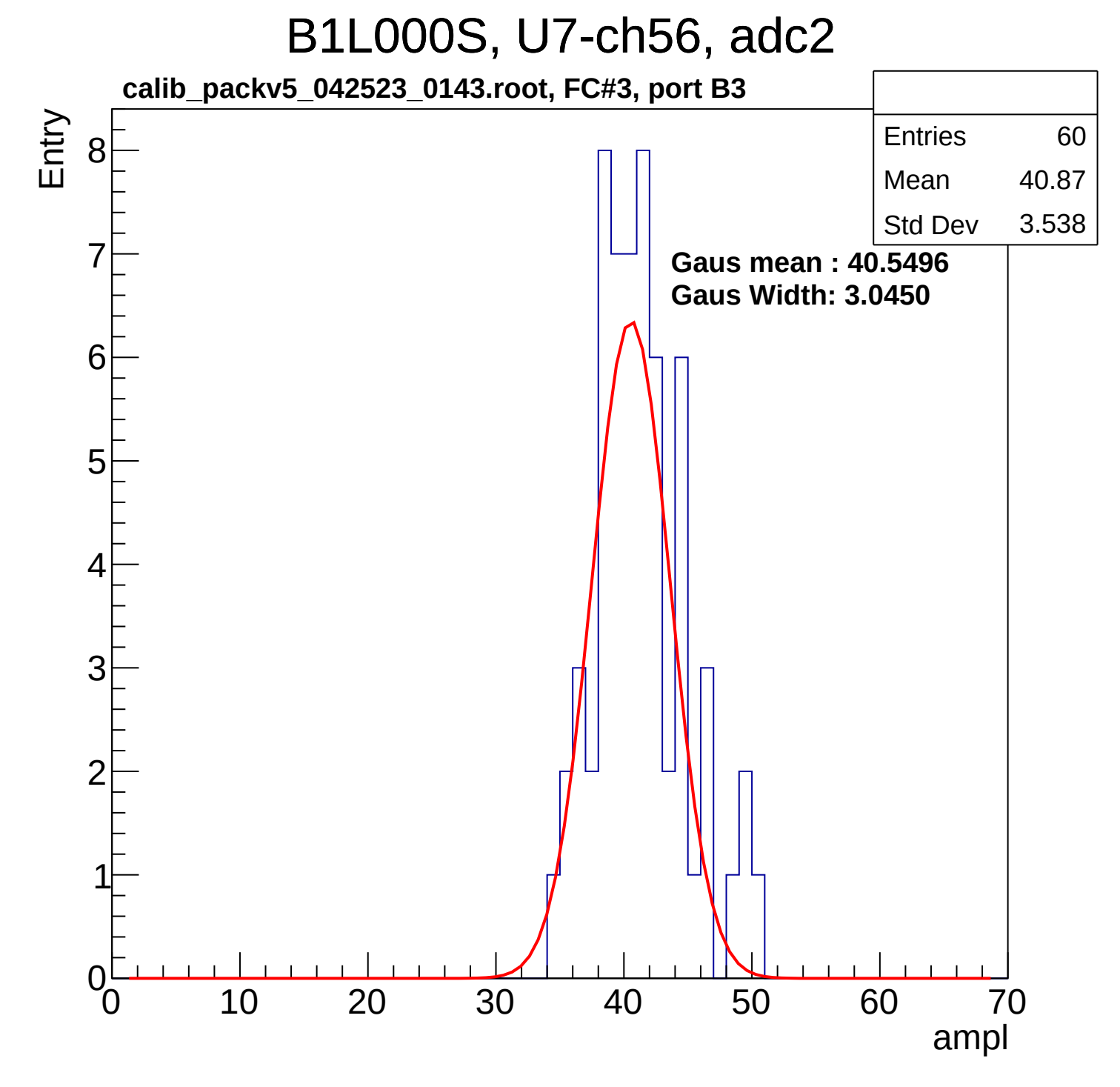

B1L000S, U7-ch55, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 59.28
Std Dev 2.51
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

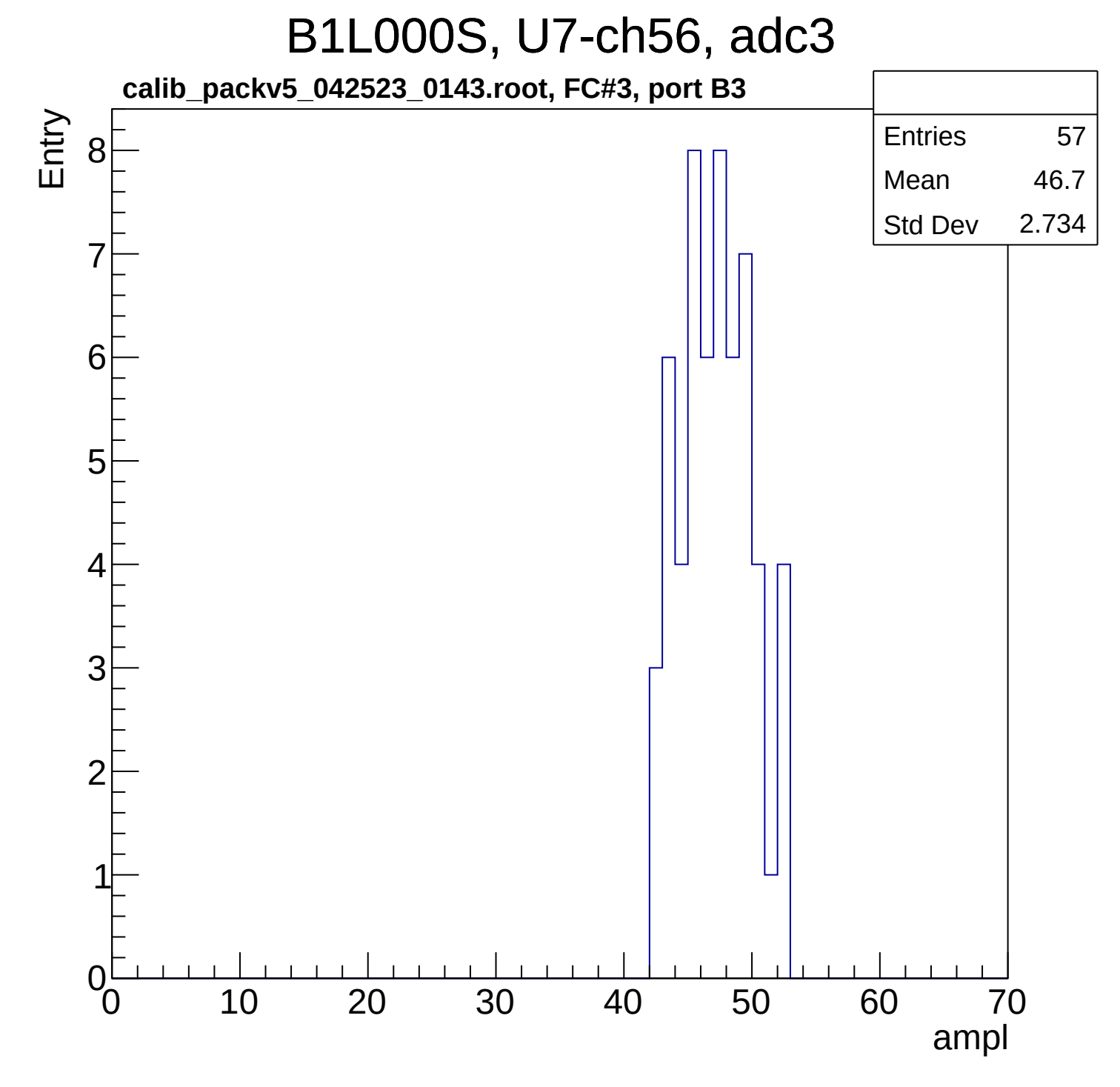

B1L000S, U7-ch55, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.38
Std Dev 16.39
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch55, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch56, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 27.99
Std Dev 5.508
Gaus mean : 29.3354
Gaus Width: 3.0800
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch56, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.25
Std Dev 3.343
Gaus mean : 35.5158
Gaus Width: 3.3031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch56, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 40.87
Std Dev 3.538
Gaus mean : 40.5496
Gaus Width: 3.0450
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch56, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 46.7
Std Dev 2.734
Entry
0
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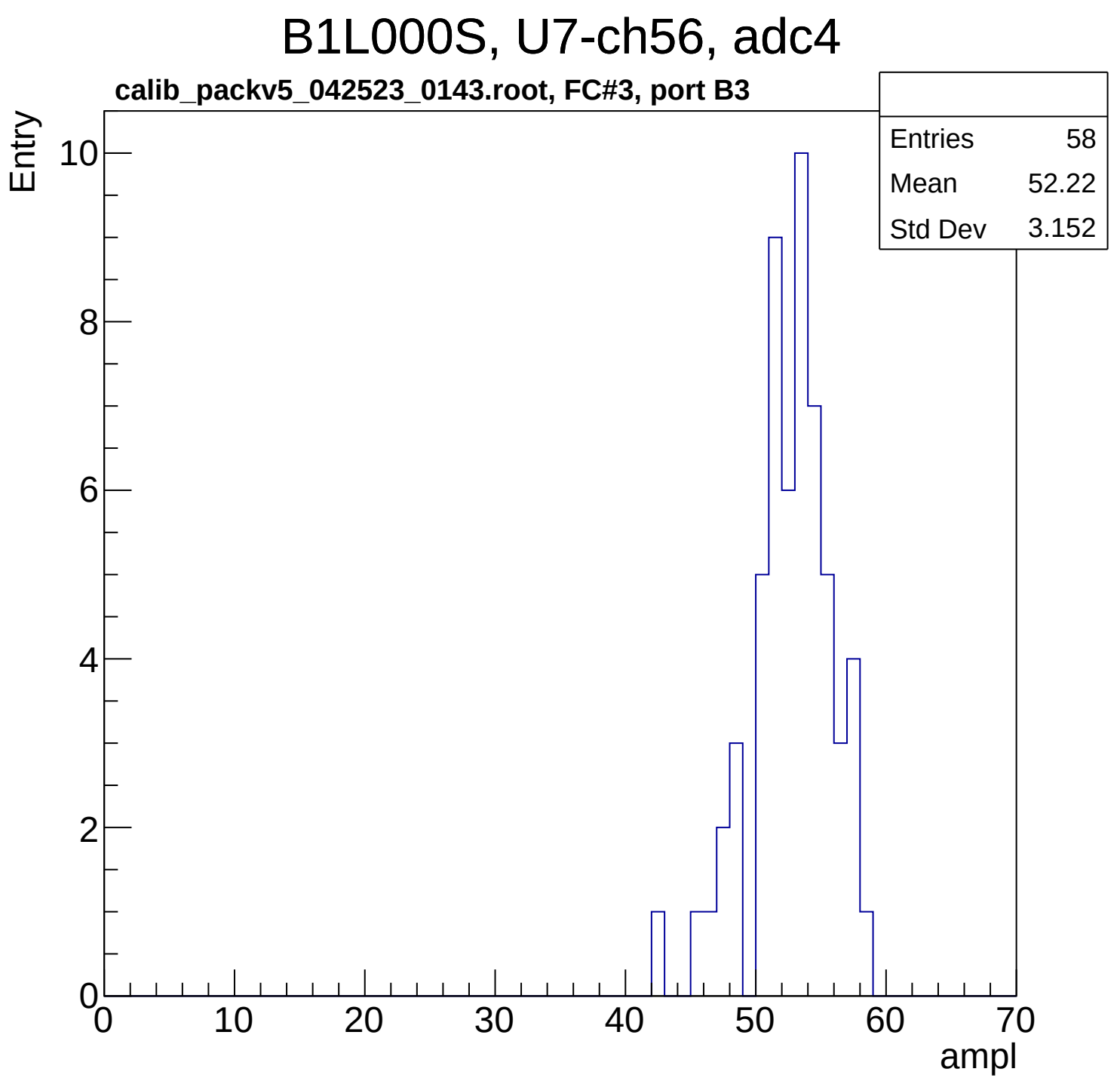
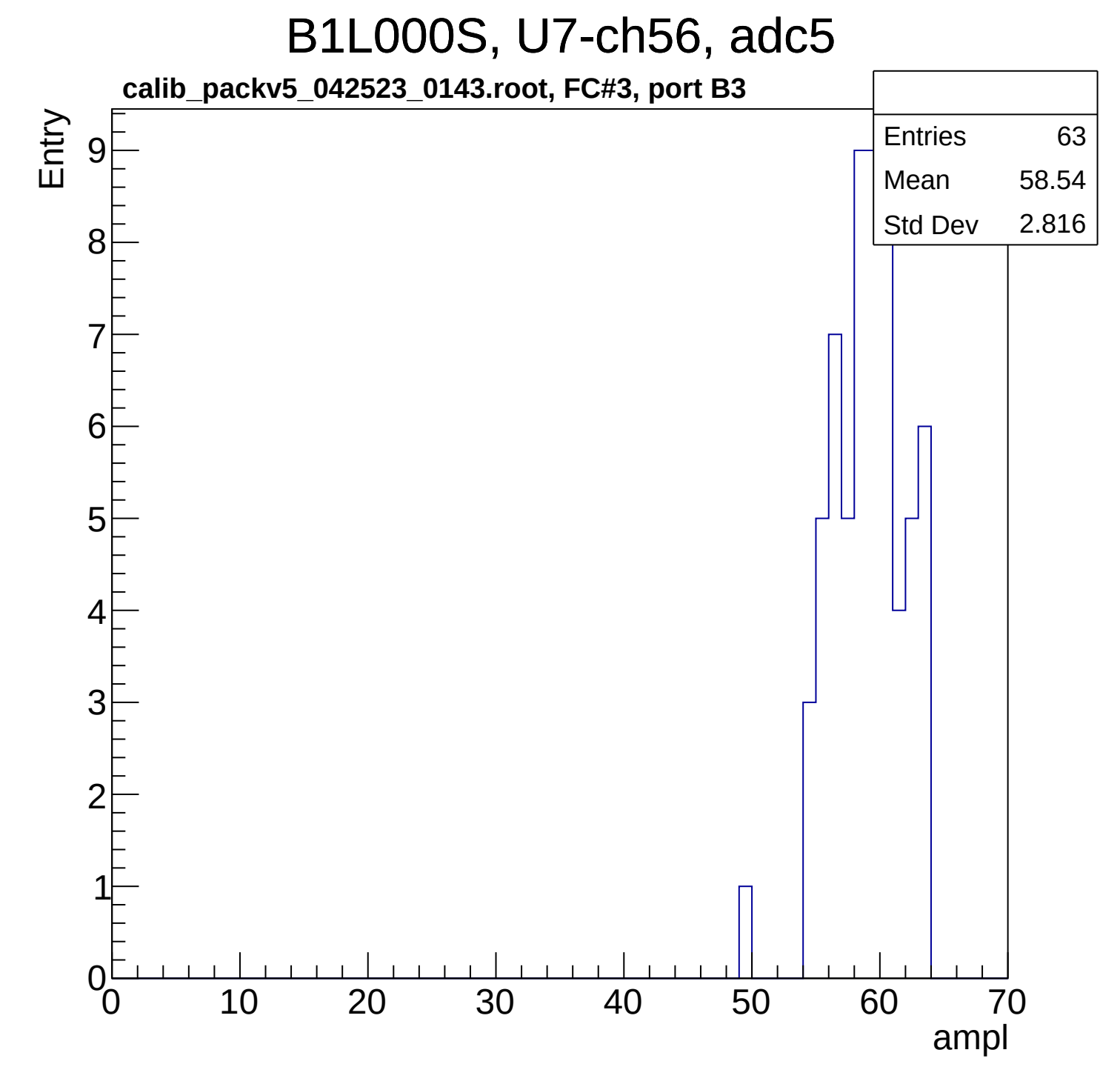
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch56, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 52.22
Std Dev 3.152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch56, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 58.54
Std Dev 2.816
Entry
ampl
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2
3
4
5
6
7
8
9
0
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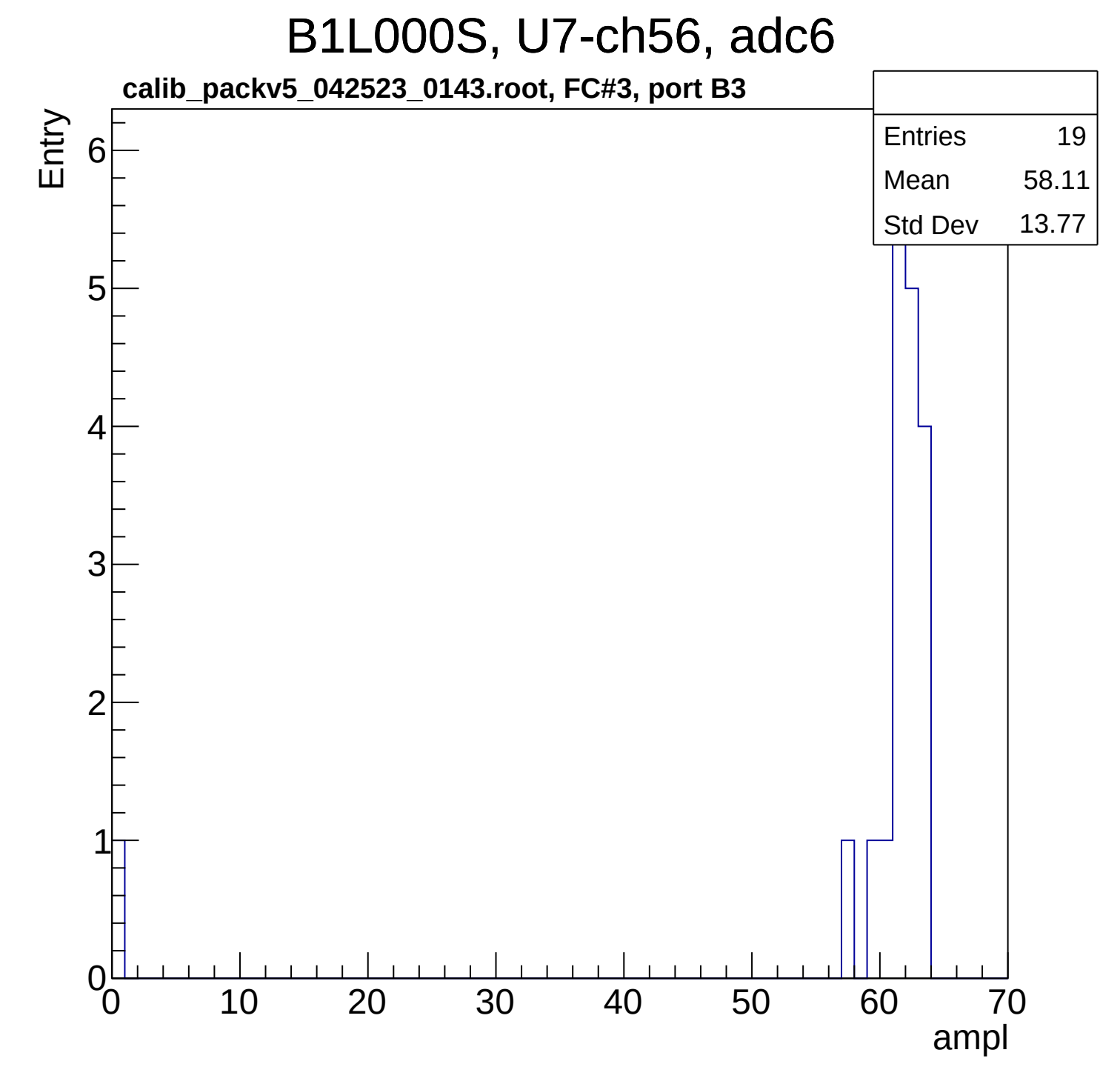
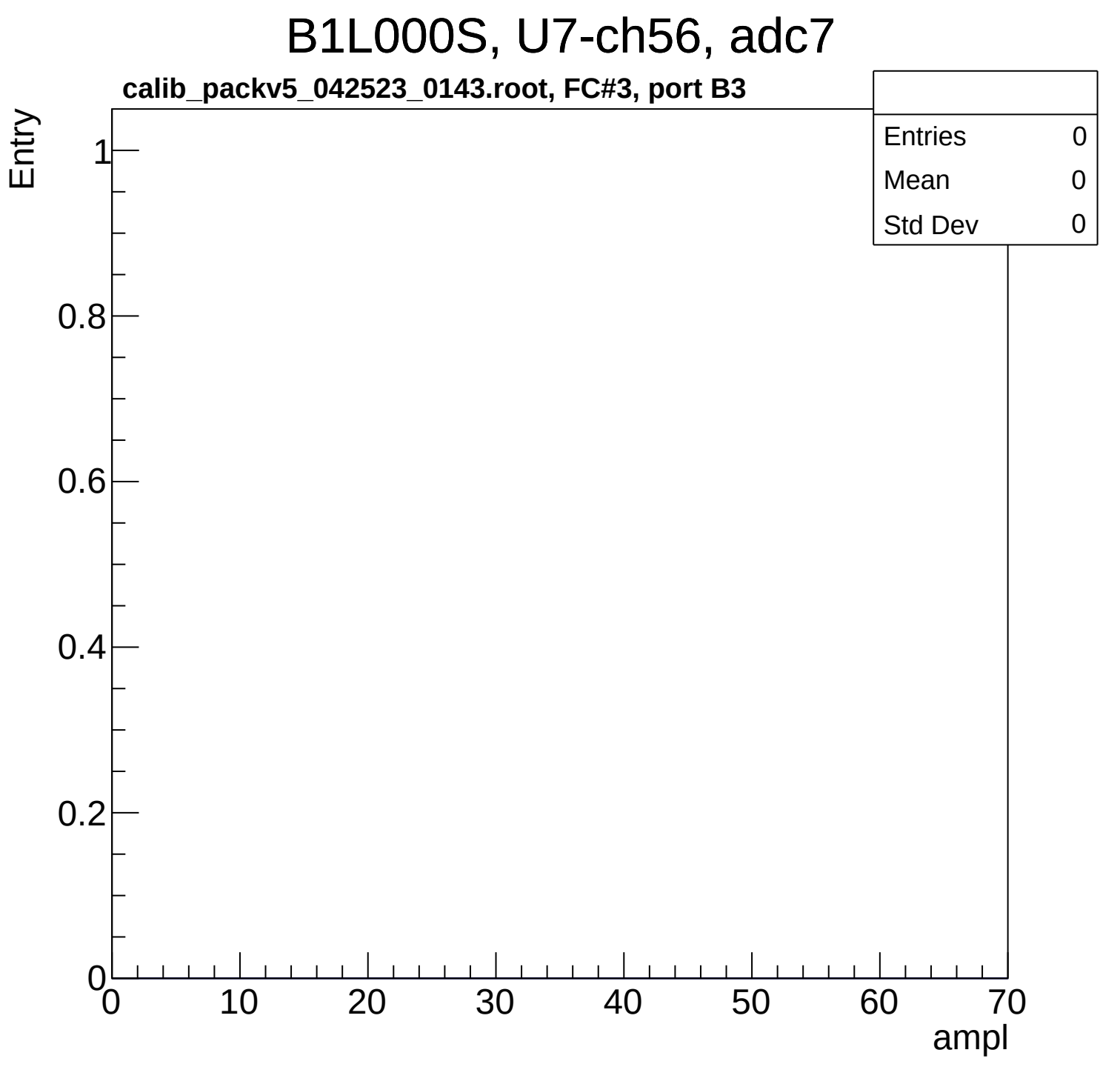
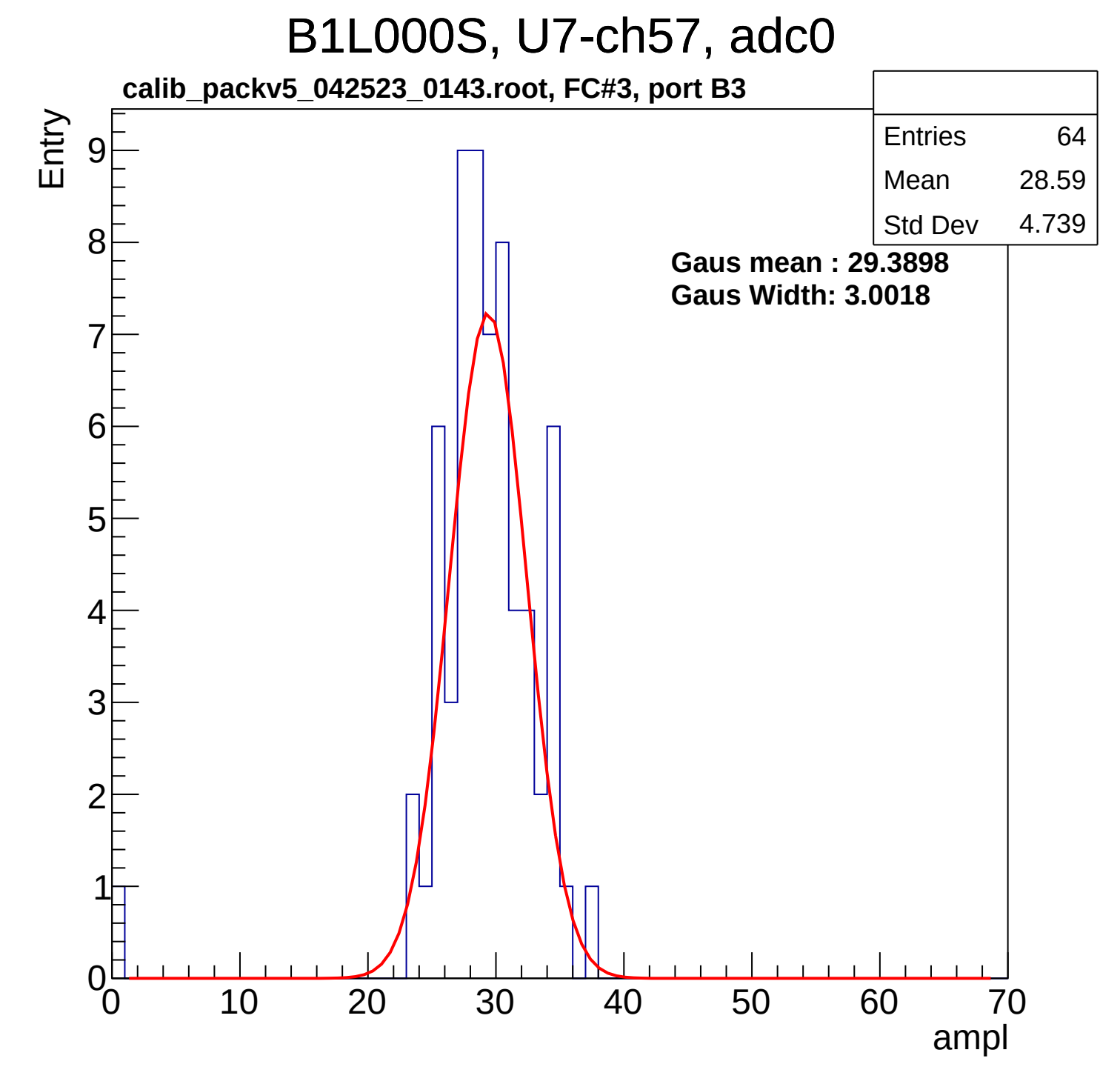
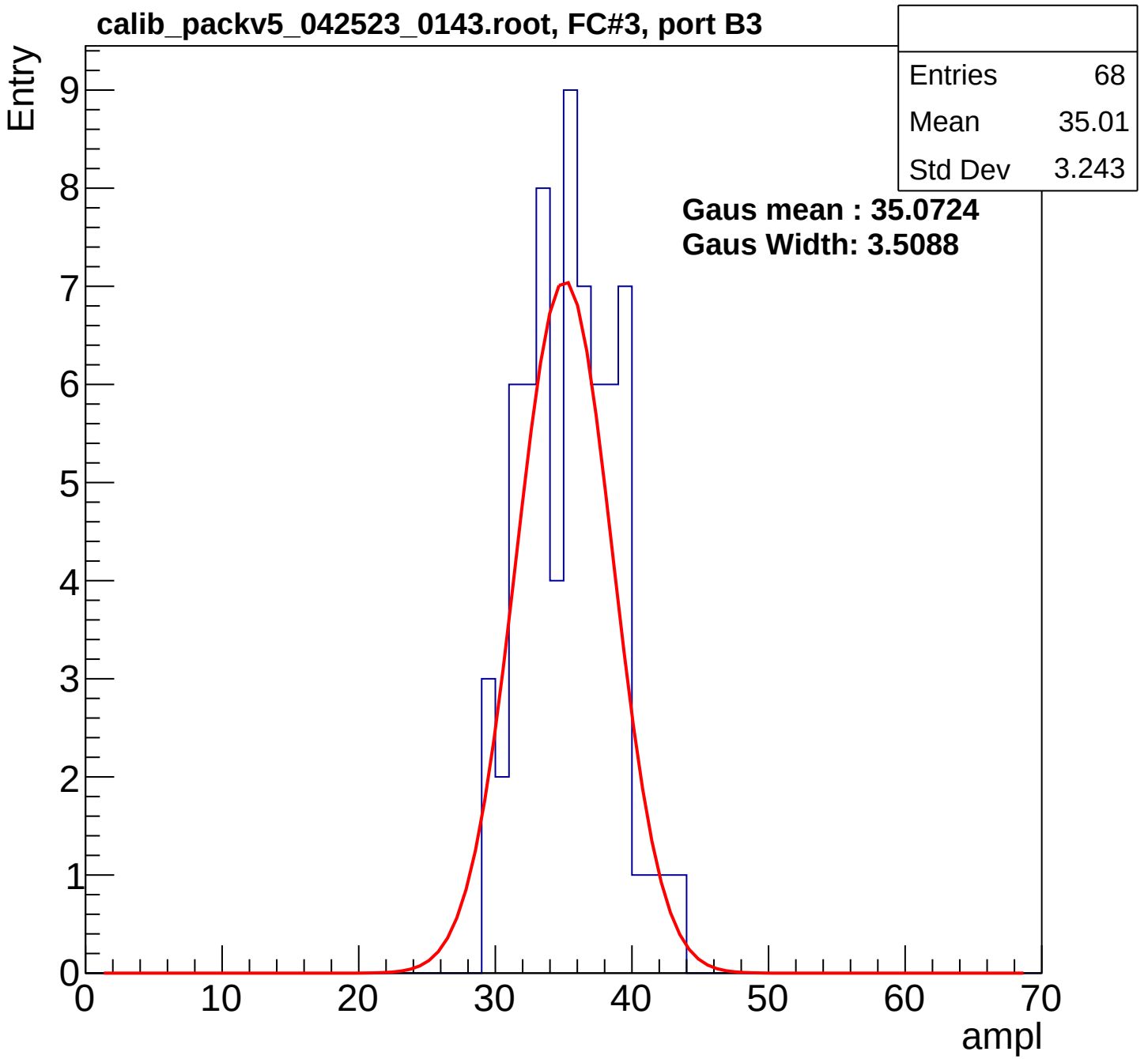
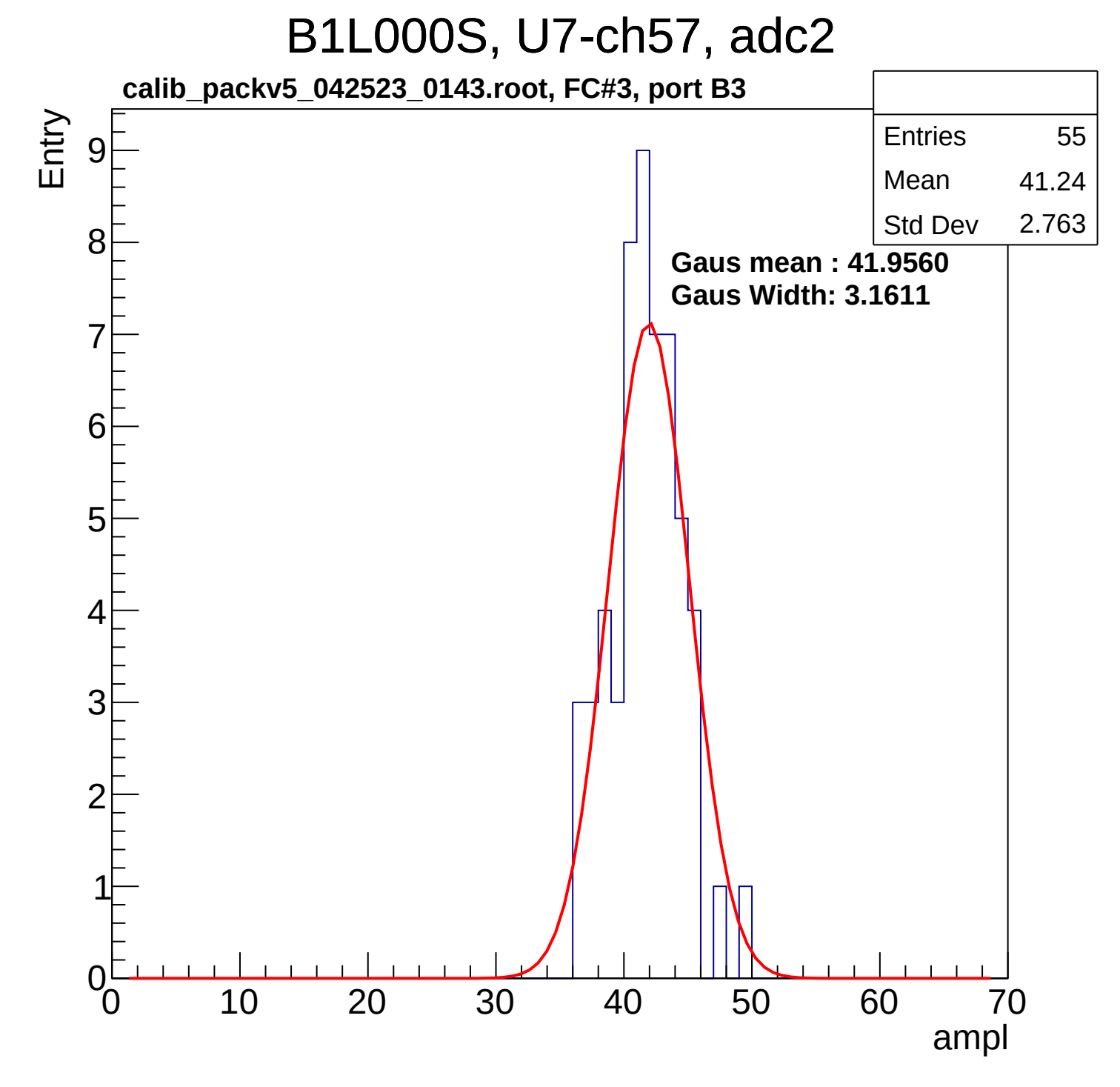
60
70

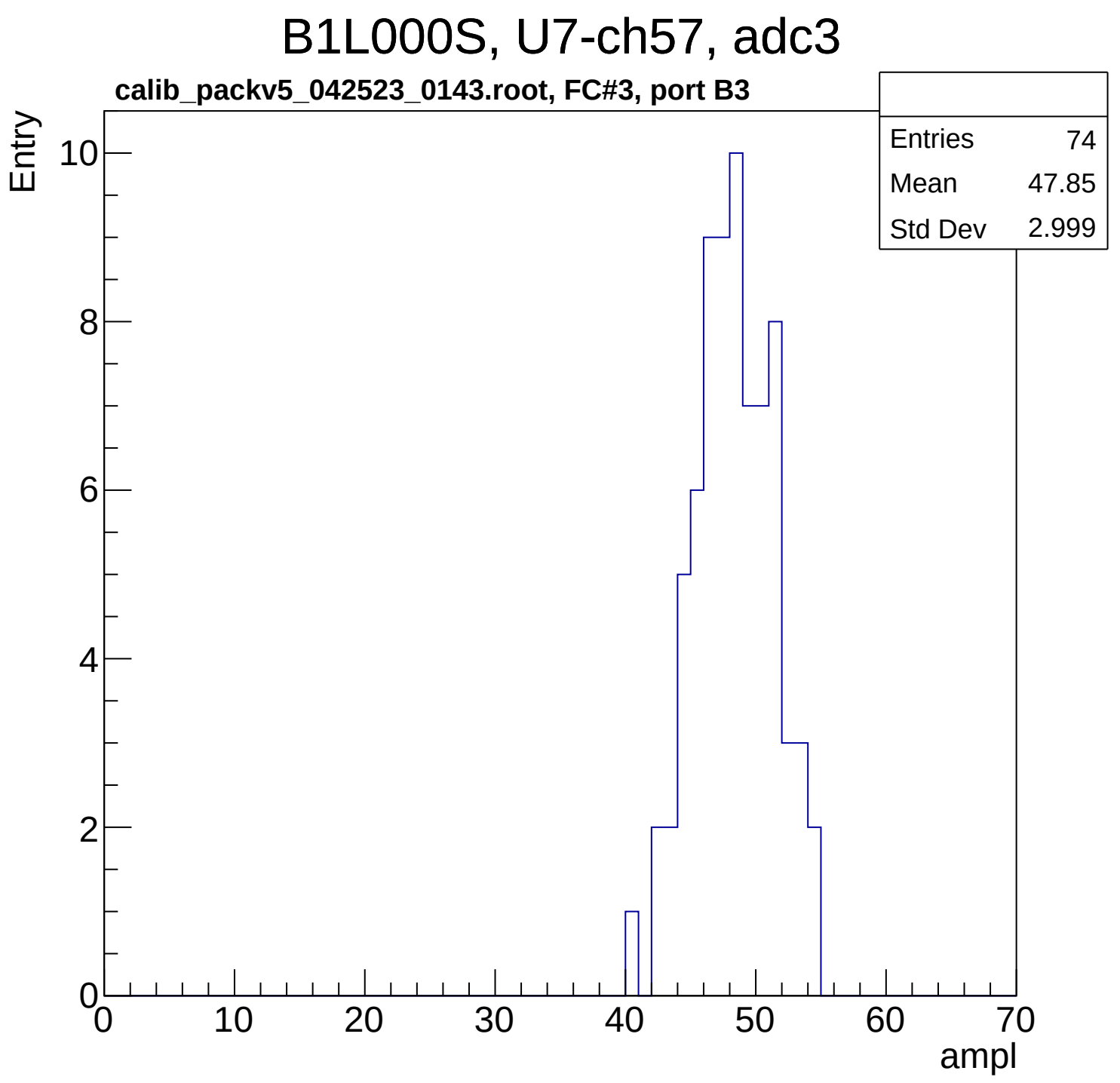

B1L000S, U7-ch56, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 58.11
Std Dev 13.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

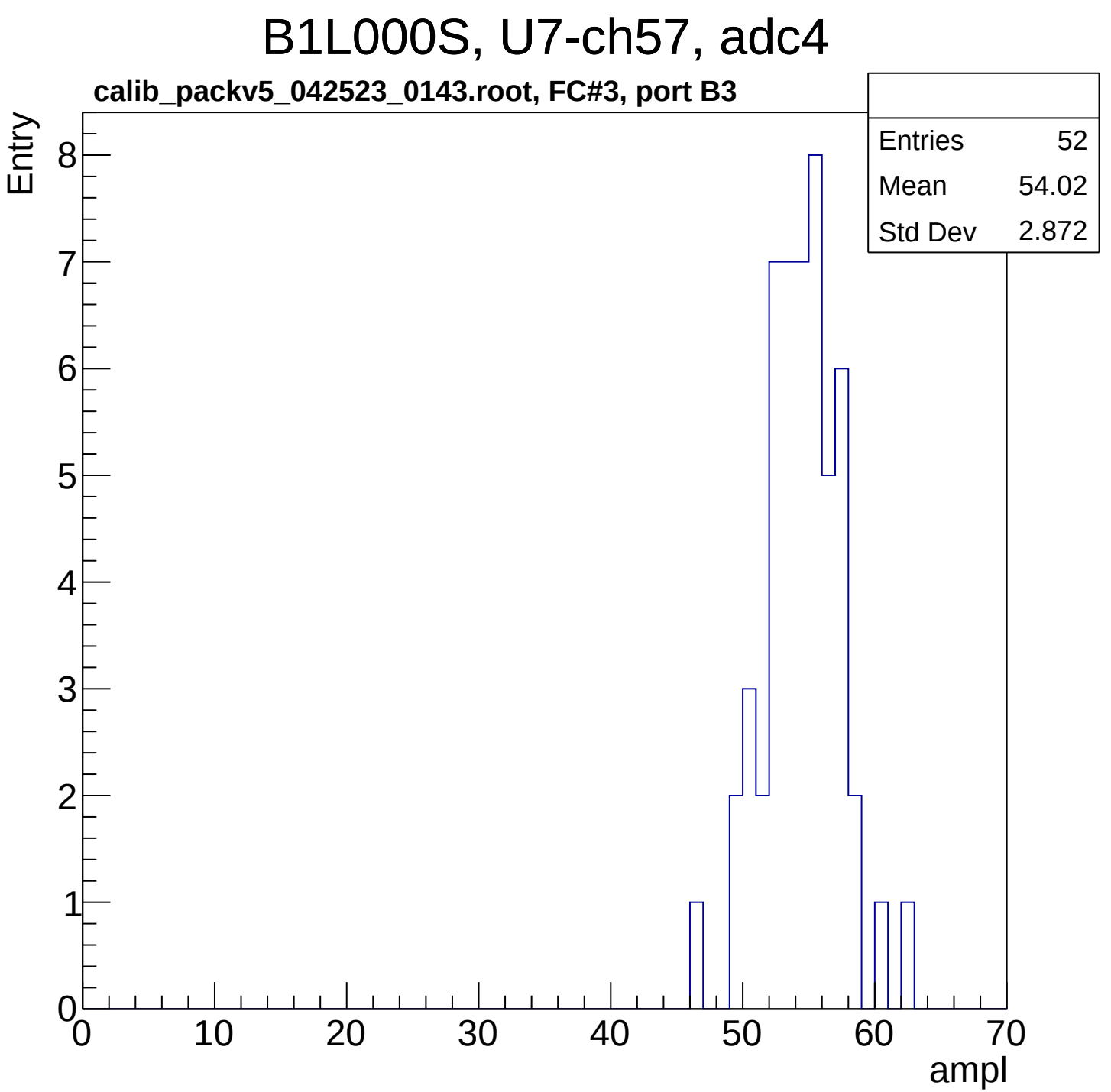

B1L000S, U7-ch56, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

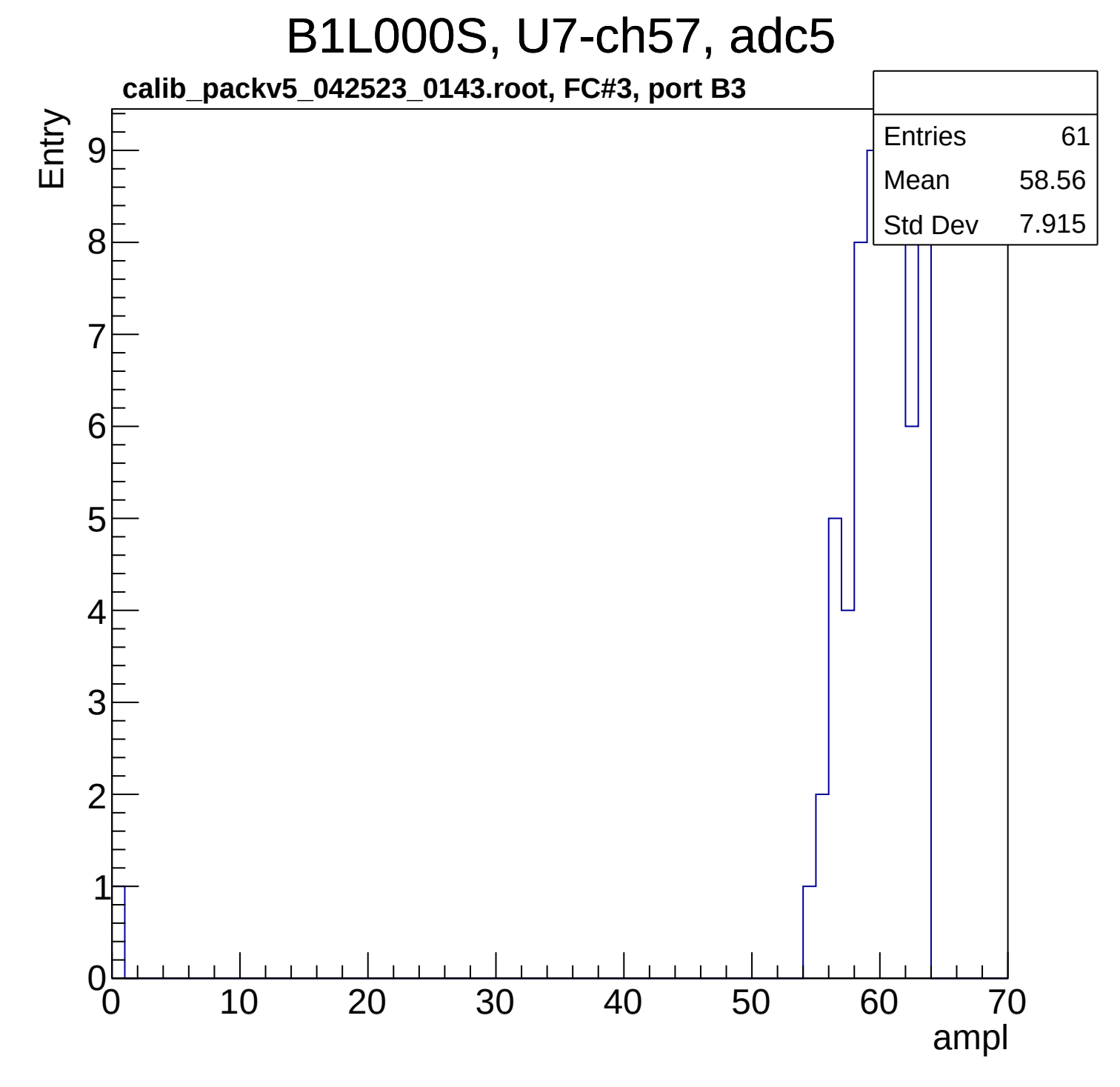

B1L000S, U7-ch57, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 28.59
Std Dev 4.739
Gaus mean : 29.3898
Gaus Width: 3.0018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch57, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 35.01
Std Dev 3.243
Gaus mean : 35.0724
Gaus Width: 3.5088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch57, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 41.24
Std Dev 2.763
Gaus mean : 41.9560
Gaus Width: 3.1611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch57, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 47.85
Std Dev 2.999
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch57, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 54.02
Std Dev 2.872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch57, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.56
Std Dev 7.915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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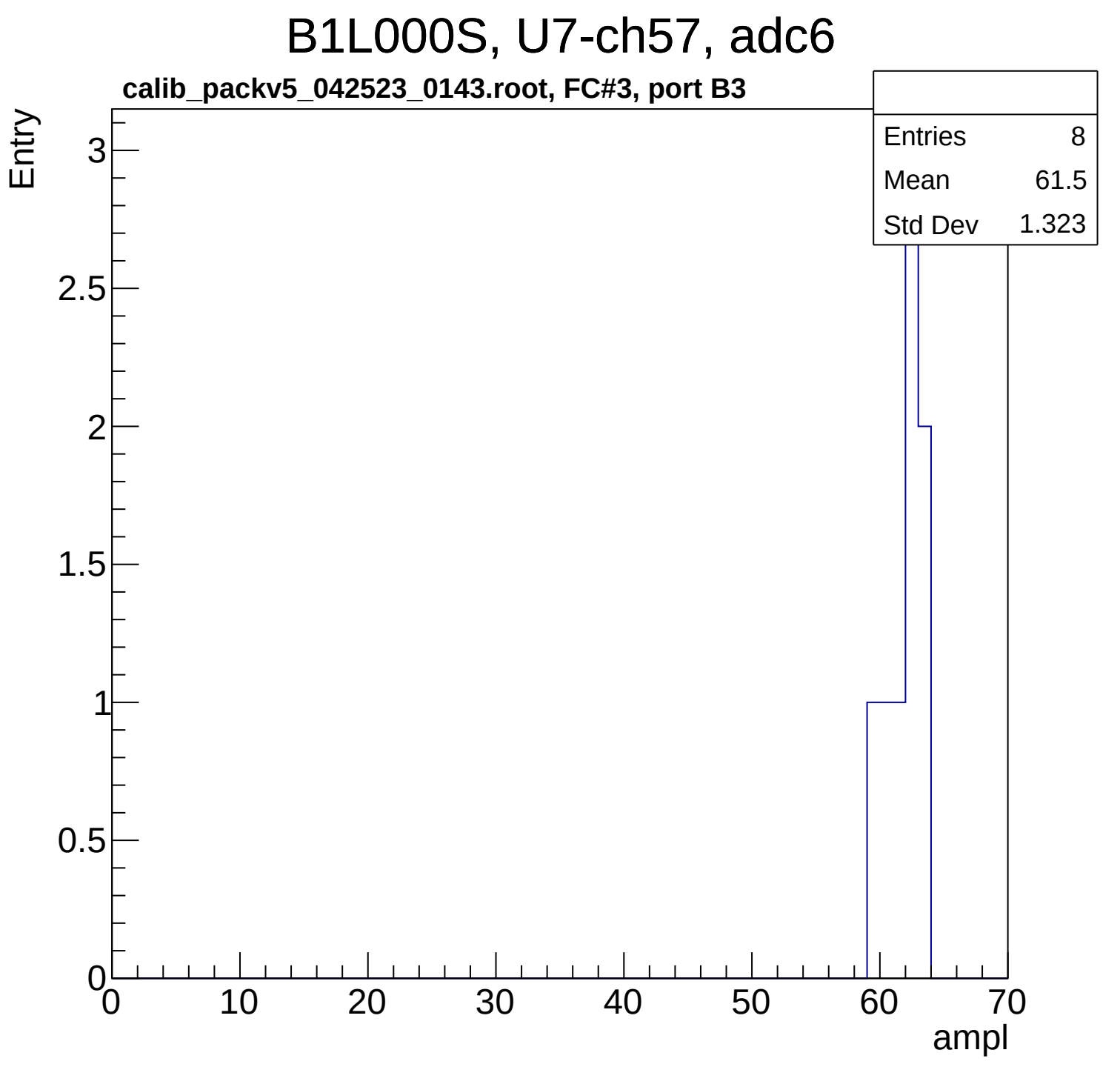
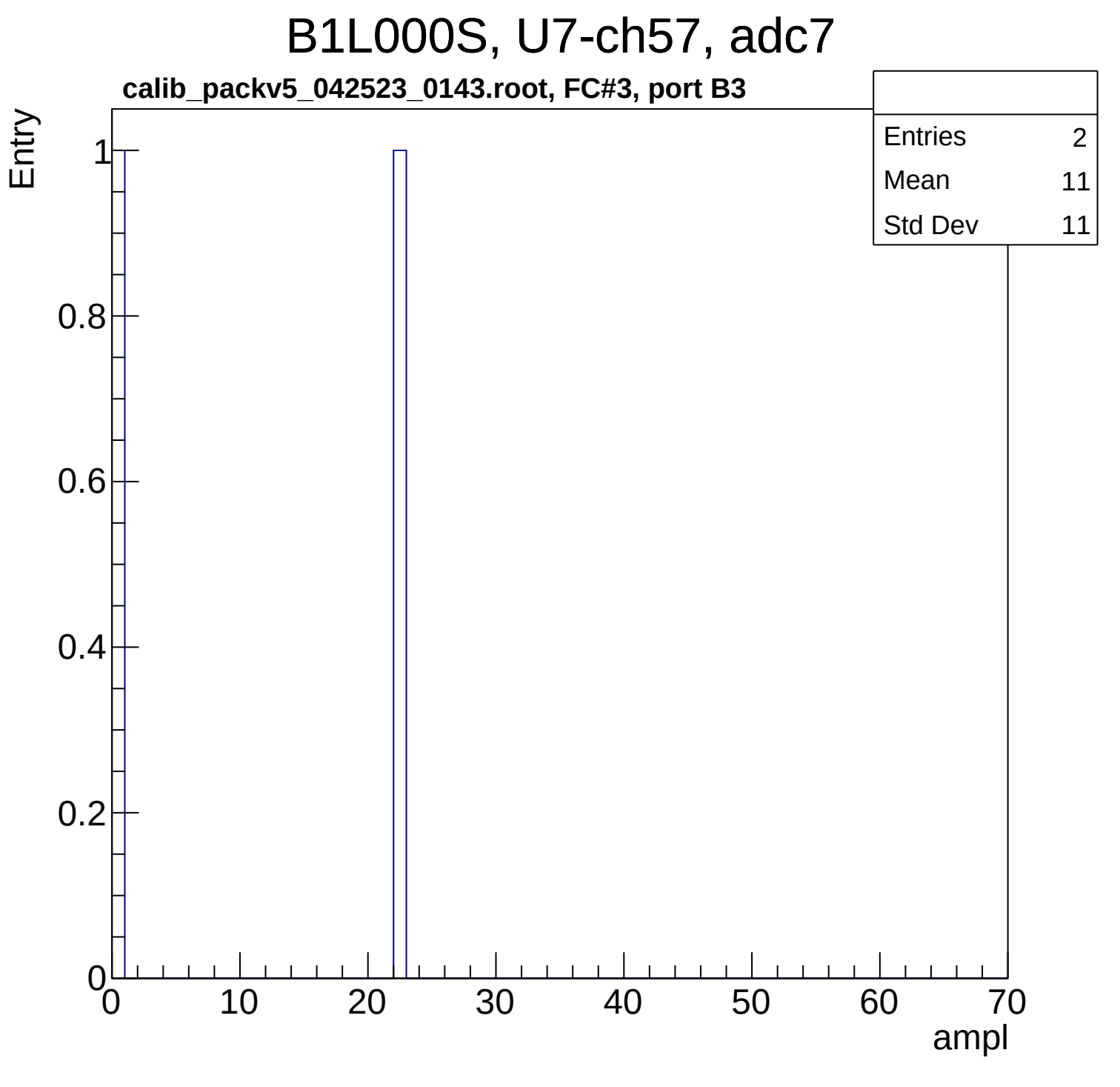

B1L000S, U7-ch57, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 61.5
Std Dev 1.323
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch57, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
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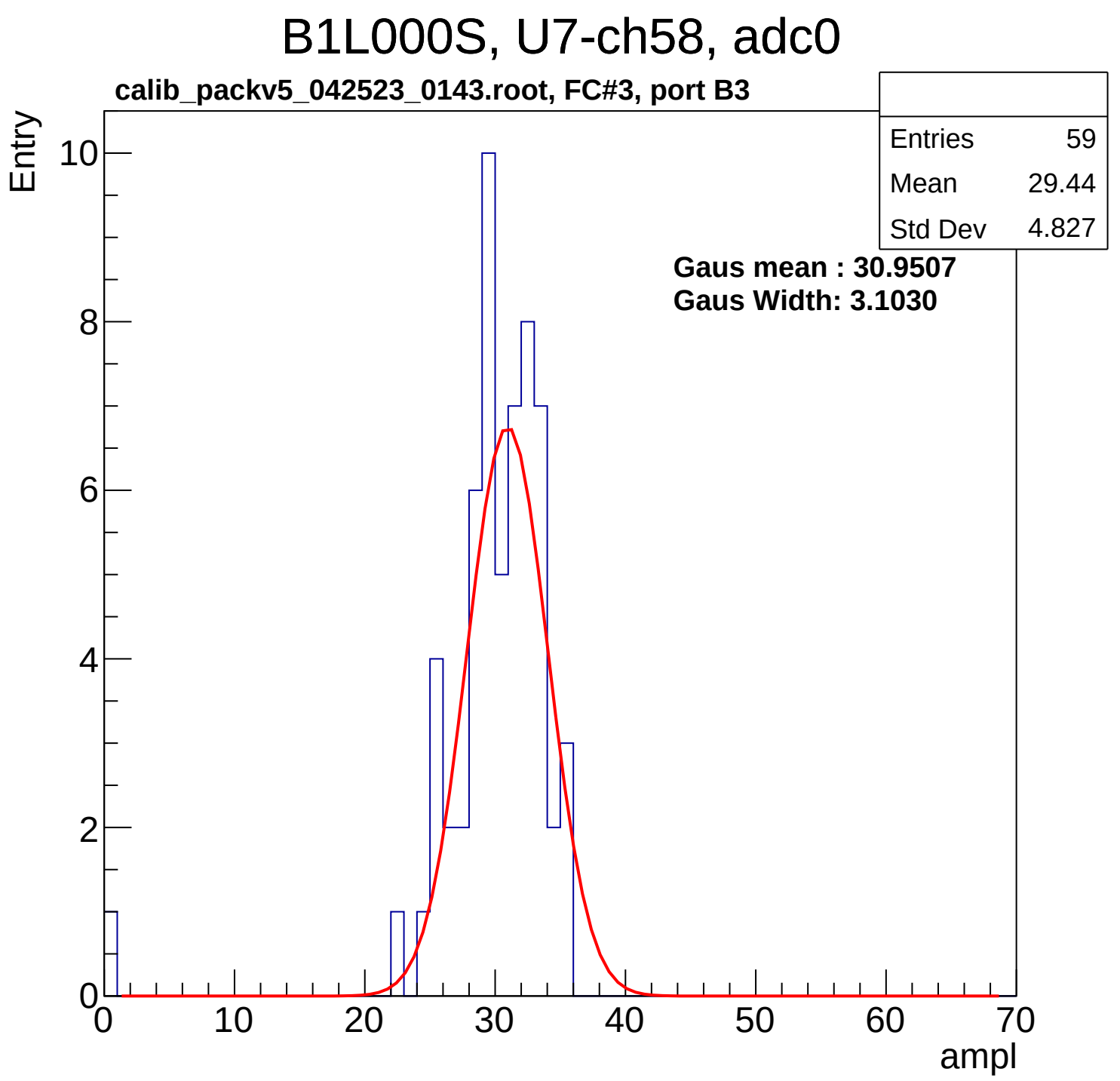
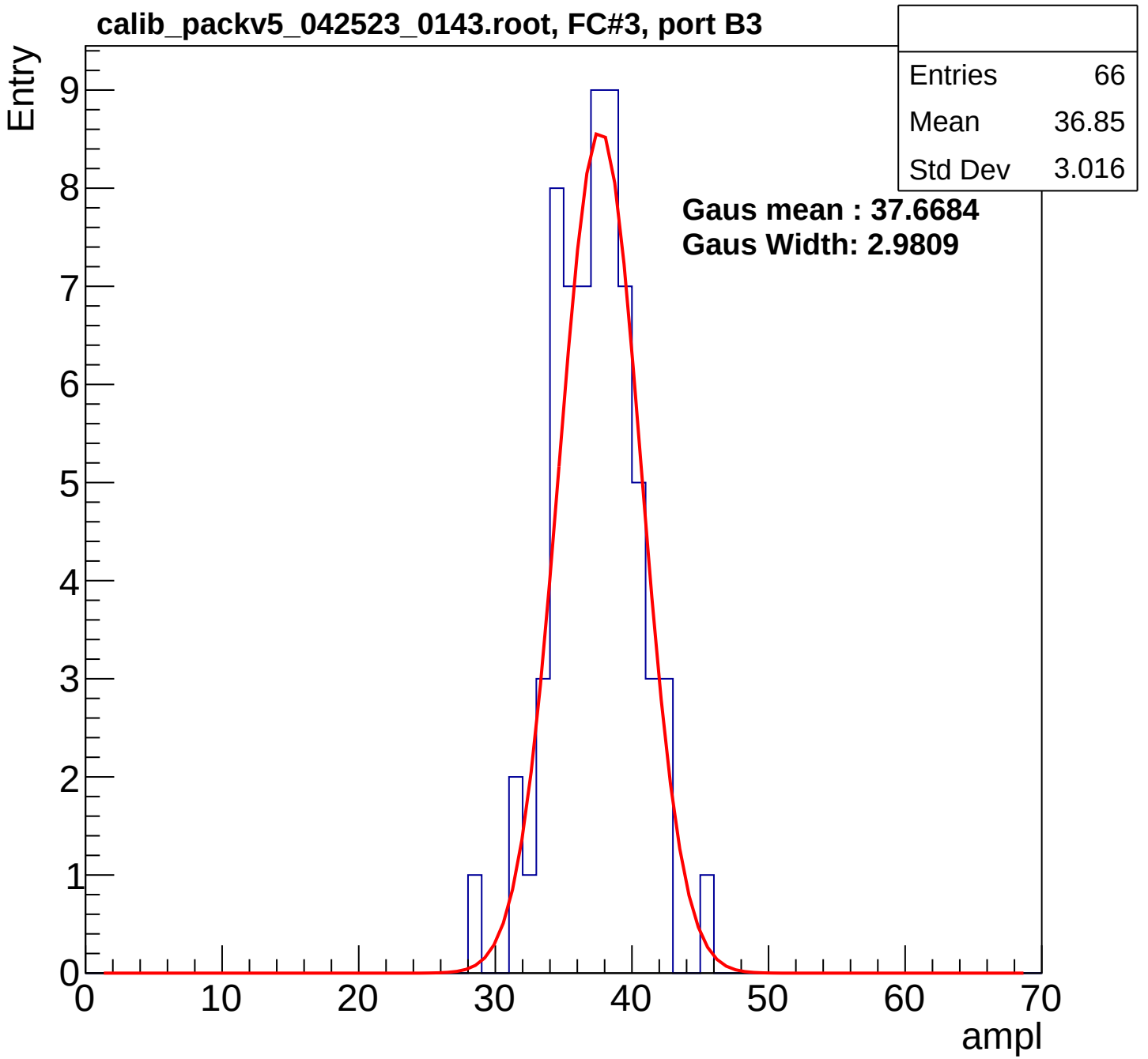
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch58, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 29.44
Std Dev 4.827
Gaus mean : 30.9507
Gaus Width: 3.1030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch58, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 36.85
Std Dev 3.016
Gaus mean : 37.6684
Gaus Width: 2.9809
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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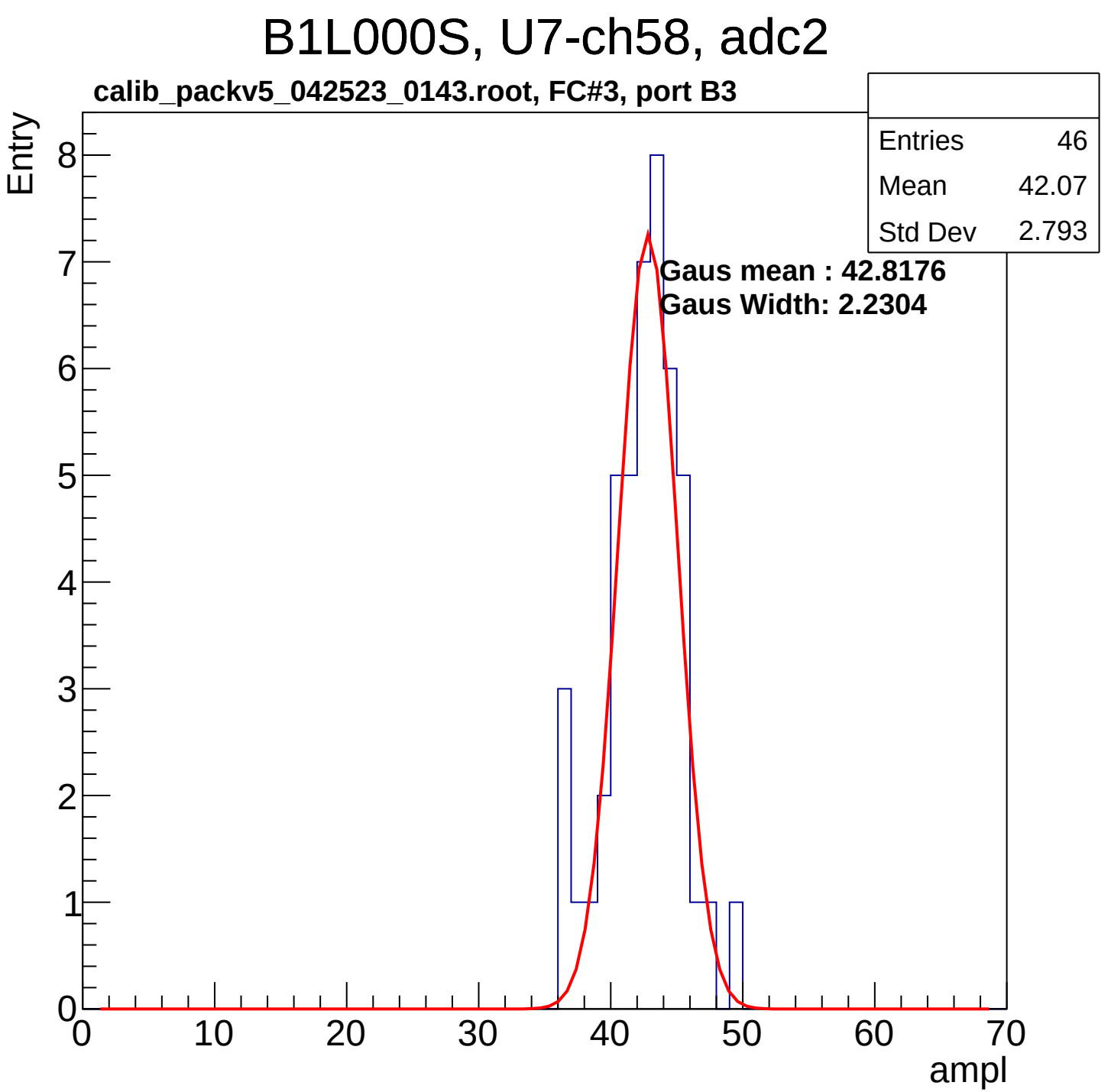
B1L000S, U7-ch58, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 42.07
Std Dev 2.793
Gaus mean : 42.8176
Gaus Width: 2.2304
Entry
ampl
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1
2
3
4
5
6
7
8
0
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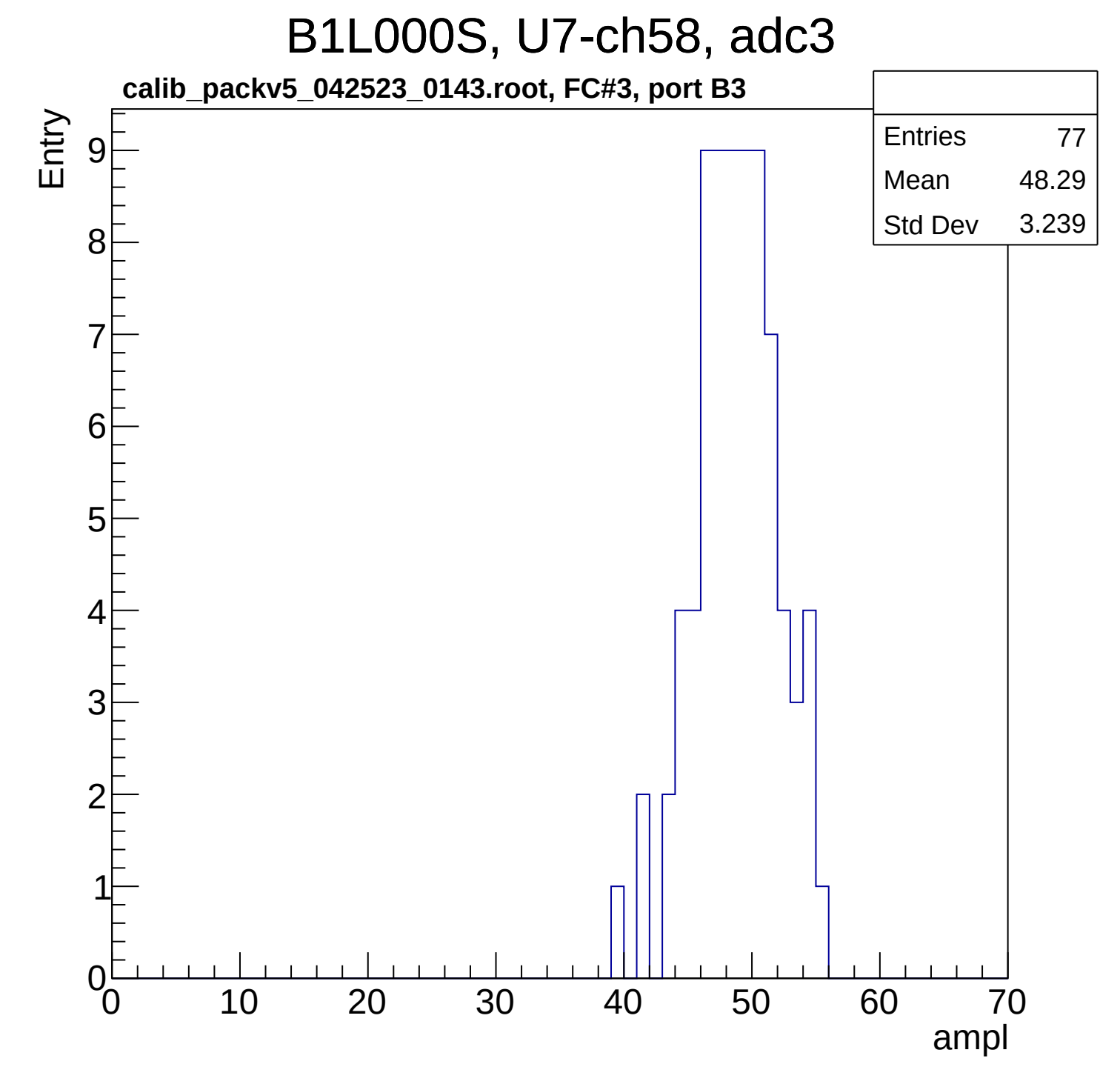
B1L000S, U7-ch58, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 48.29
Std Dev 3.239
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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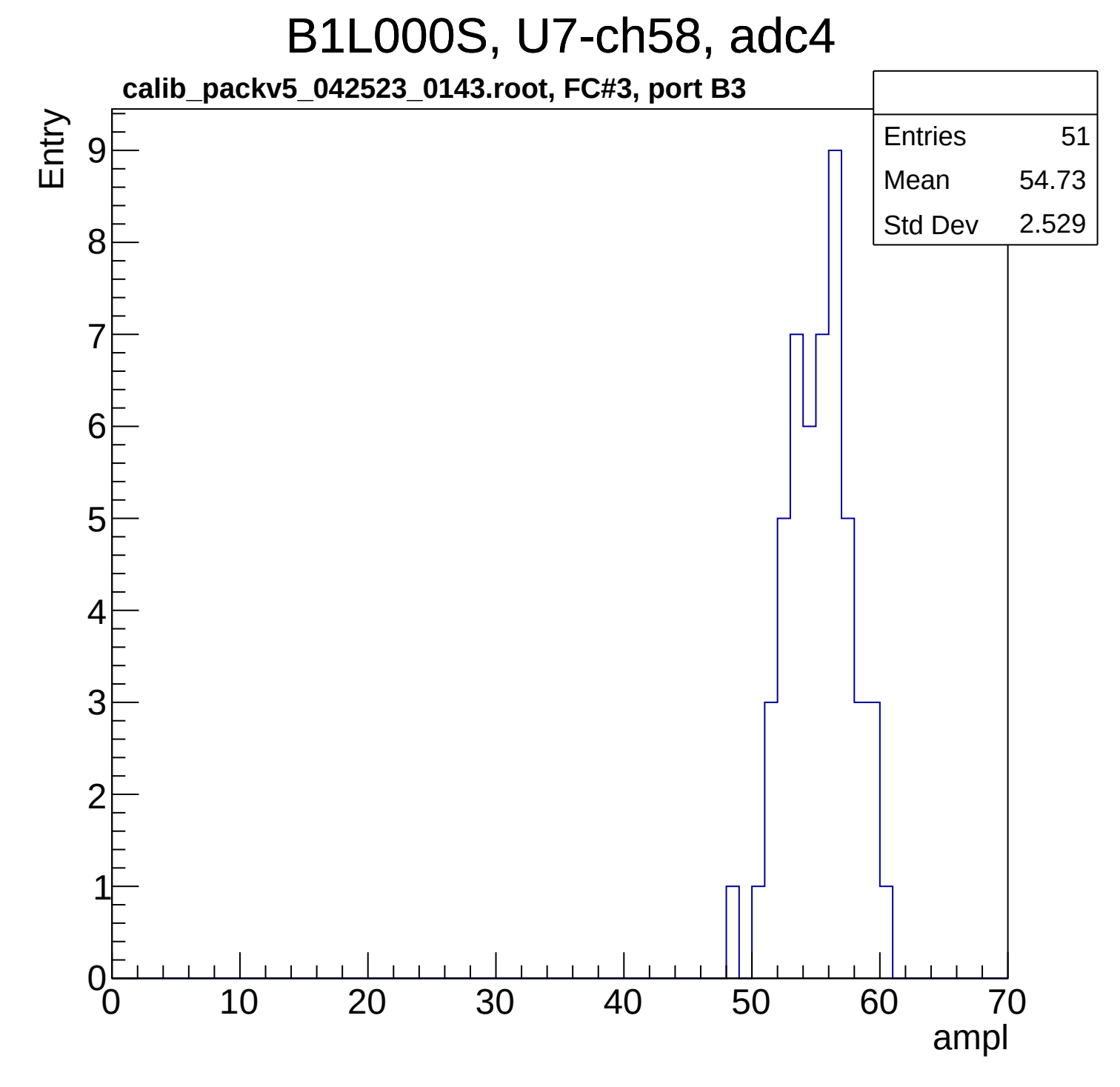
B1L000S, U7-ch58, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 54.73
Std Dev 2.529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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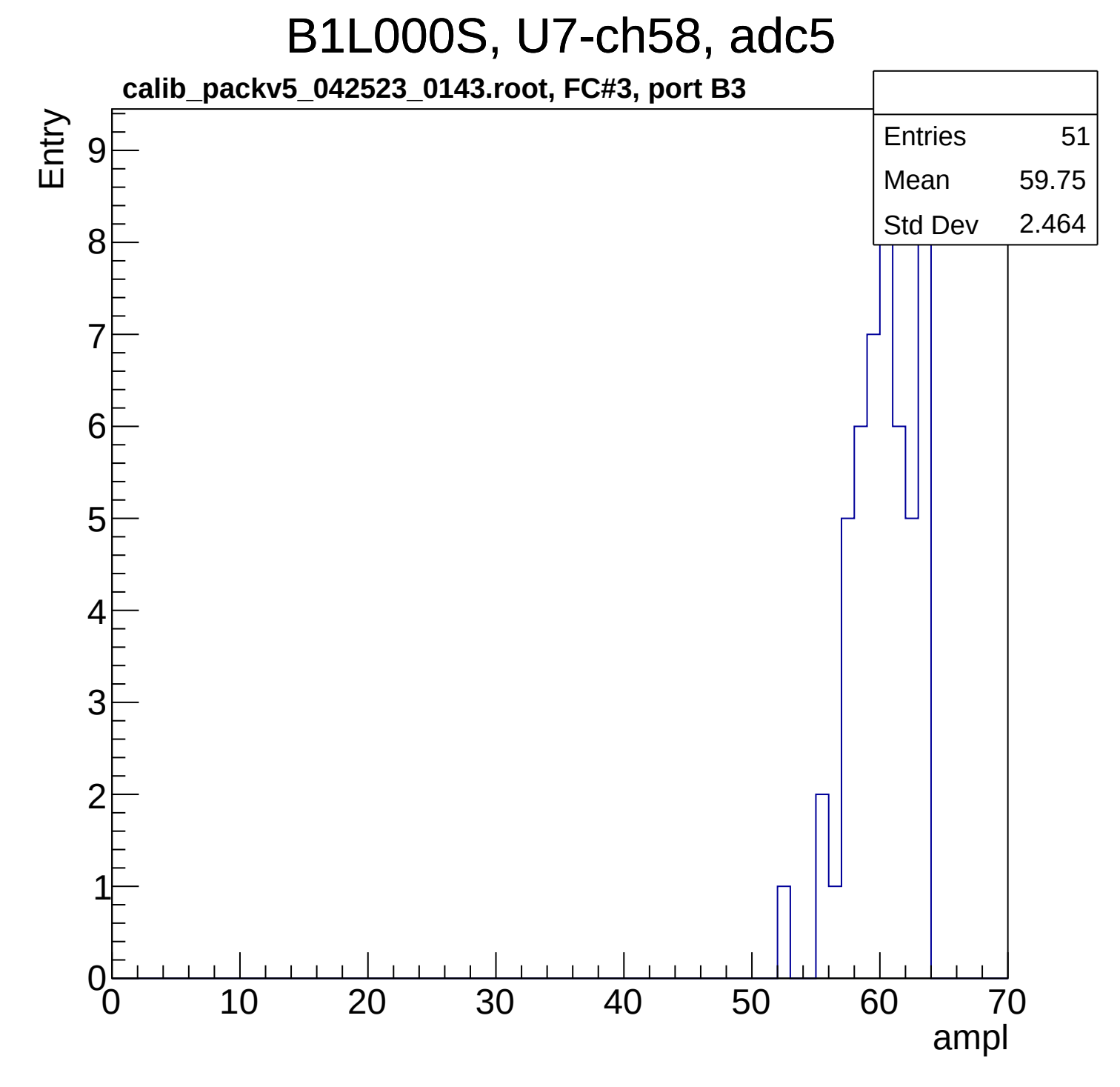
B1L000S, U7-ch58, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 59.75
Std Dev 2.464
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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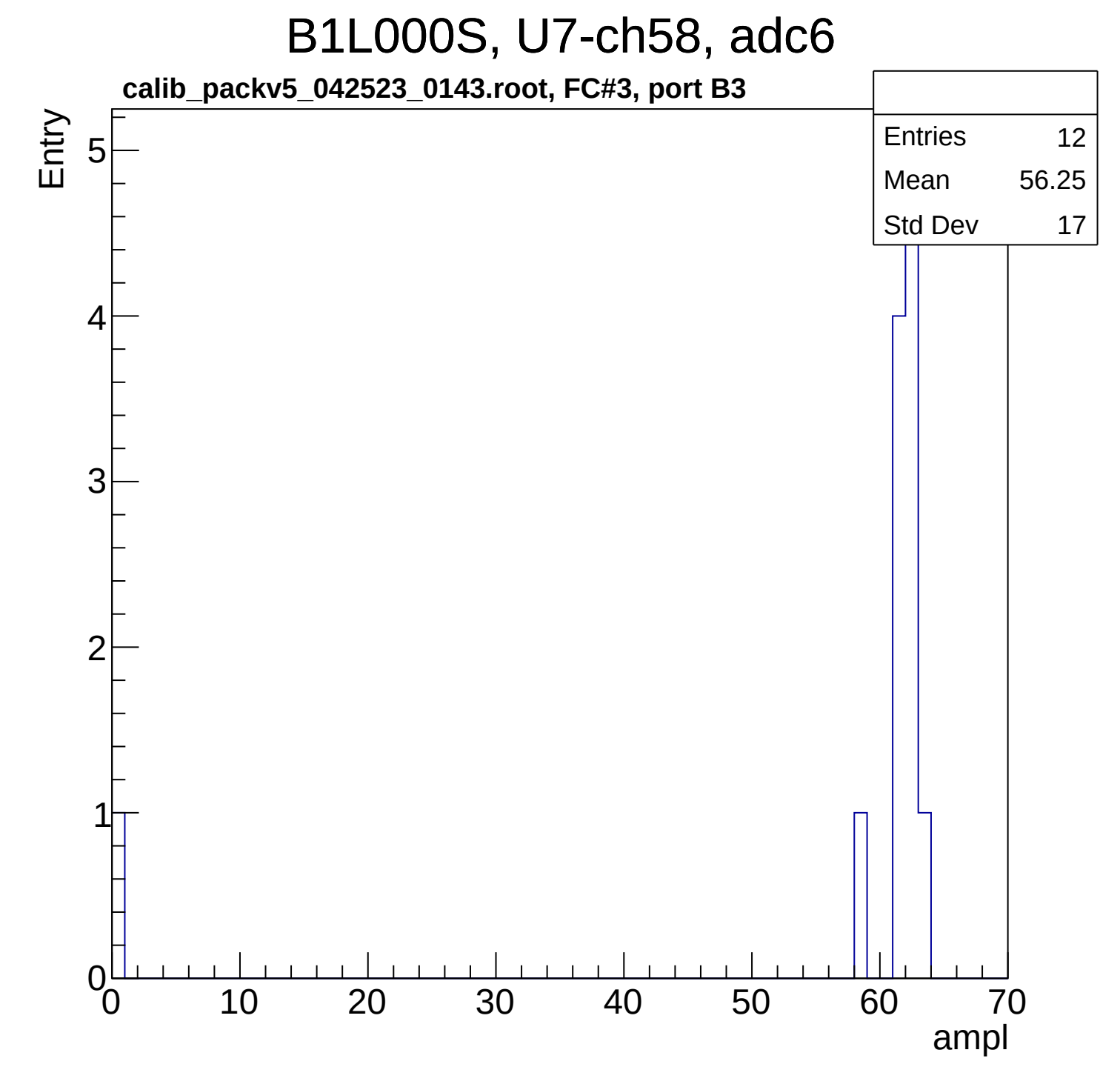
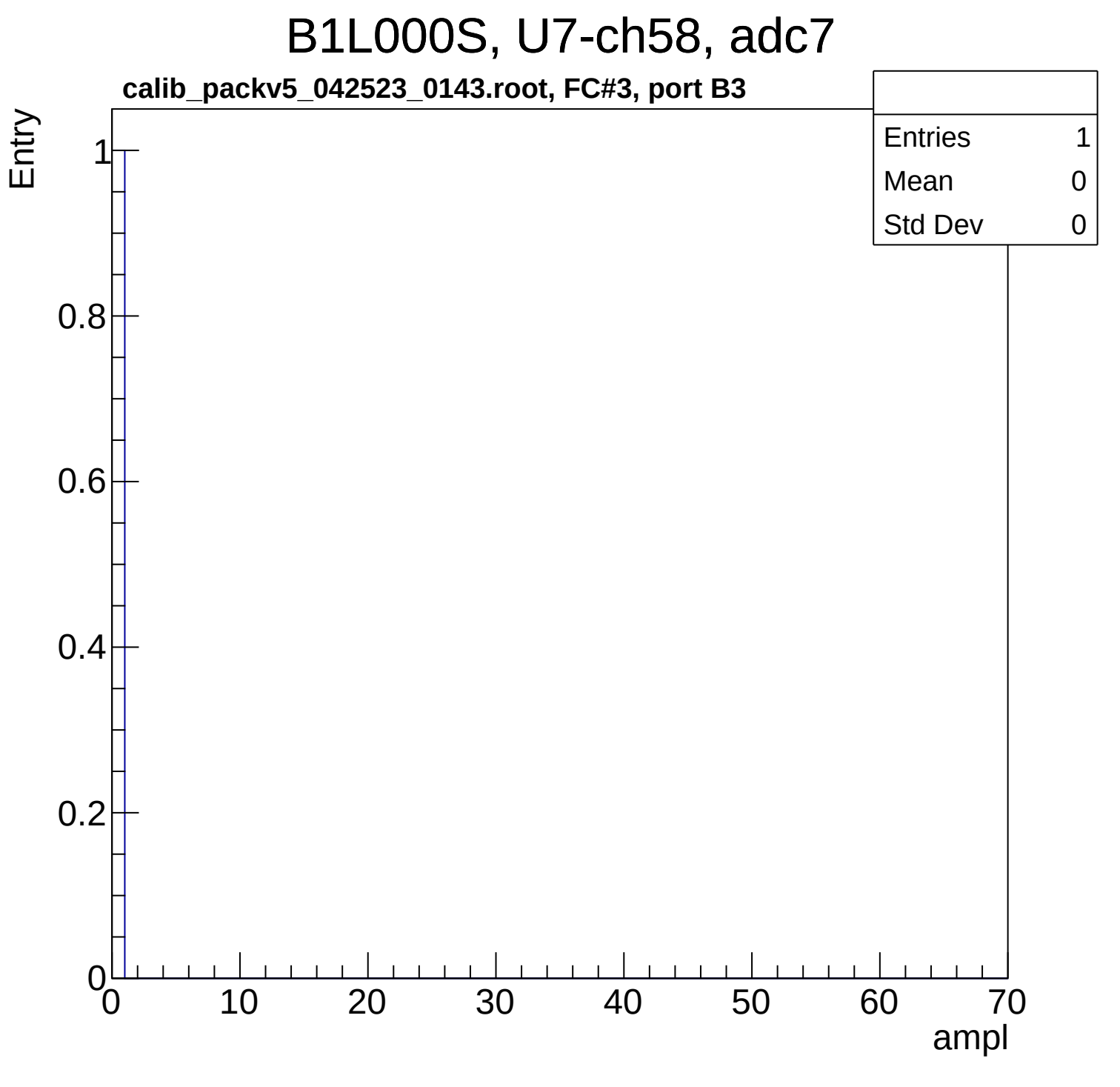
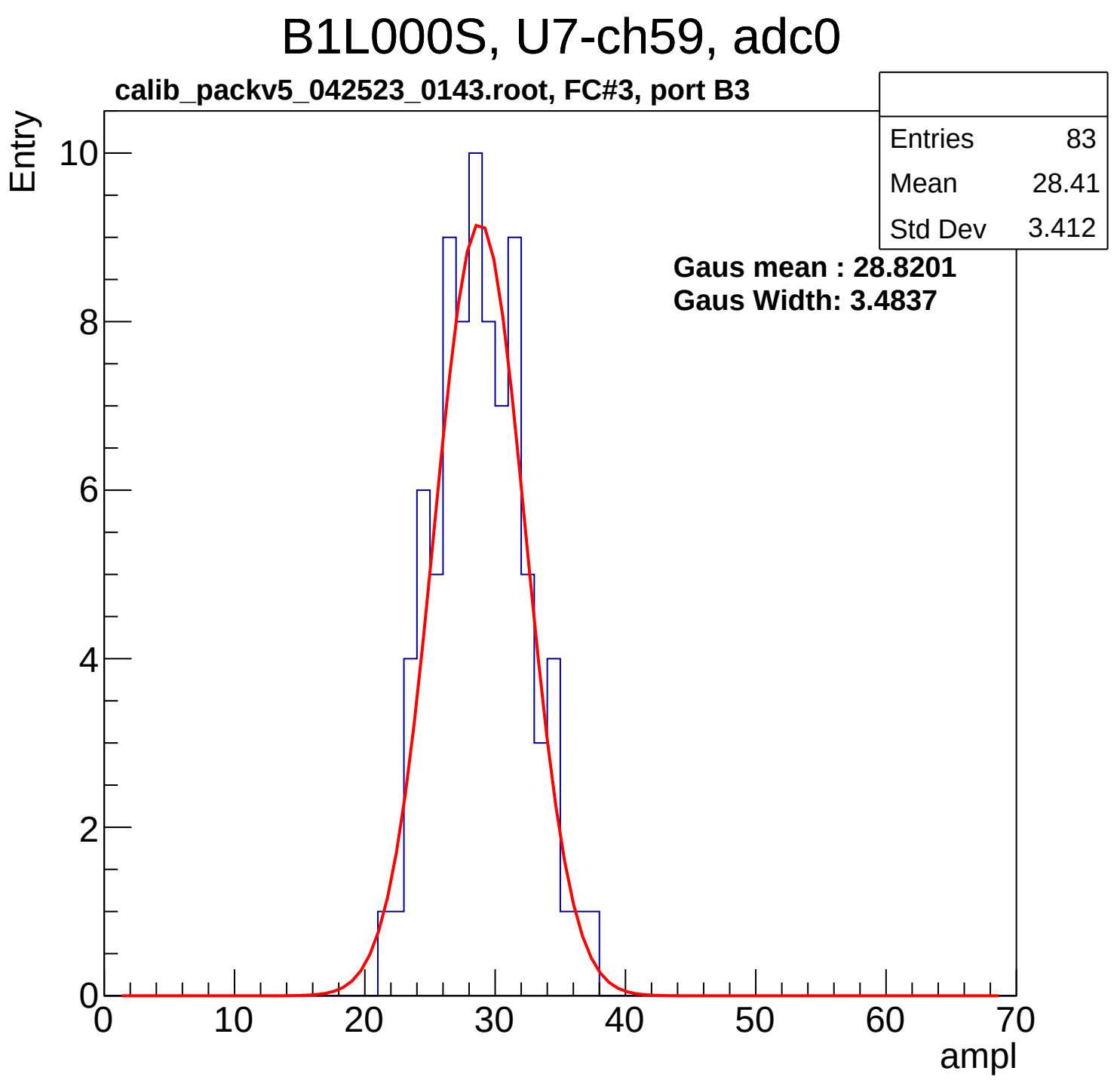
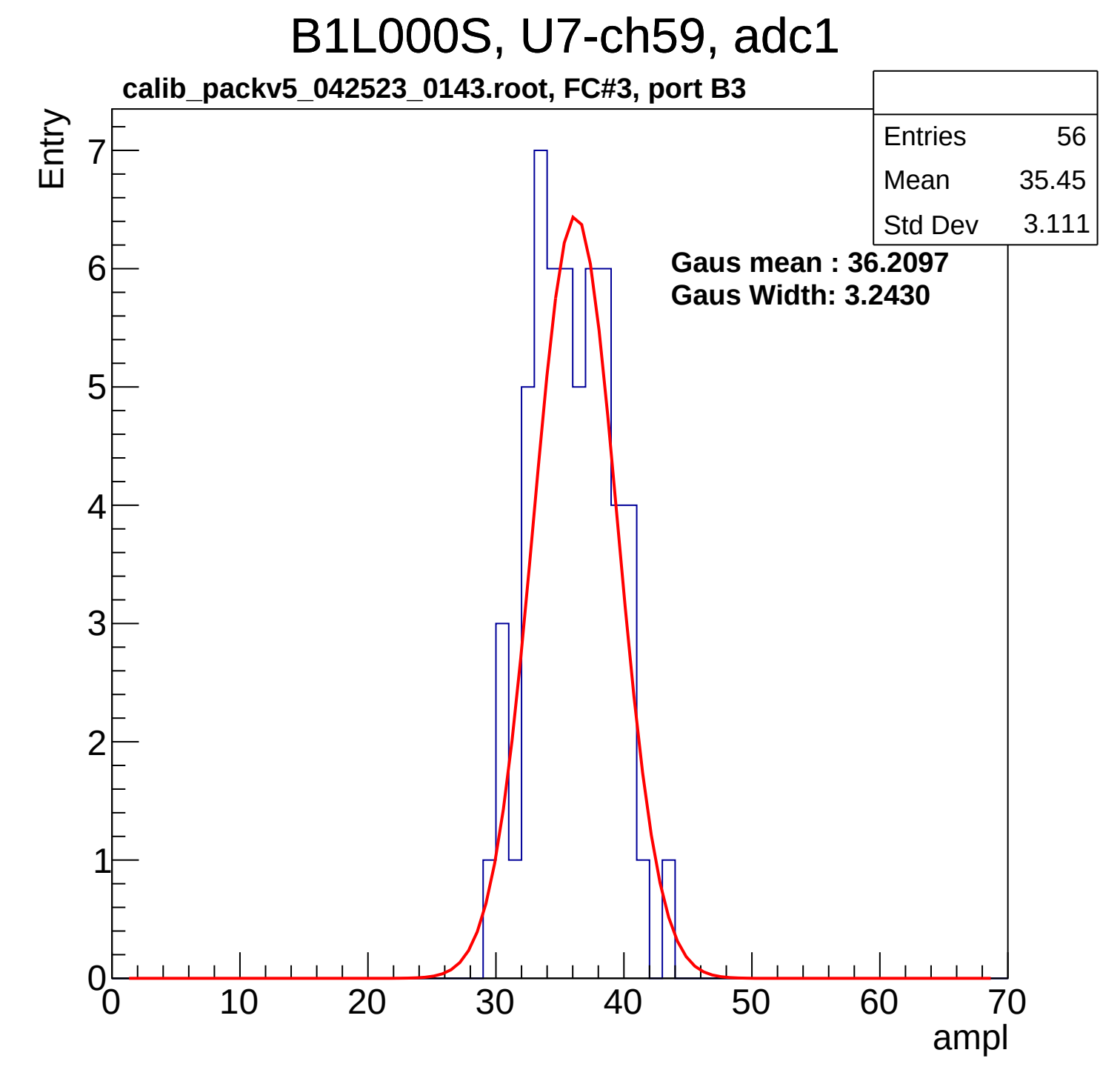
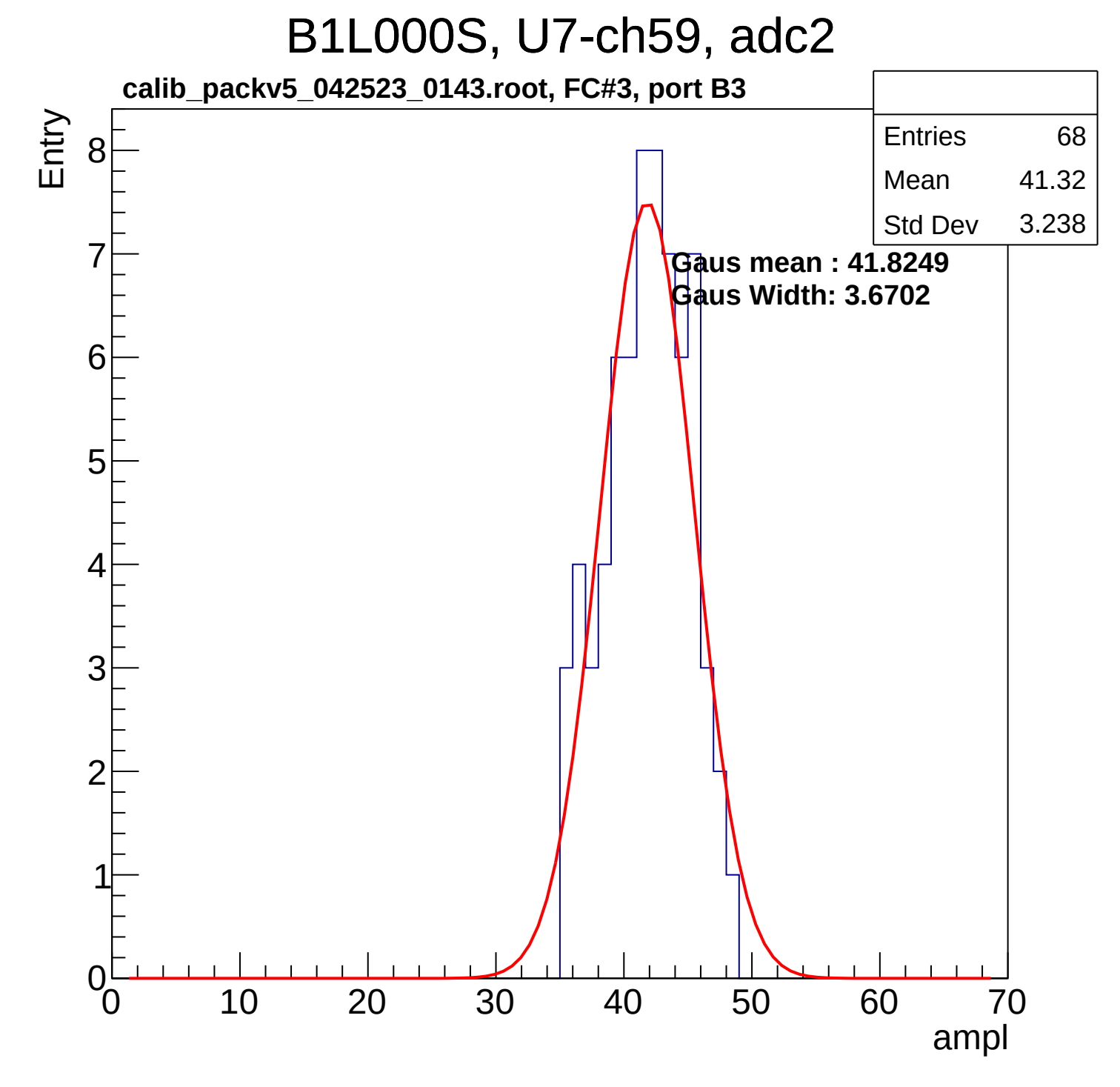
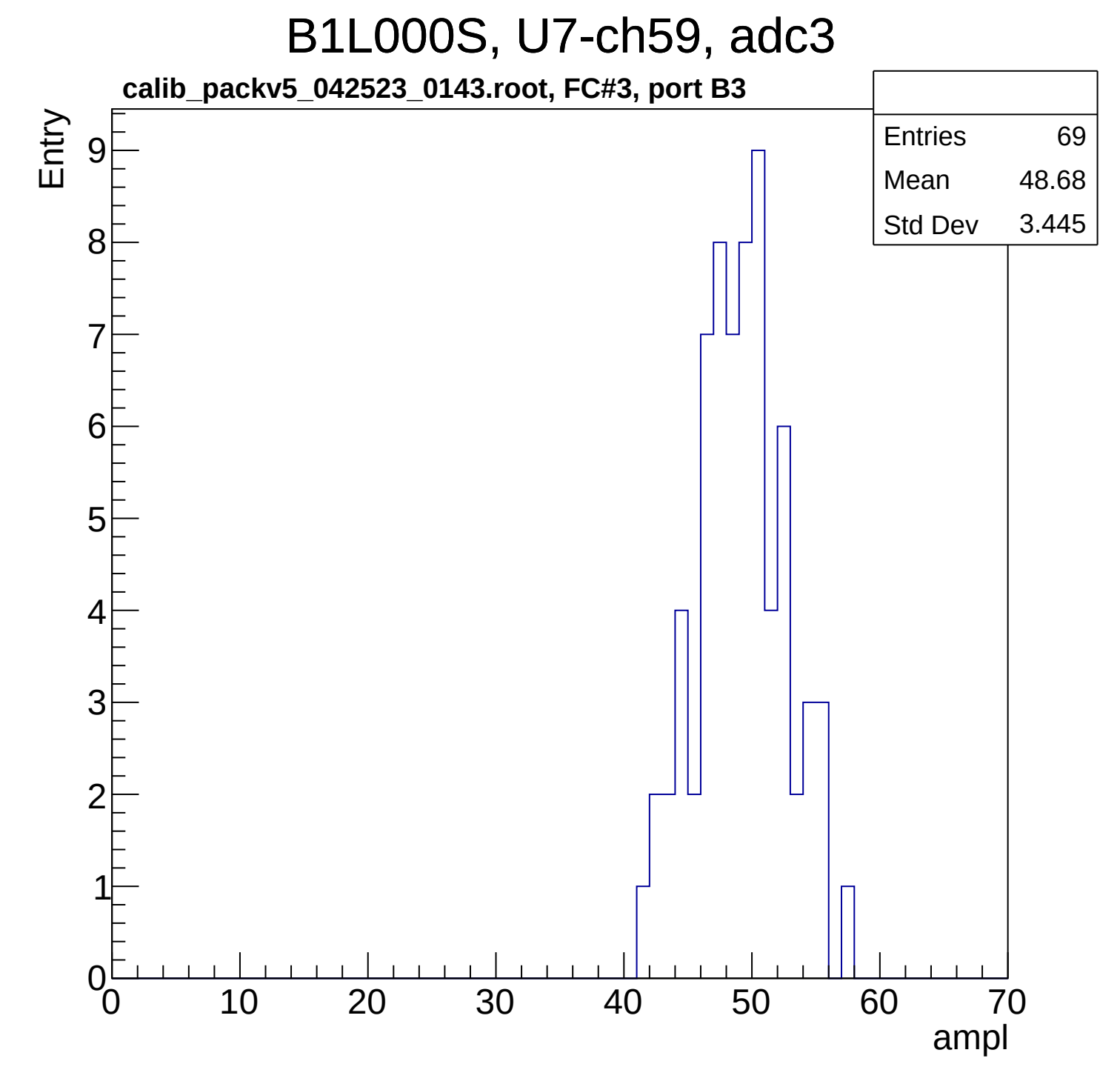

B1L000S, U7-ch58, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.25
Std Dev 17
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch58, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch59, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 28.41
Std Dev 3.412
Gaus mean : 28.8201
Gaus Width: 3.4837
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.45
Std Dev 3.111
Gaus mean : 36.2097
Gaus Width: 3.2430
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 41.32
Std Dev 3.238
Gaus mean : 41.8249
Gaus Width: 3.6702
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.68
Std Dev 3.445
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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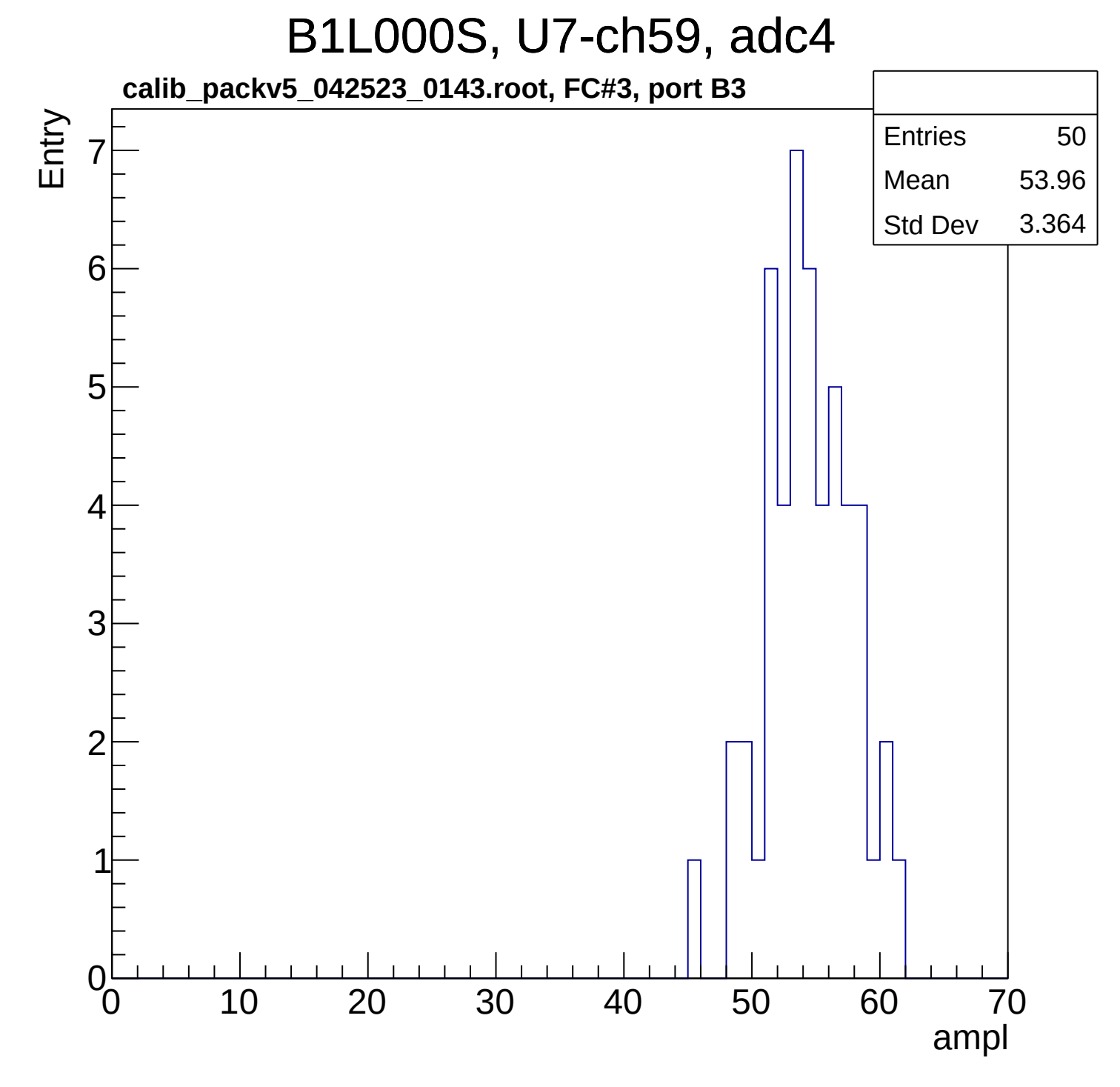
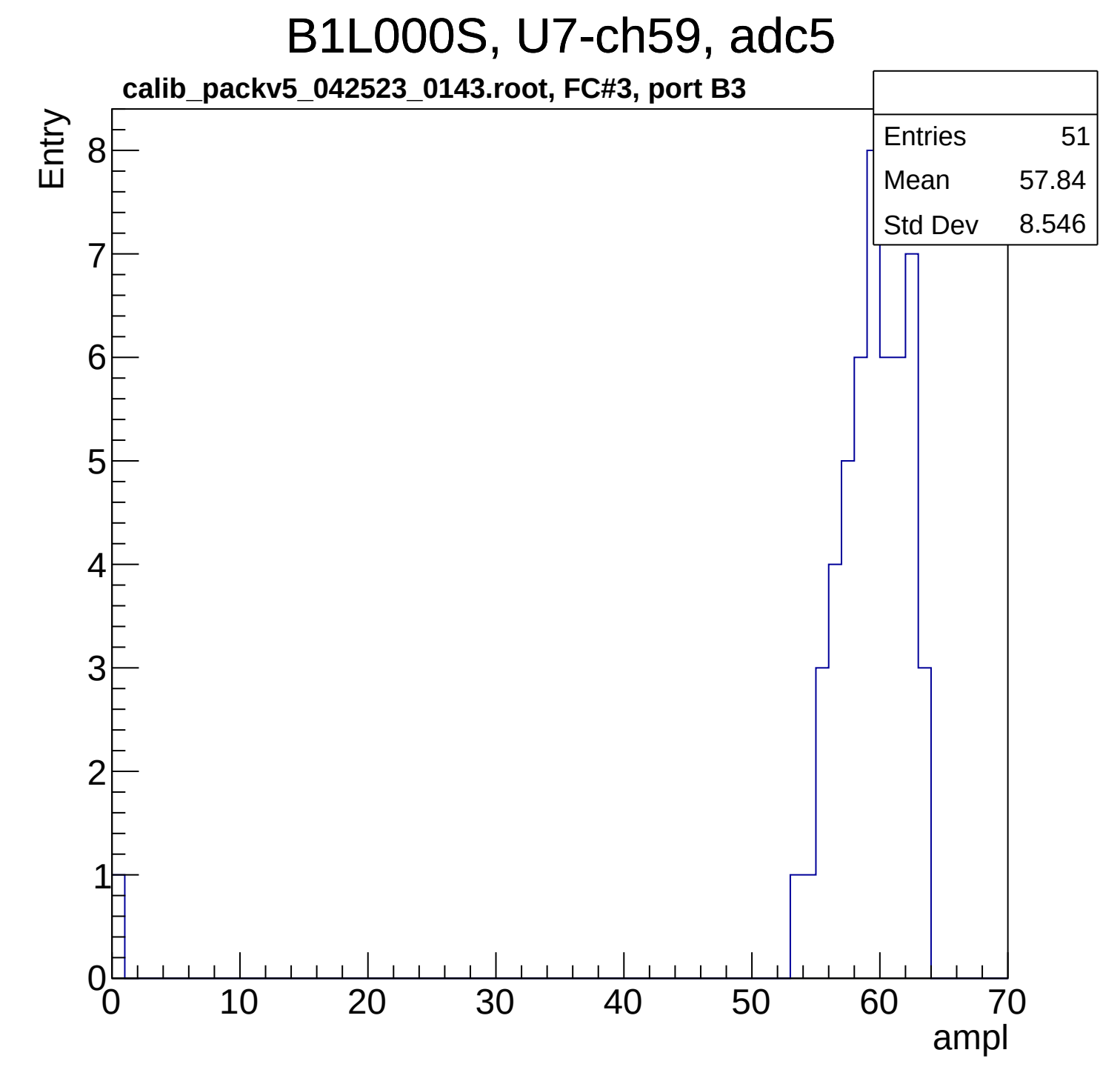
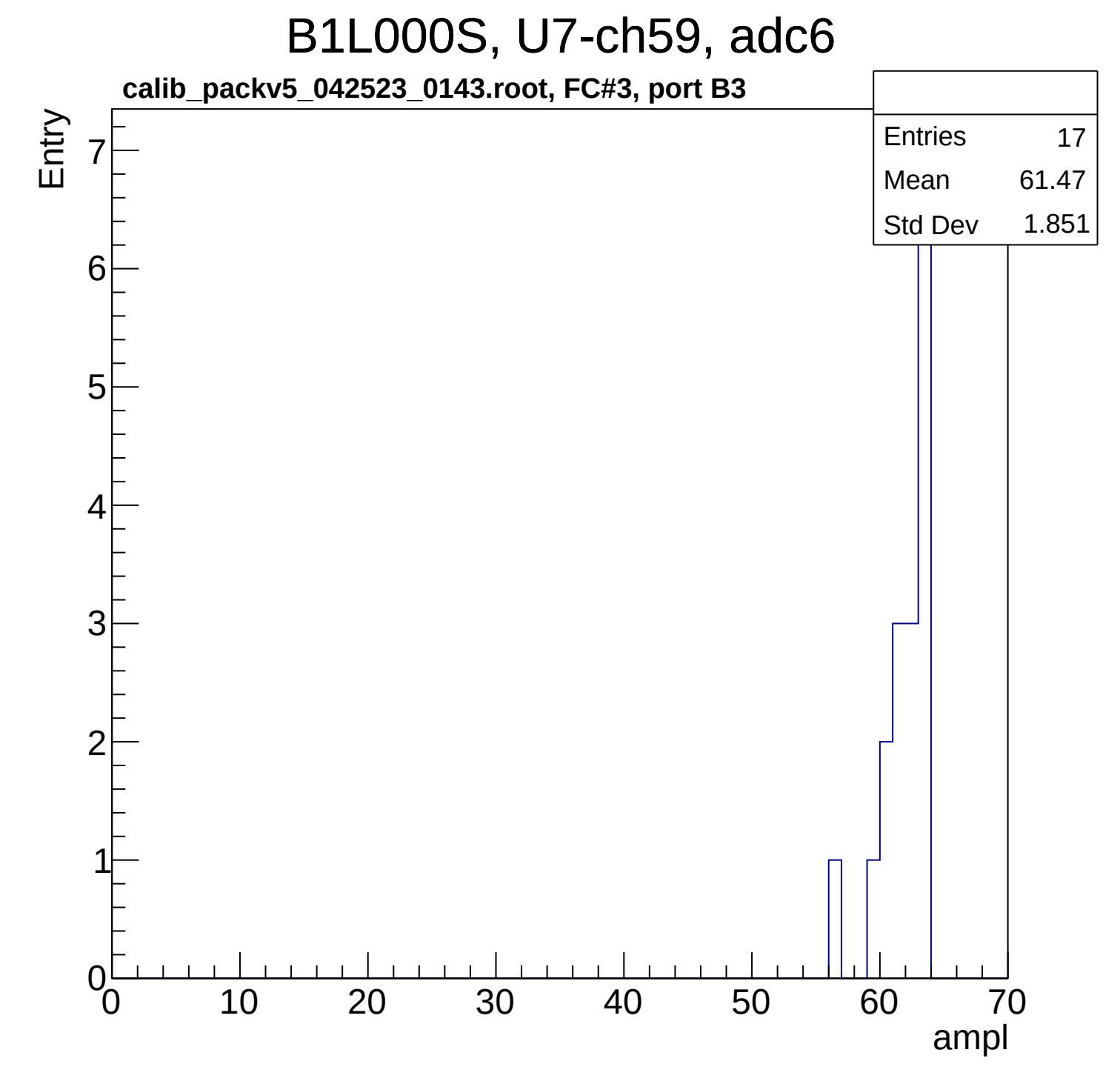
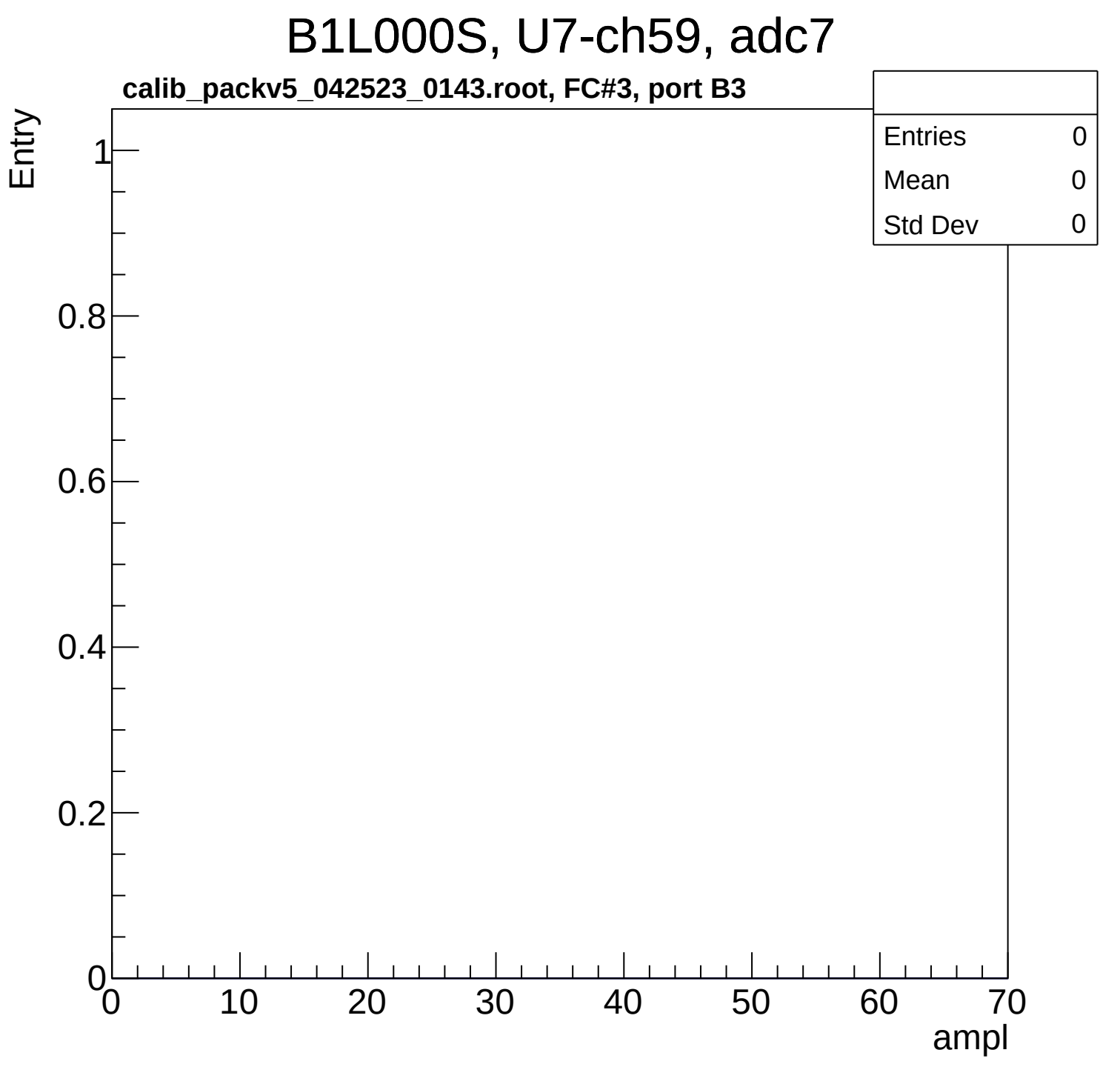
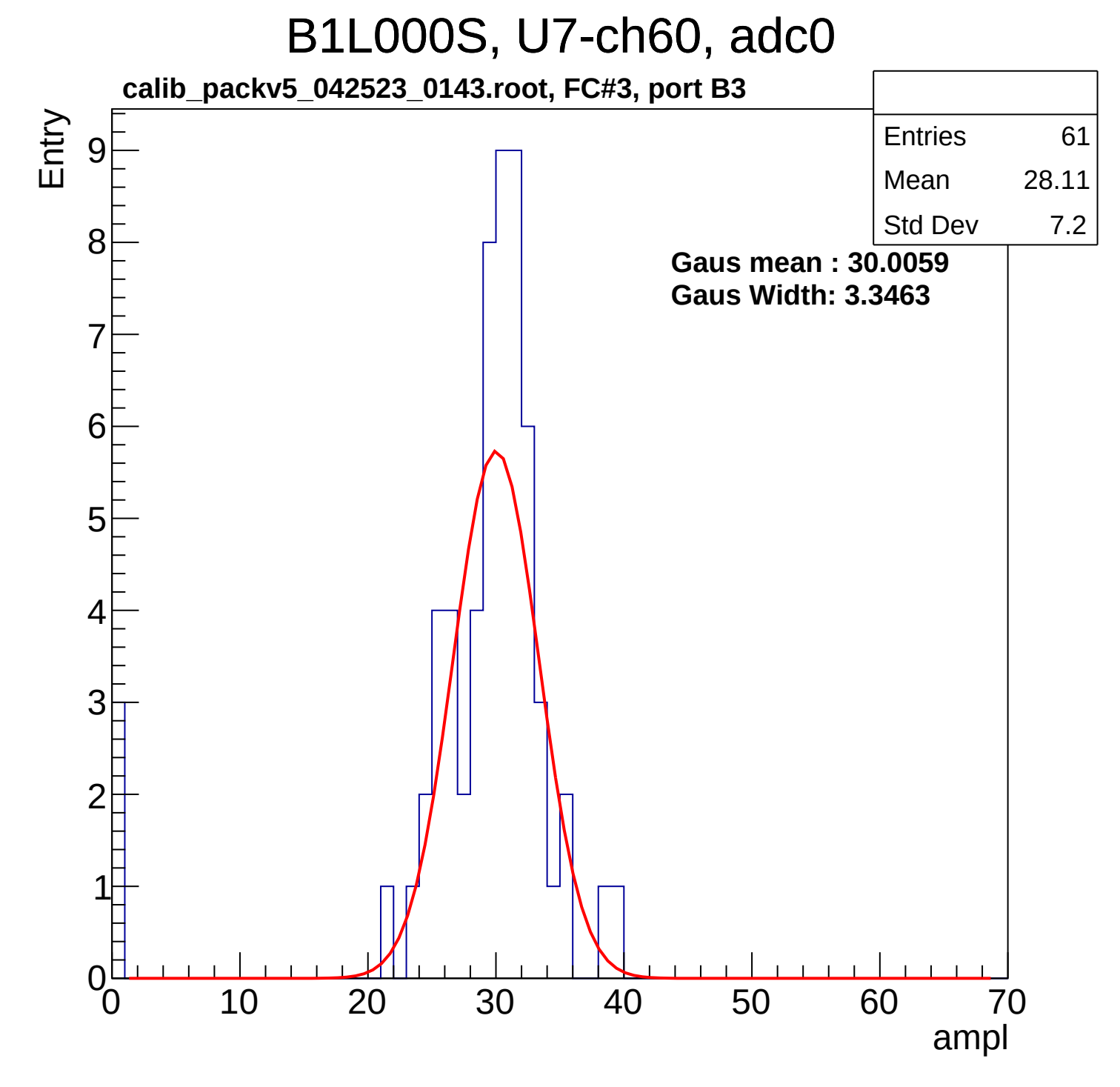

B1L000S, U7-ch59, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 53.96
Std Dev 3.364
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 57.84
Std Dev 8.546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.47
Std Dev 1.851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch59, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch60, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.11
Std Dev 7.2
Gaus mean : 30.0059
Gaus Width: 3.3463
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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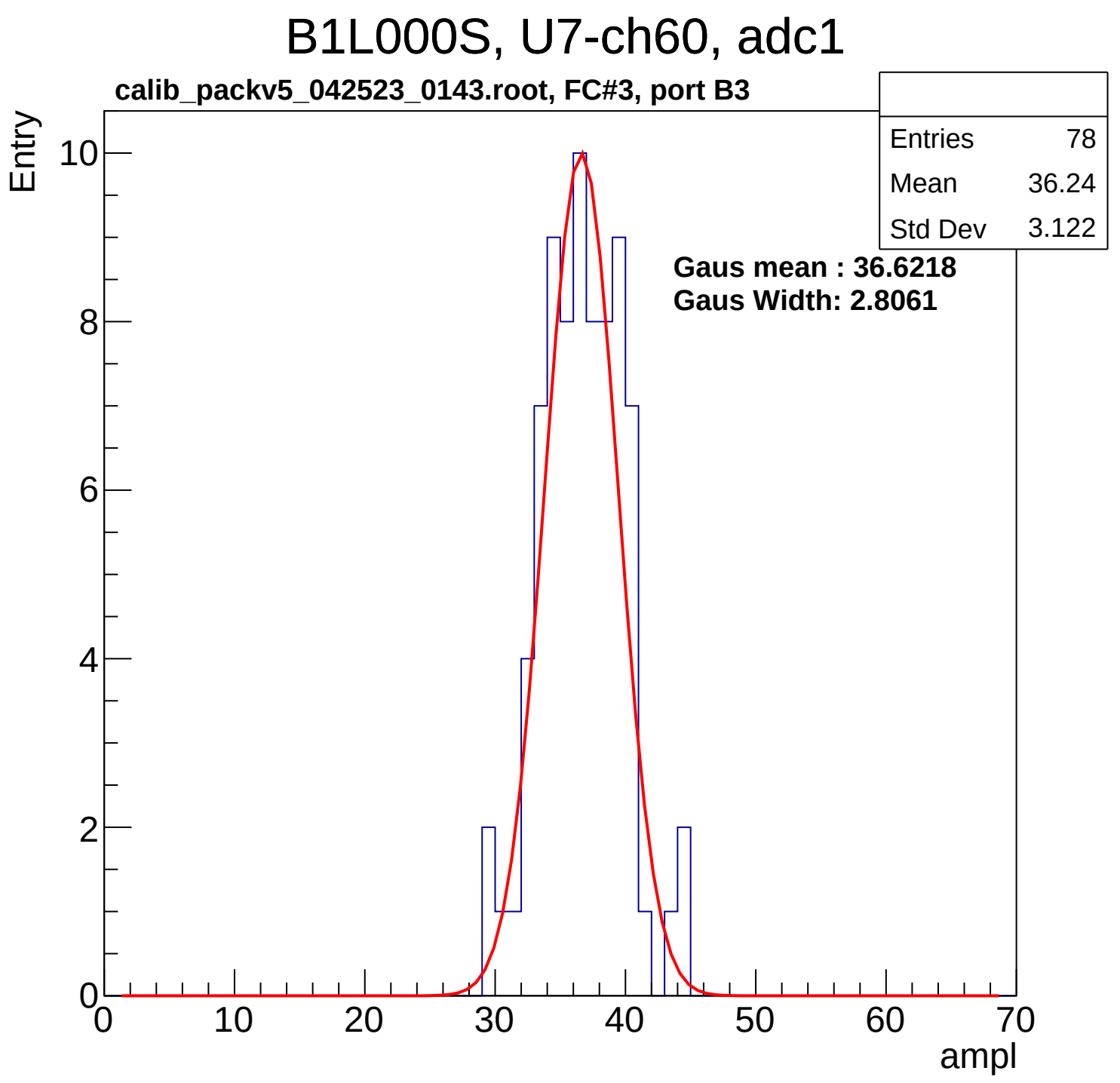
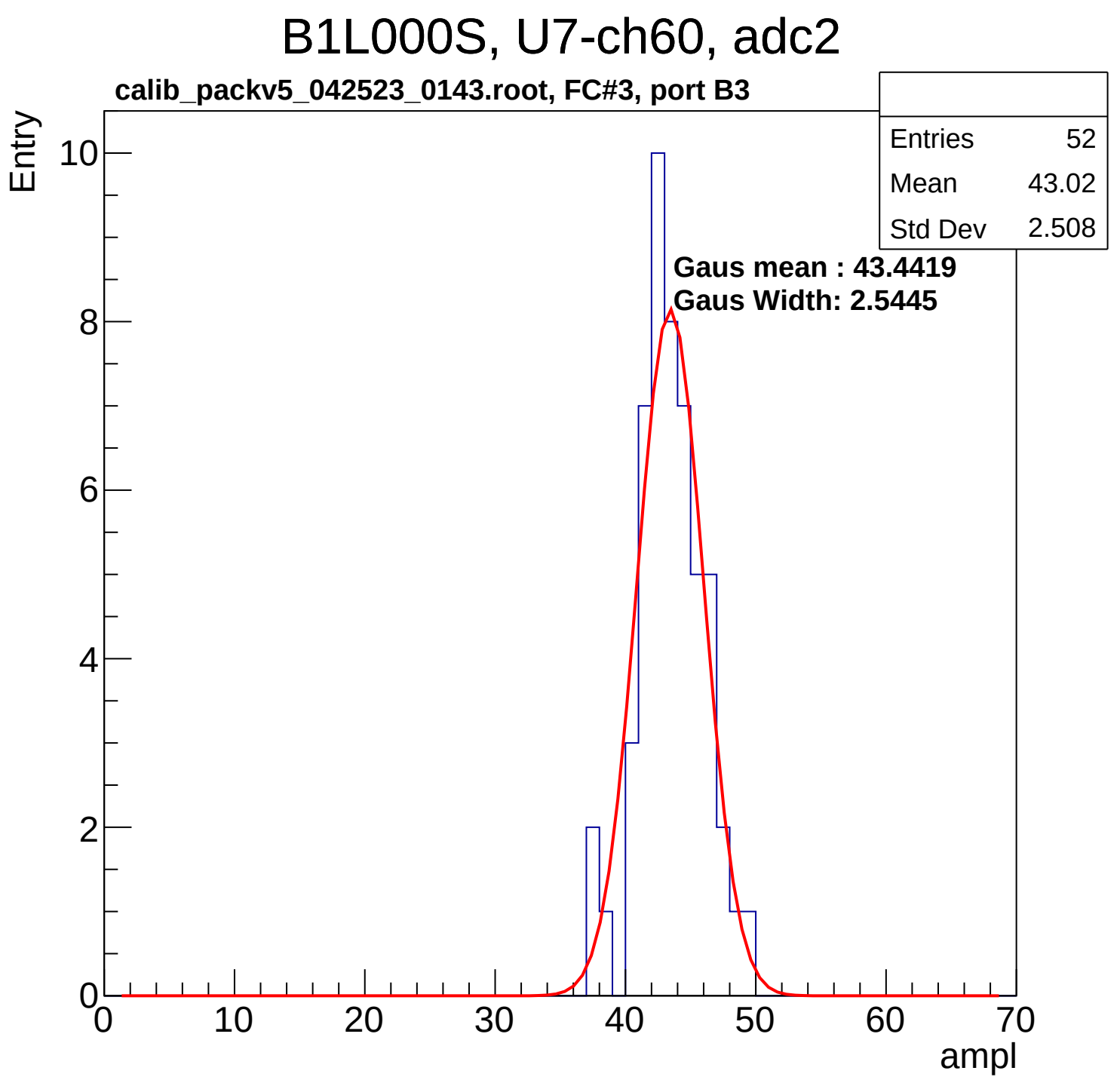
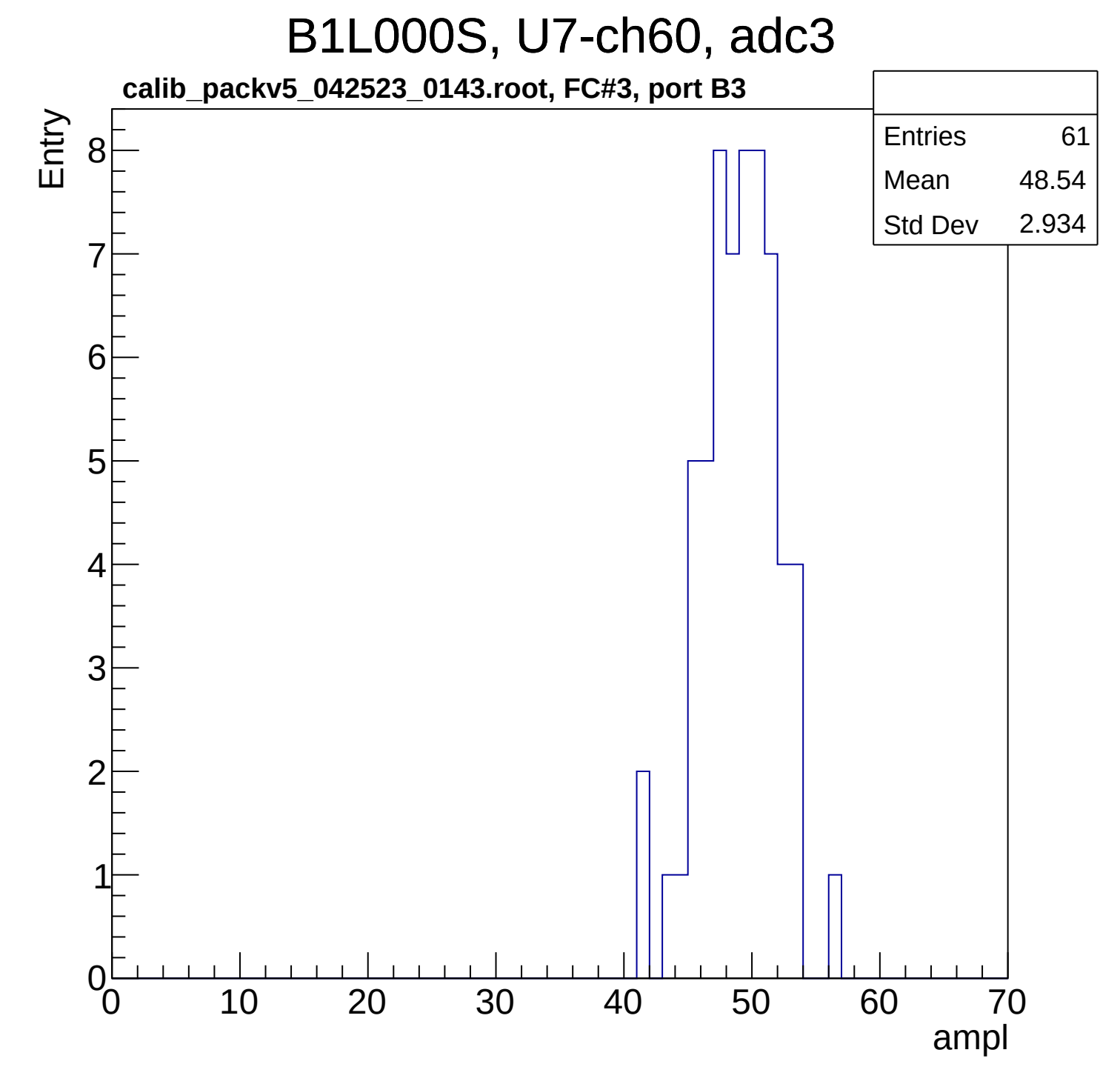

B1L000S, U7-ch60, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 36.24
Std Dev 3.122
Gaus mean : 36.6218
Gaus Width: 2.8061
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch60, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 43.02
Std Dev 2.508
Gaus mean : 43.4419
Gaus Width: 2.5445
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch60, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 48.54
Std Dev 2.934
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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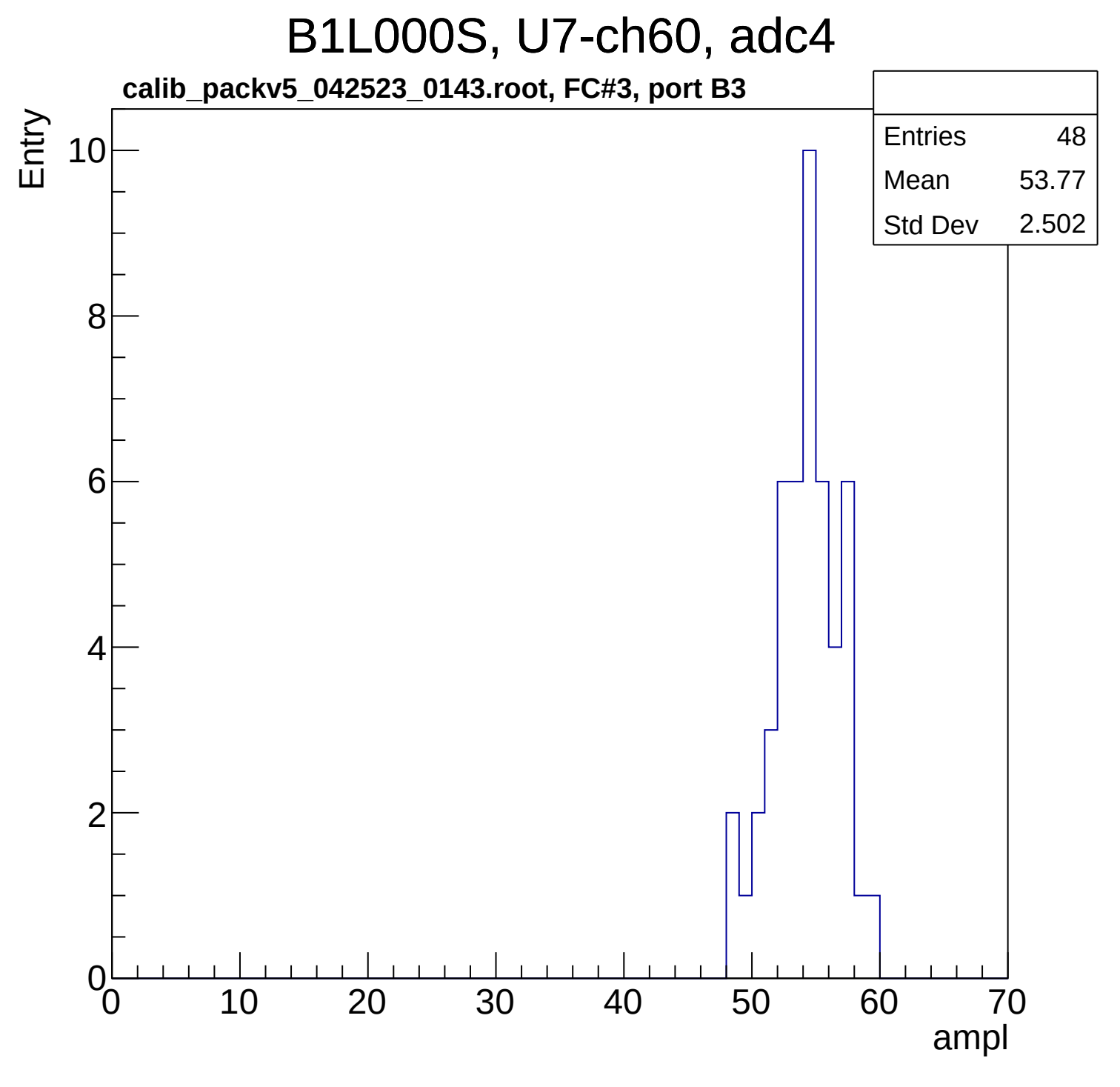
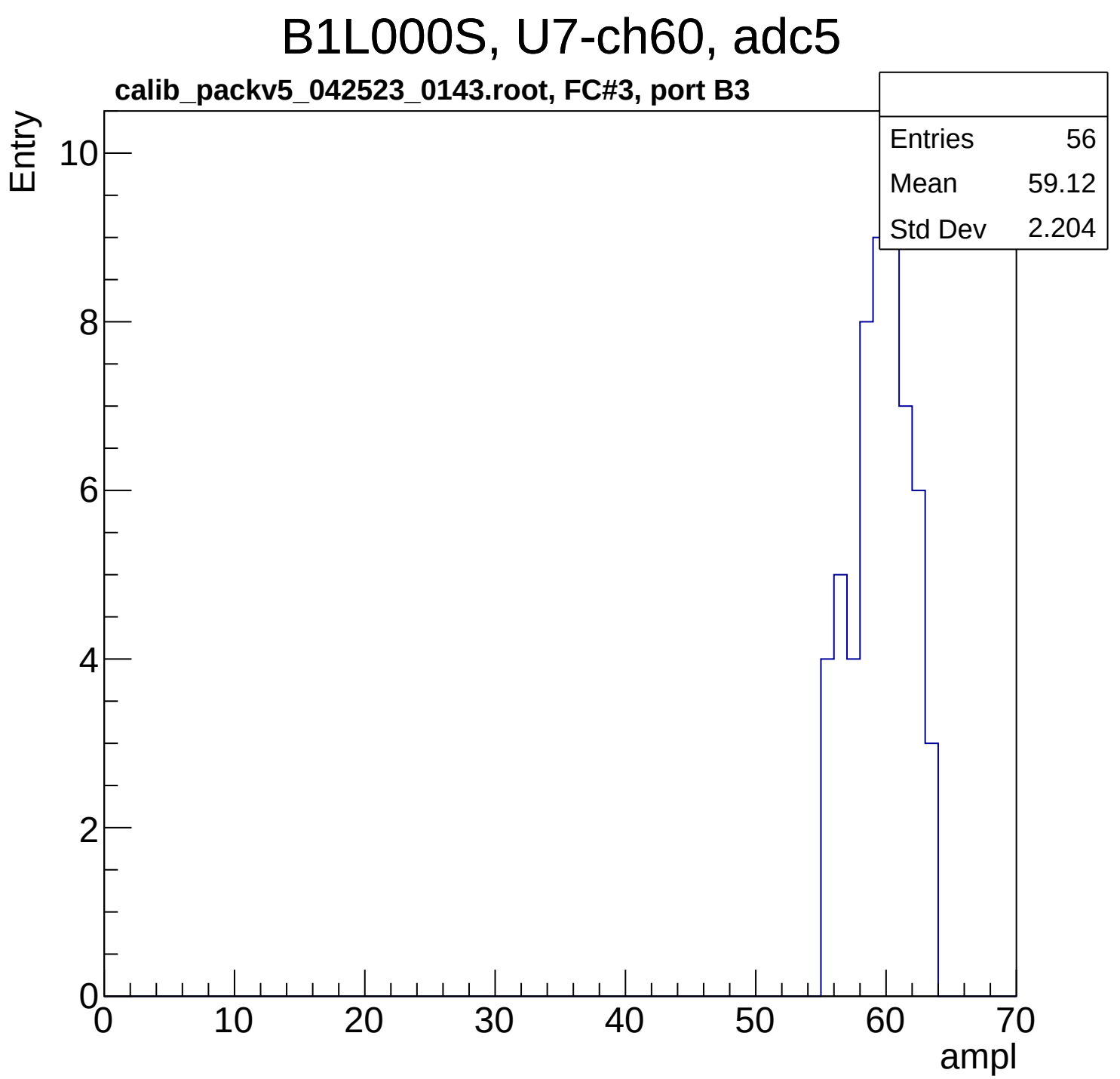
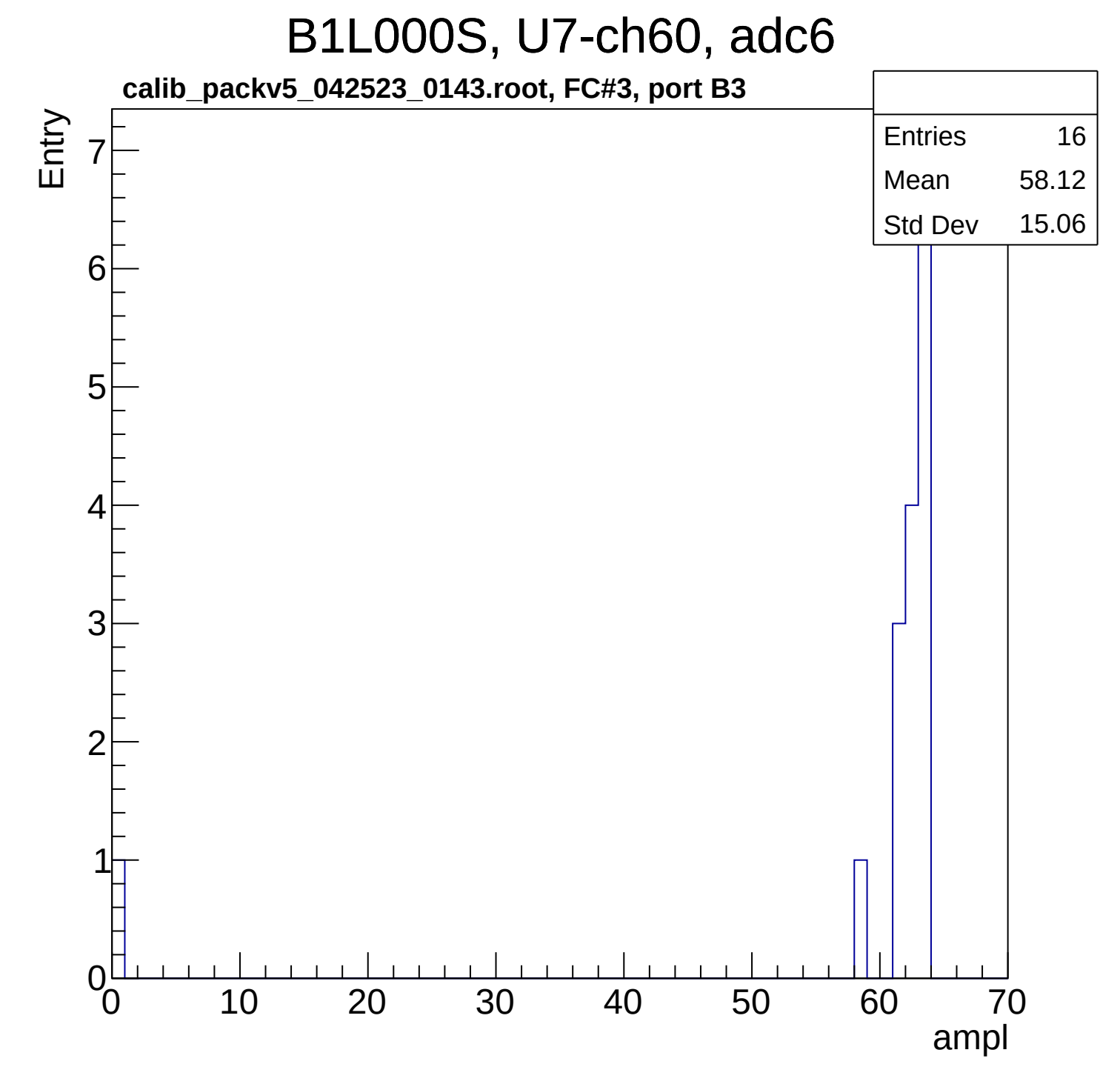
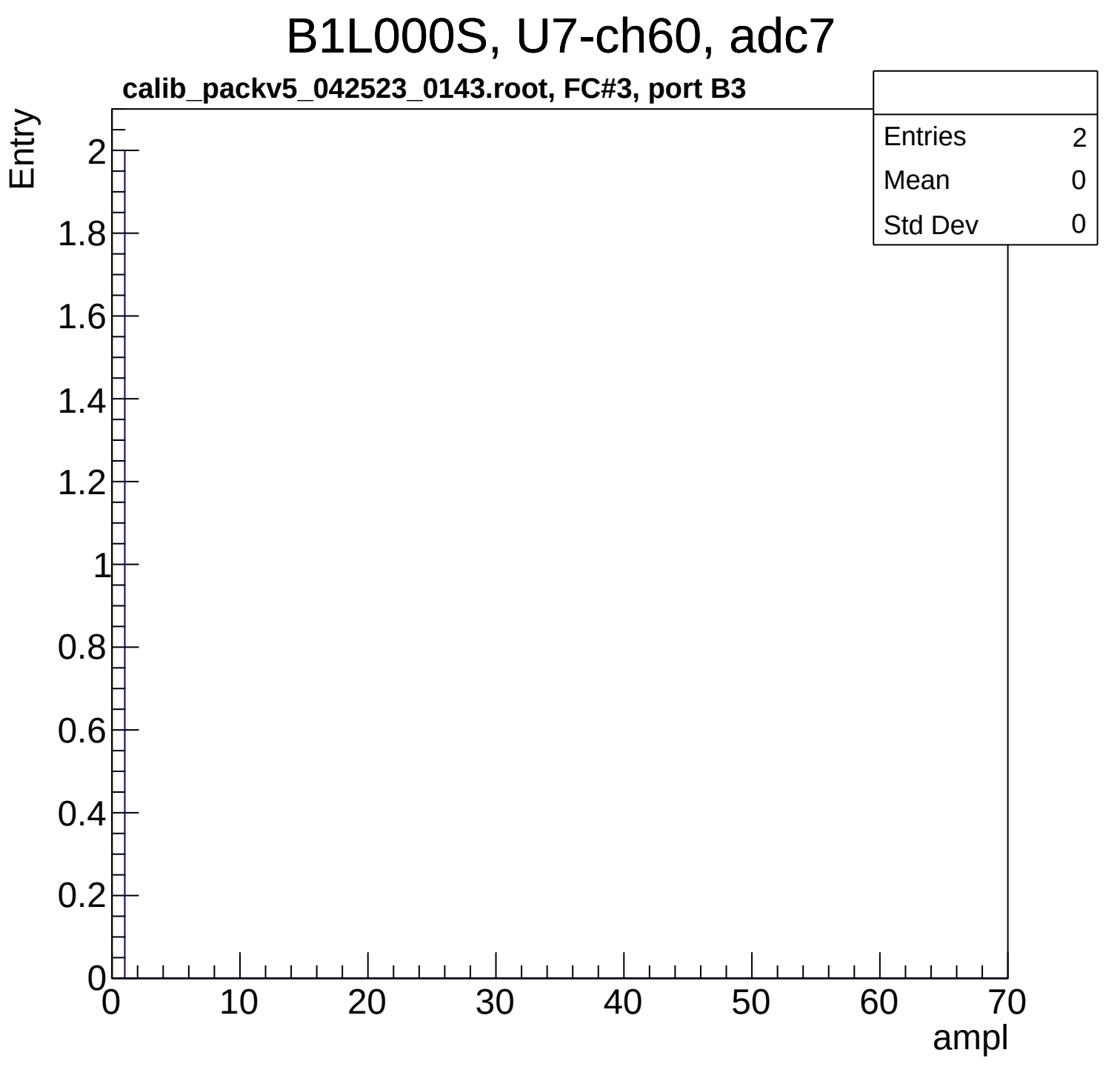
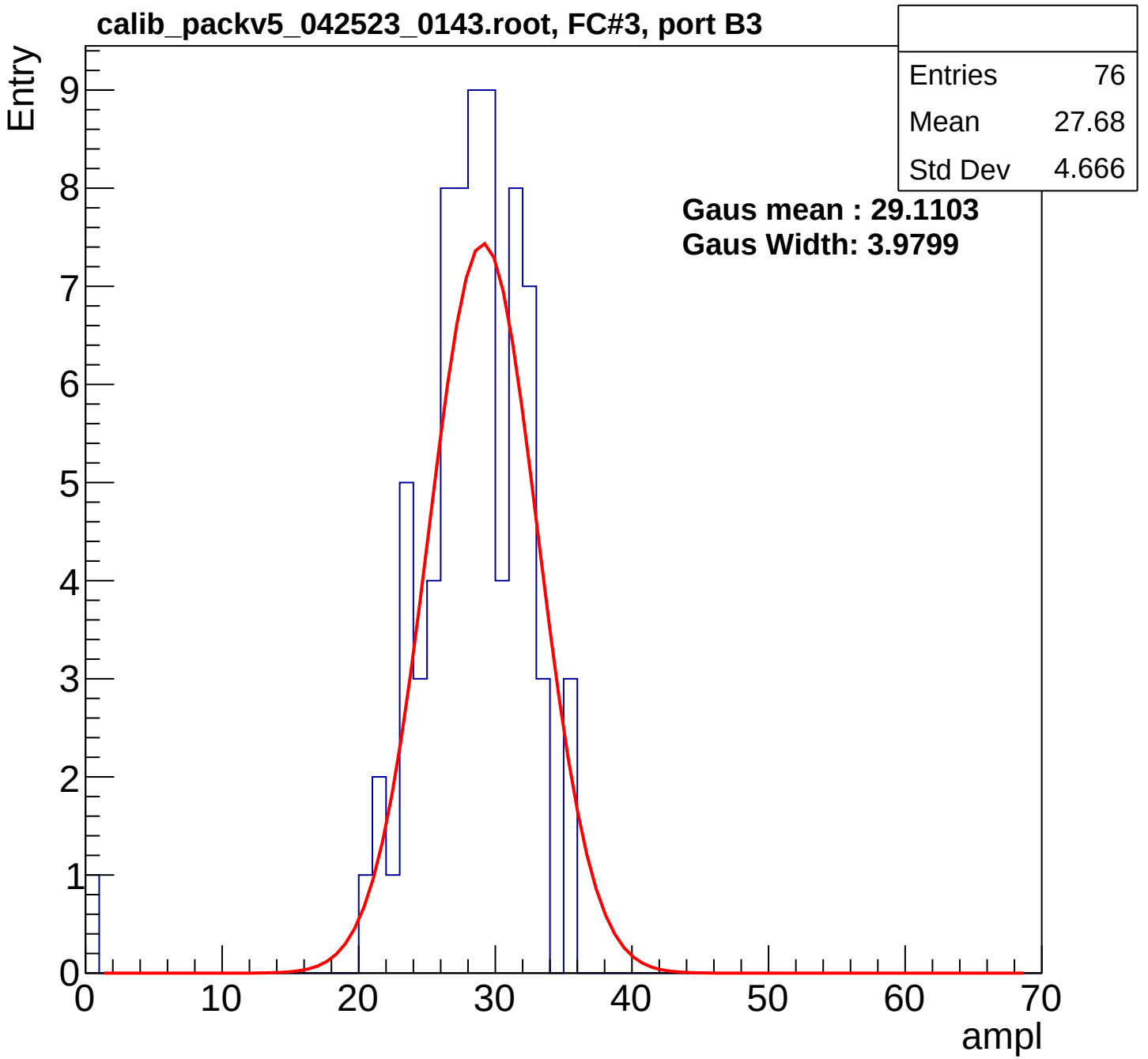
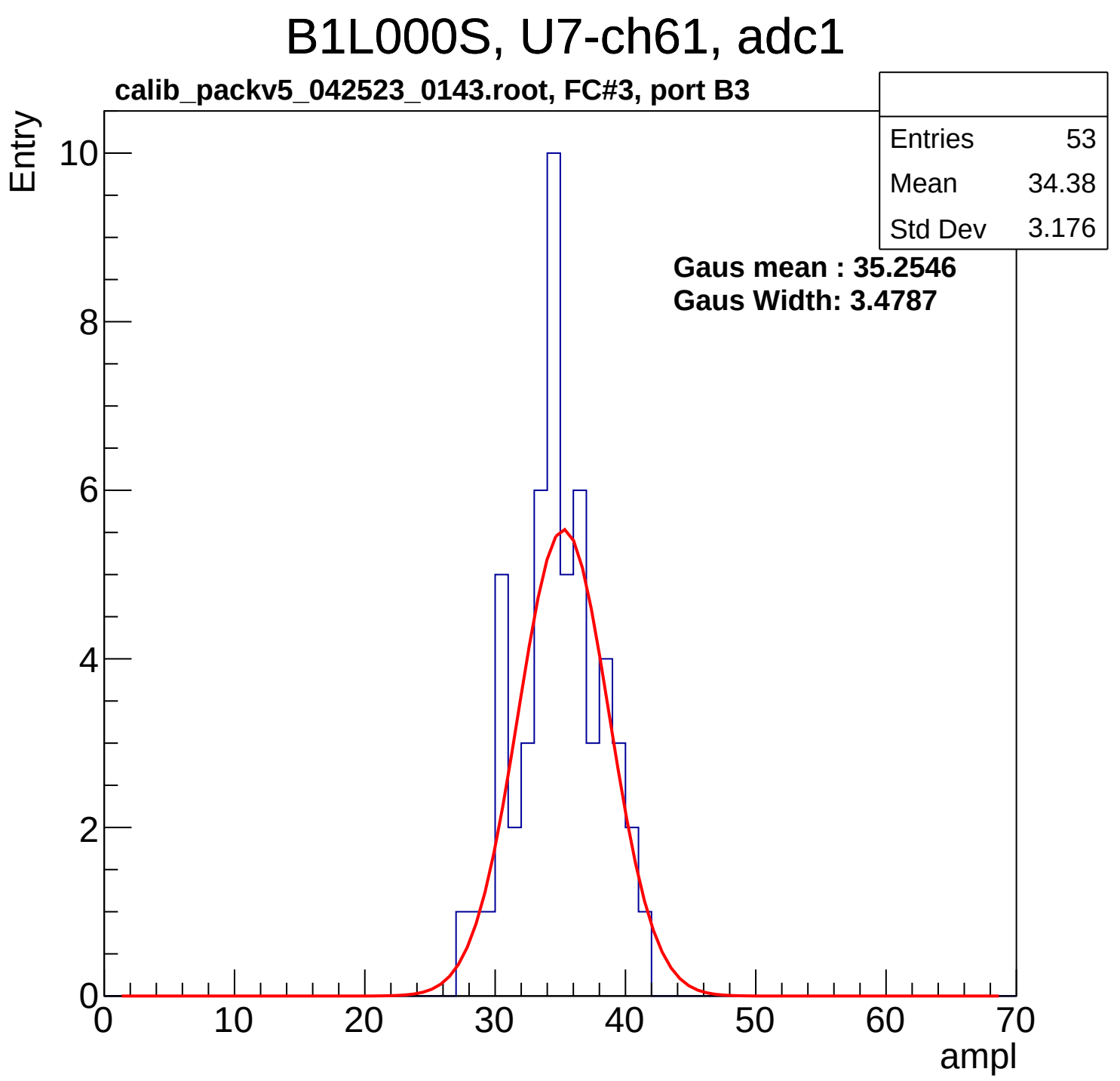

B1L000S, U7-ch60, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.77
Std Dev 2.502
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

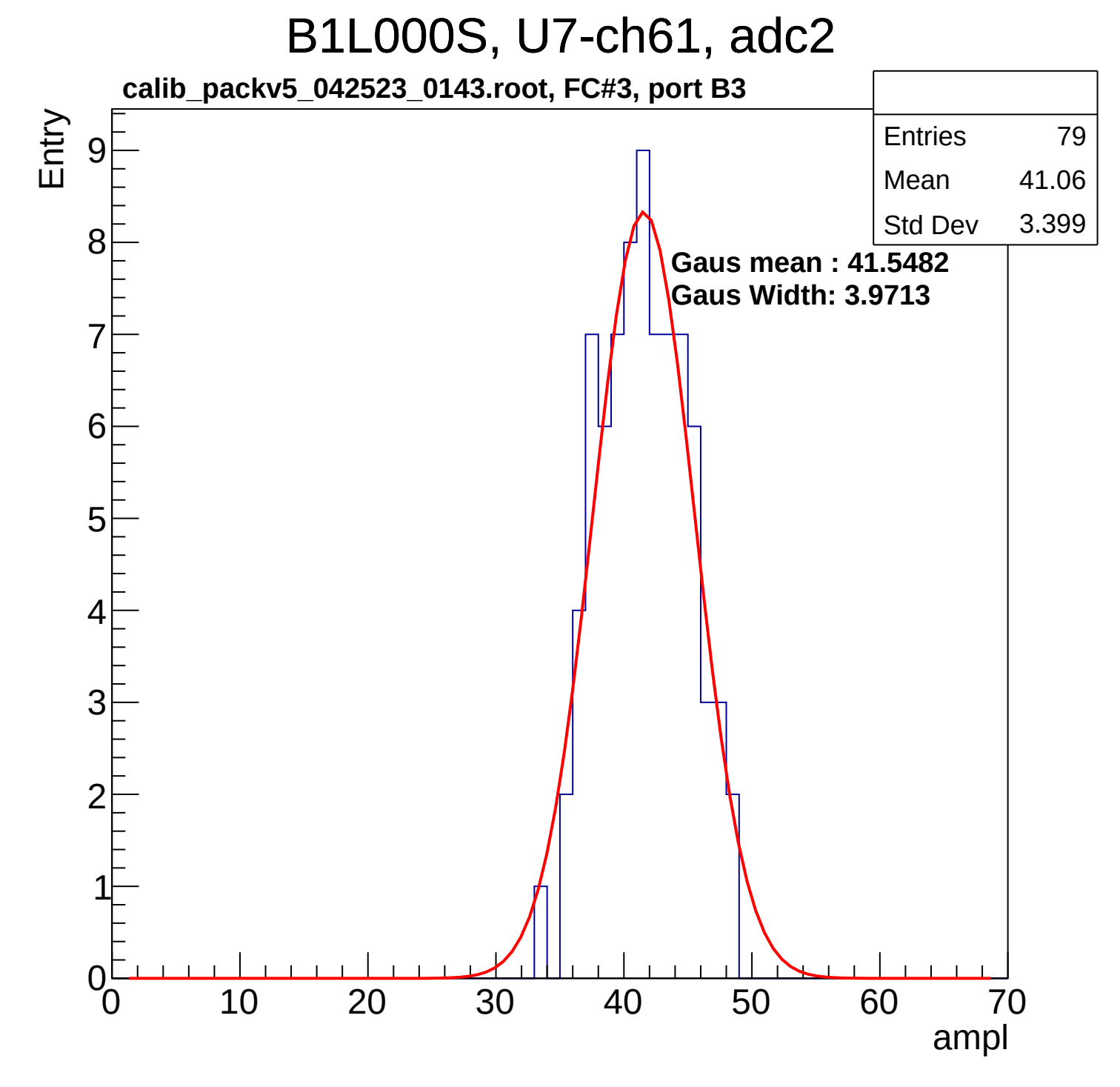

B1L000S, U7-ch60, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 59.12
Std Dev 2.204
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch60, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 58.12
Std Dev 15.06
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch60, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch61, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 27.68
Std Dev 4.666
Gaus mean : 29.1103
Gaus Width: 3.9799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch61, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 34.38
Std Dev 3.176
Gaus mean : 35.2546
Gaus Width: 3.4787
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch61, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 41.06
Std Dev 3.399
Gaus mean : 41.5482
Gaus Width: 3.9713
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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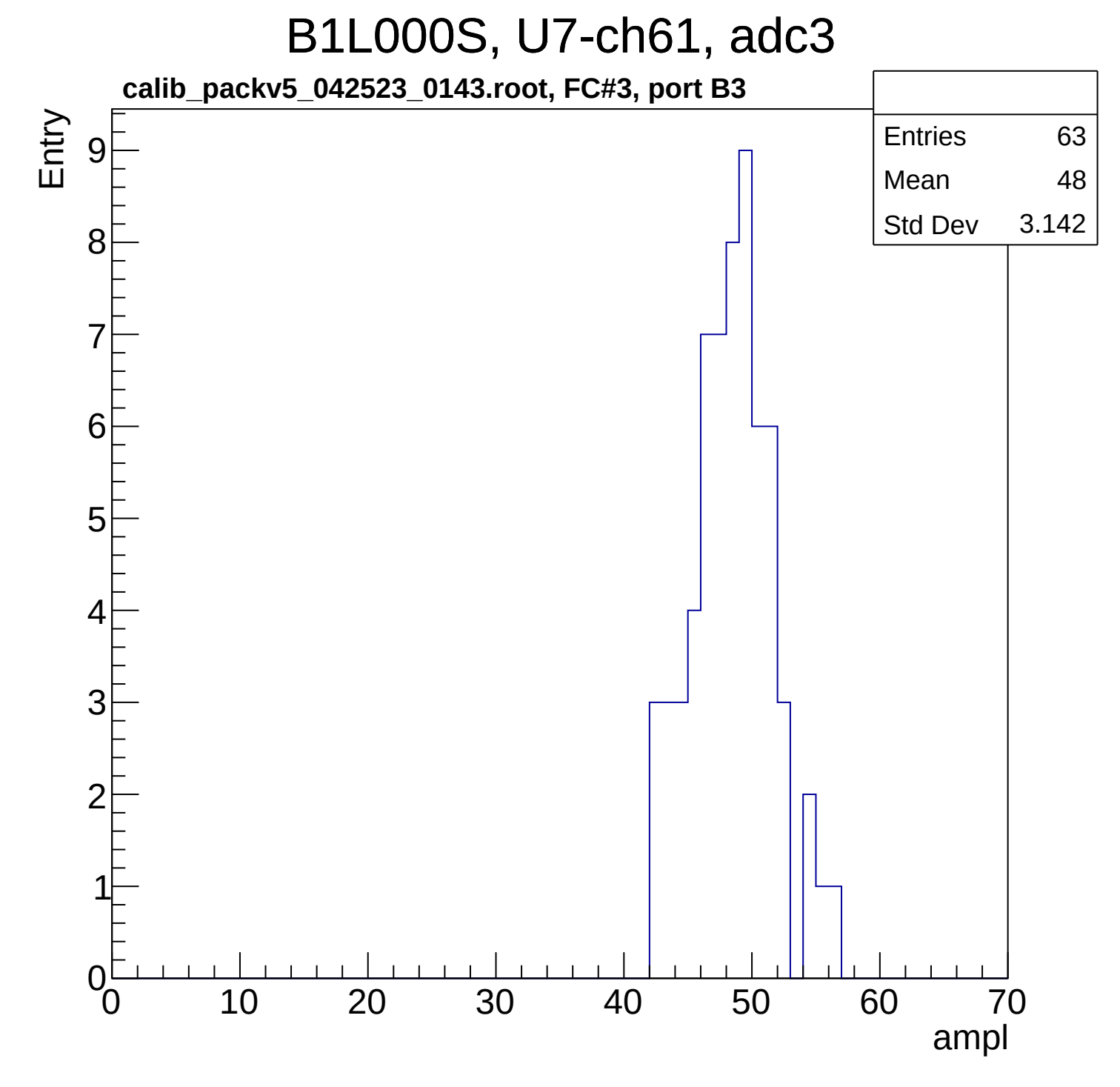
40
50
60
70


B1L000S, U7-ch61, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 48
Std Dev 3.142
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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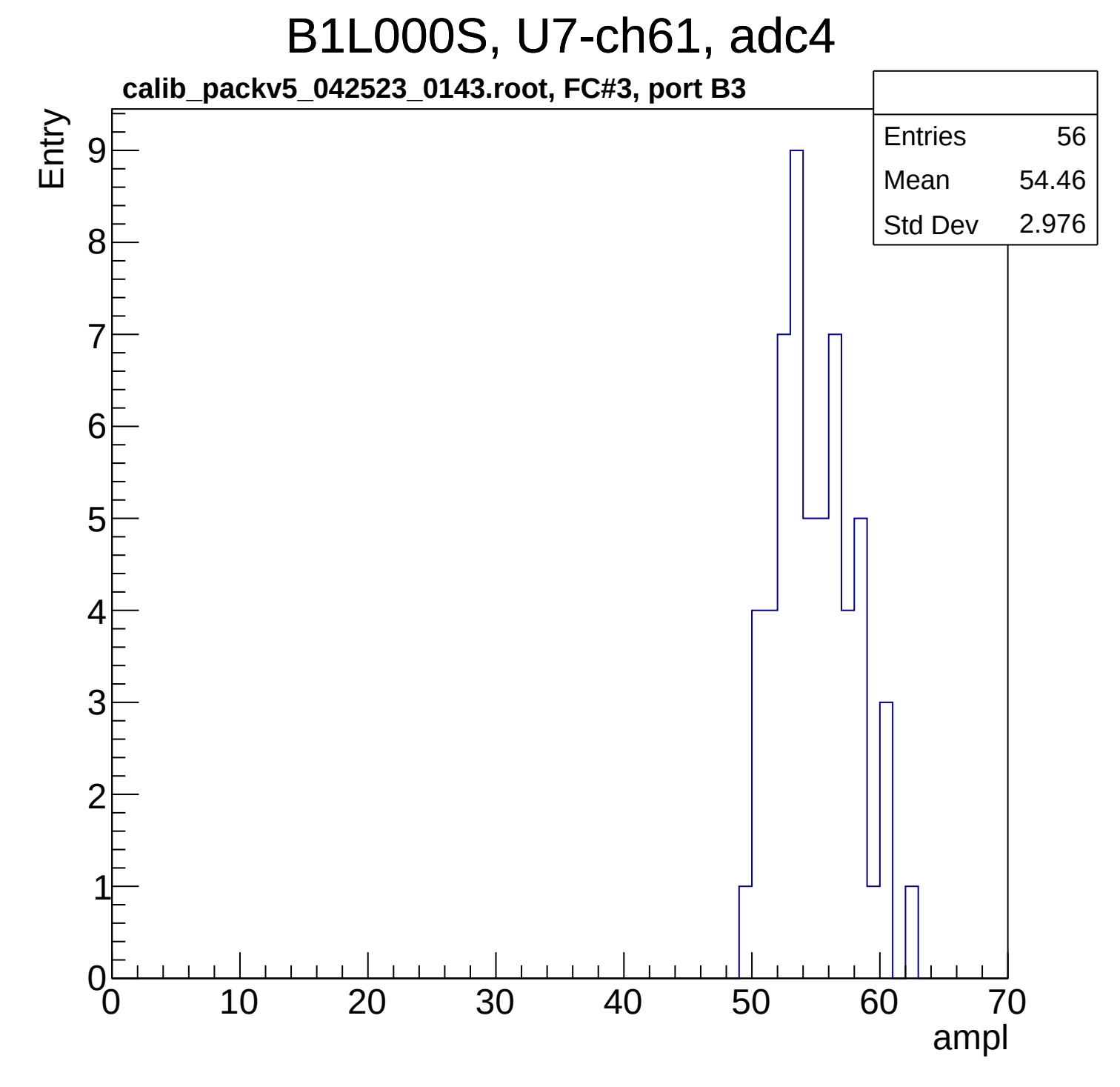
B1L000S, U7-ch61, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 54.46
Std Dev 2.976
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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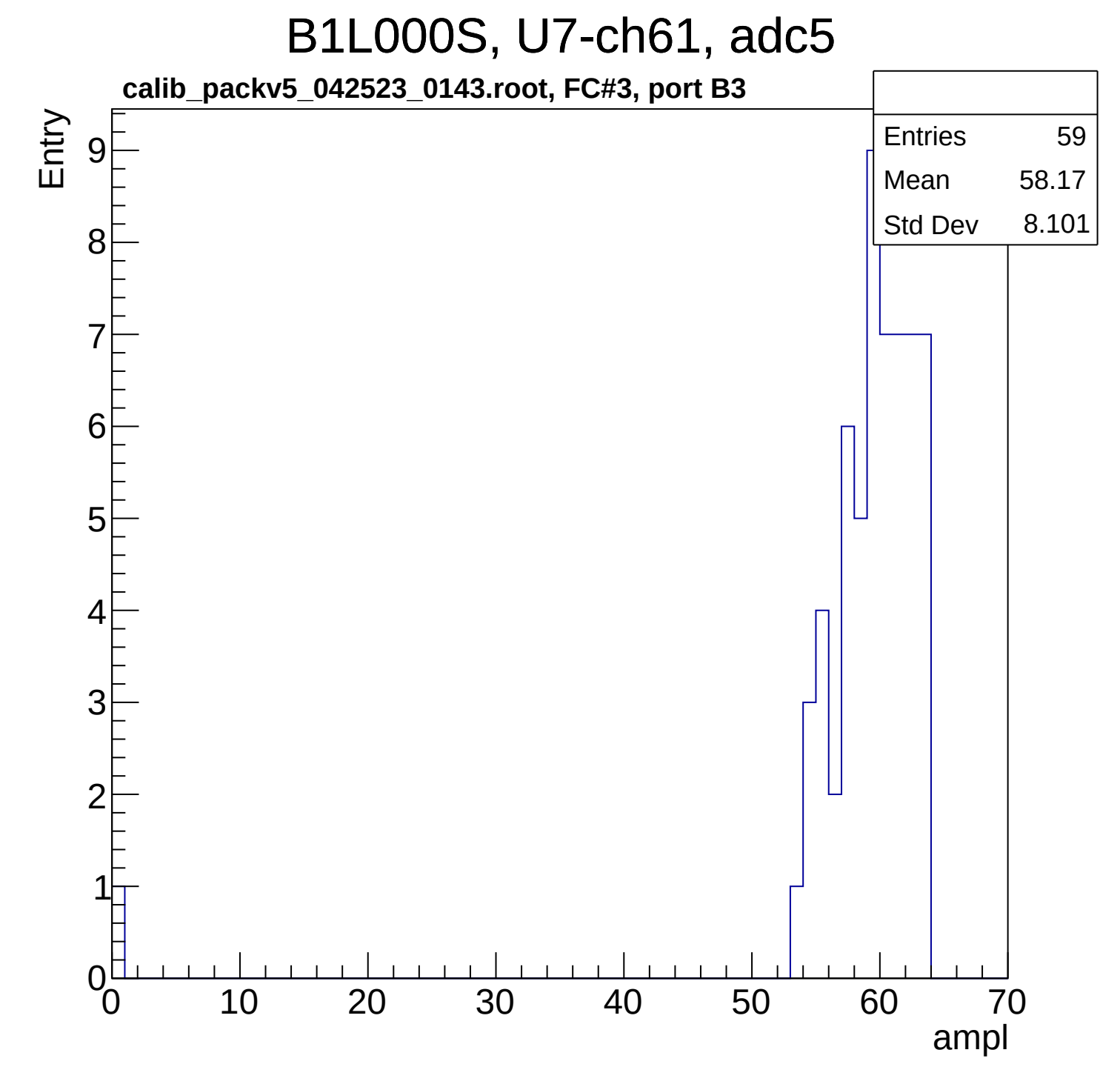
B1L000S, U7-ch61, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.17
Std Dev 8.101
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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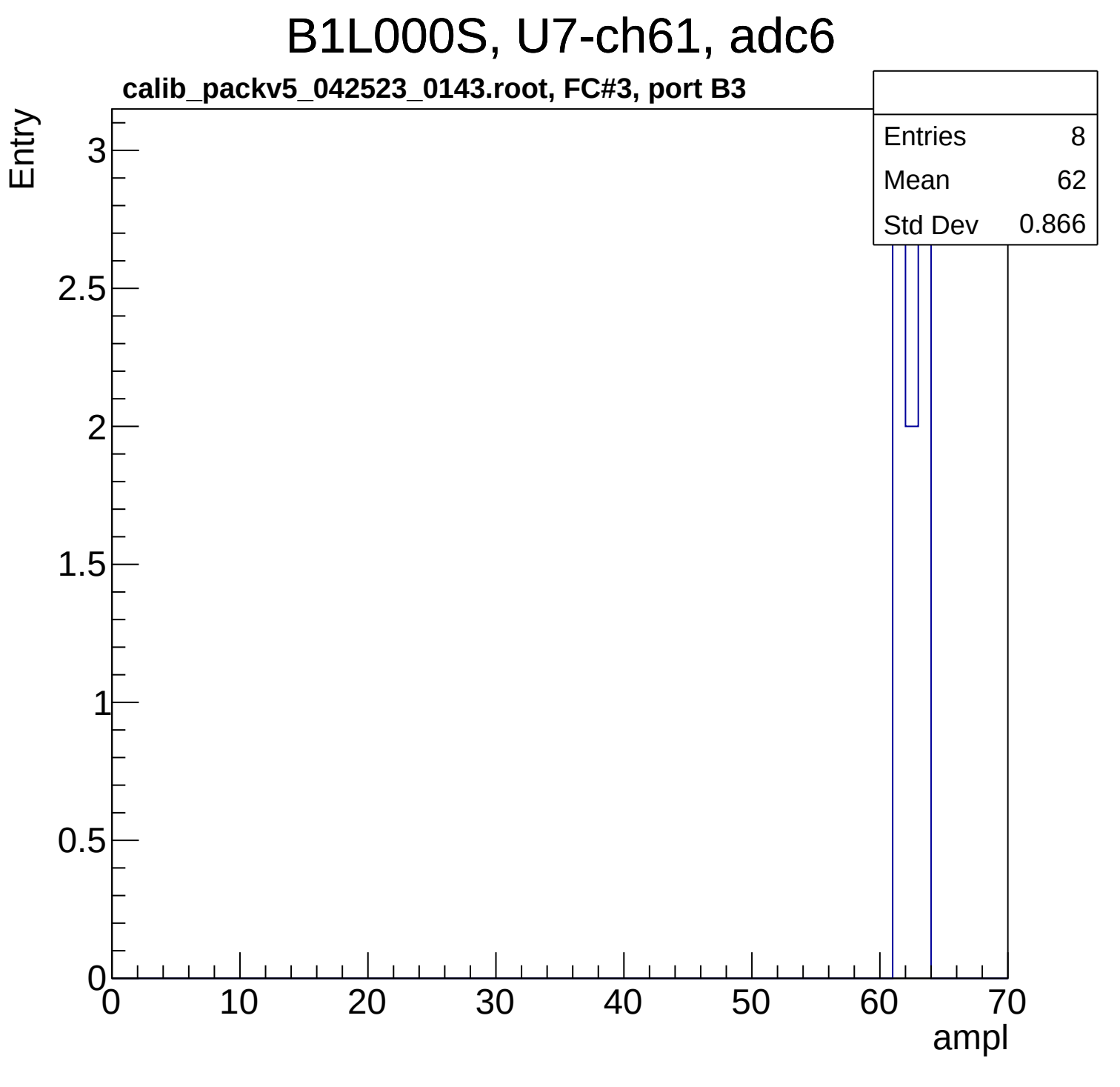
B1L000S, U7-ch61, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62
Std Dev 0.866
Entry
ampl
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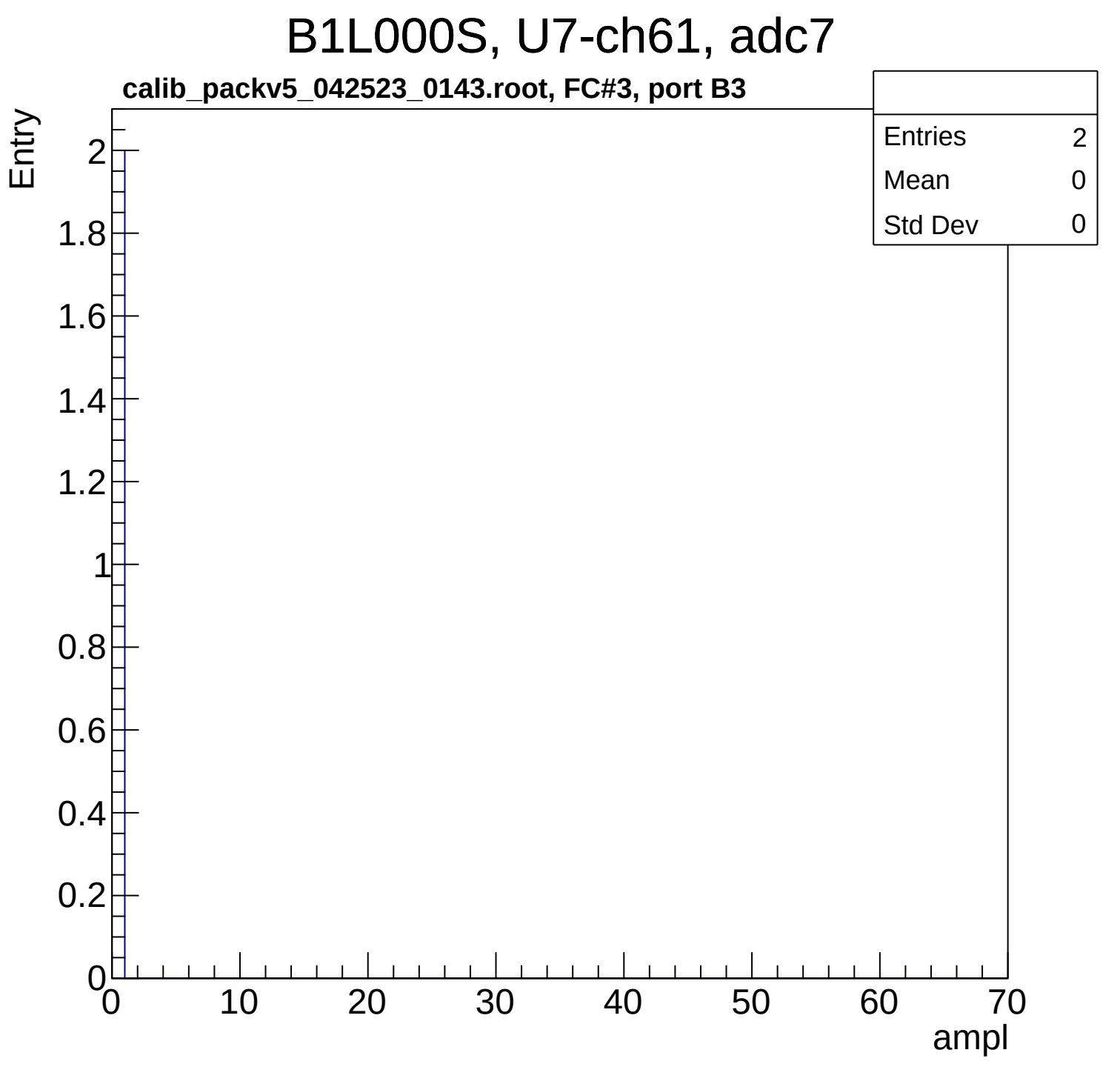
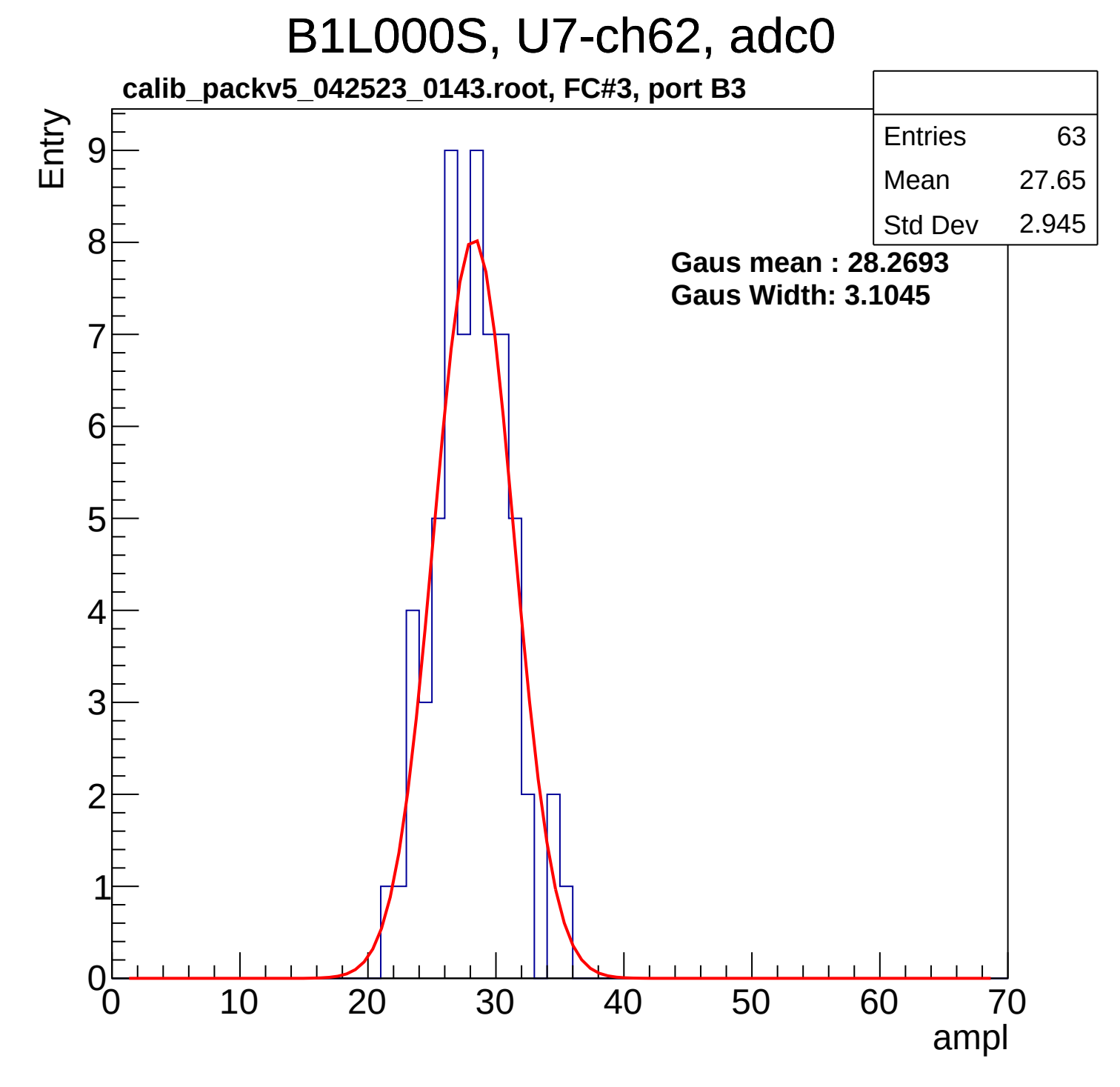
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch61, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch62, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 27.65
Std Dev 2.945
Gaus mean : 28.2693
Gaus Width: 3.1045
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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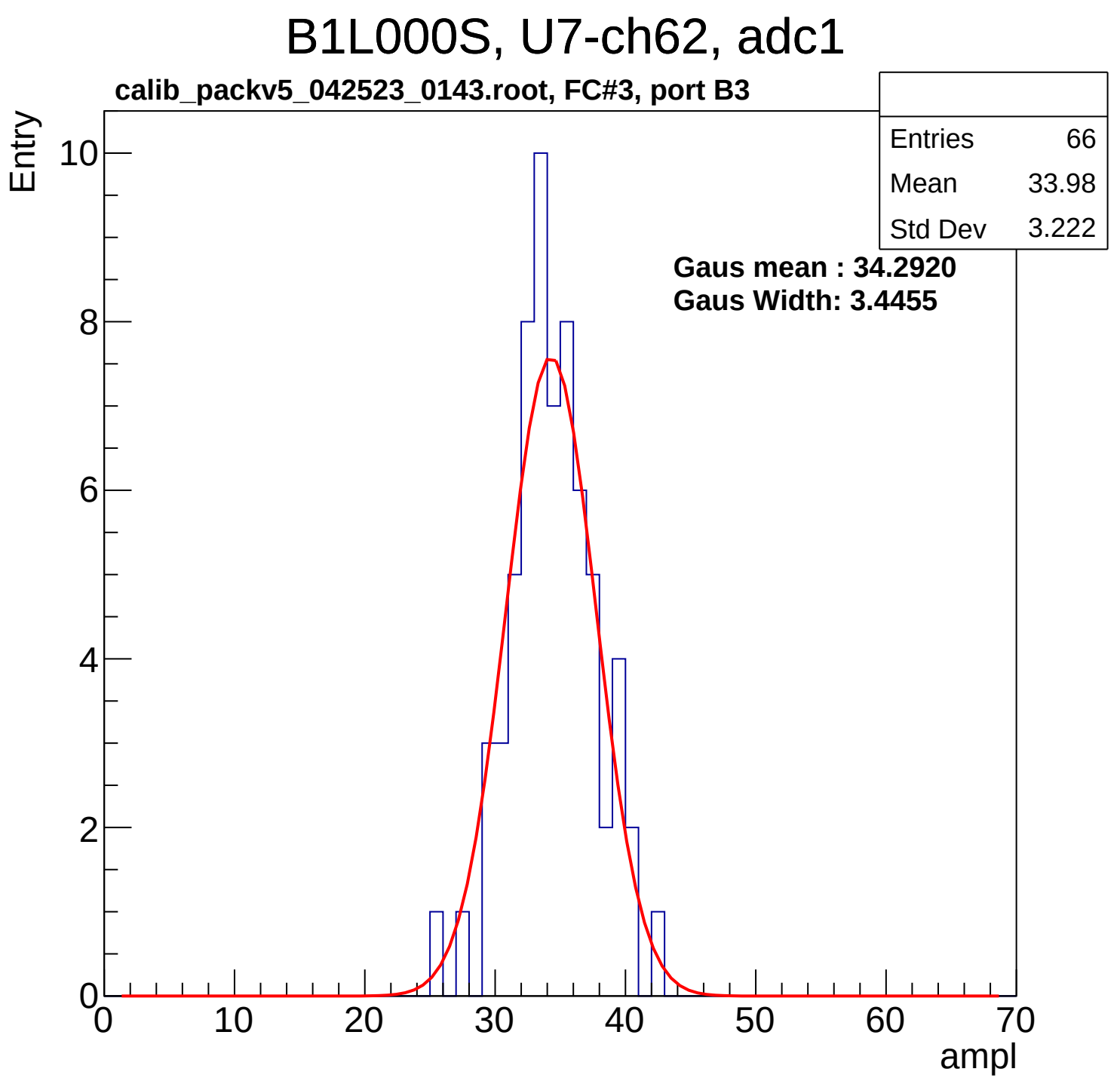
B1L000S, U7-ch62, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 33.98
Std Dev 3.222
Gaus mean : 34.2920
Gaus Width: 3.4455
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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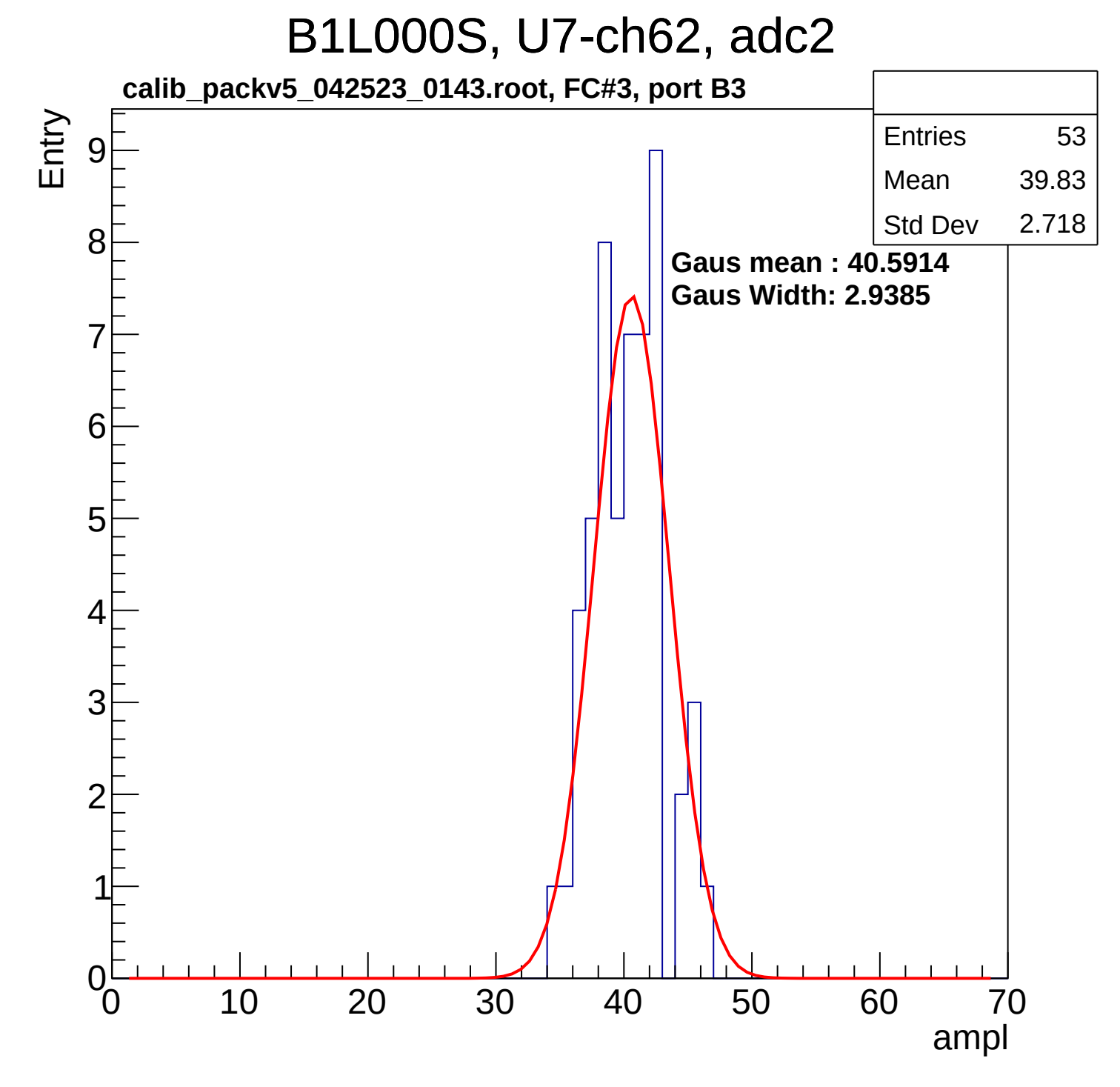
B1L000S, U7-ch62, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 39.83
Std Dev 2.718
Gaus mean : 40.5914
Gaus Width: 2.9385
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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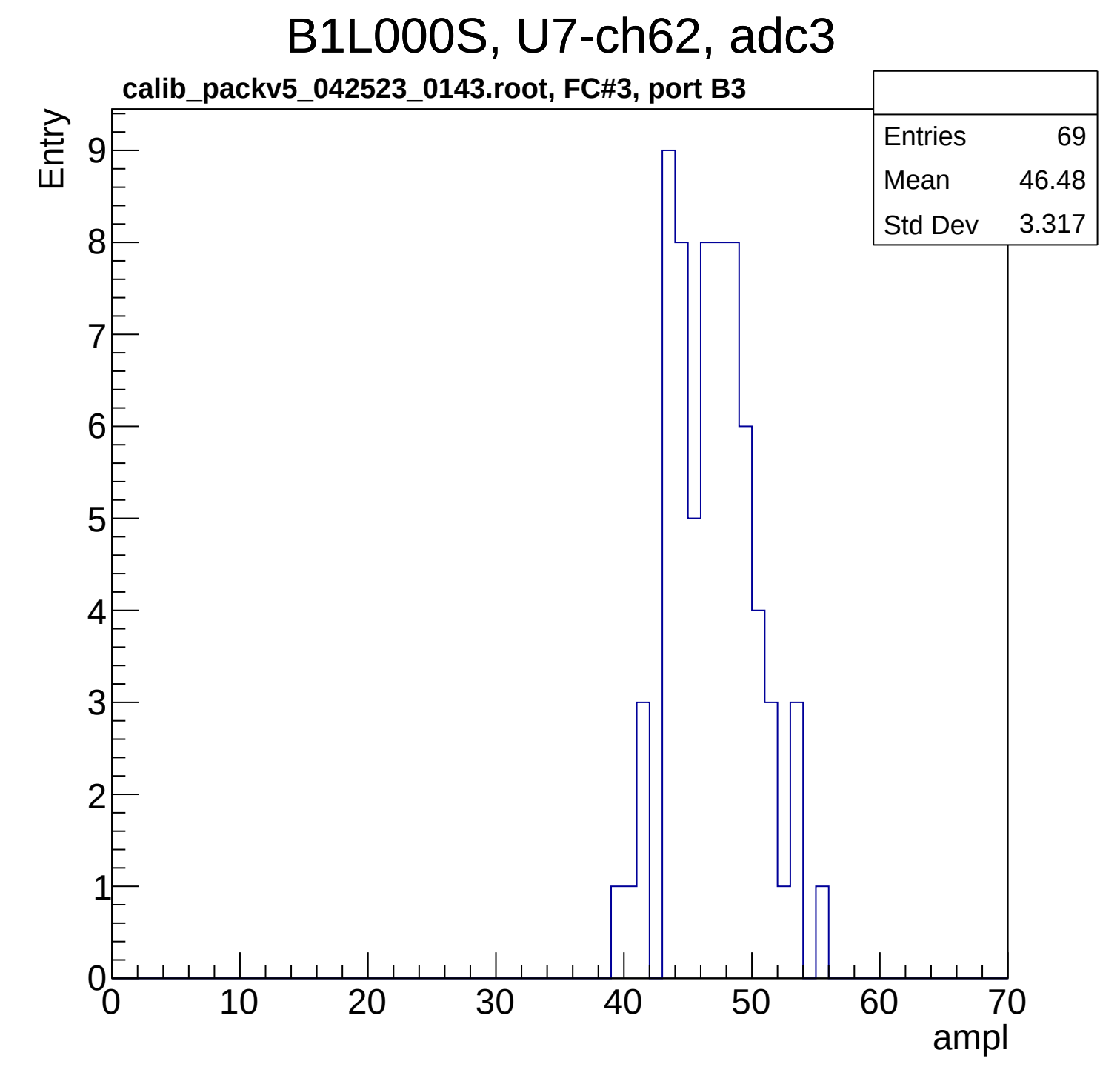
B1L000S, U7-ch62, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 46.48
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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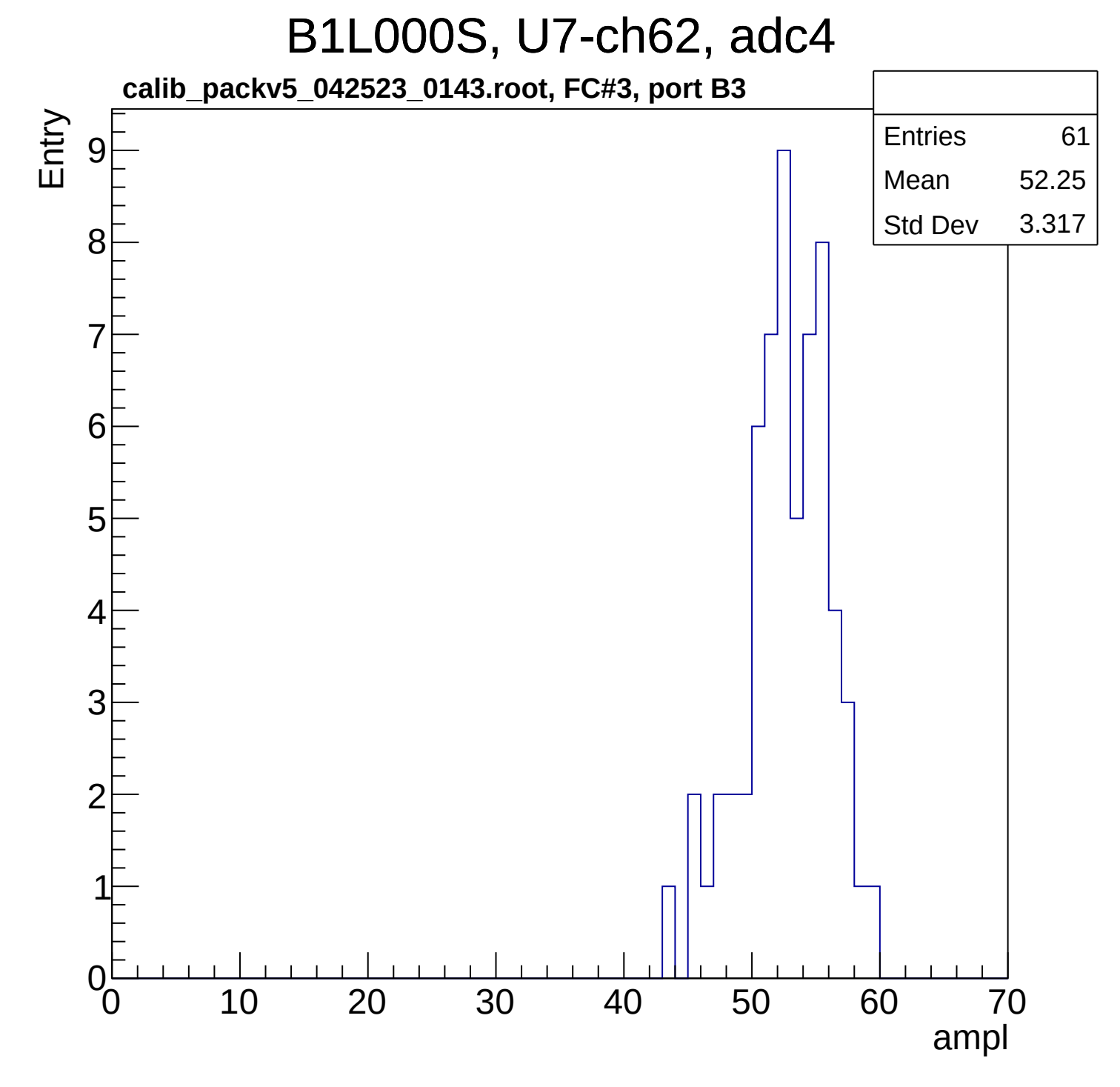
B1L000S, U7-ch62, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 52.25
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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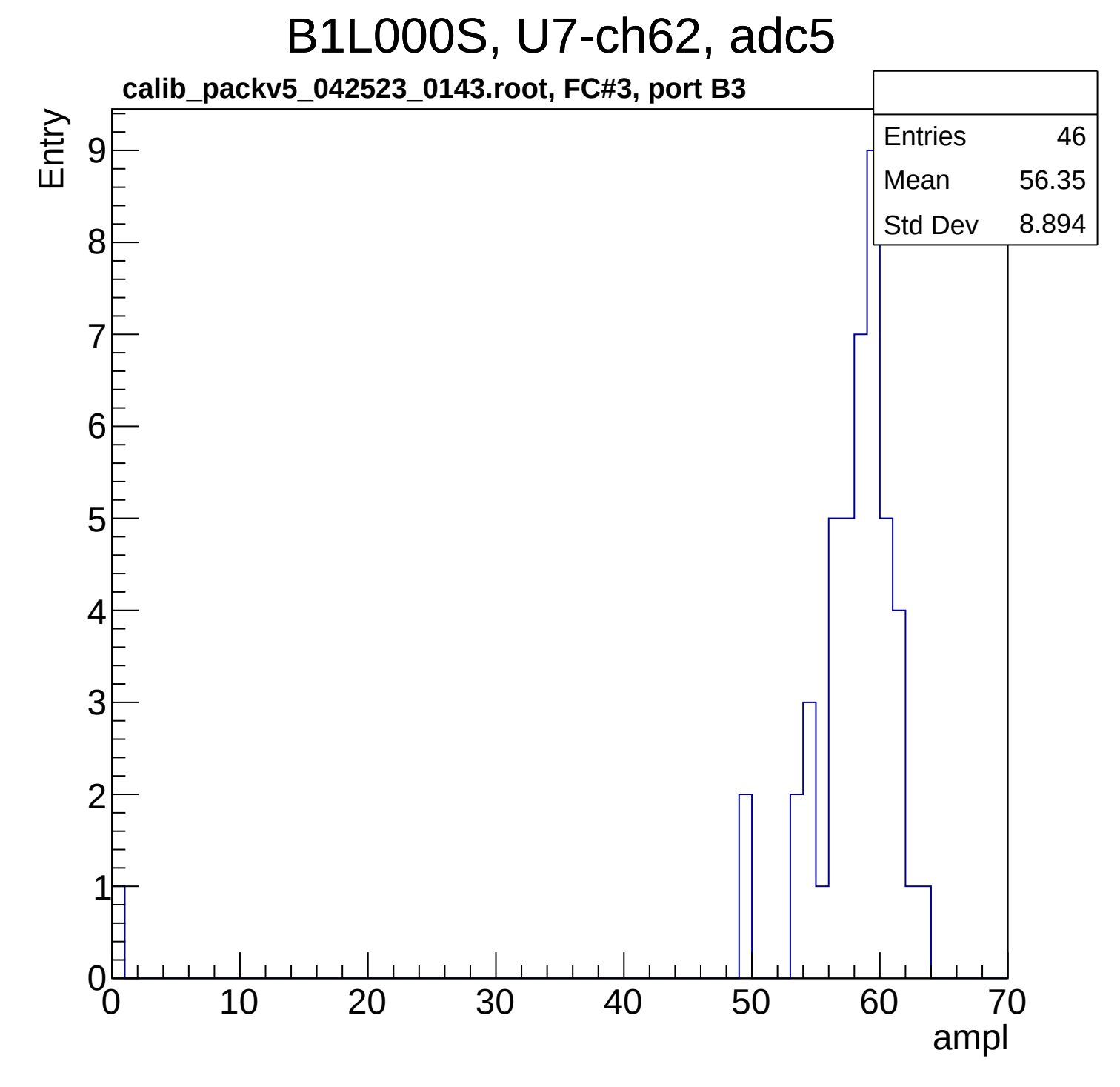
B1L000S, U7-ch62, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 56.35
Std Dev 8.894
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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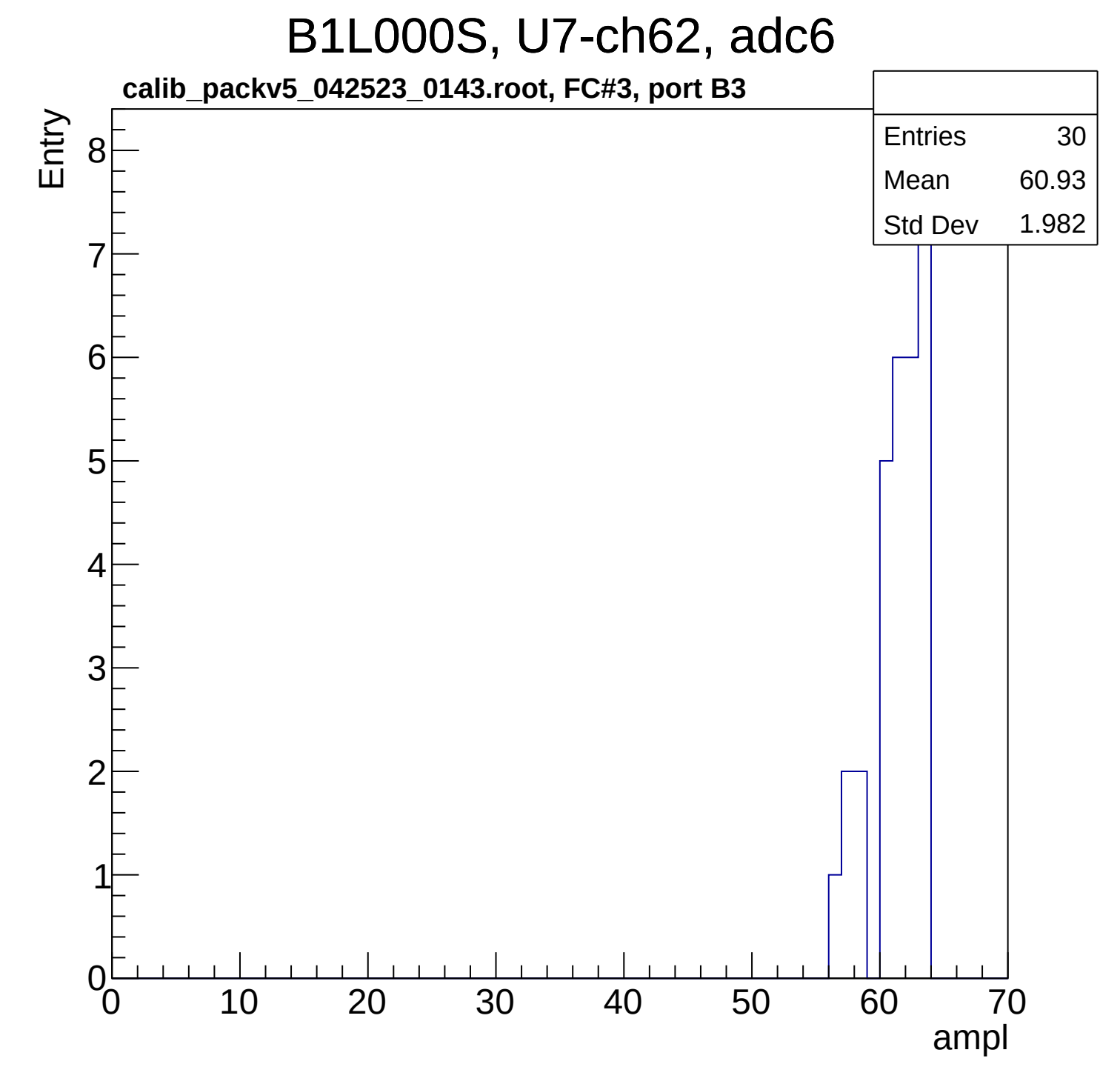
B1L000S, U7-ch62, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 60.93
Std Dev 1.982
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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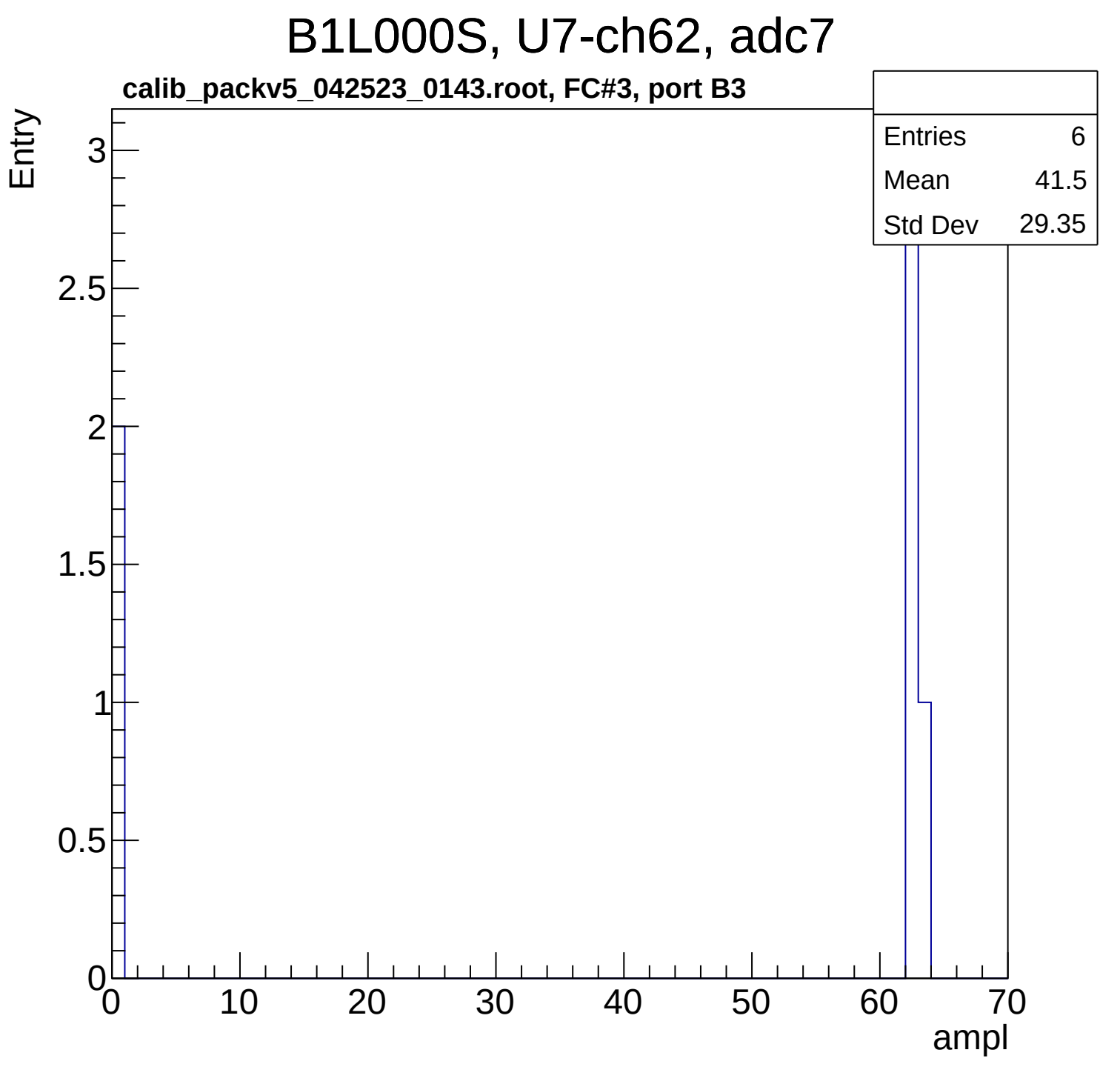
B1L000S, U7-ch62, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 41.5
Std Dev 29.35
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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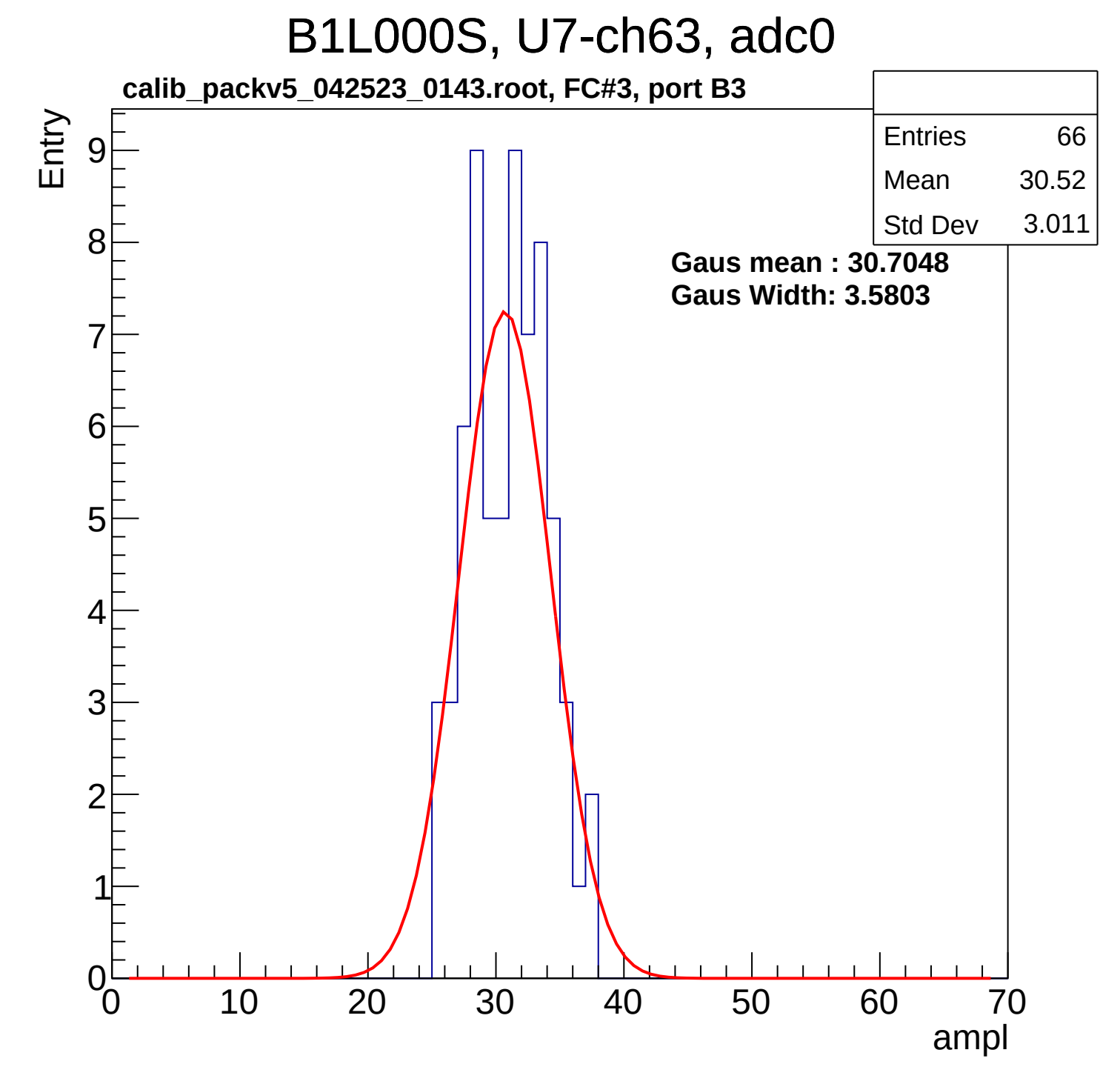
B1L000S, U7-ch63, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 30.52
Std Dev 3.011
Gaus mean : 30.7048
Gaus Width: 3.5803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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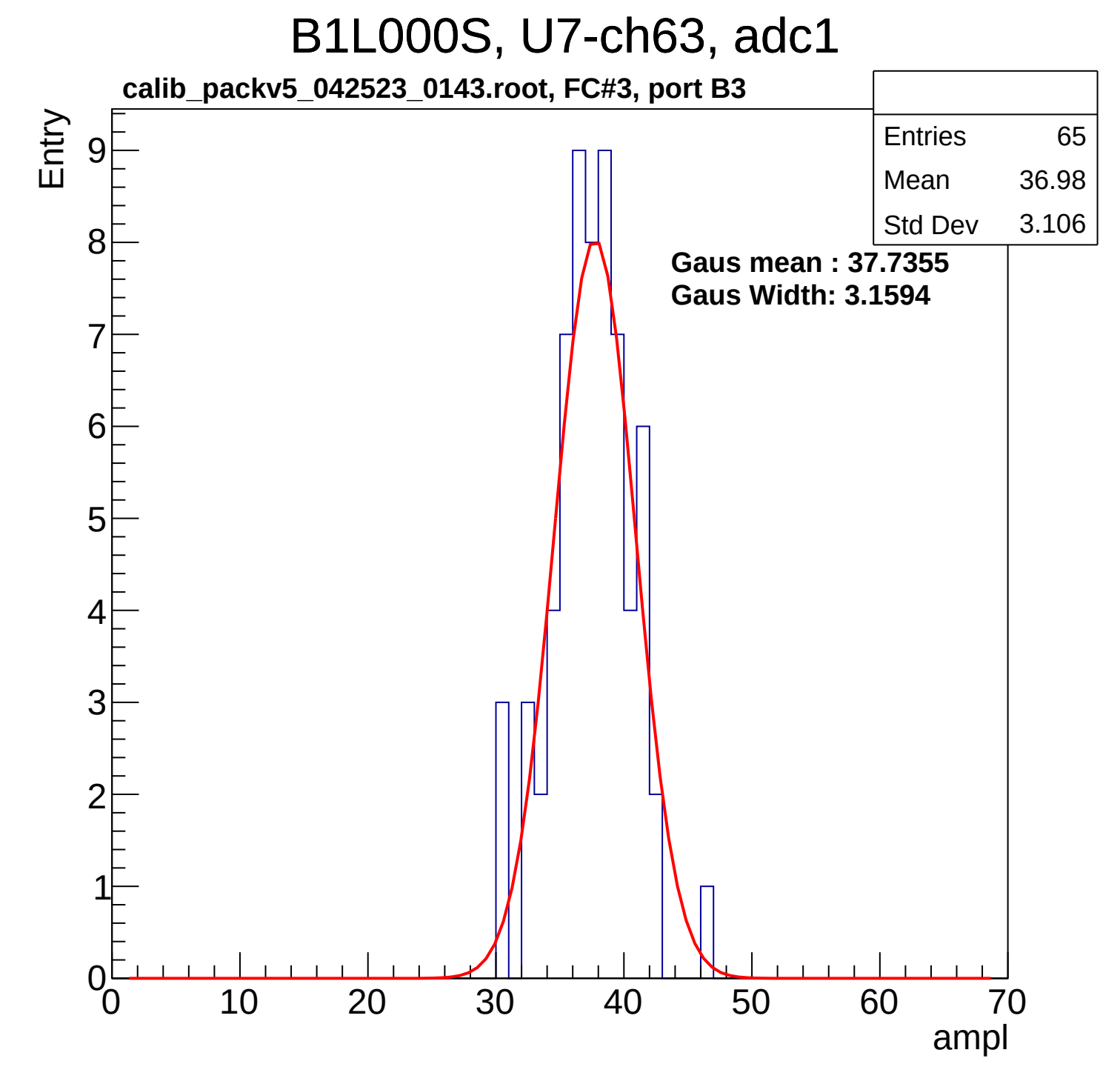
B1L000S, U7-ch63, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 36.98
Std Dev 3.106
Gaus mean : 37.7355
Gaus Width: 3.1594
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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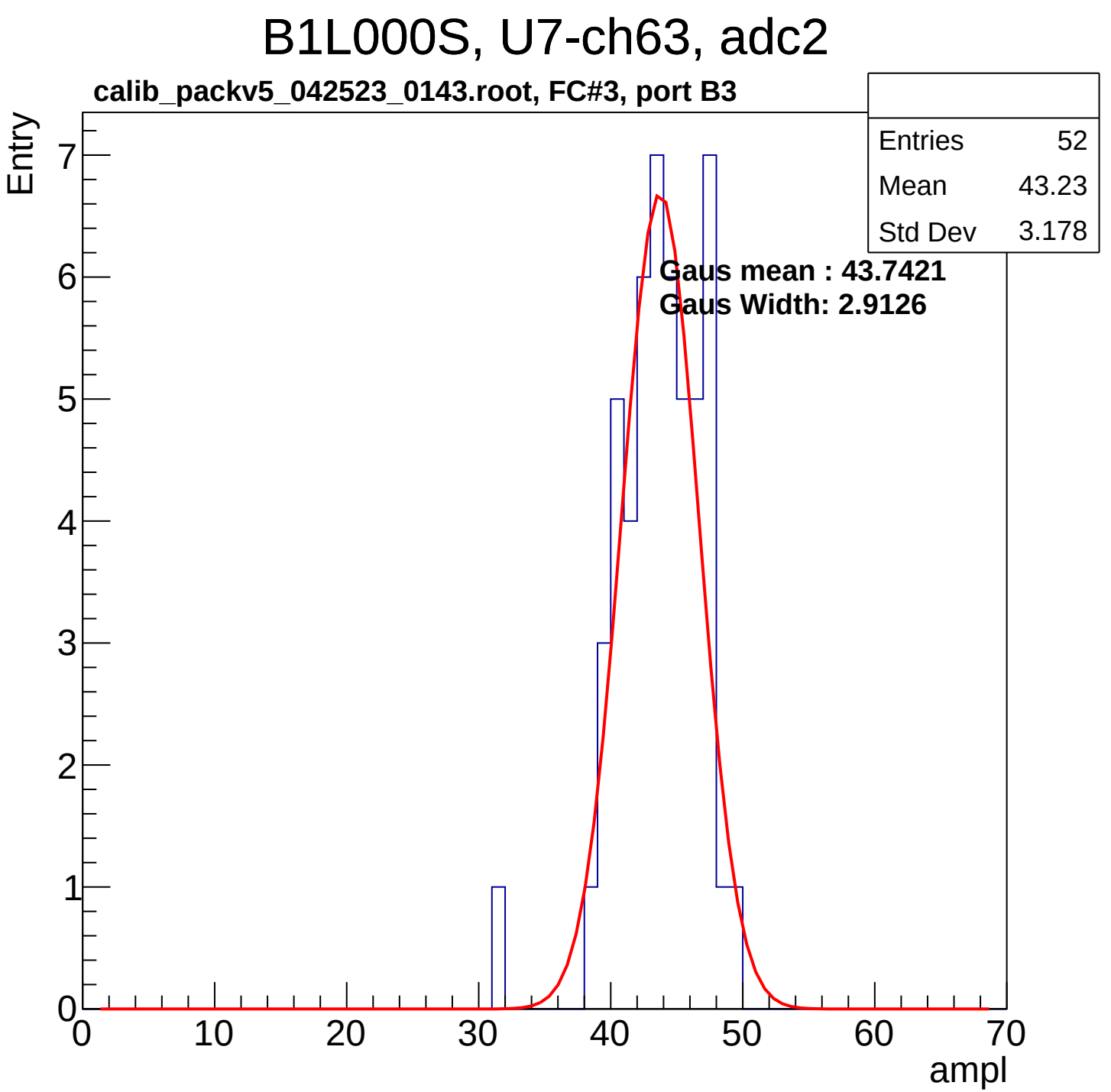
B1L000S, U7-ch63, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 43.23
Std Dev 3.178
Gaus mean : 43.7421
Gaus Width: 2.9126
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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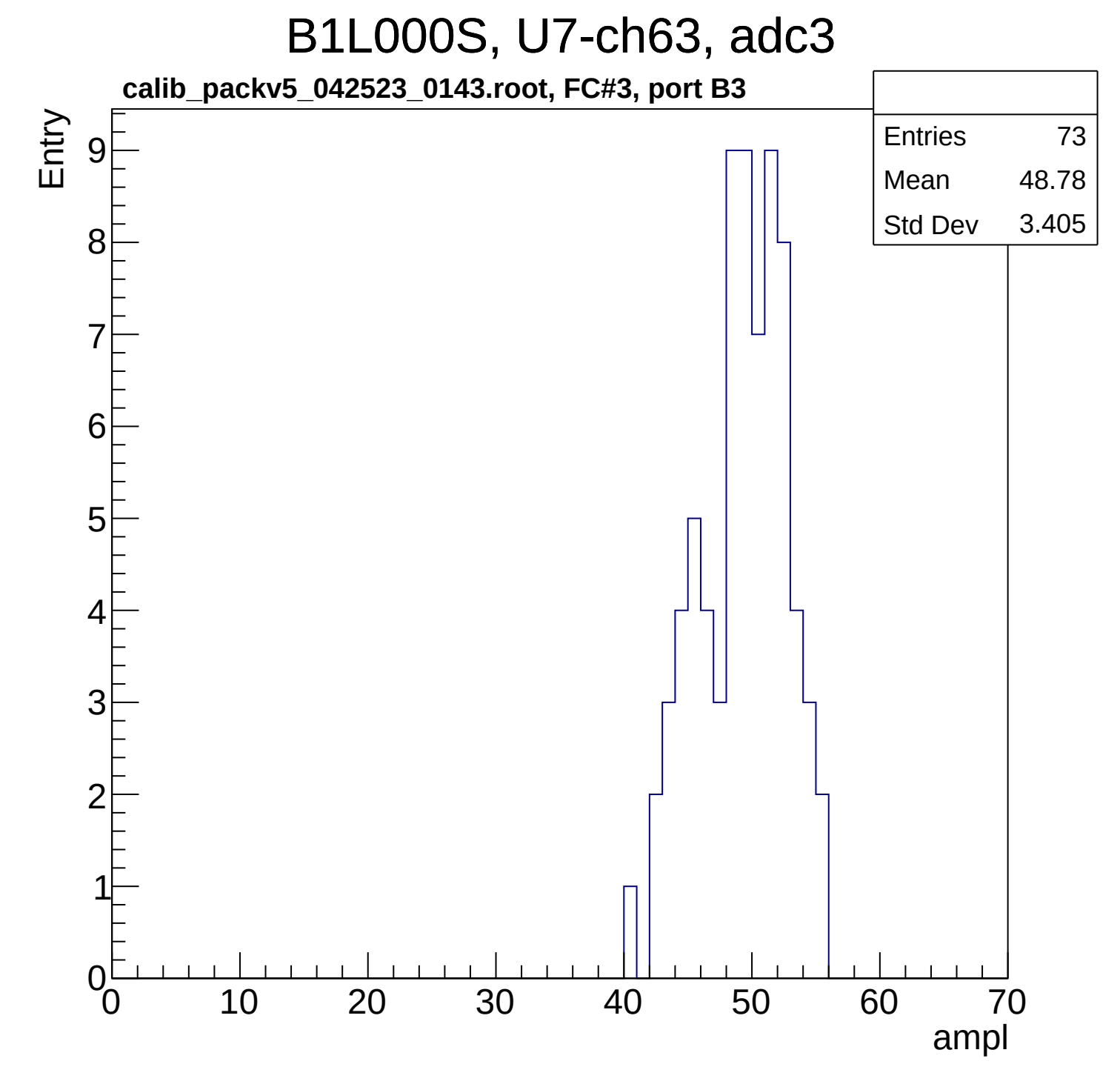
B1L000S, U7-ch63, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 48.78
Std Dev 3.405
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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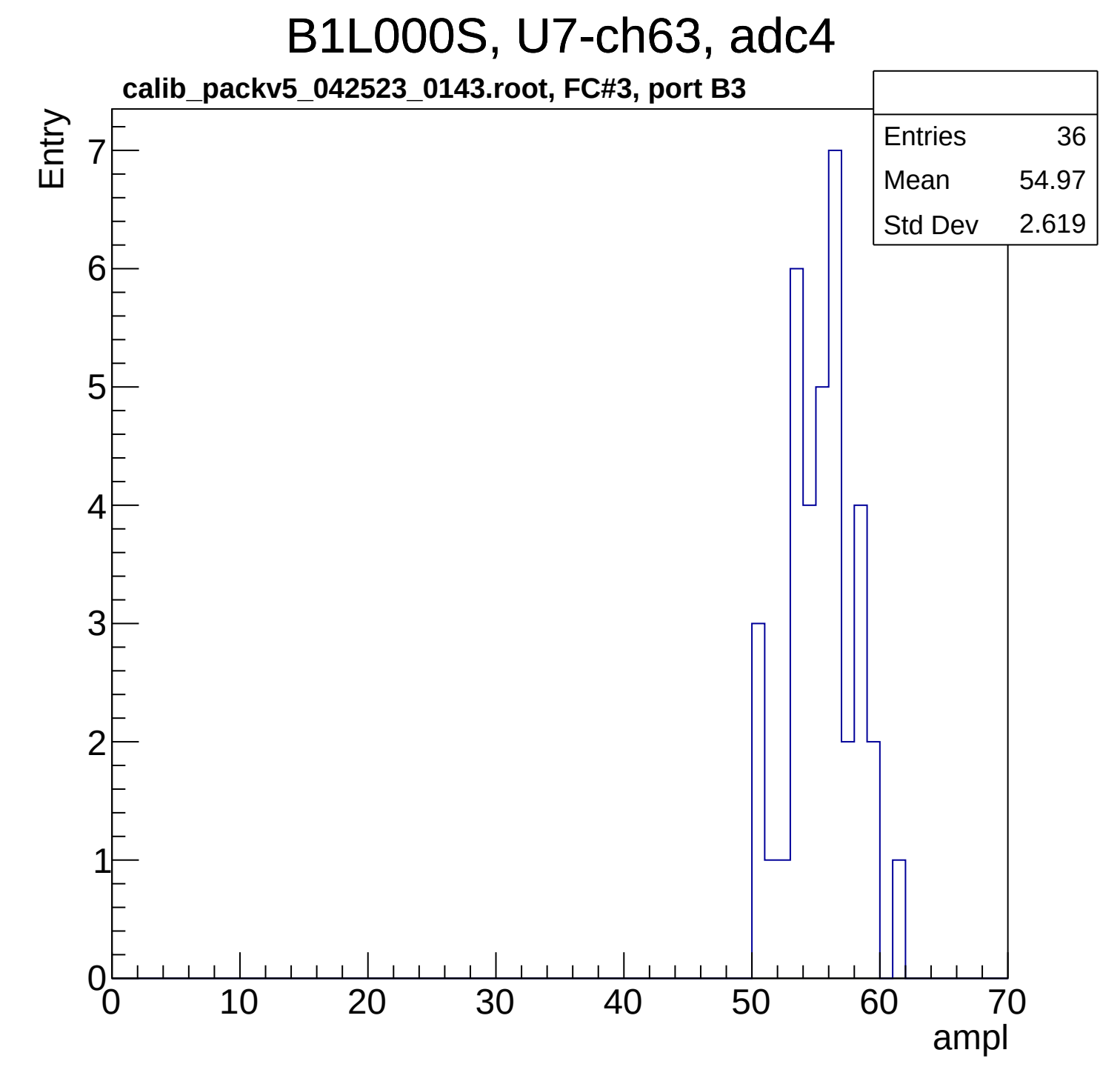
B1L000S, U7-ch63, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 36
Mean 54.97
Std Dev 2.619
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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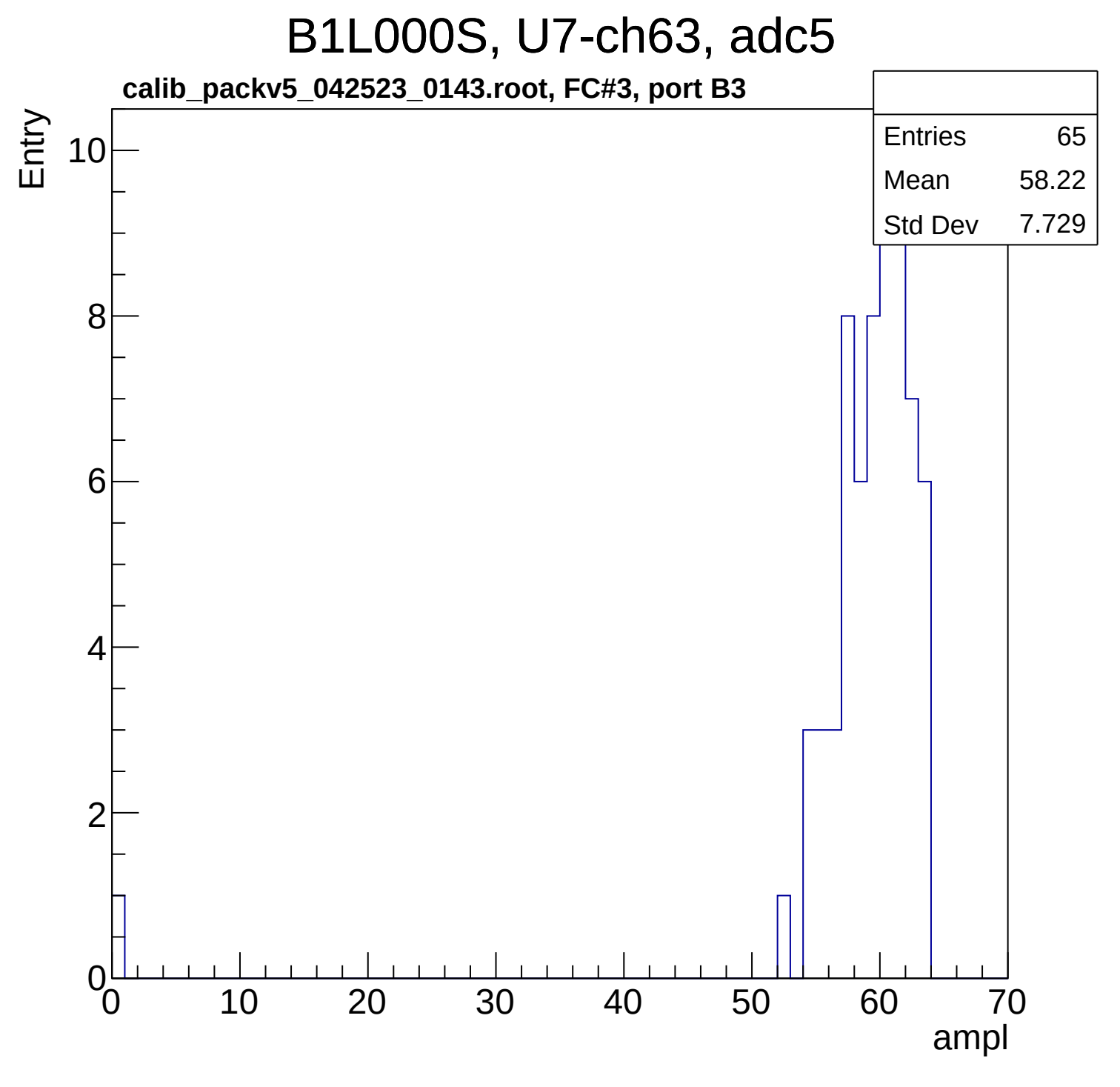
B1L000S, U7-ch63, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 58.22
Std Dev 7.729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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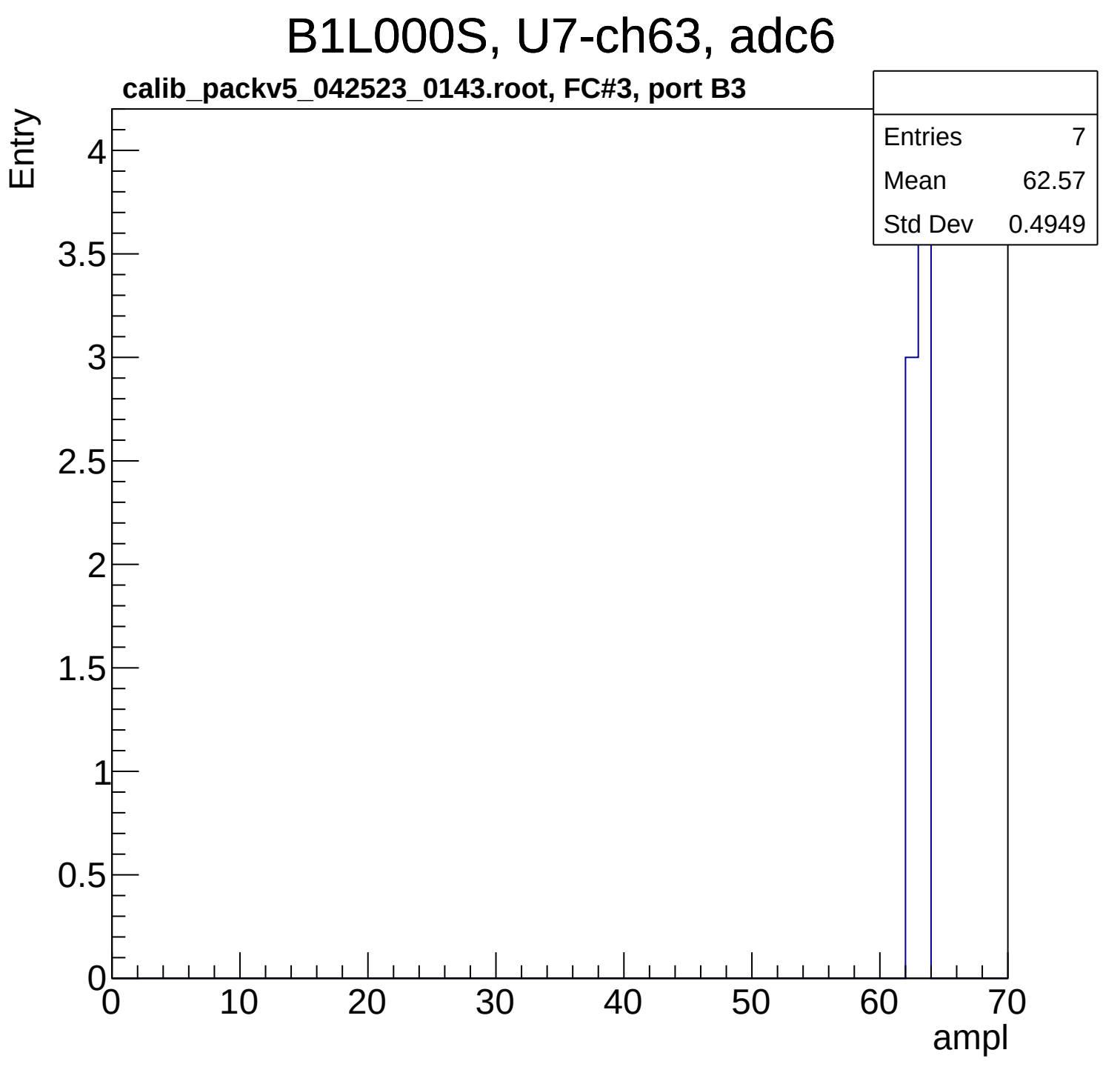
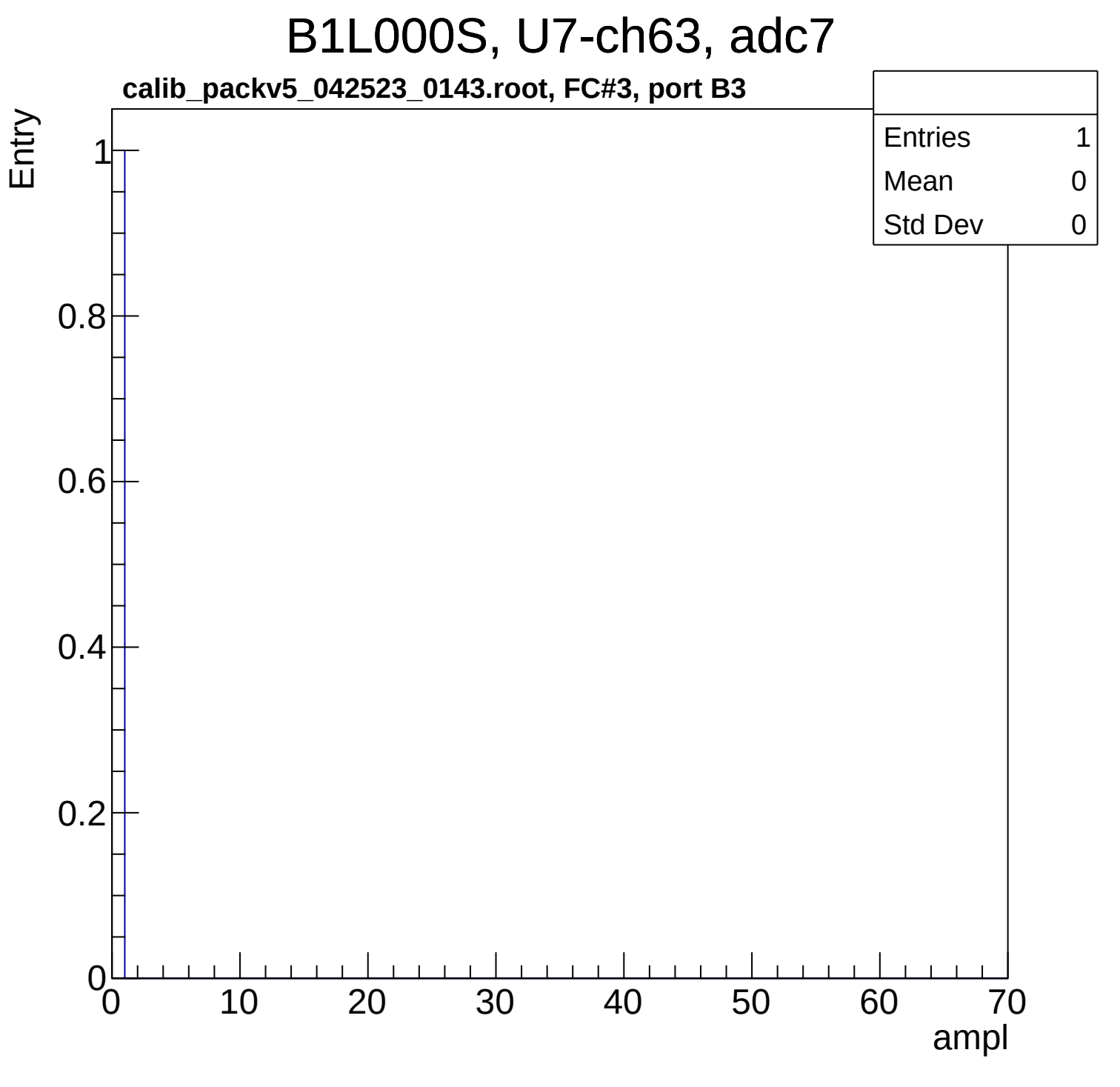

B1L000S, U7-ch63, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.57
Std Dev 0.4949
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch63, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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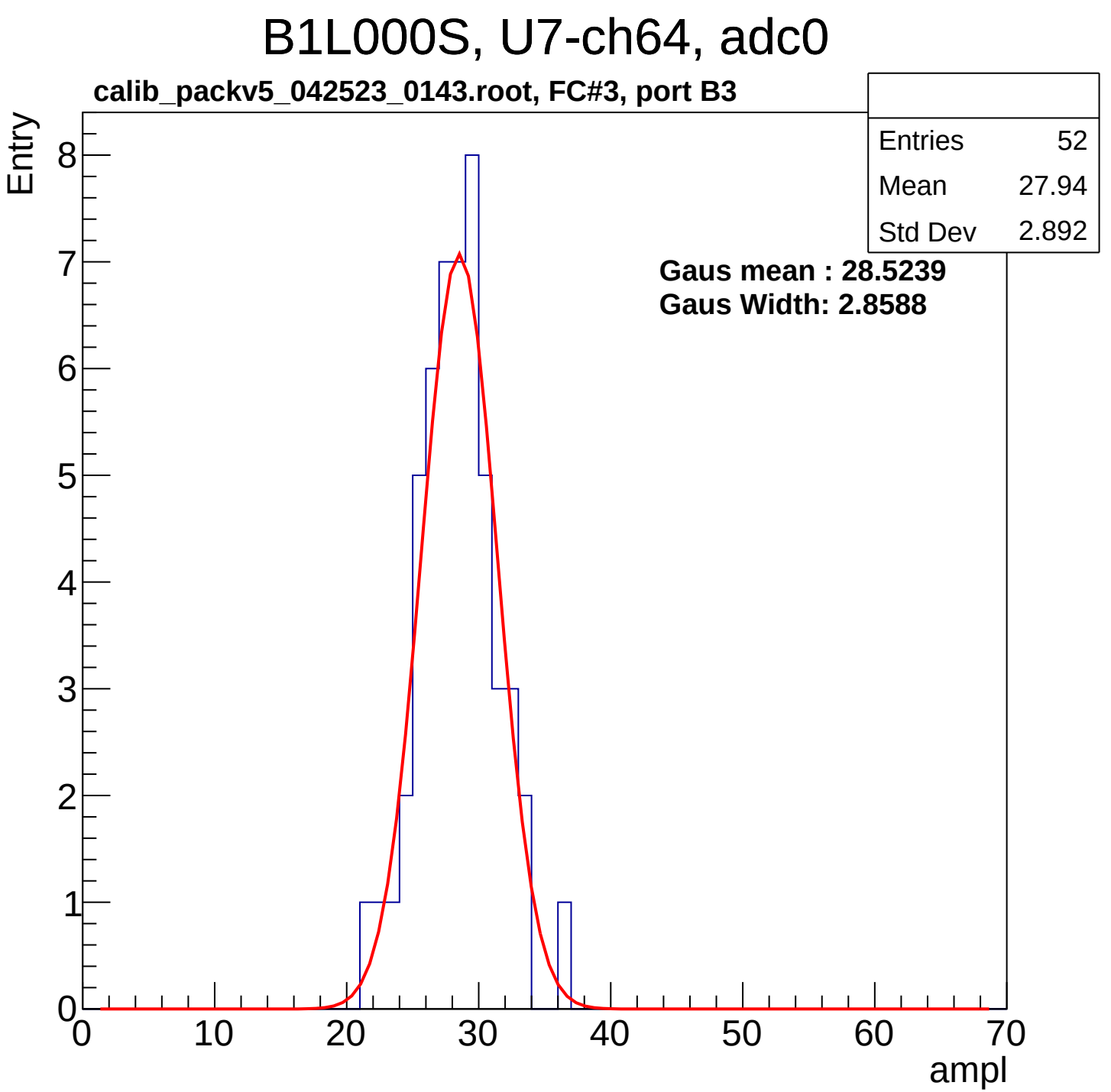
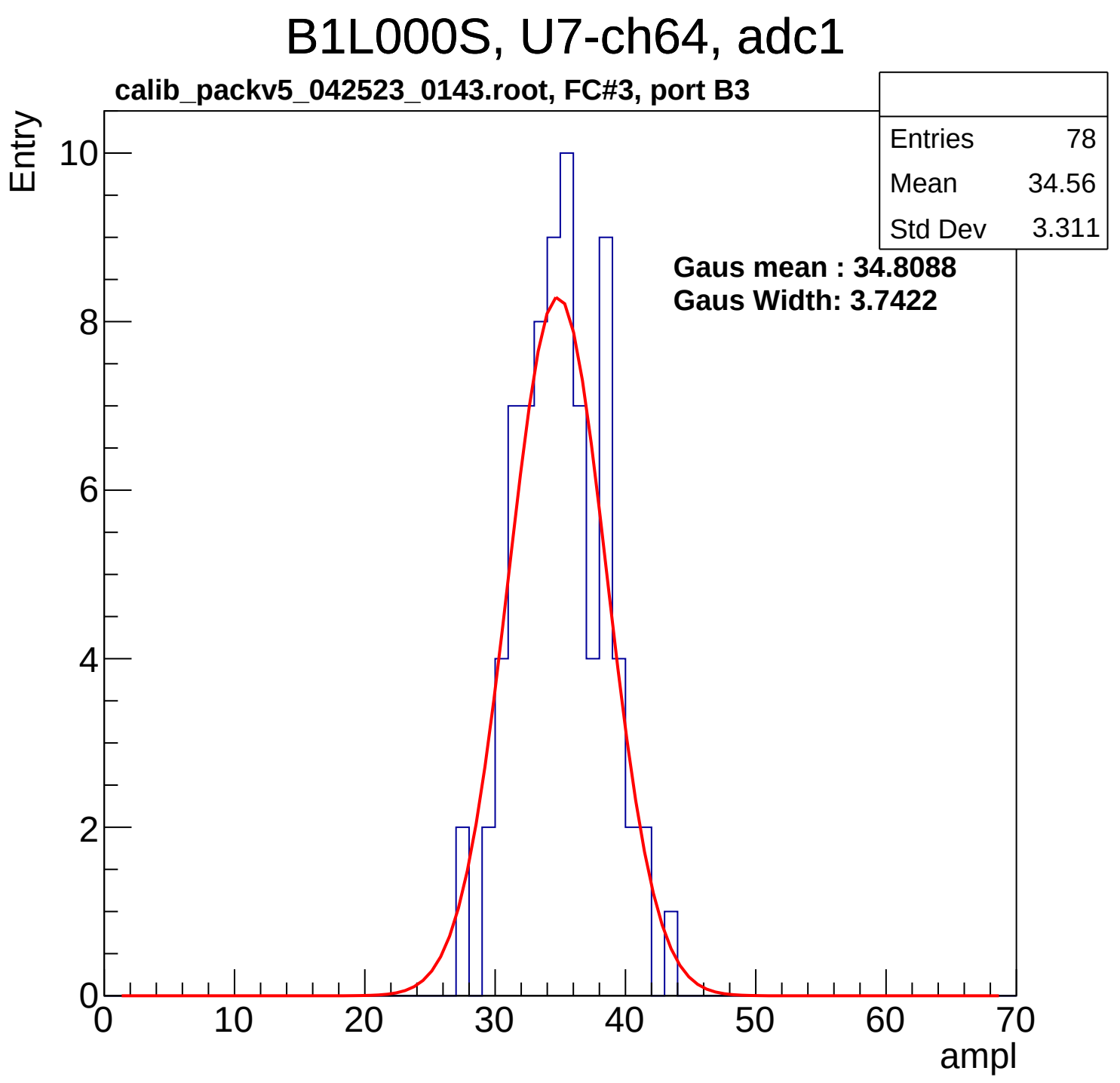
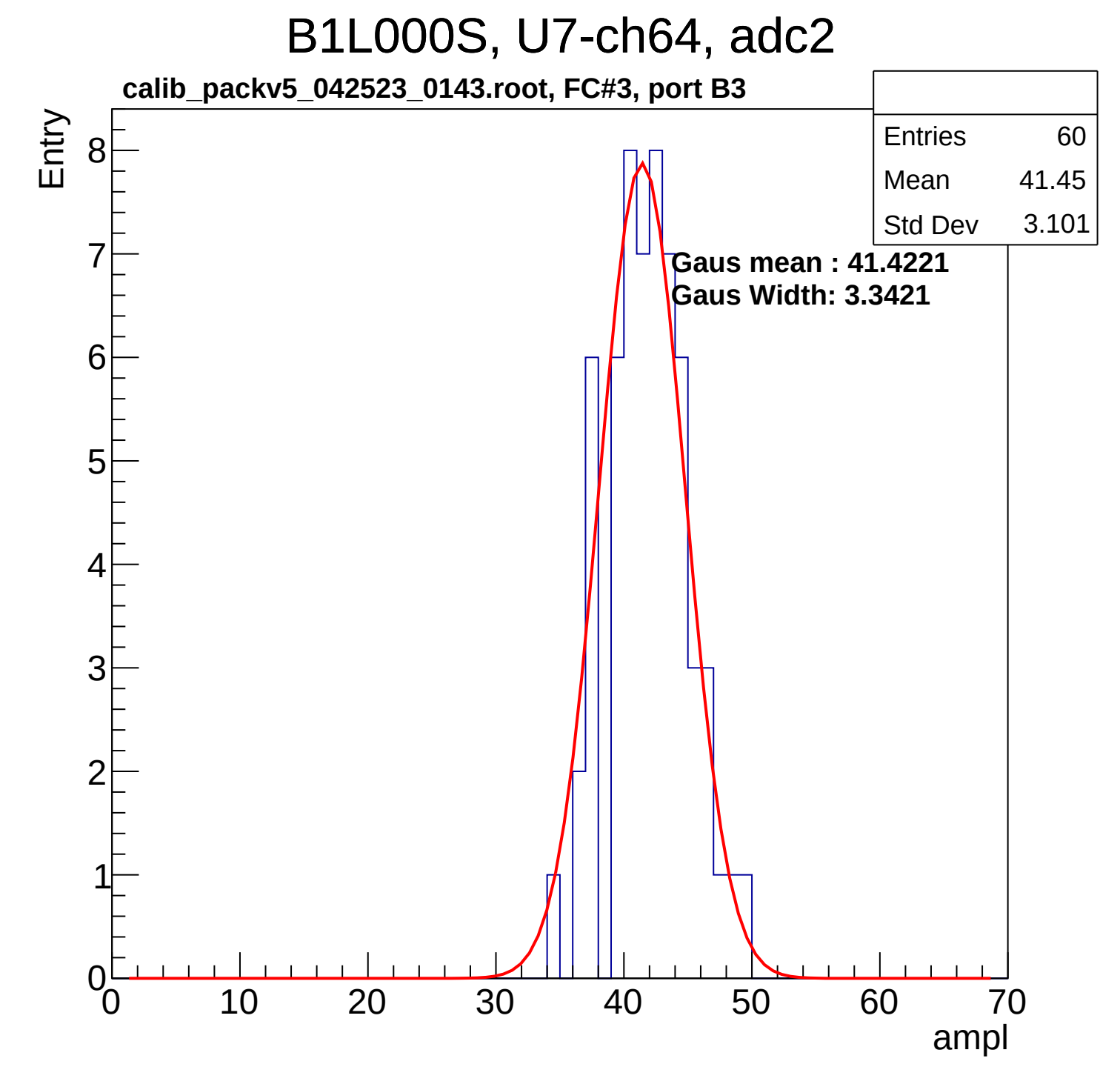
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch64, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 27.94
Std Dev 2.892
Gaus mean : 28.5239
Gaus Width: 2.8588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch64, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 34.56
Std Dev 3.311
Gaus mean : 34.8088
Gaus Width: 3.7422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch64, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 41.45
Std Dev 3.101
Gaus mean : 41.4221
Gaus Width: 3.3421
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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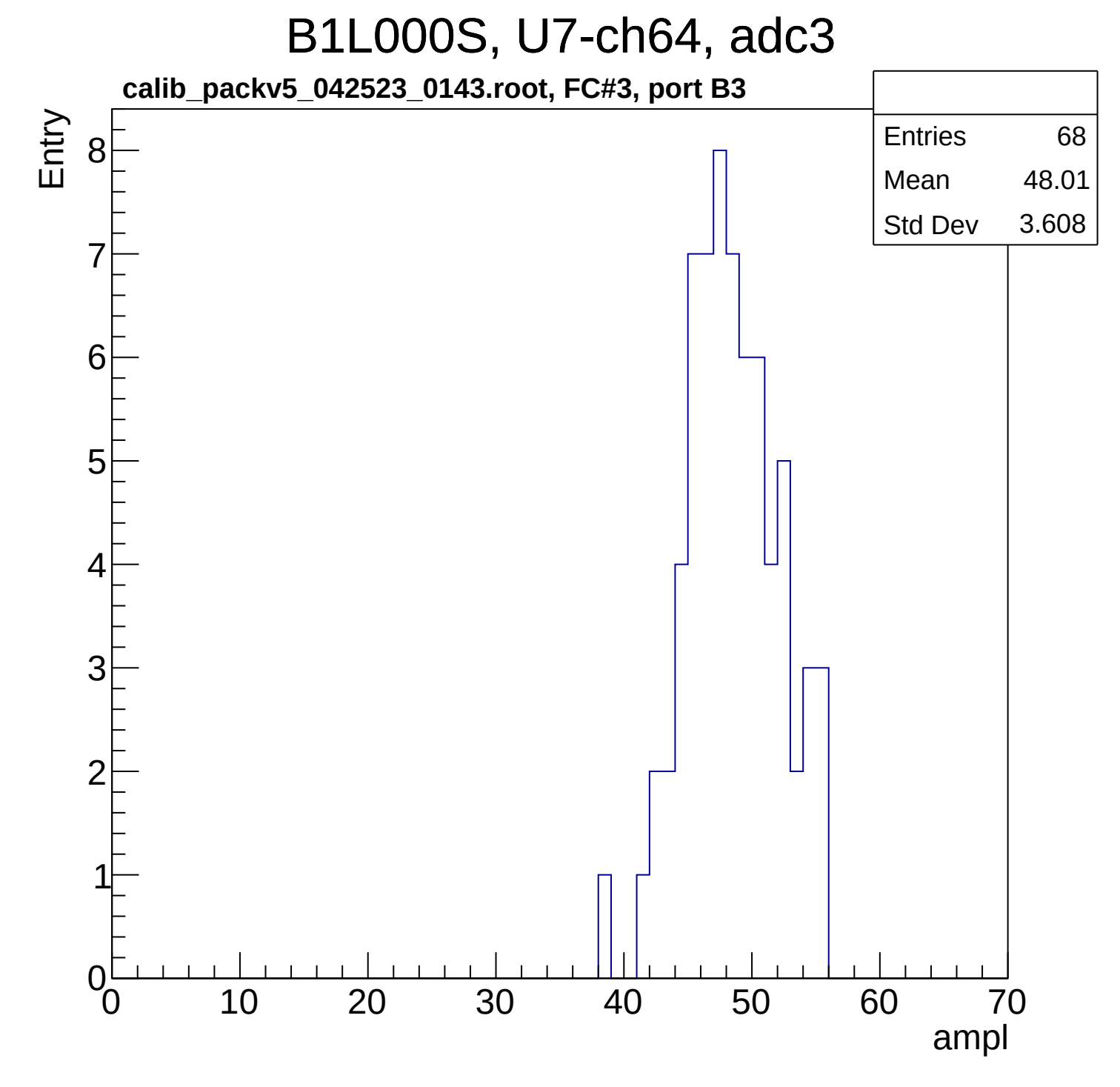
B1L000S, U7-ch64, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 48.01
Std Dev 3.608
Entry
ampl
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5
6
7
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0
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40
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60
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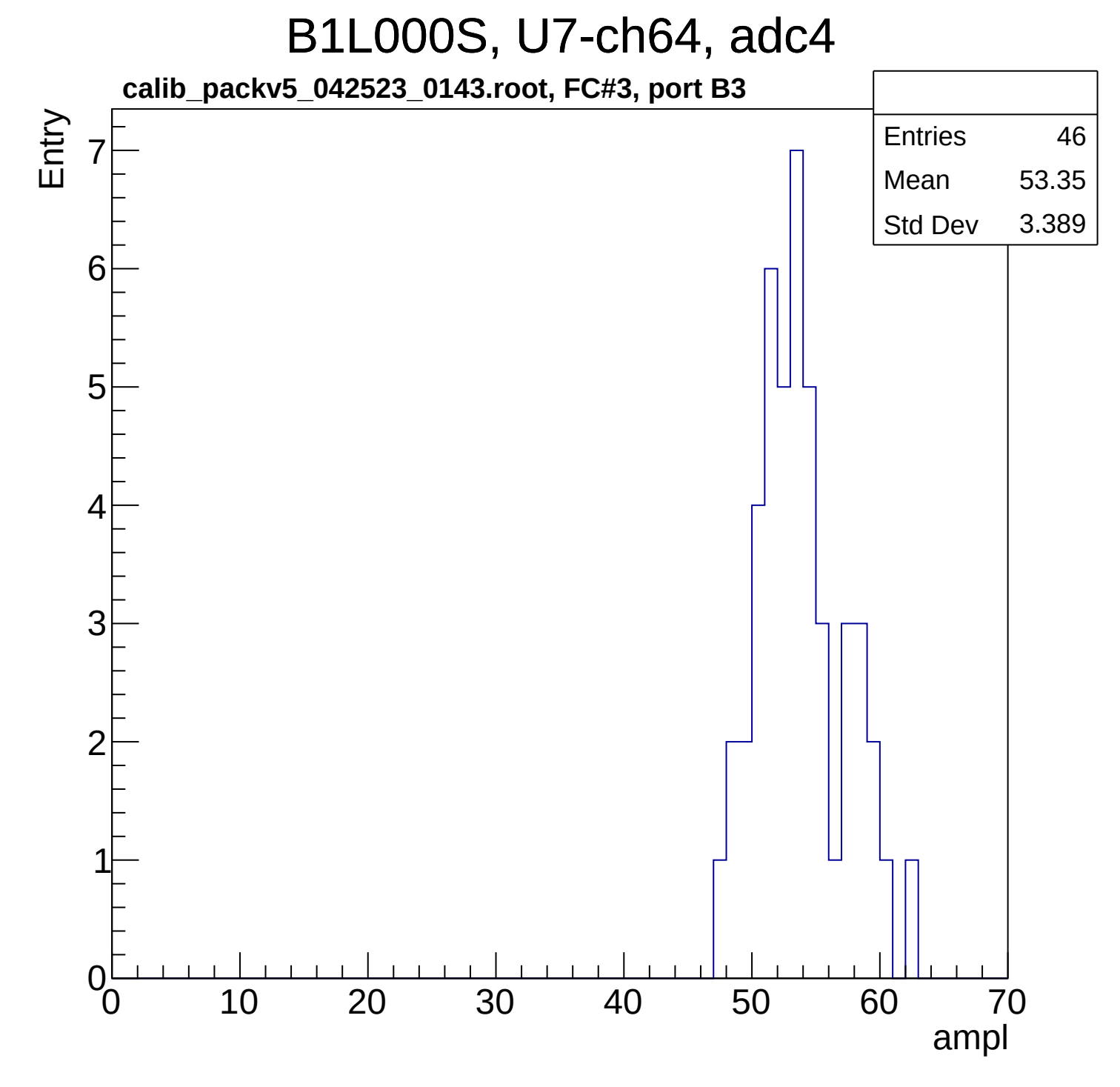
B1L000S, U7-ch64, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 53.35
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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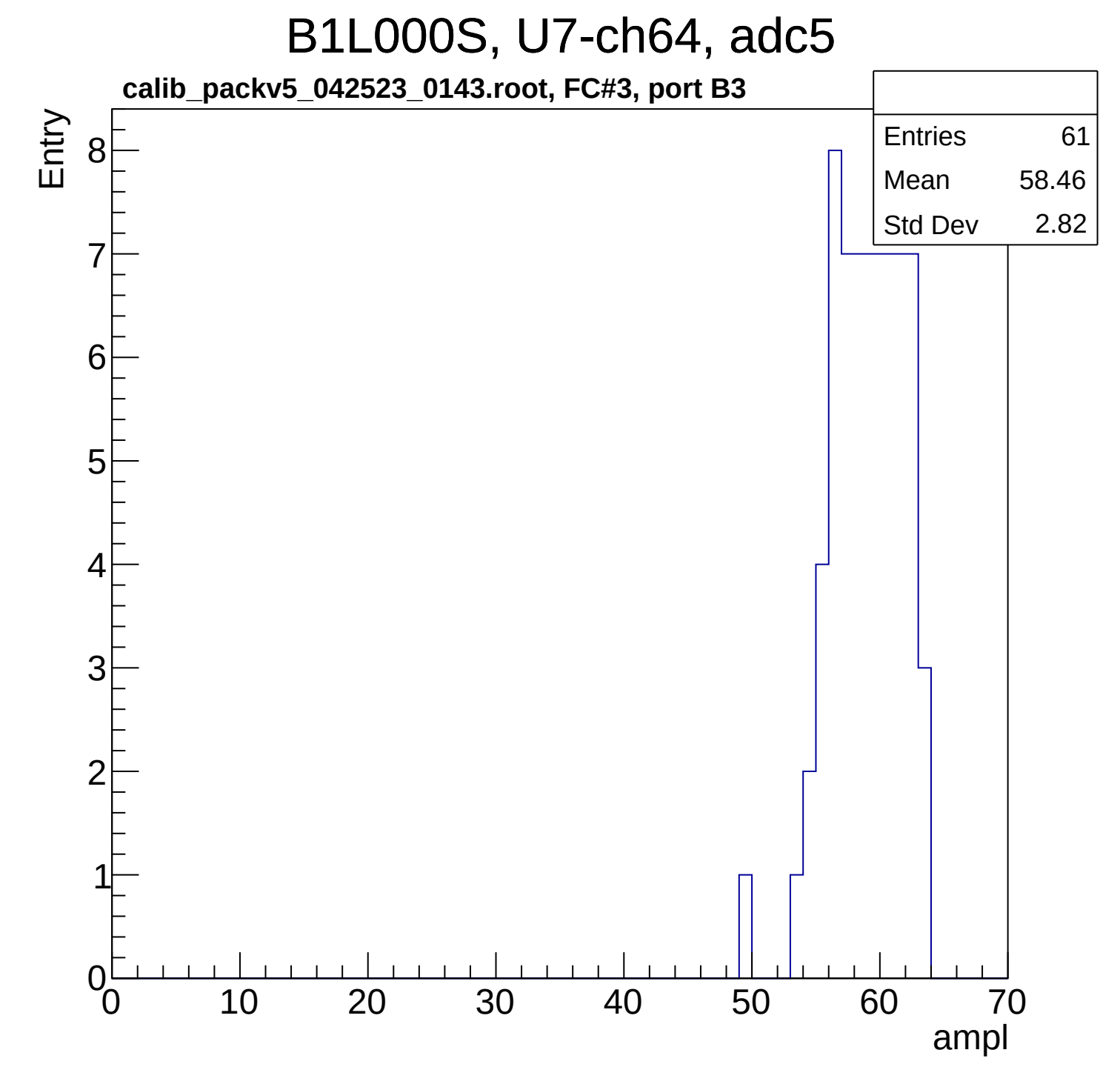
B1L000S, U7-ch64, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.46
Std Dev 2.82
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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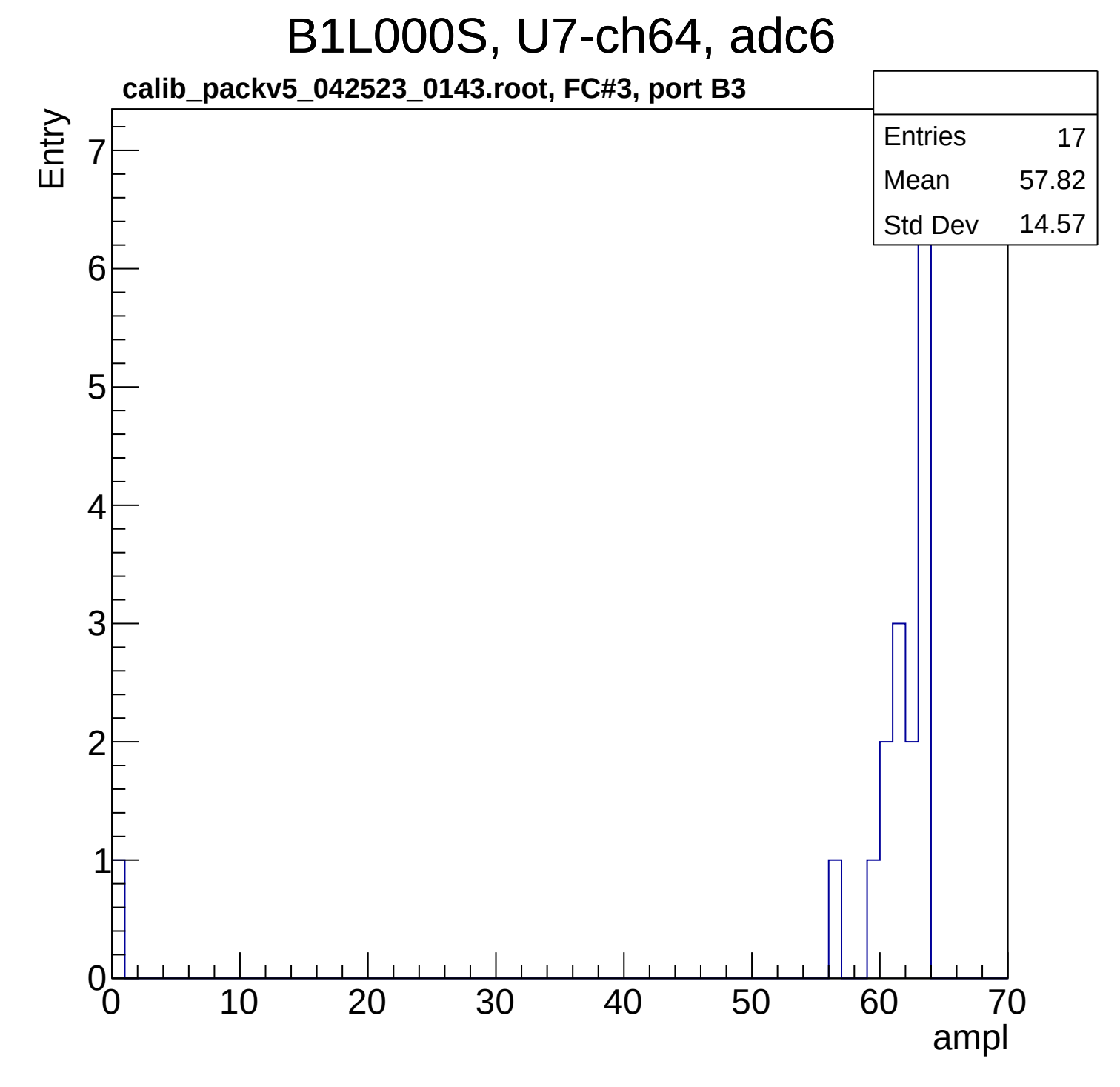
B1L000S, U7-ch64, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 57.82
Std Dev 14.57
Entry
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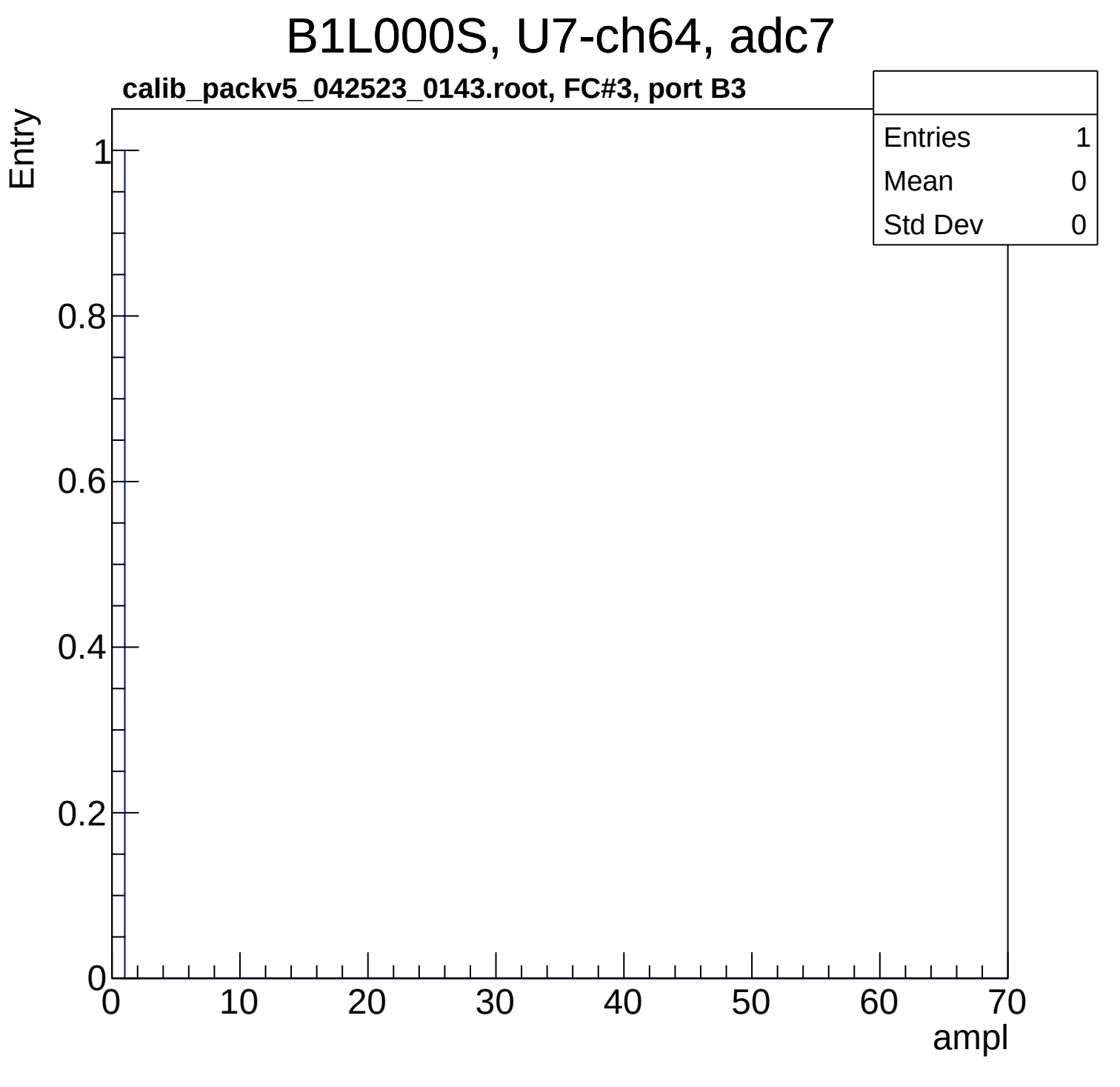
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch64, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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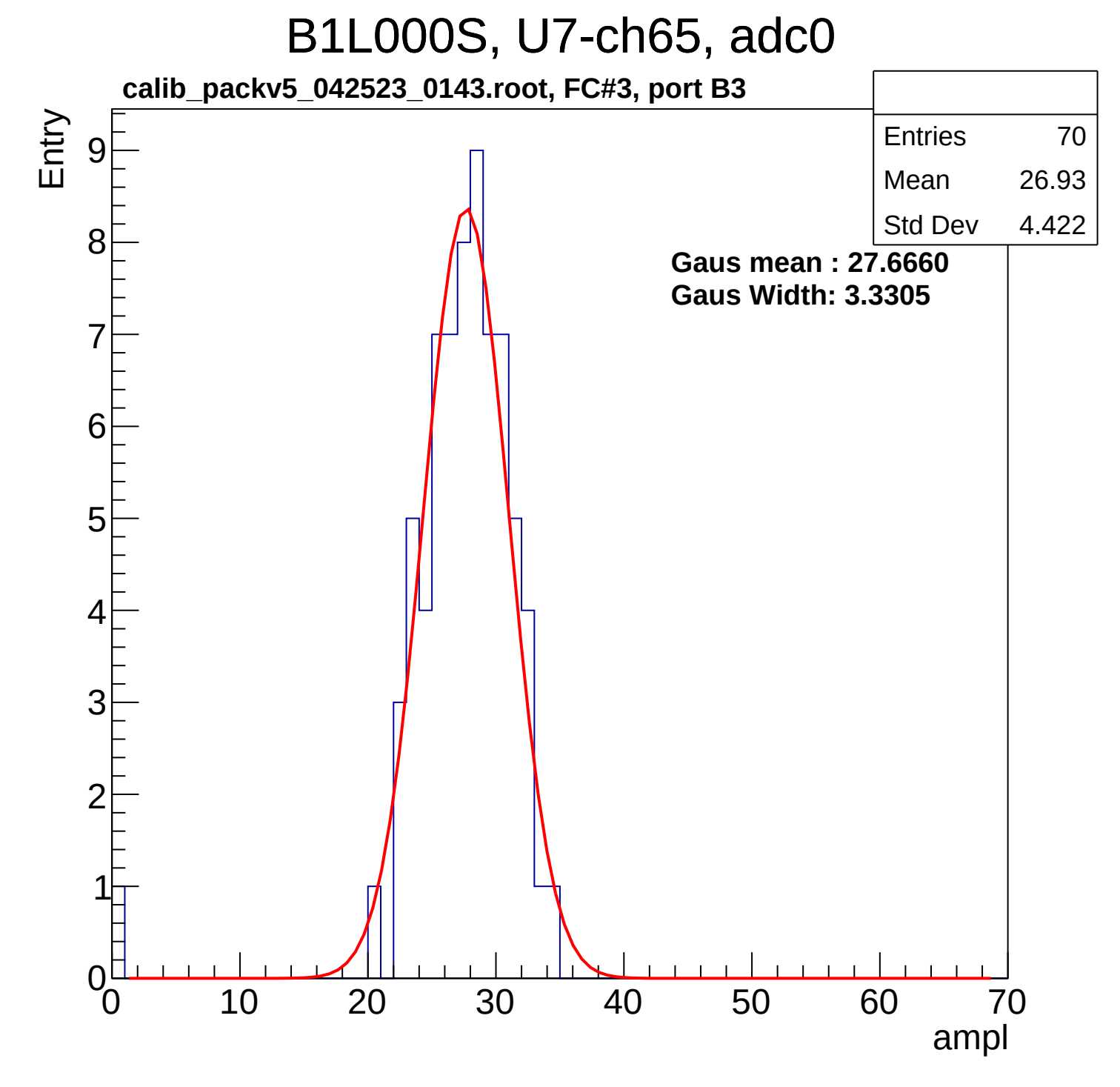
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch65, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 26.93
Std Dev 4.422
Gaus mean : 27.6660
Gaus Width: 3.3305
Entry
ampl
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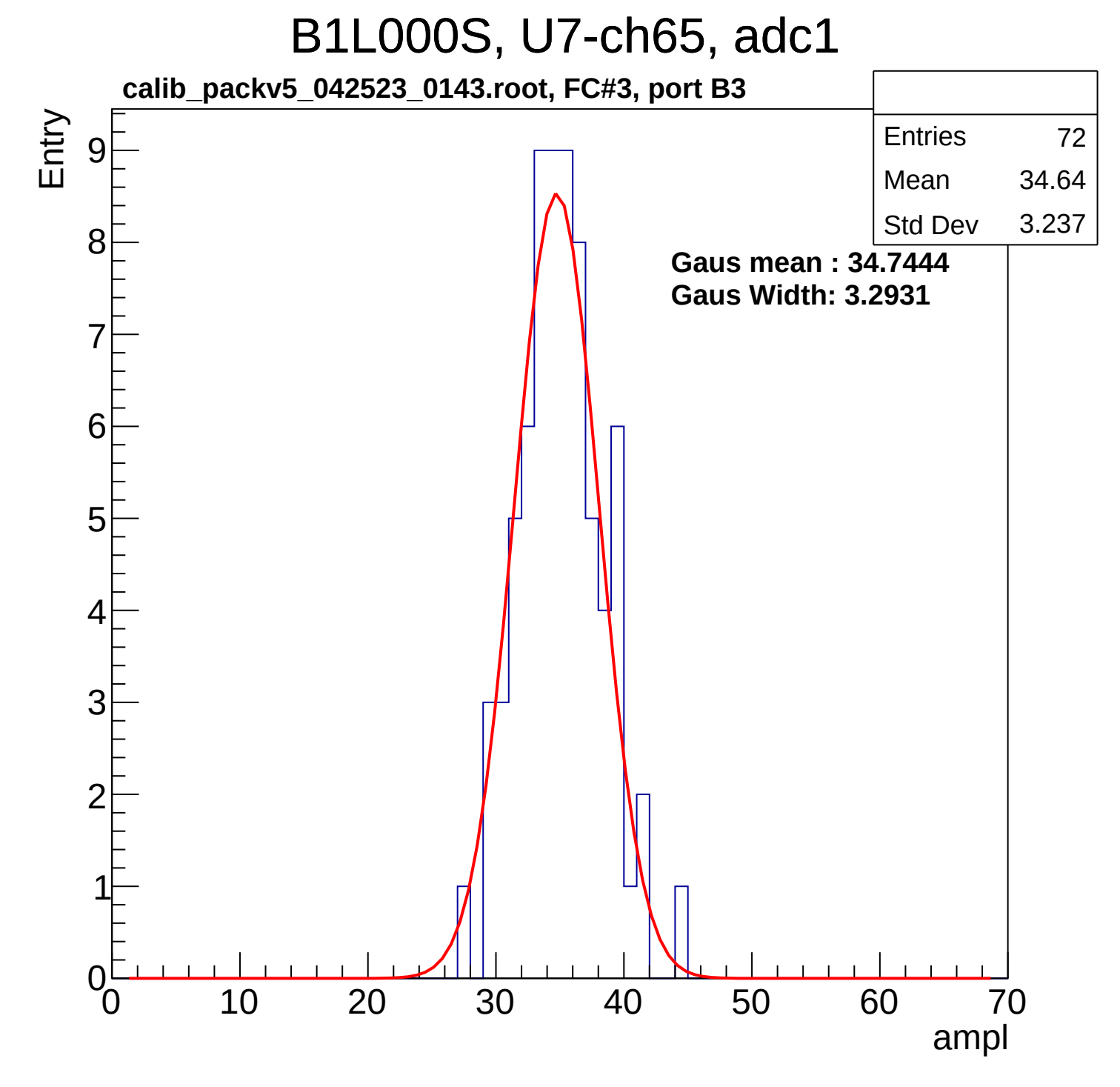
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch65, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 34.64
Std Dev 3.237
Gaus mean : 34.7444
Gaus Width: 3.2931
Entry
ampl
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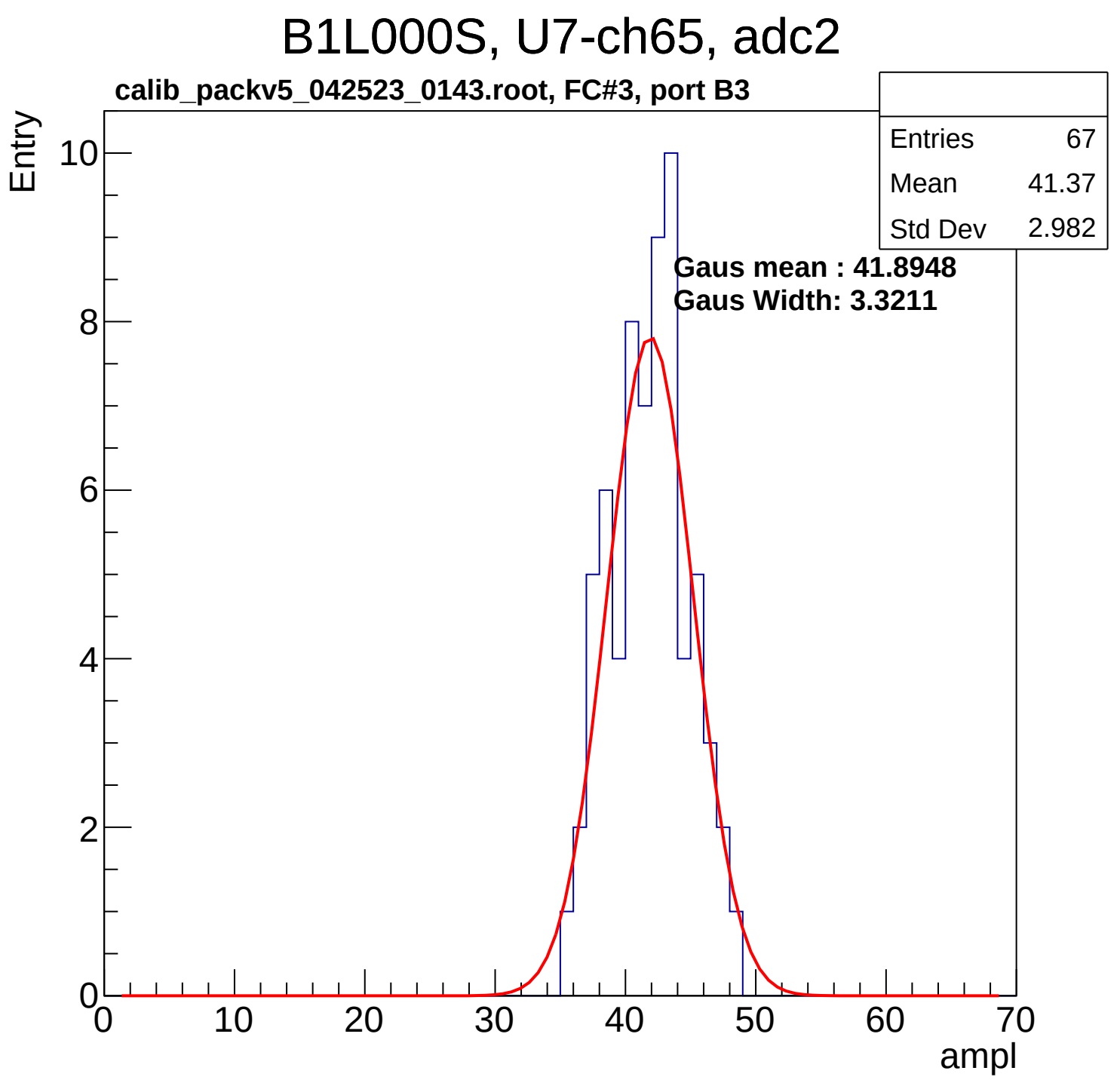
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch65, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.37
Std Dev 2.982
Gaus mean : 41.8948
Gaus Width: 3.3211
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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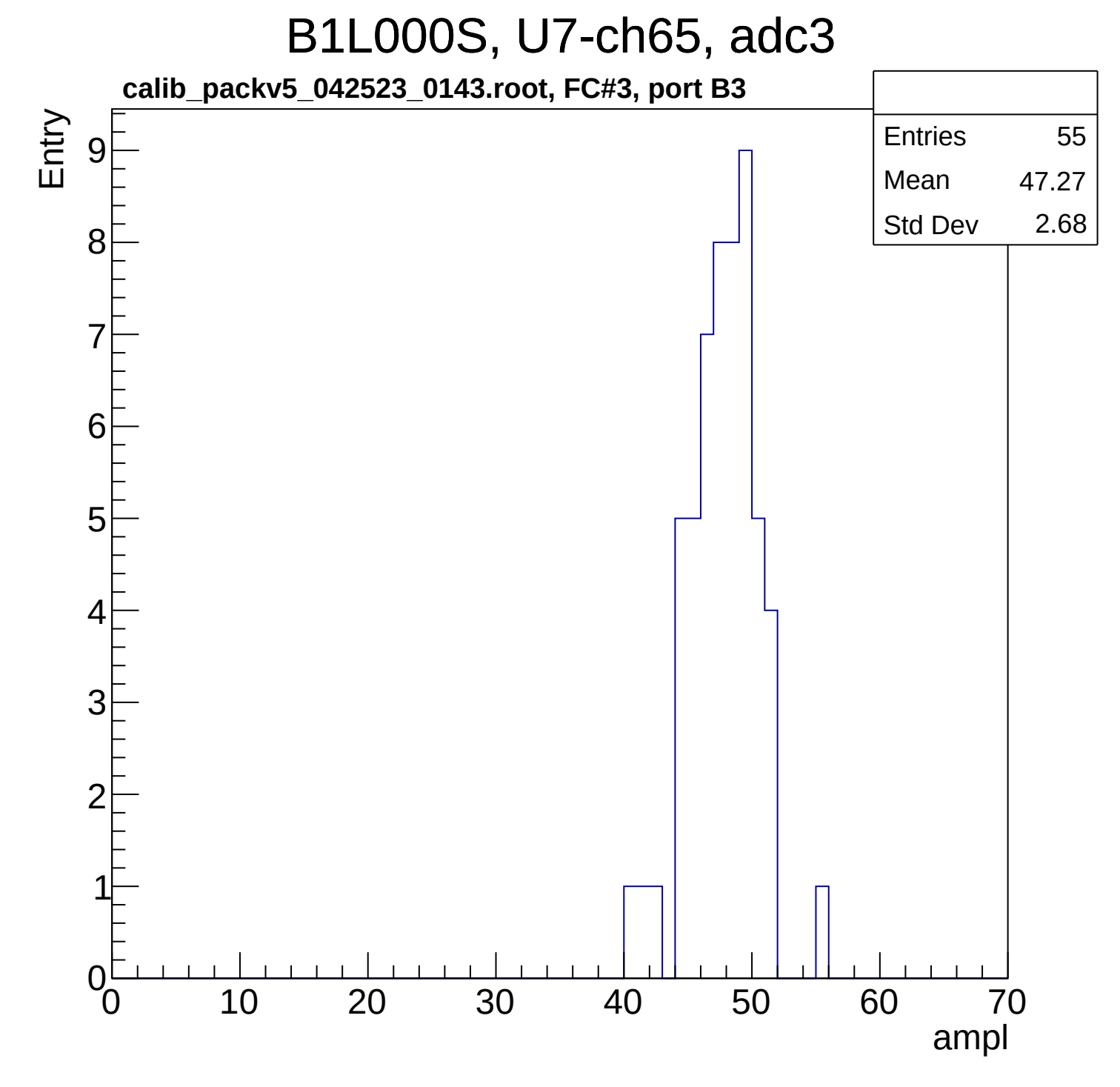
B1L000S, U7-ch65, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 47.27
Std Dev 2.68
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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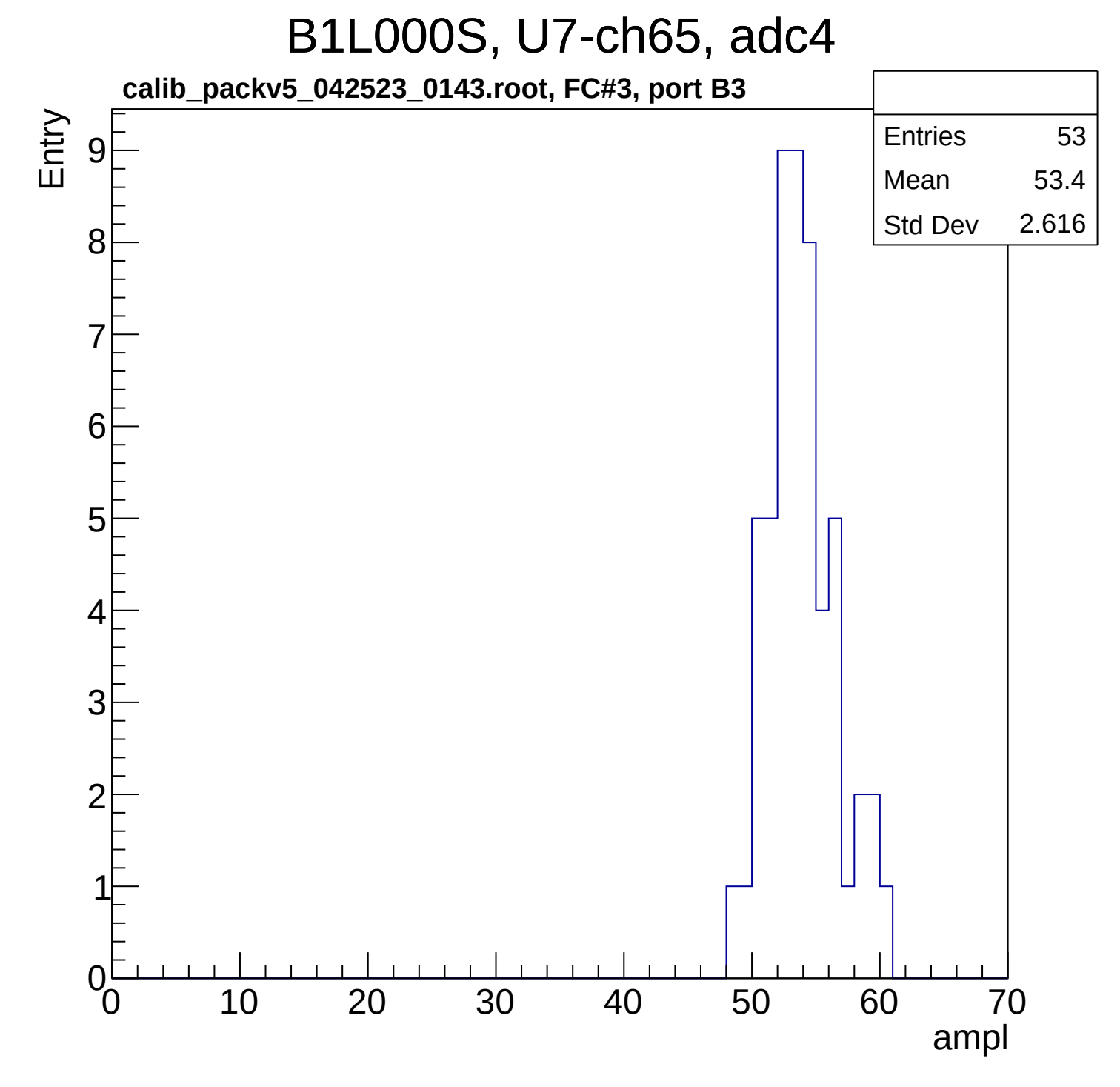
B1L000S, U7-ch65, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 53.4
Std Dev 2.616
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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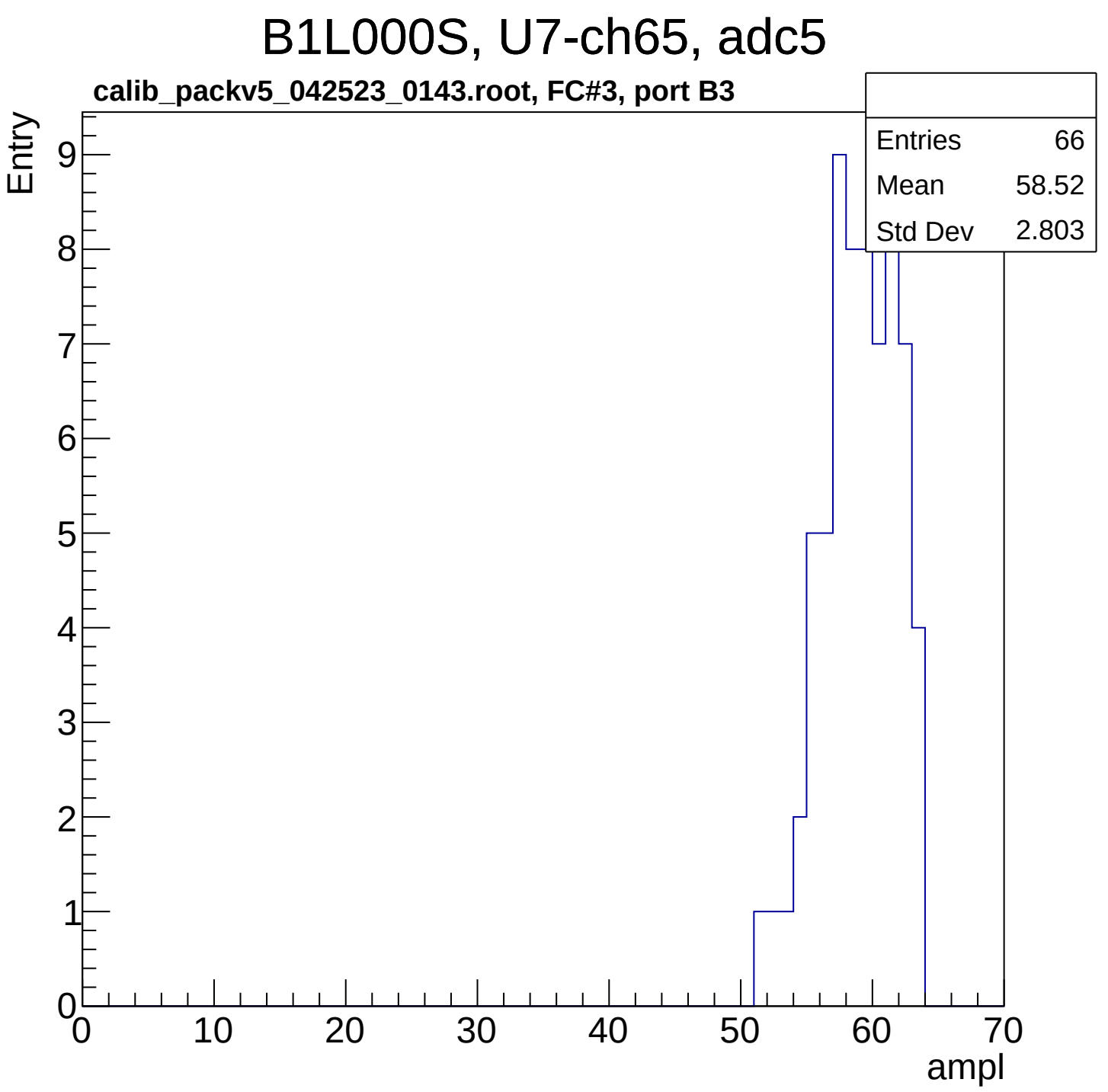
B1L000S, U7-ch65, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 58.52
Std Dev 2.803
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
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60
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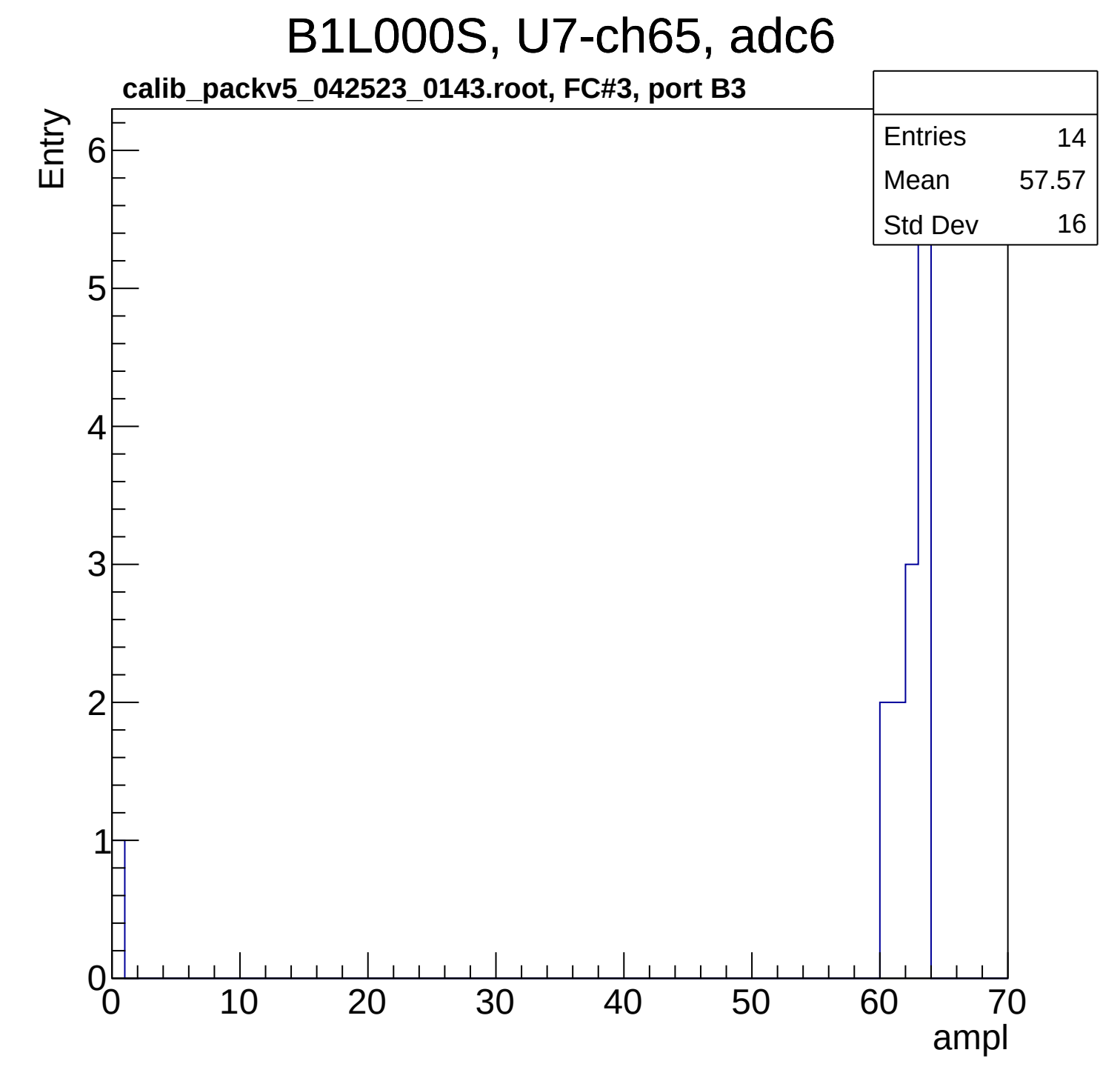
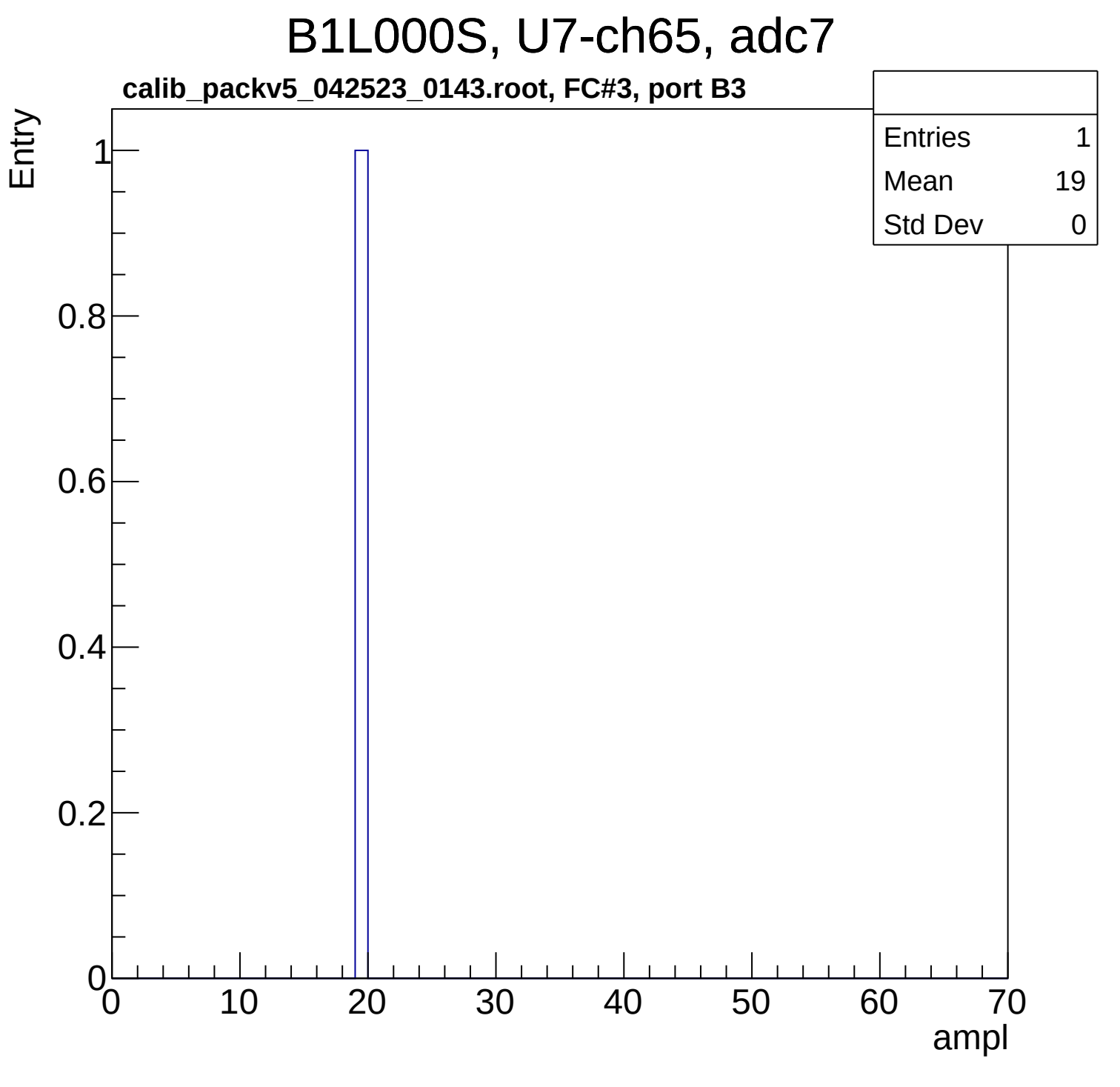

B1L000S, U7-ch65, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.57
Std Dev 16
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch65, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
20
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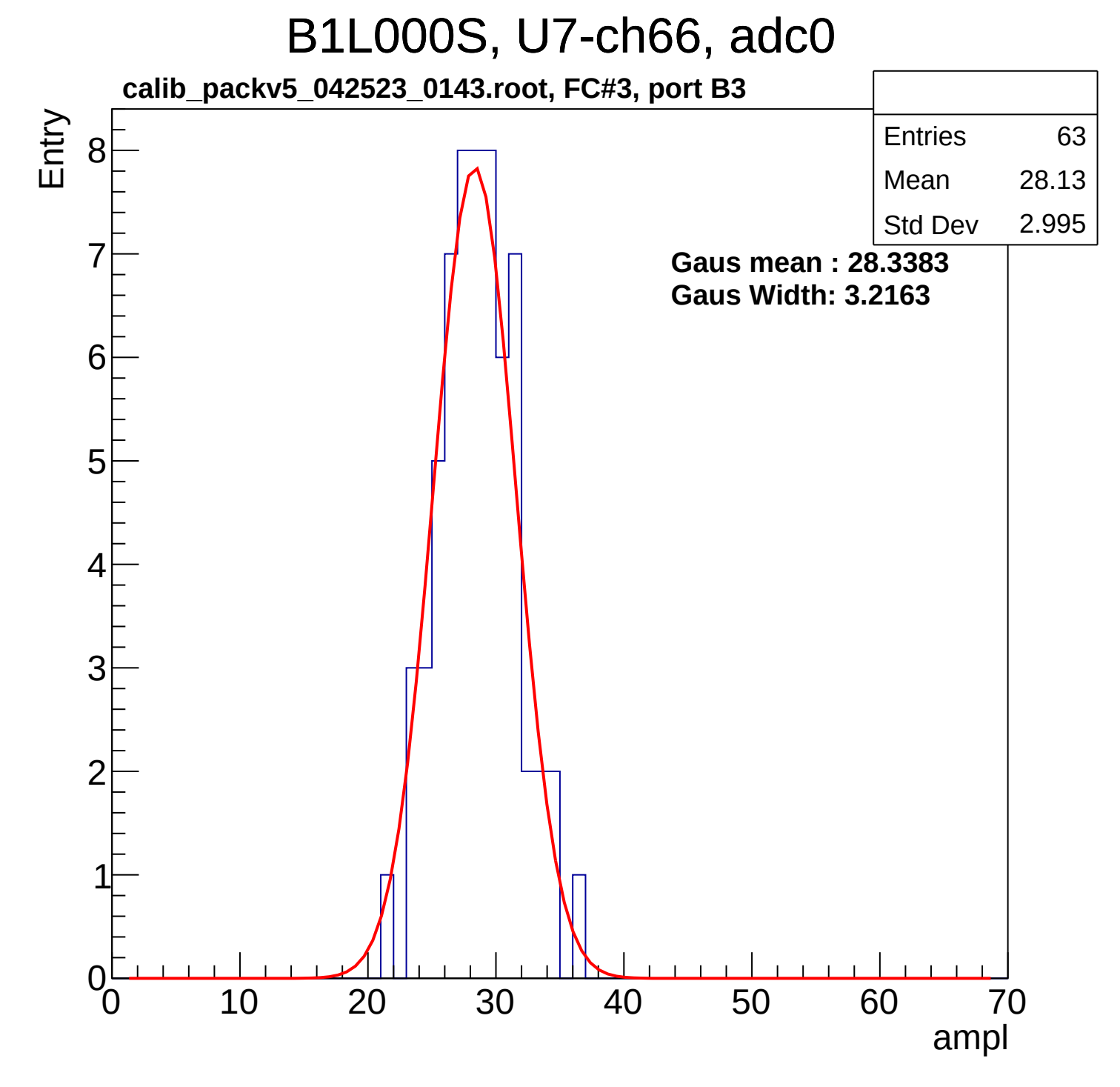
40
50
60
70
ampl


B1L000S, U7-ch66, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 28.13
Std Dev 2.995
Gaus mean : 28.3383
Gaus Width: 3.2163
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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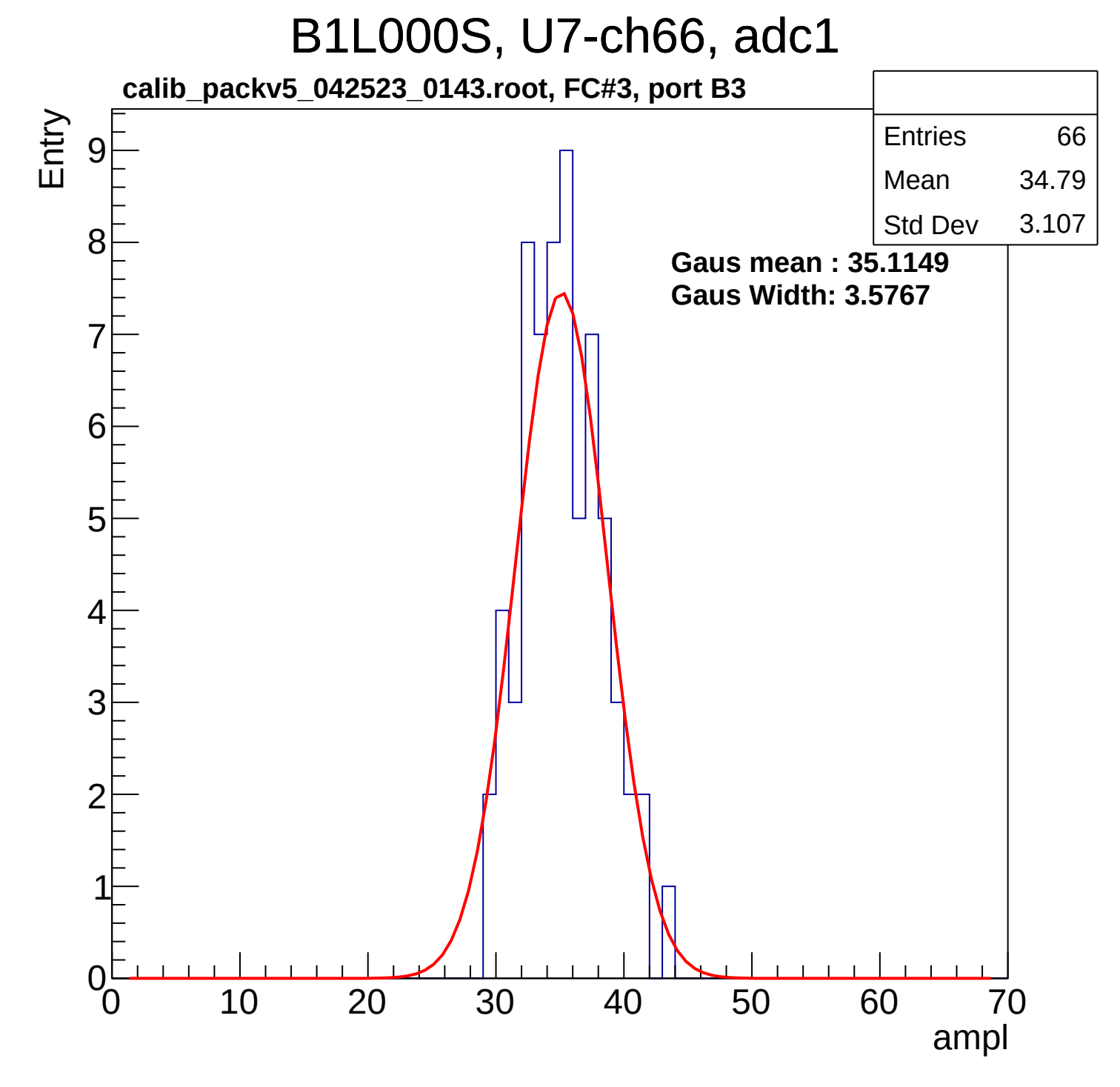
B1L000S, U7-ch66, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.79
Std Dev 3.107
Gaus mean : 35.1149
Gaus Width: 3.5767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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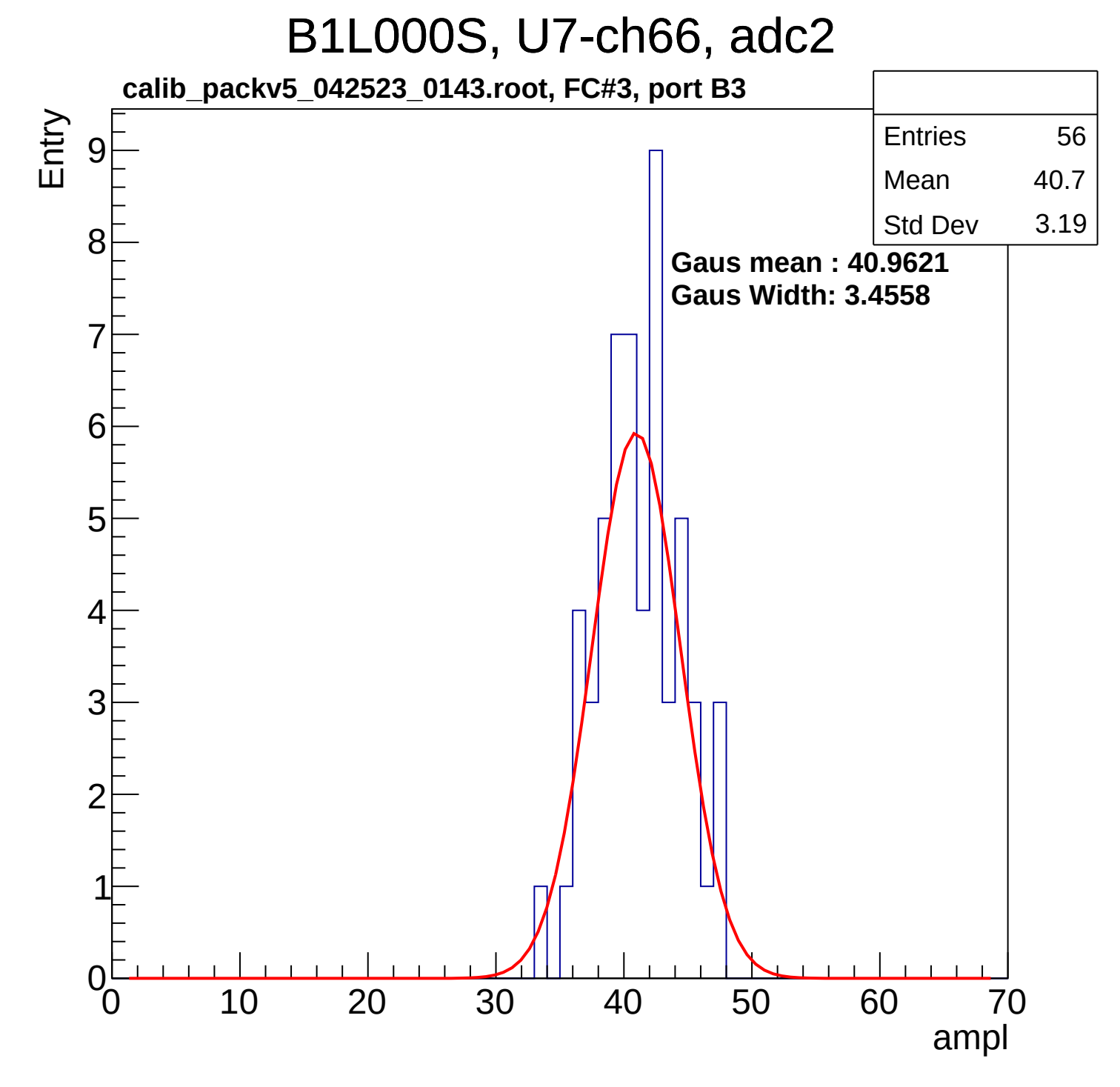
B1L000S, U7-ch66, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 40.7
Std Dev 3.19
Gaus mean : 40.9621
Gaus Width: 3.4558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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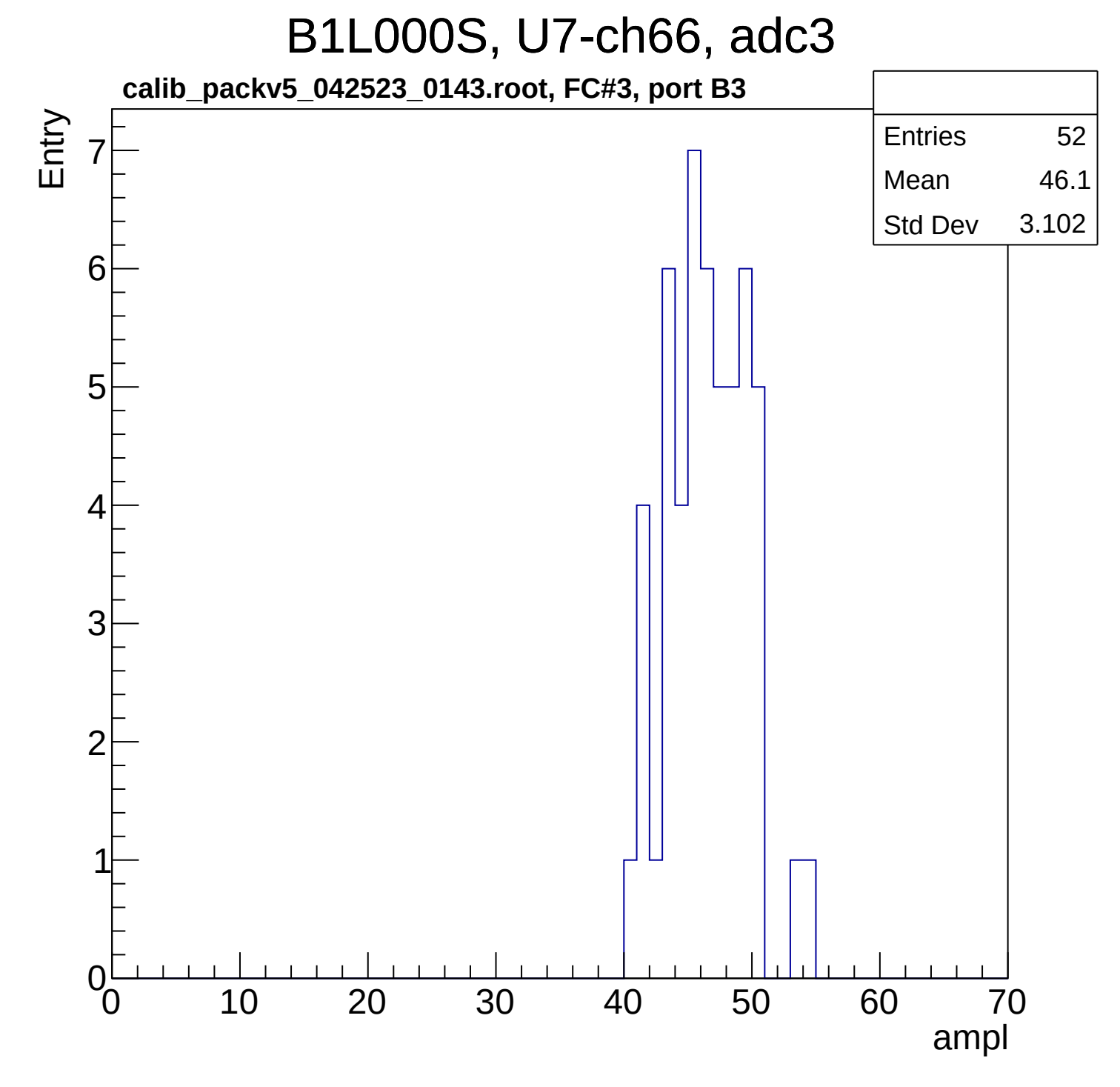
B1L000S, U7-ch66, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 46.1
Std Dev 3.102
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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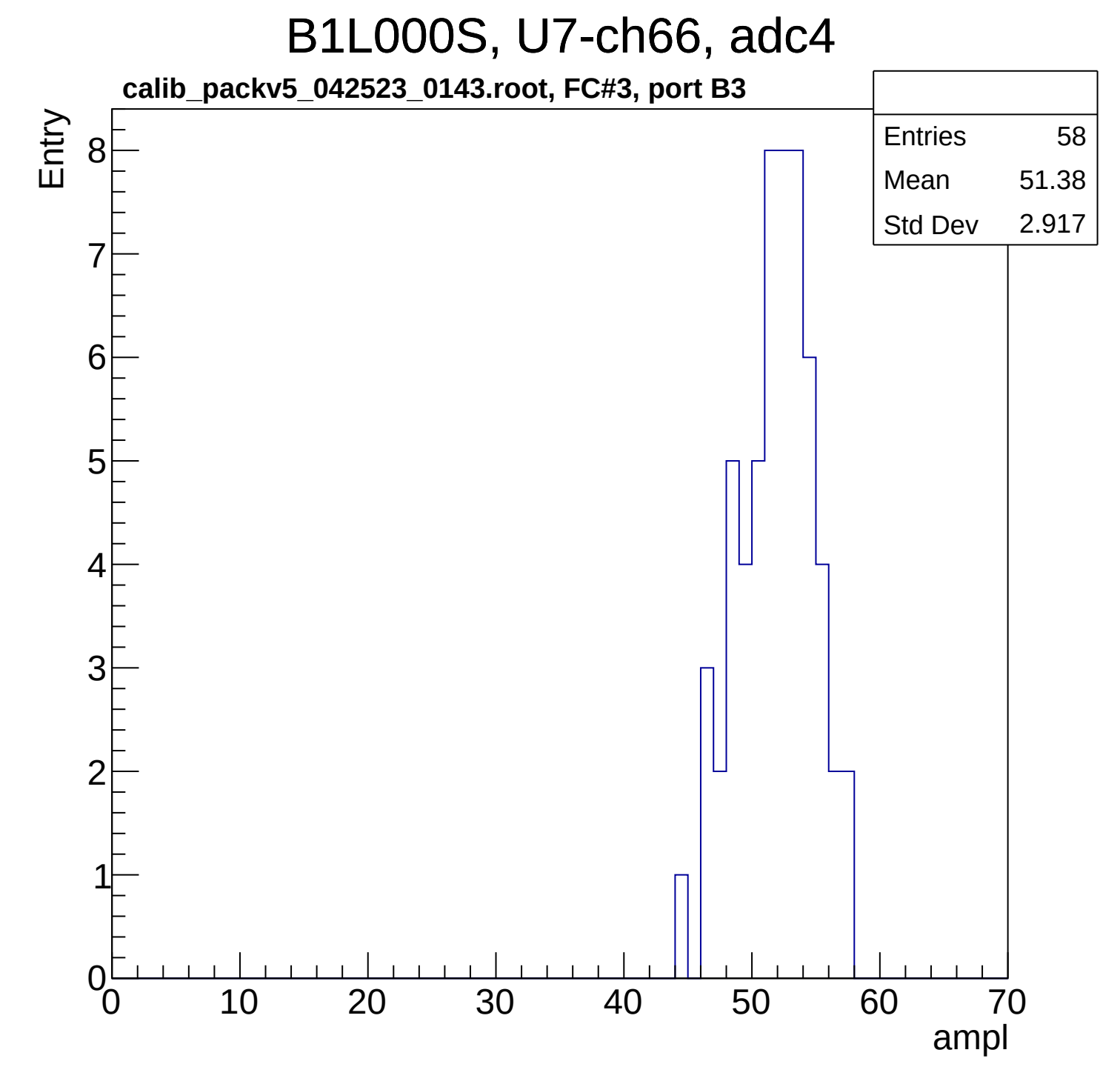
B1L000S, U7-ch66, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 51.38
Std Dev 2.917
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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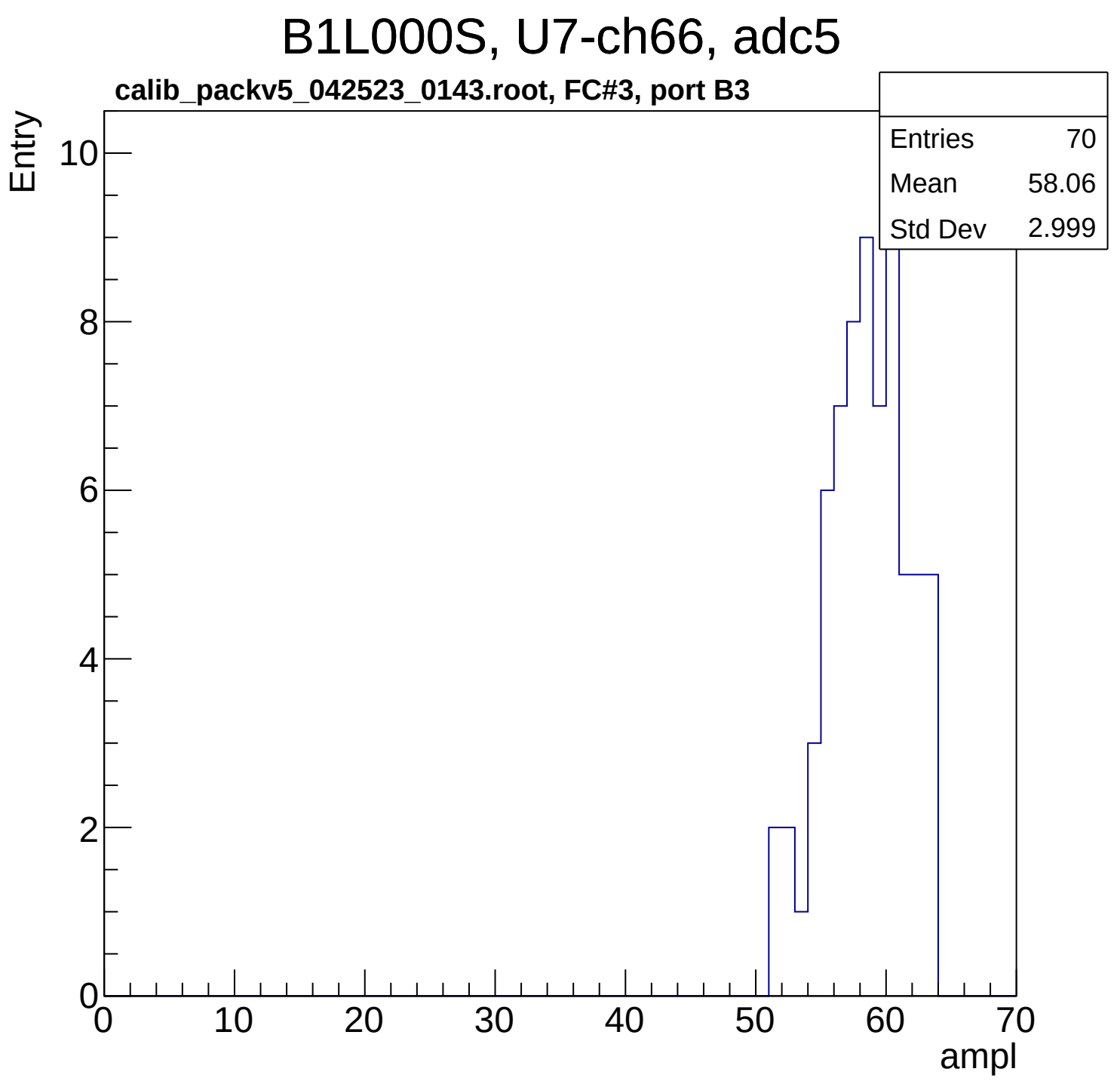
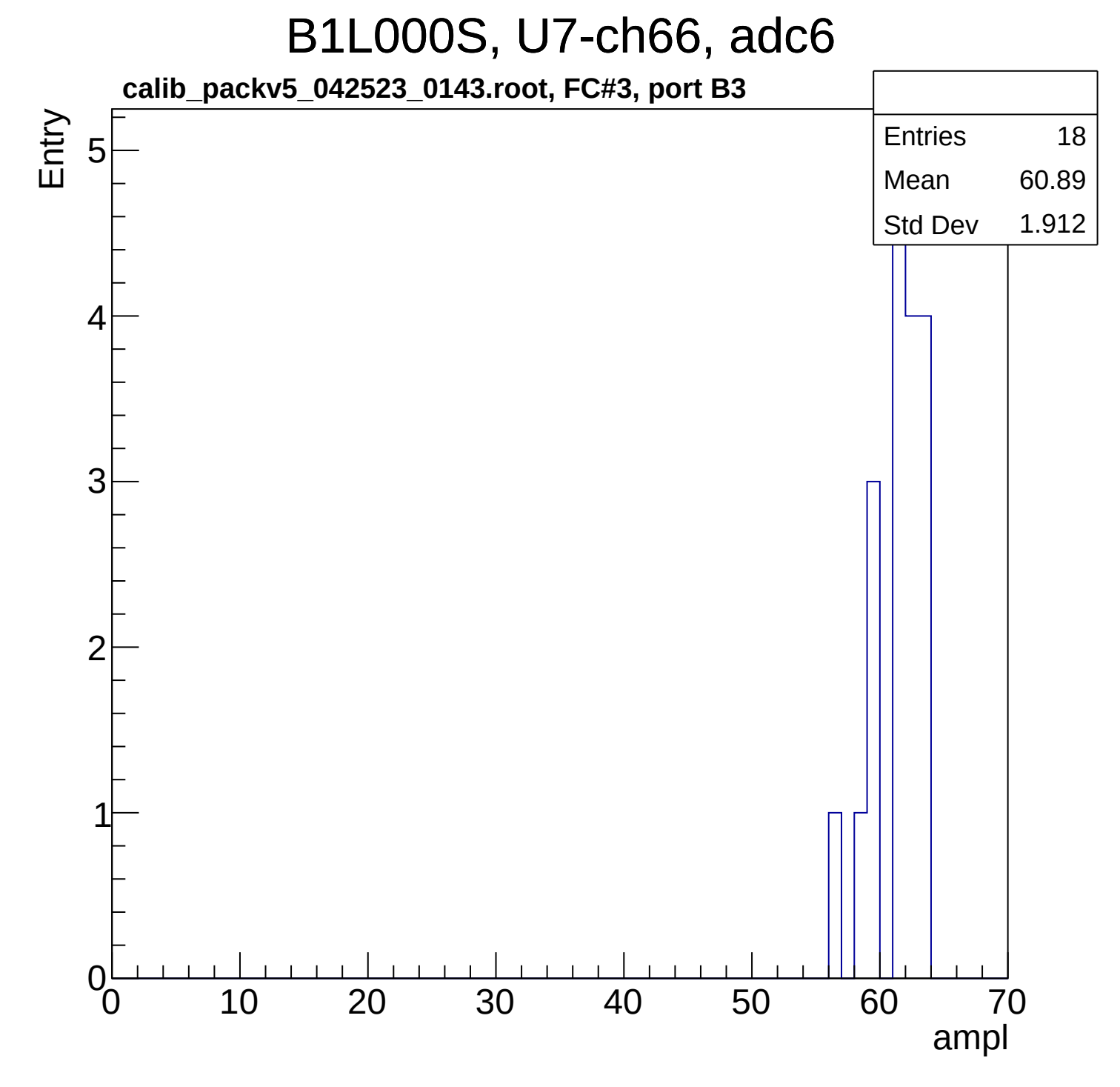
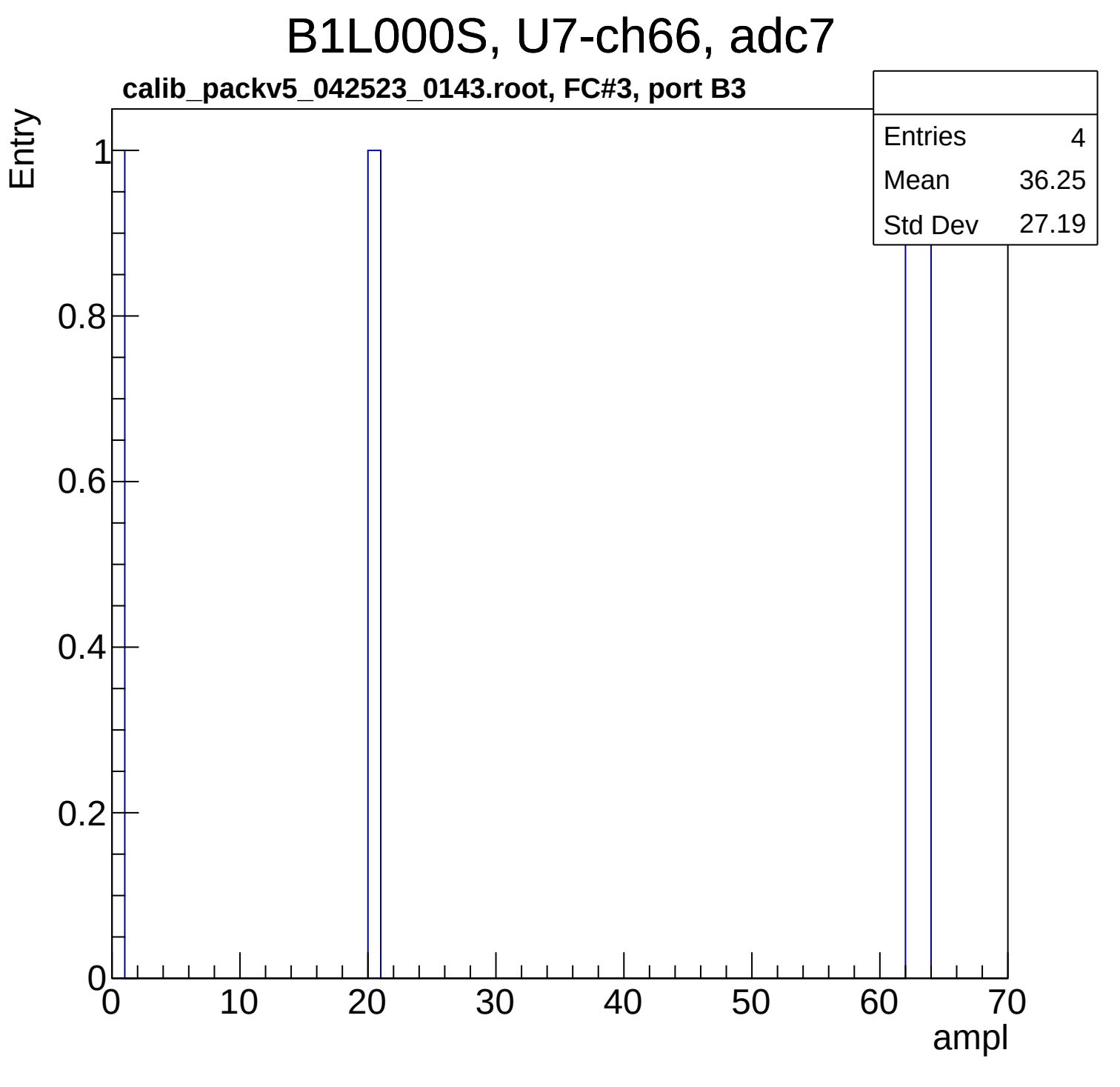

B1L000S, U7-ch66, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 58.06
Std Dev 2.999
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch66, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 60.89
Std Dev 1.912
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L000S, U7-ch66, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 36.25
Std Dev 27.19
Entry
ampl
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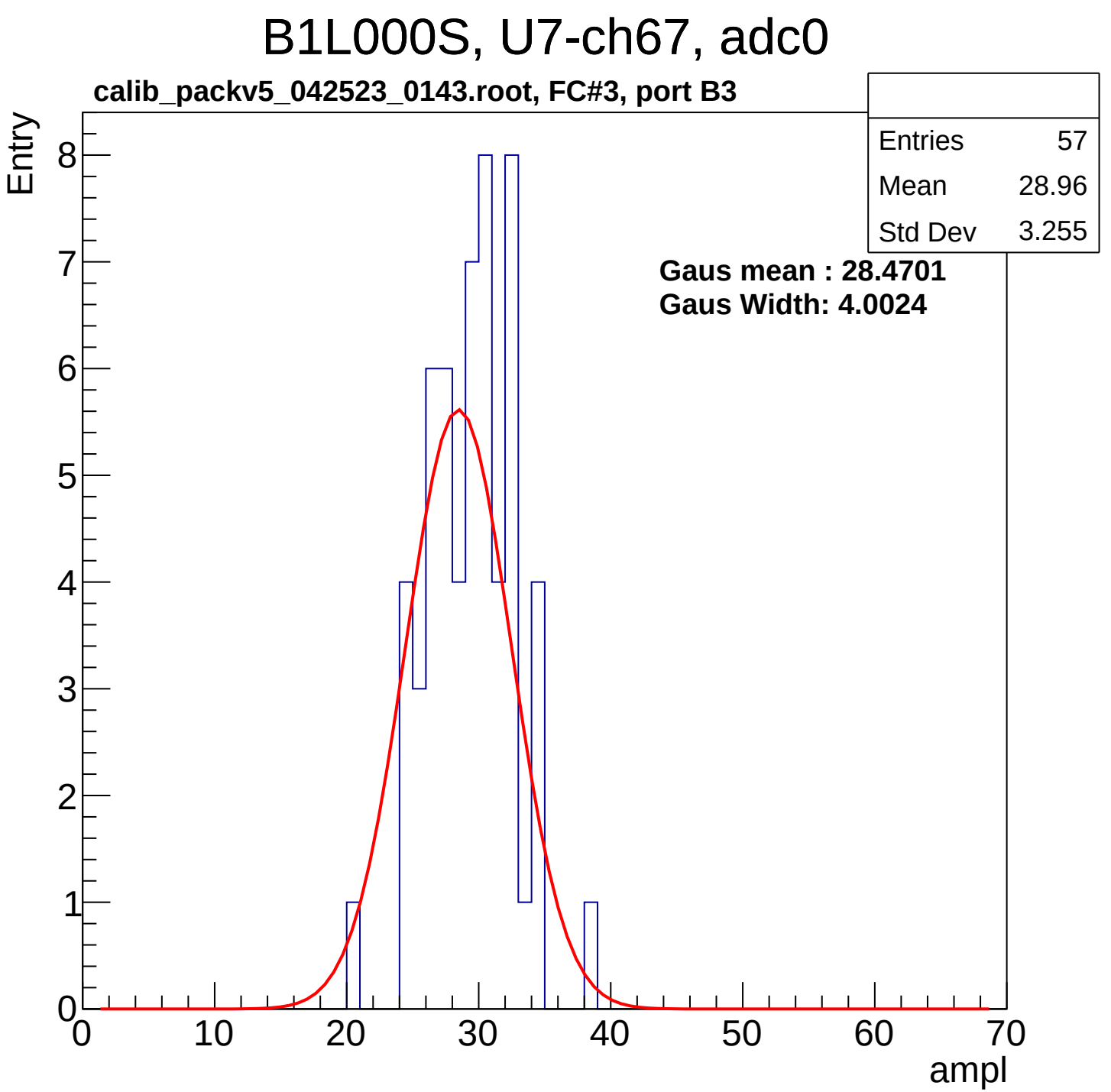
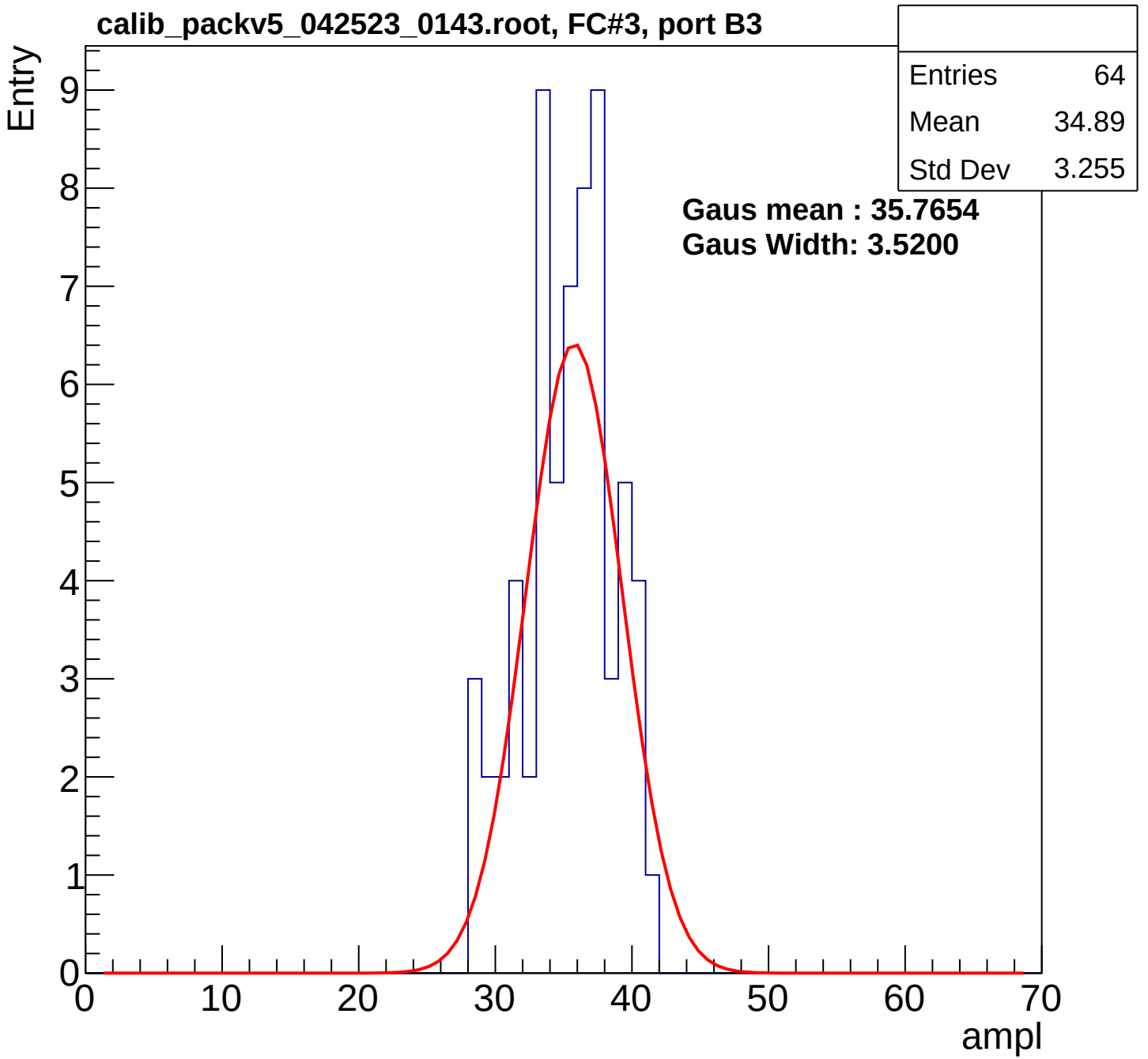
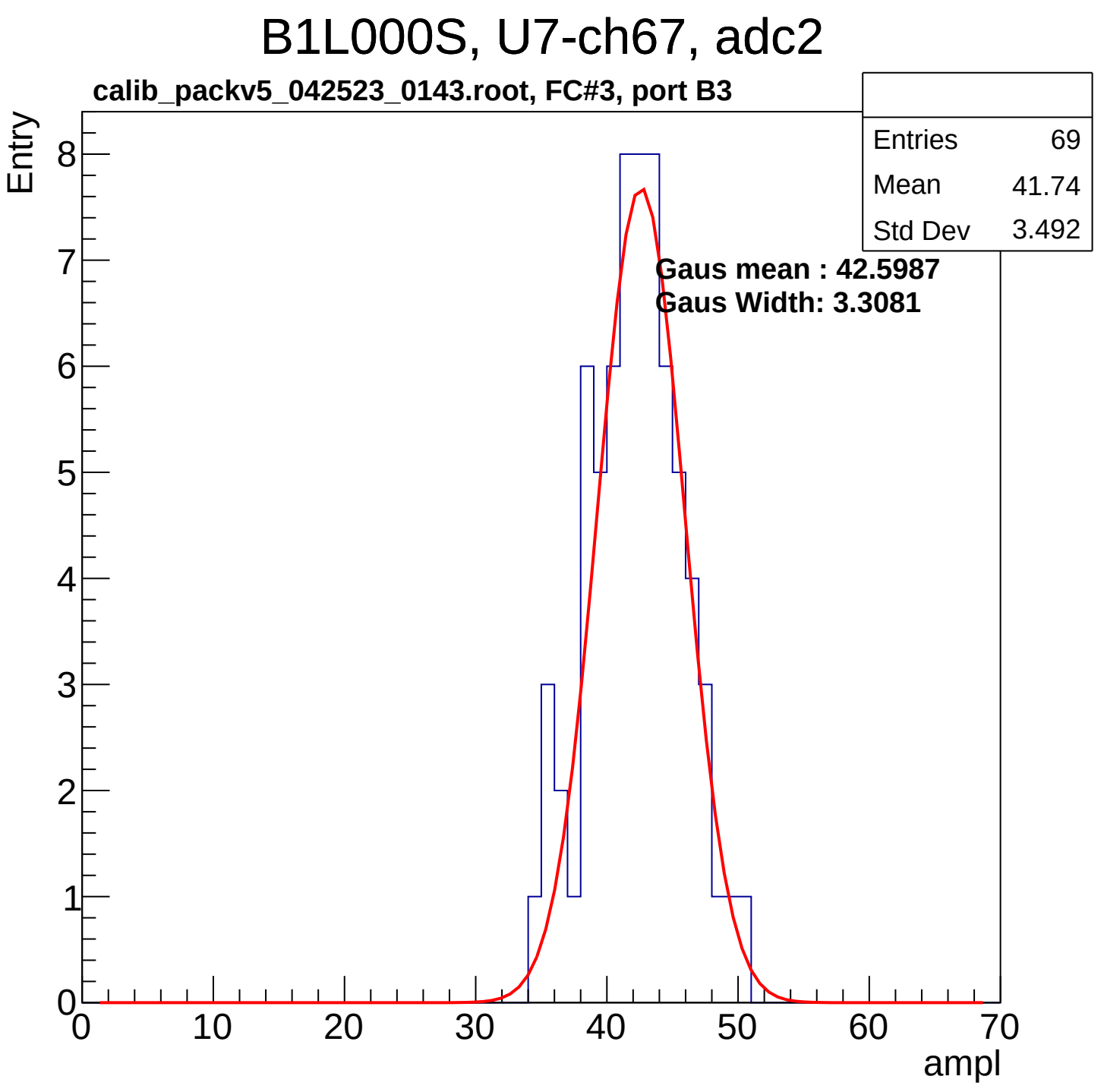
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L000S, U7-ch67, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 28.96
Std Dev 3.255
Gaus mean : 28.4701
Gaus Width: 4.0024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch67, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 34.89
Std Dev 3.255
Gaus mean : 35.7654
Gaus Width: 3.5200
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch67, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 41.74
Std Dev 3.492
Gaus mean : 42.5987
Gaus Width: 3.3081
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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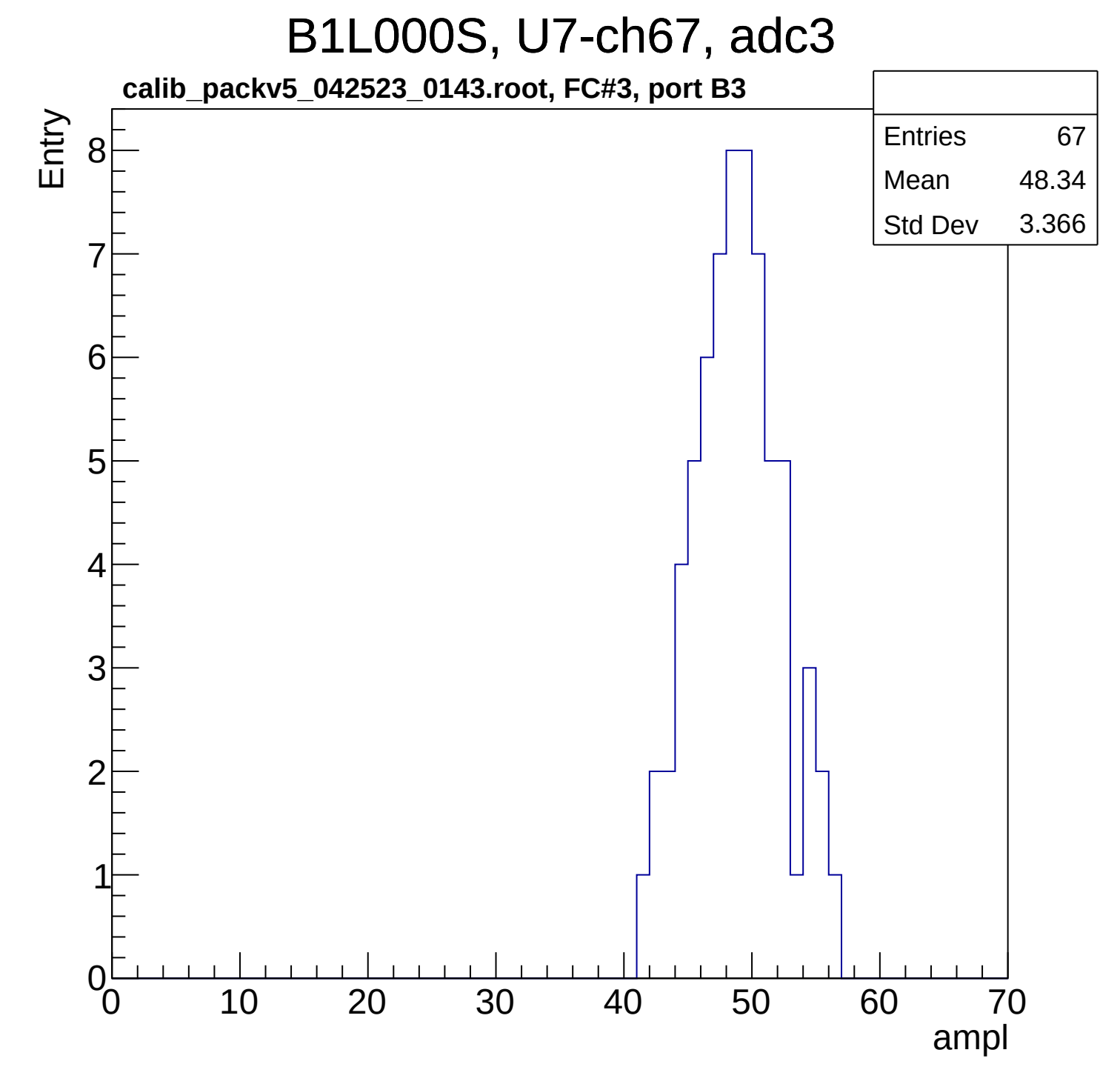
B1L000S, U7-ch67, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 48.34
Std Dev 3.366
Entry
ampl
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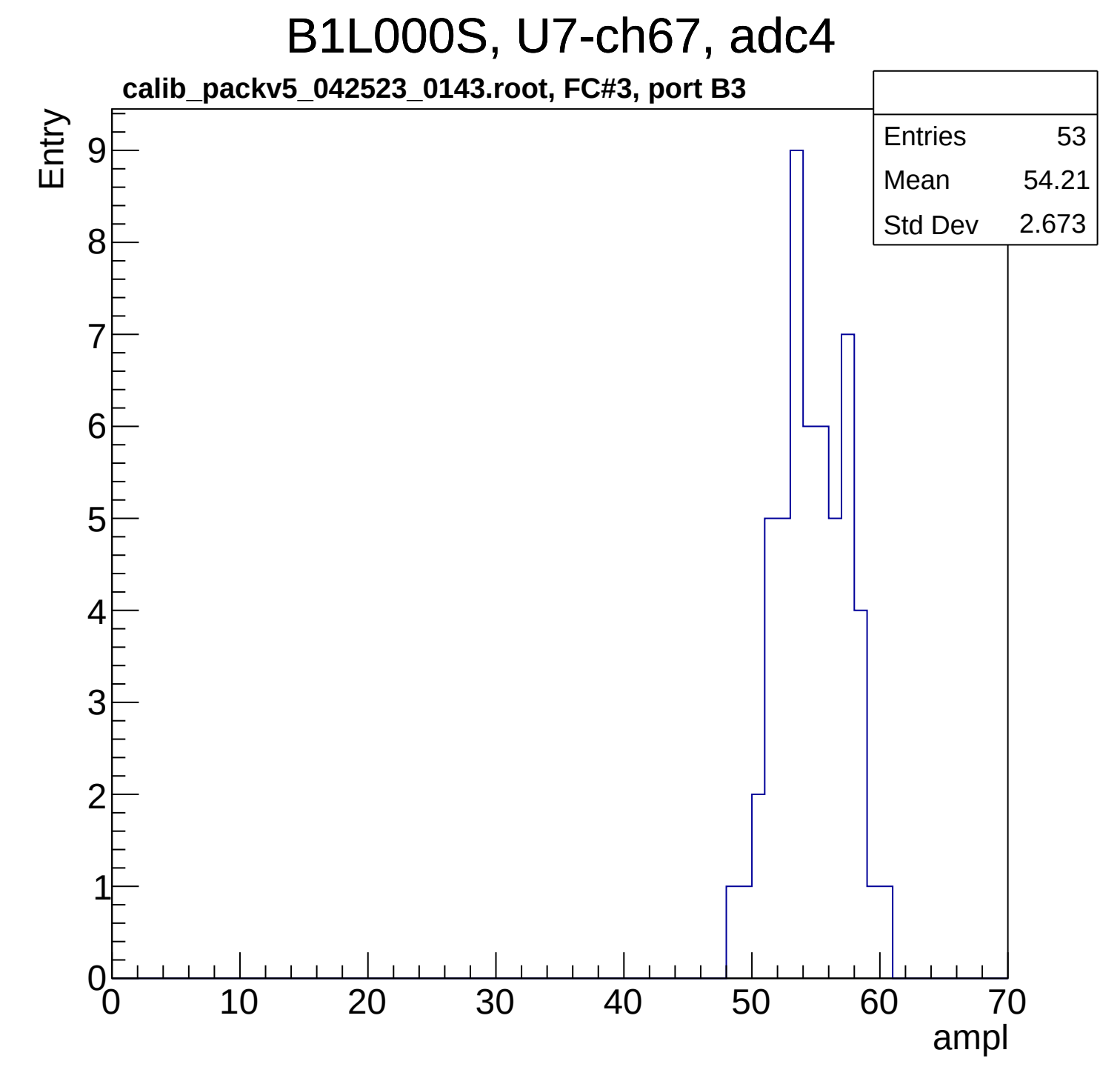
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch67, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 54.21
Std Dev 2.673
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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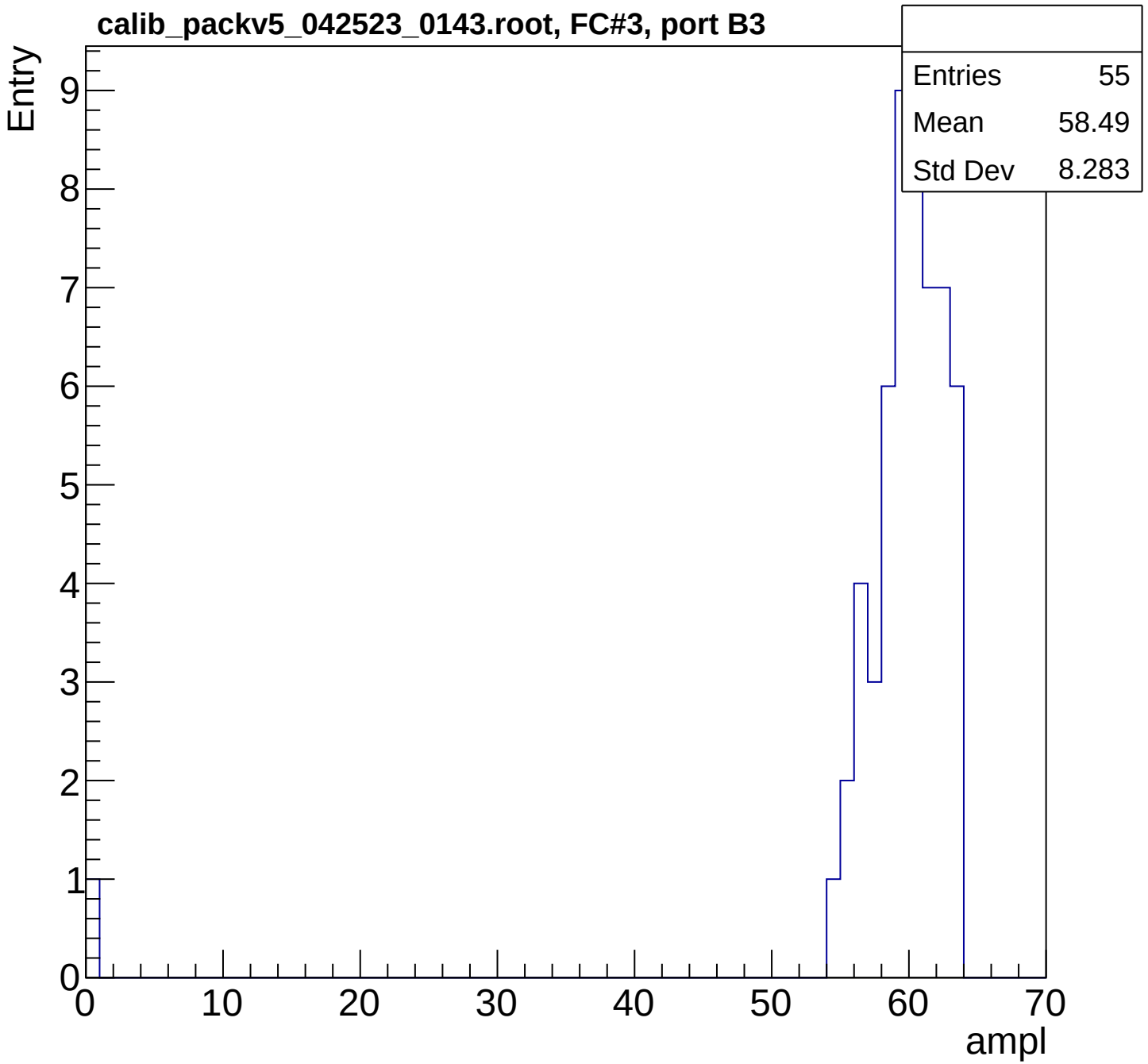
B1L000S, U7-ch67, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 58.49
Std Dev 8.283
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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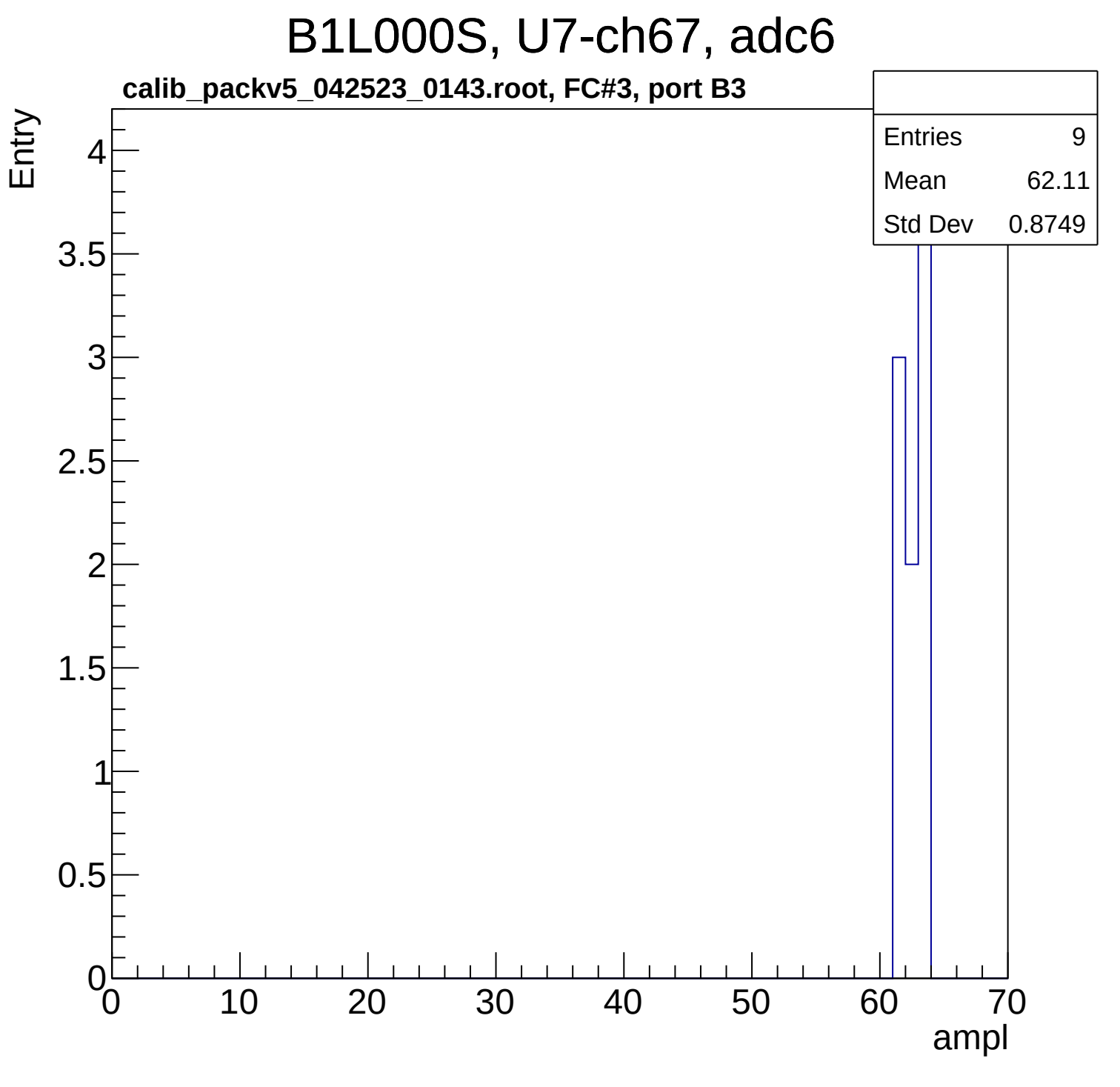
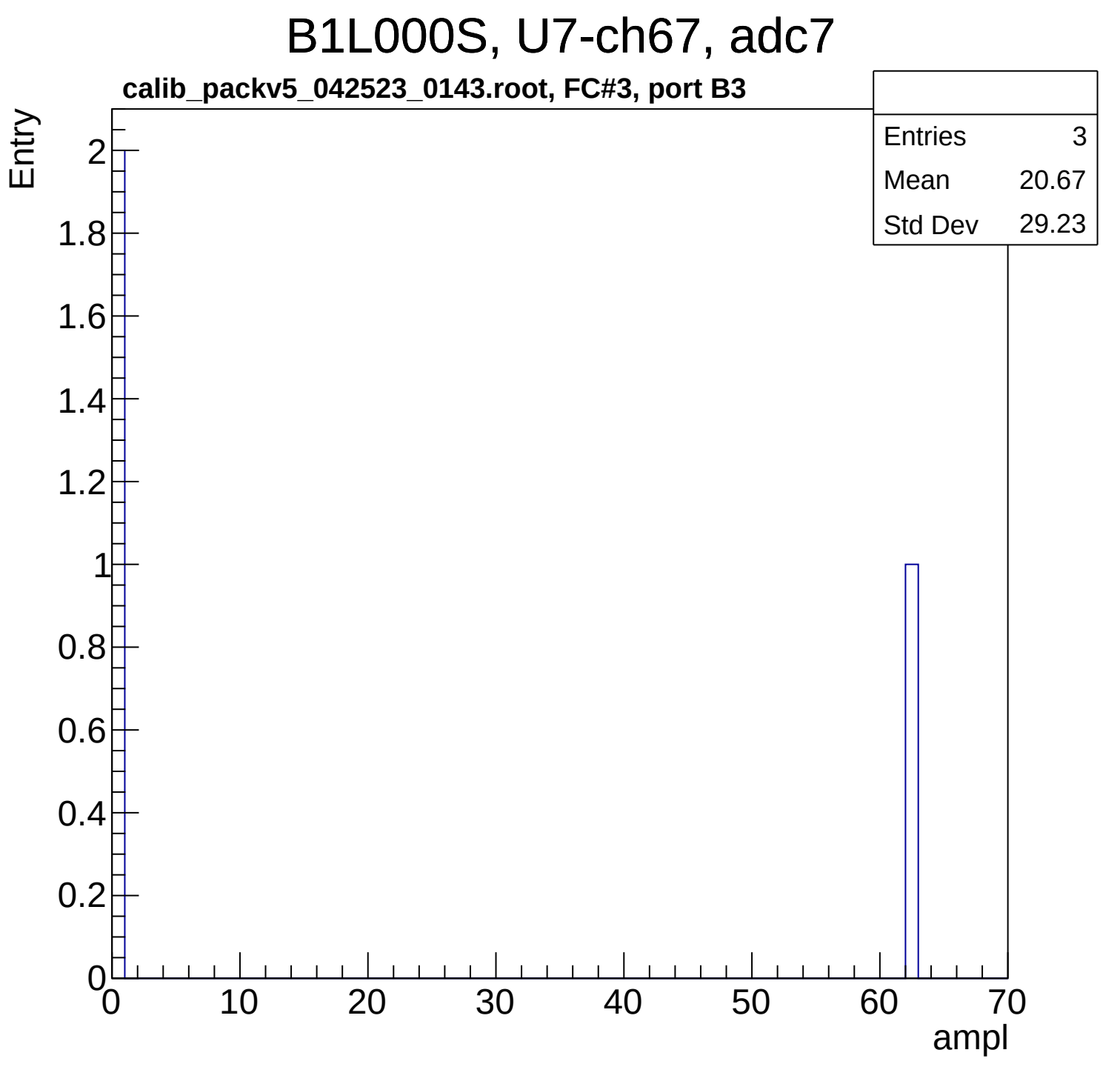

B1L000S, U7-ch67, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 62.11
Std Dev 0.8749
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch67, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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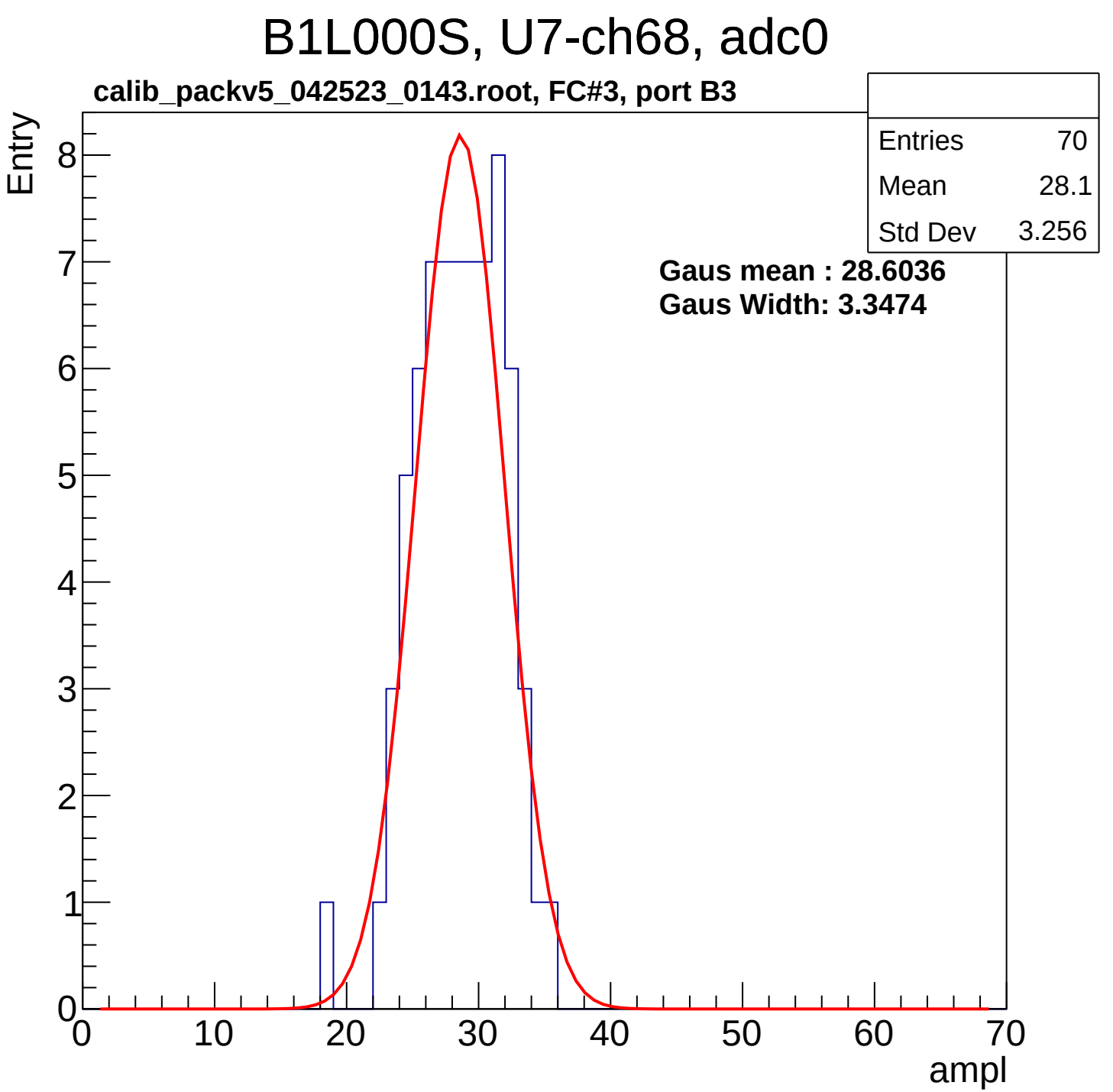
B1L000S, U7-ch68, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 28.1
Std Dev 3.256
Gaus mean : 28.6036
Gaus Width: 3.3474
Entry
ampl
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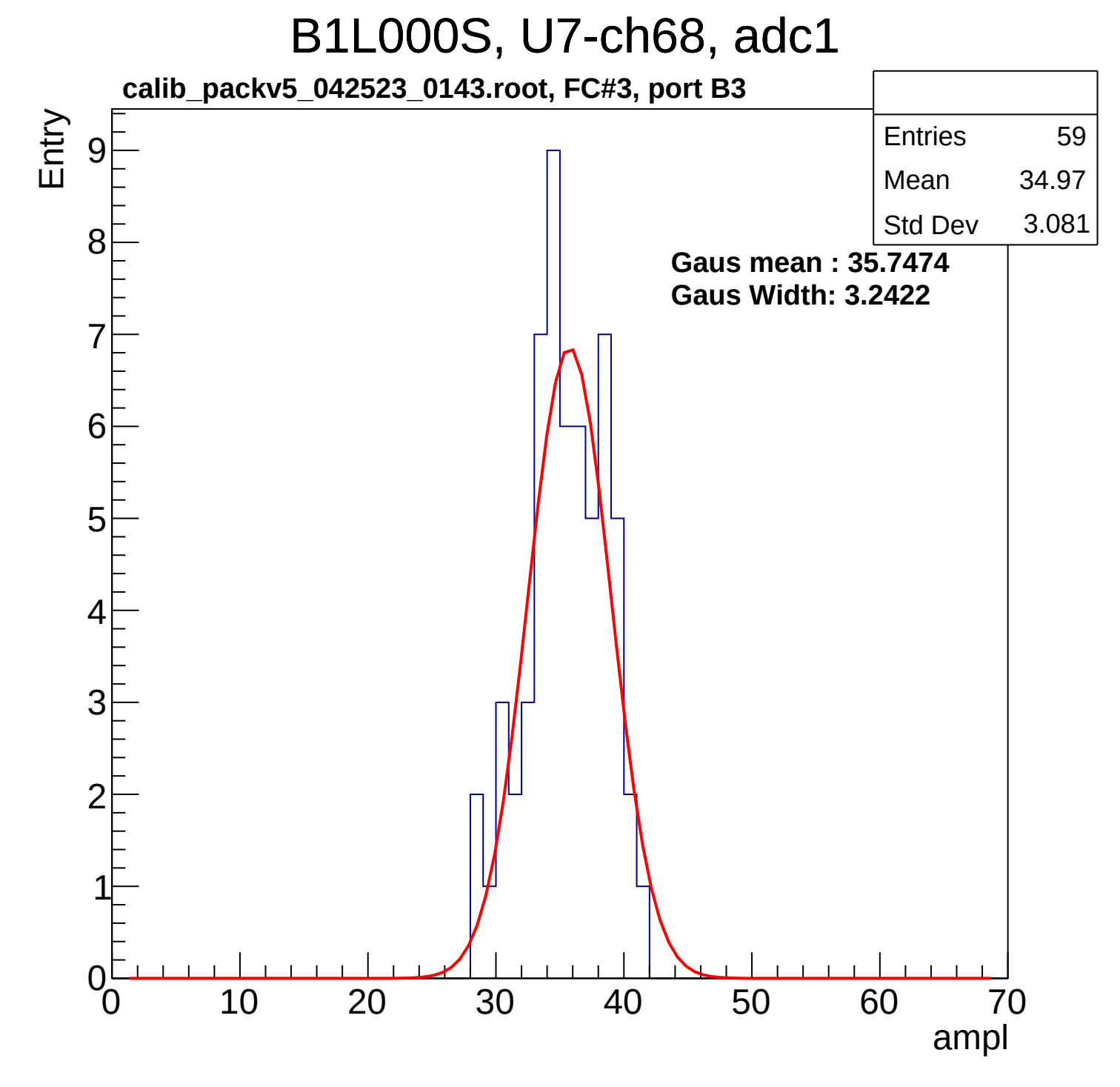
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch68, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 34.97
Std Dev 3.081
Gaus mean : 35.7474
Gaus Width: 3.2422
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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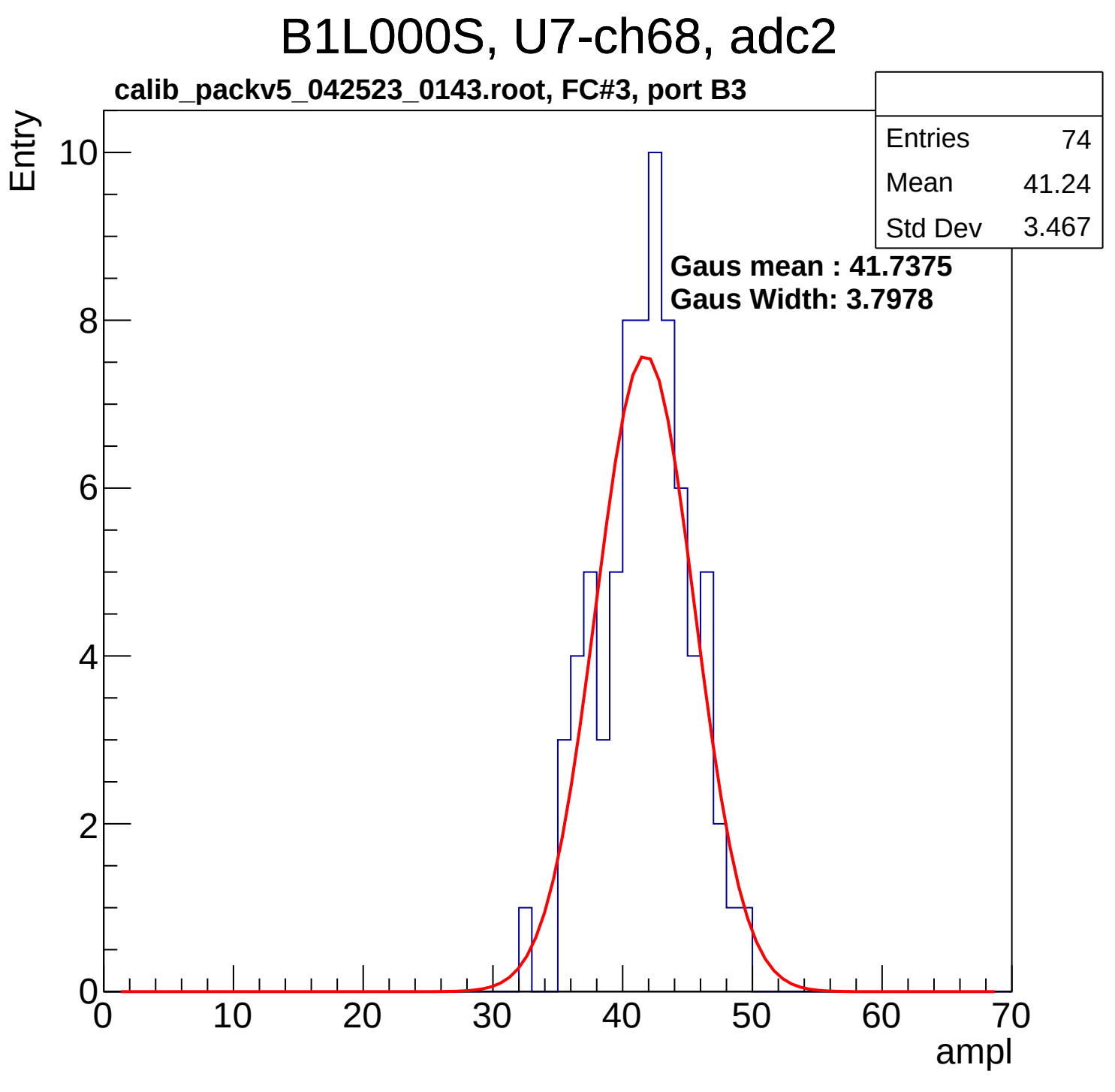
B1L000S, U7-ch68, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 41.24
Std Dev 3.467
Gaus mean : 41.7375
Gaus Width: 3.7978
Entry
ampl
0
2
4
6
8
10
0
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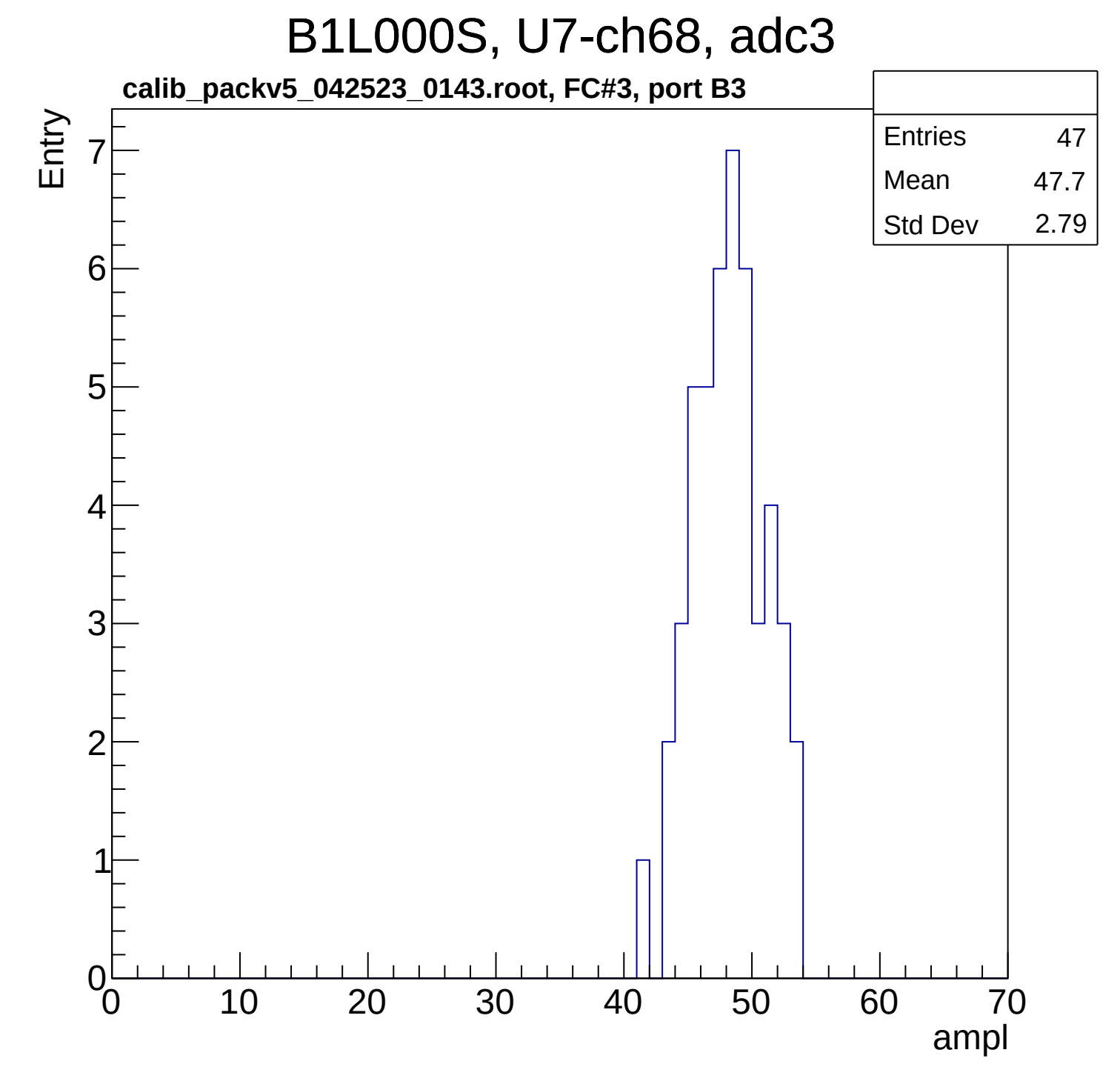
20
30
40
50
60
70


B1L000S, U7-ch68, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 47.7
Std Dev 2.79
Entry
ampl
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0
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60
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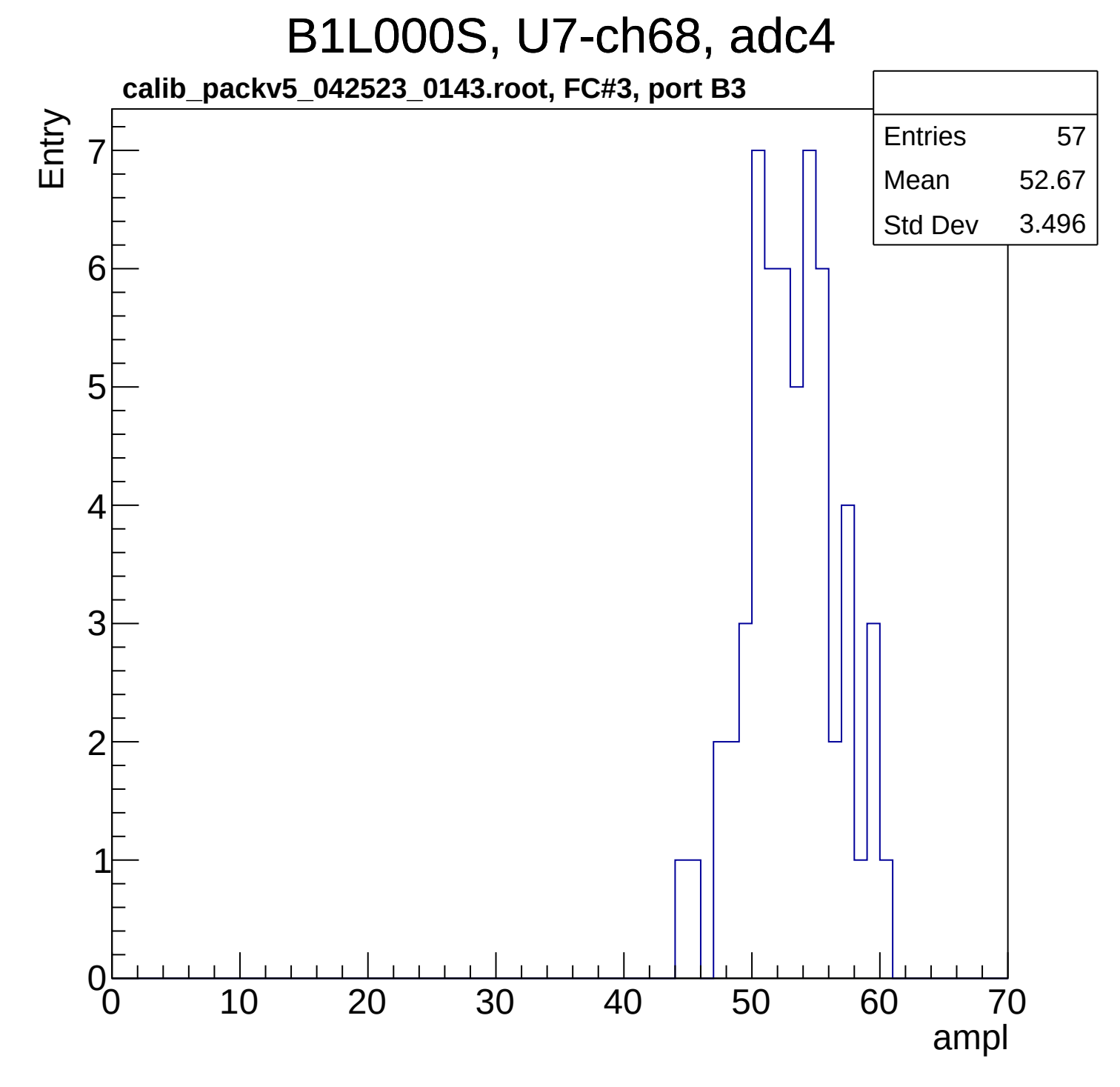
B1L000S, U7-ch68, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 52.67
Std Dev 3.496
Entry
ampl
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1
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3
4
5
6
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0
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20
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60
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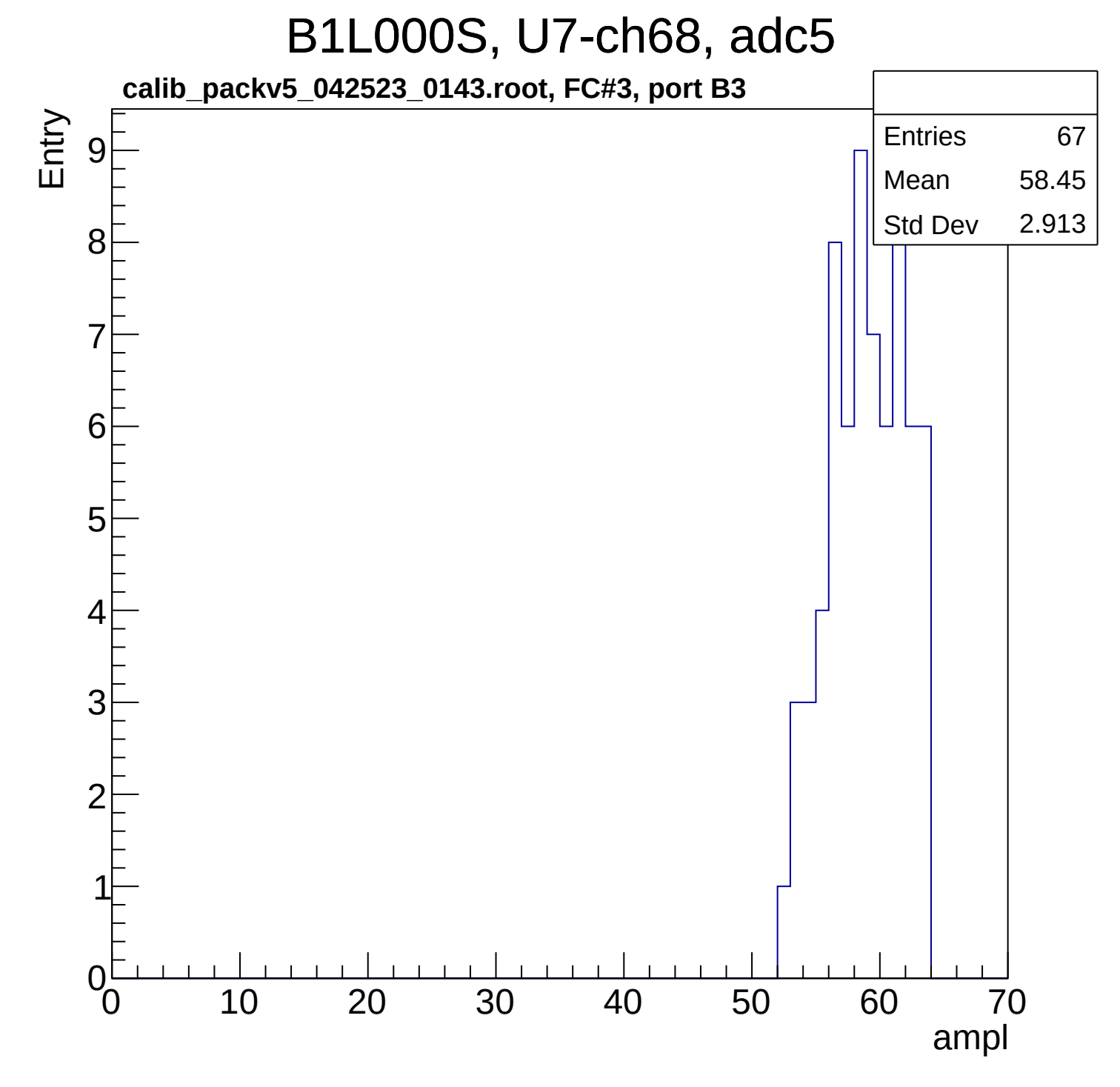
B1L000S, U7-ch68, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 58.45
Std Dev 2.913
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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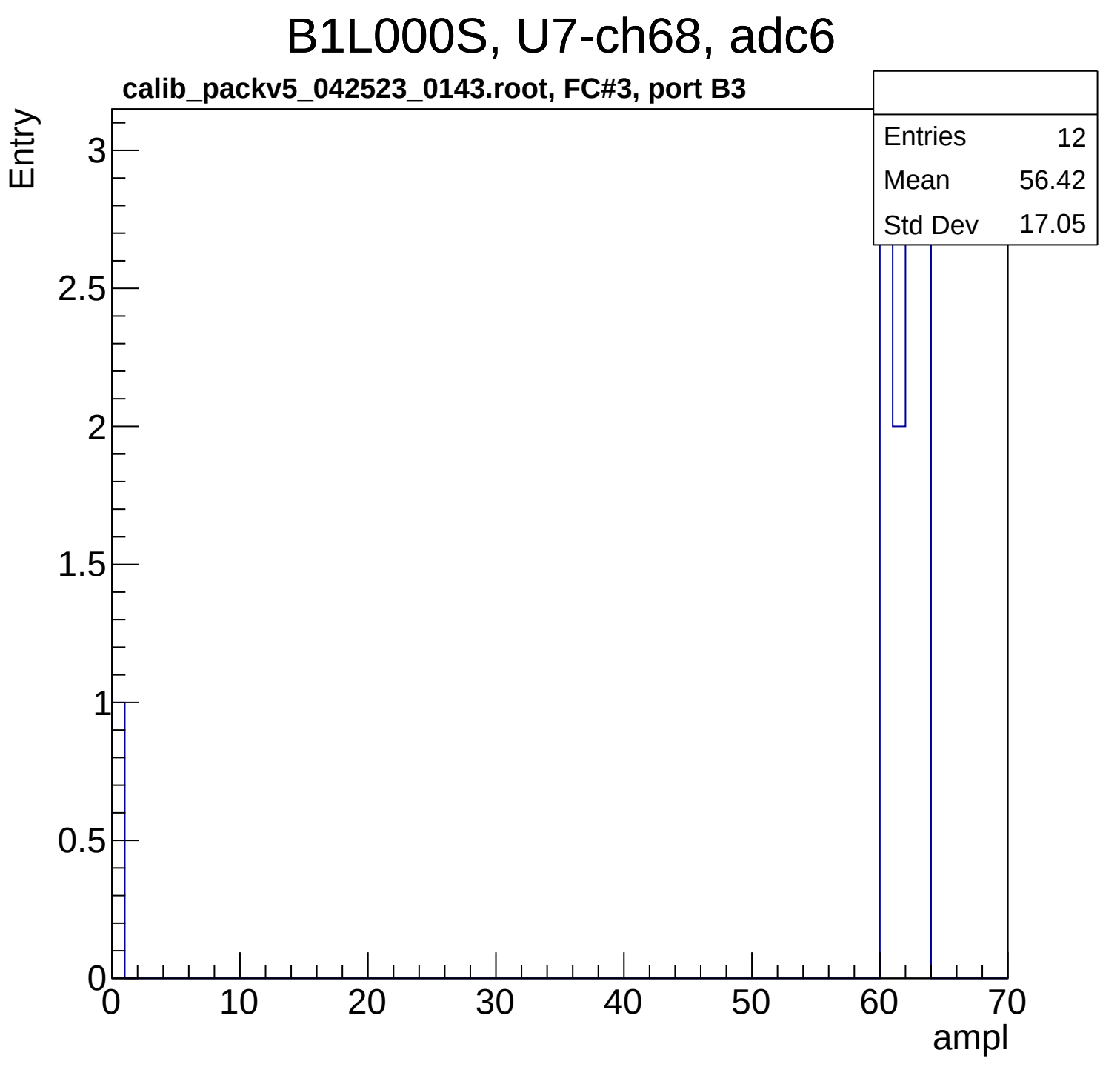
B1L000S, U7-ch68, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.42
Std Dev 17.05
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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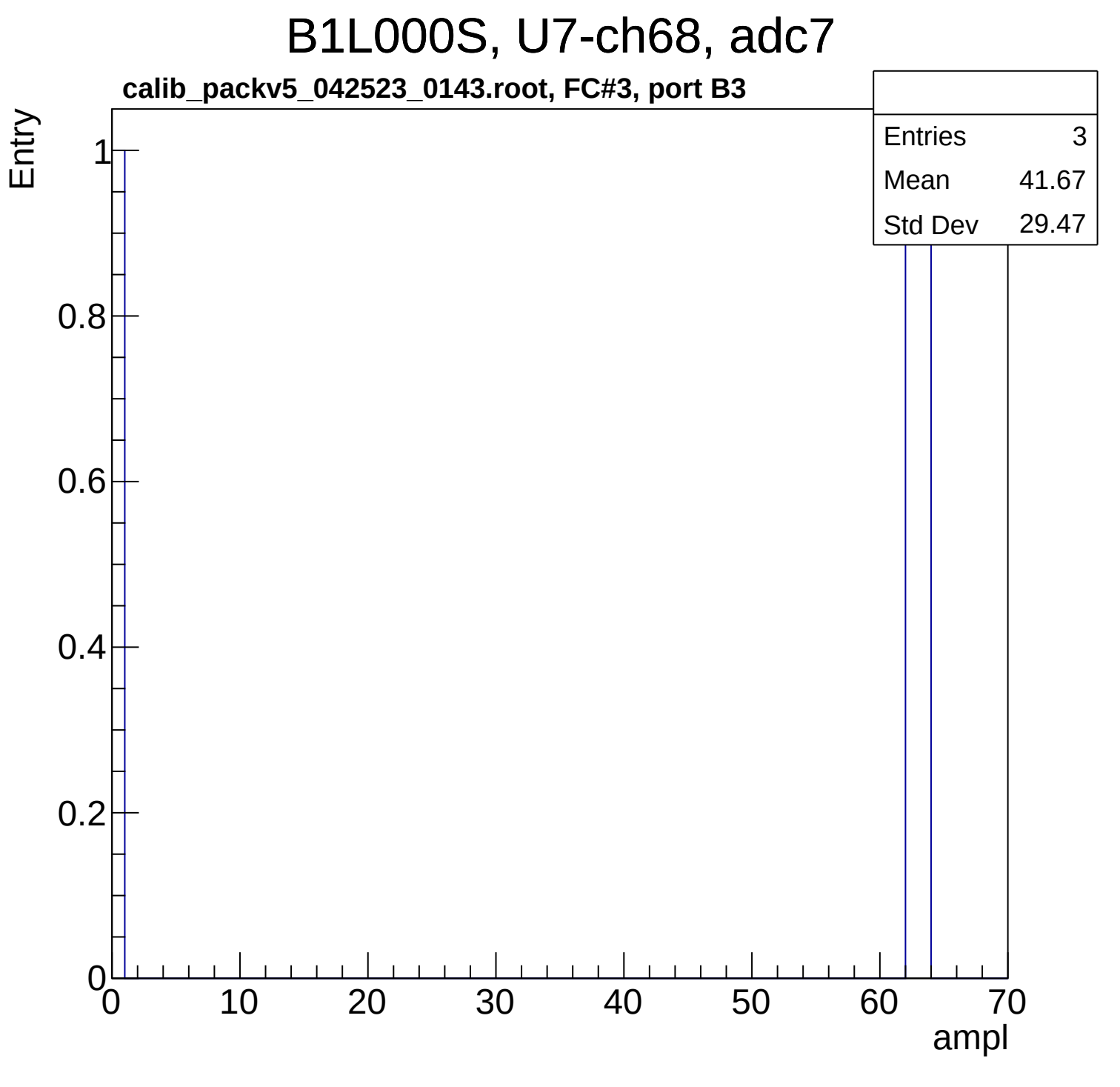
B1L000S, U7-ch68, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
0.2
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10
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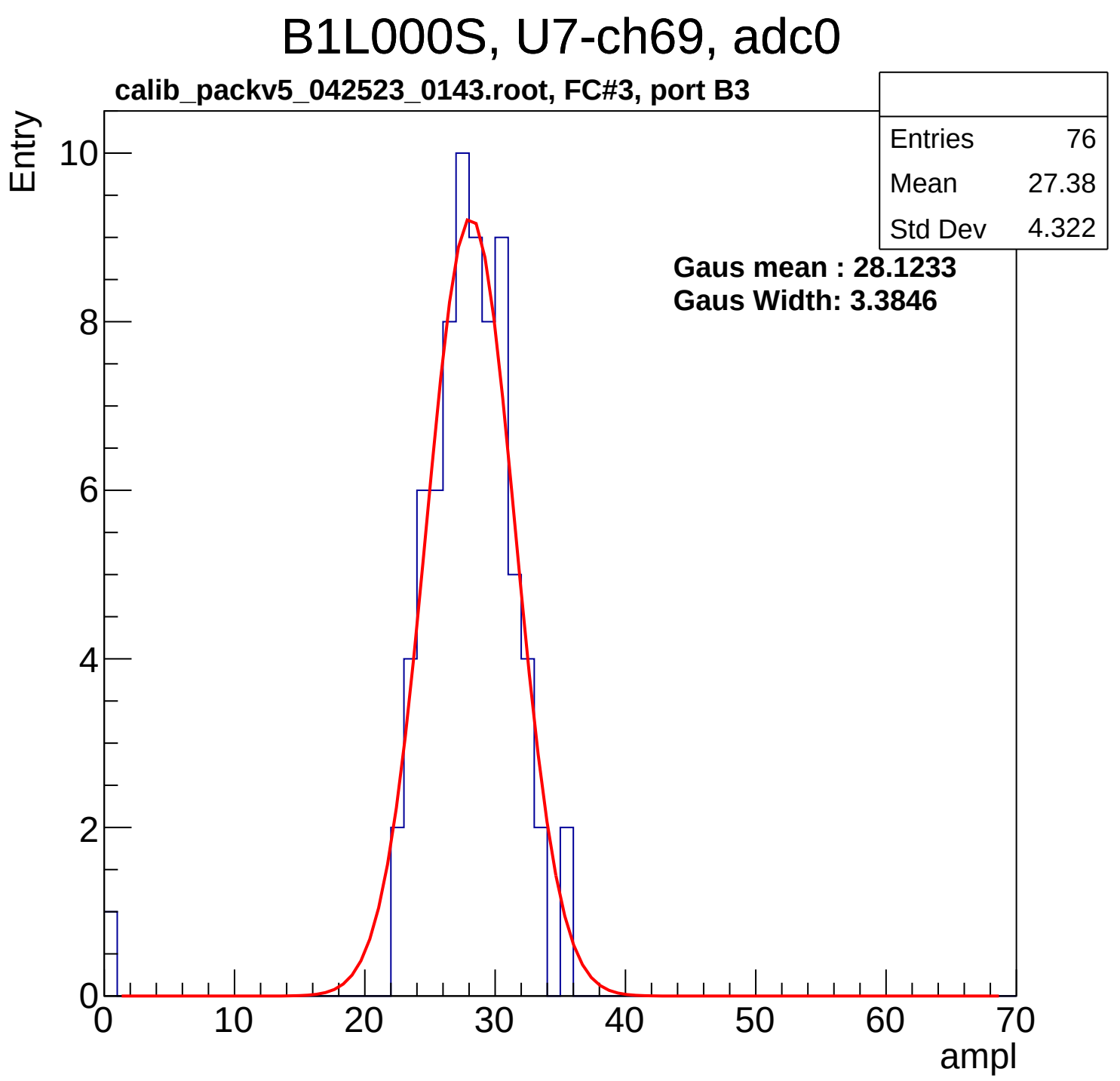
50
60
70
ampl


B1L000S, U7-ch69, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 27.38
Std Dev 4.322
Gaus mean : 28.1233
Gaus Width: 3.3846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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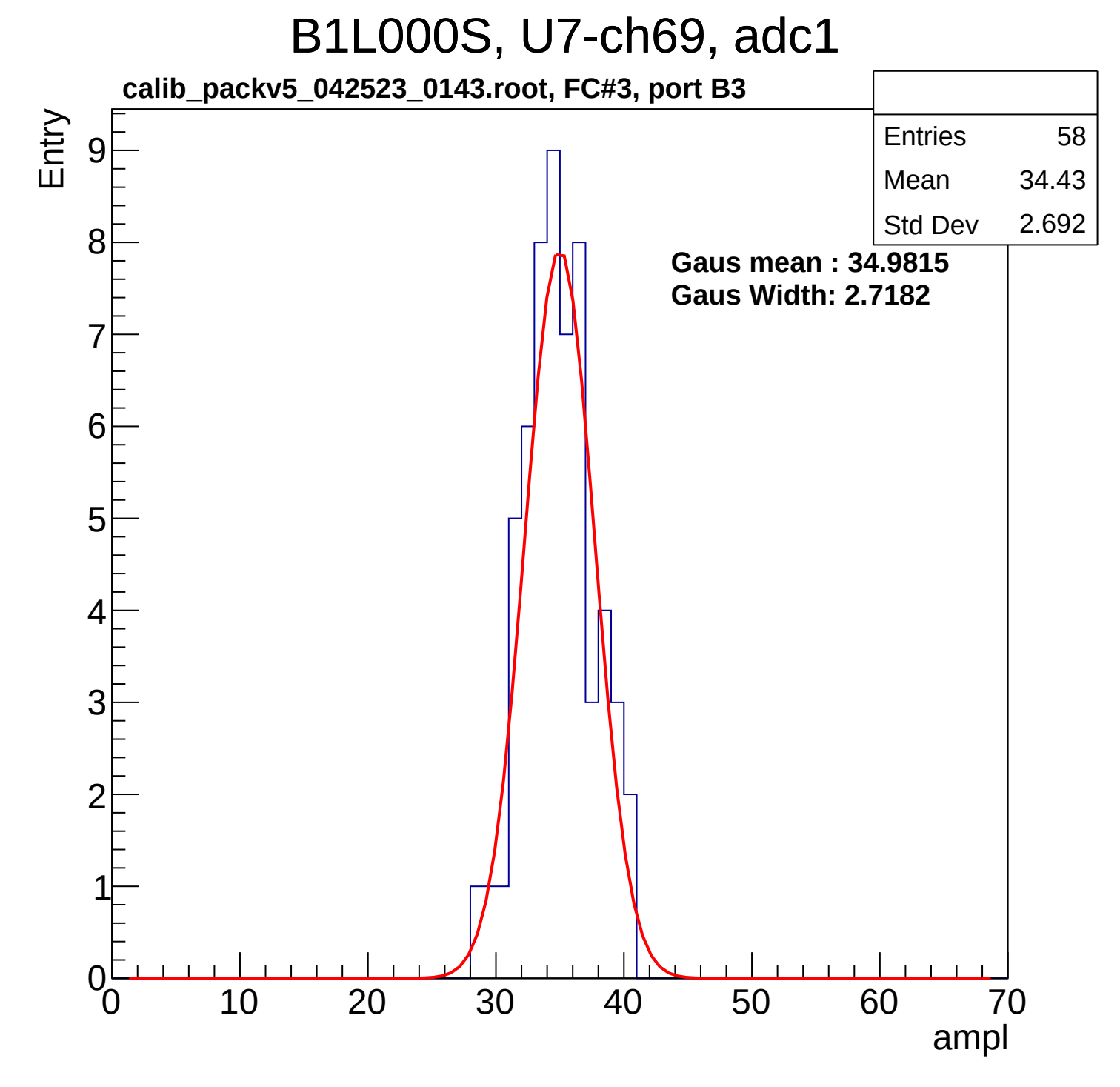
B1L000S, U7-ch69, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 34.43
Std Dev 2.692
Gaus mean : 34.9815
Gaus Width: 2.7182
Entry
ampl
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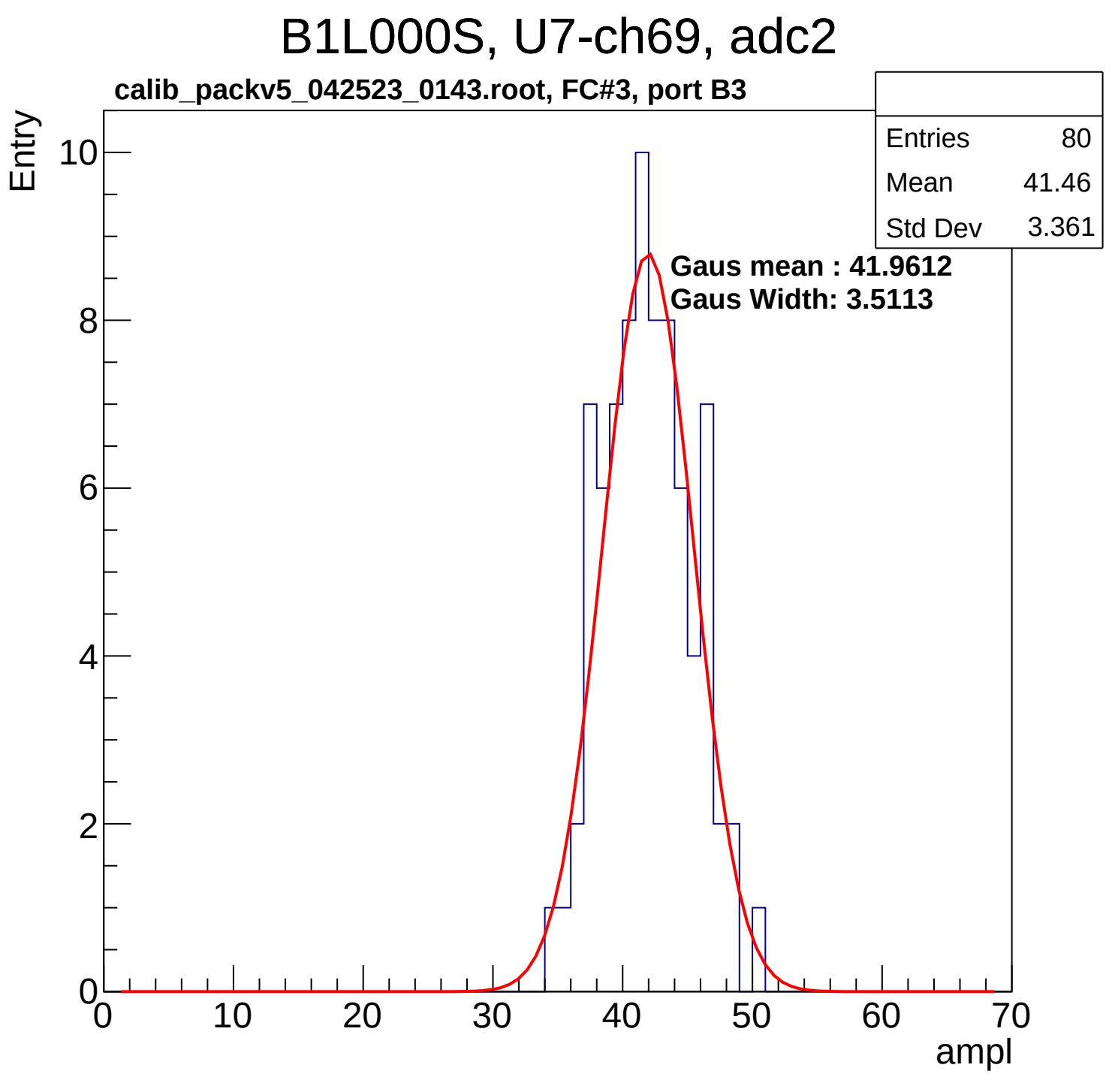
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch69, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 41.46
Std Dev 3.361
Gaus mean : 41.9612
Gaus Width: 3.5113
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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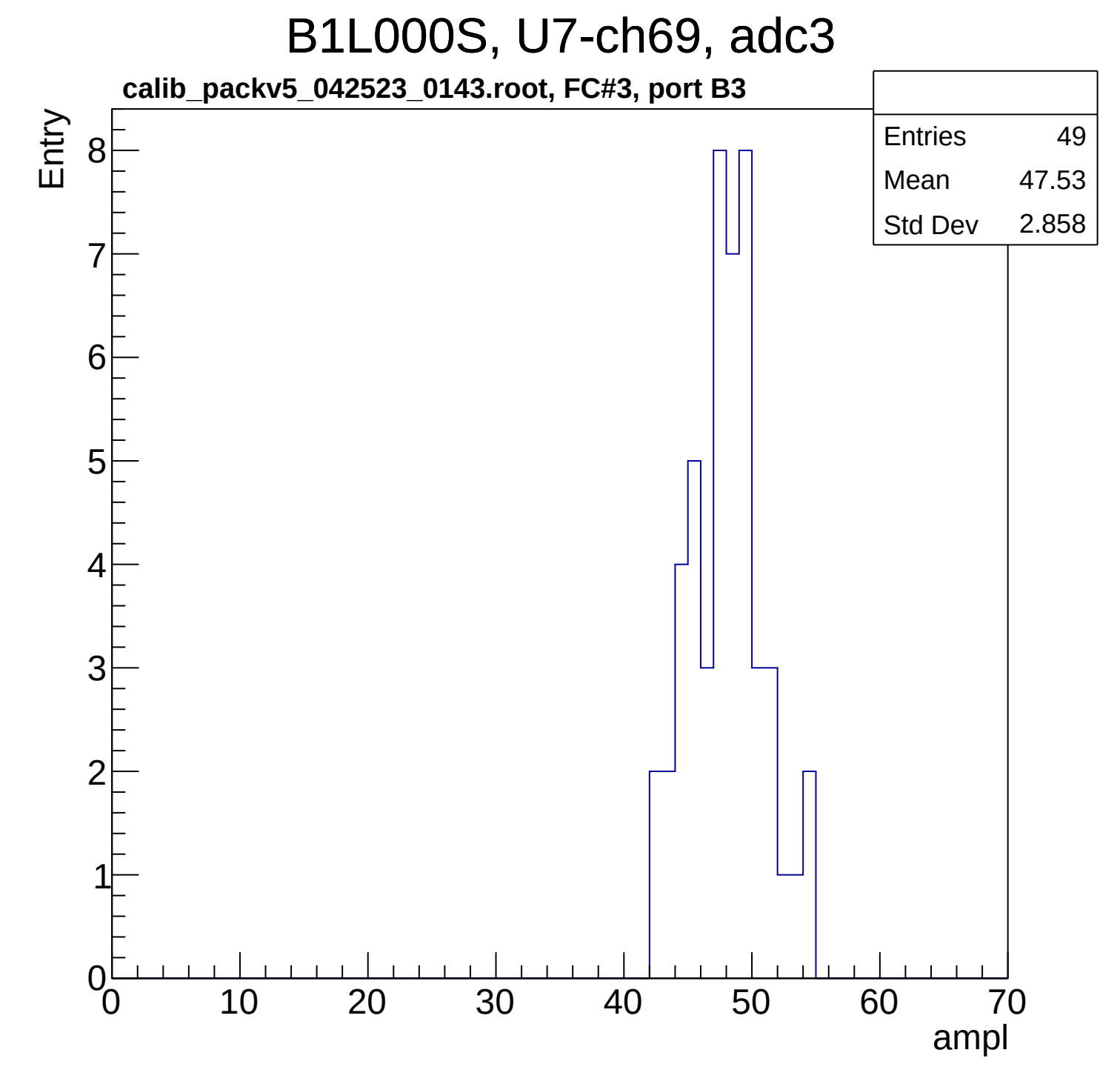
B1L000S, U7-ch69, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 47.53
Std Dev 2.858
Entry
ampl
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3
4
5
6
7
8
0
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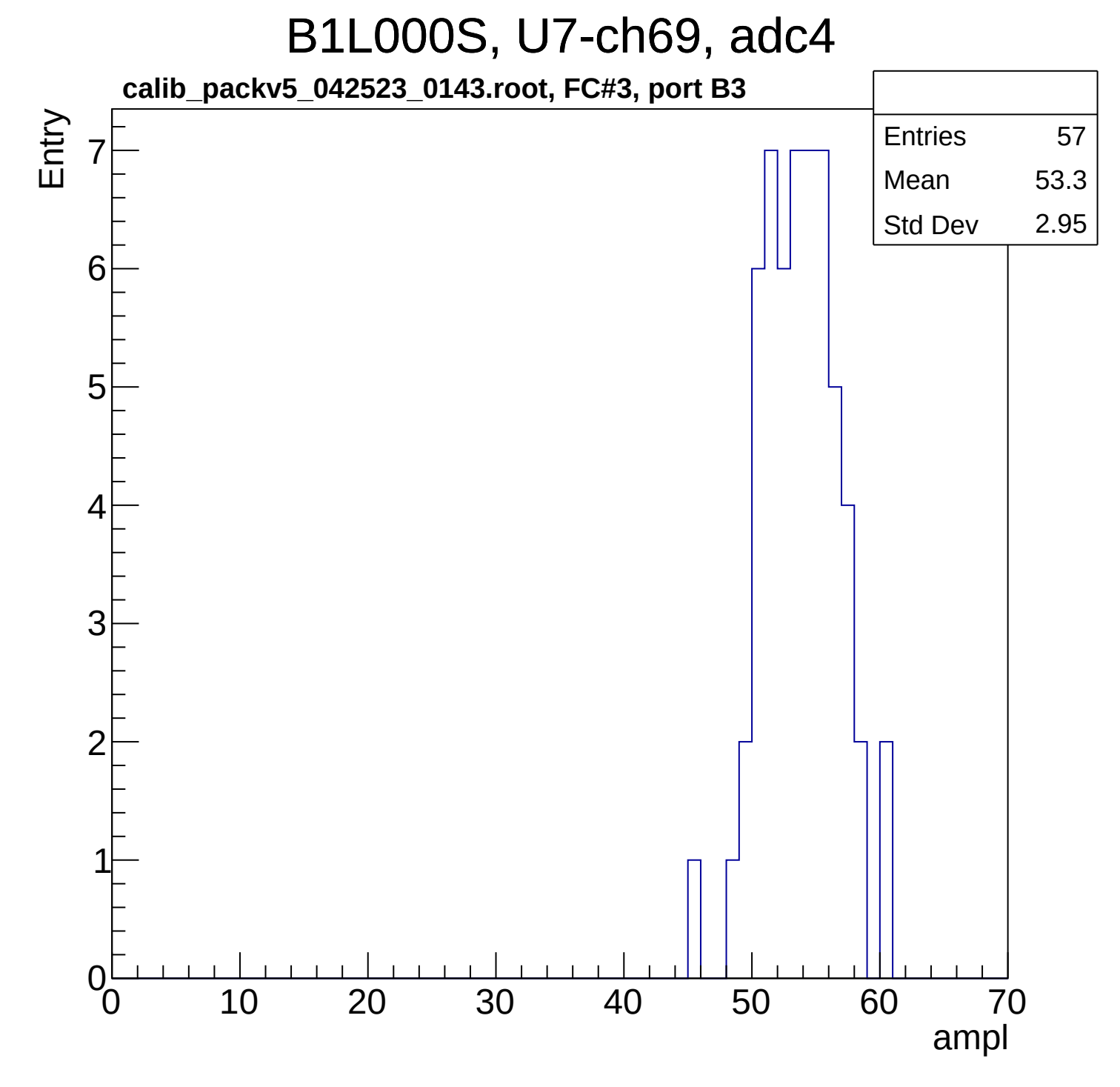
B1L000S, U7-ch69, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 53.3
Std Dev 2.95
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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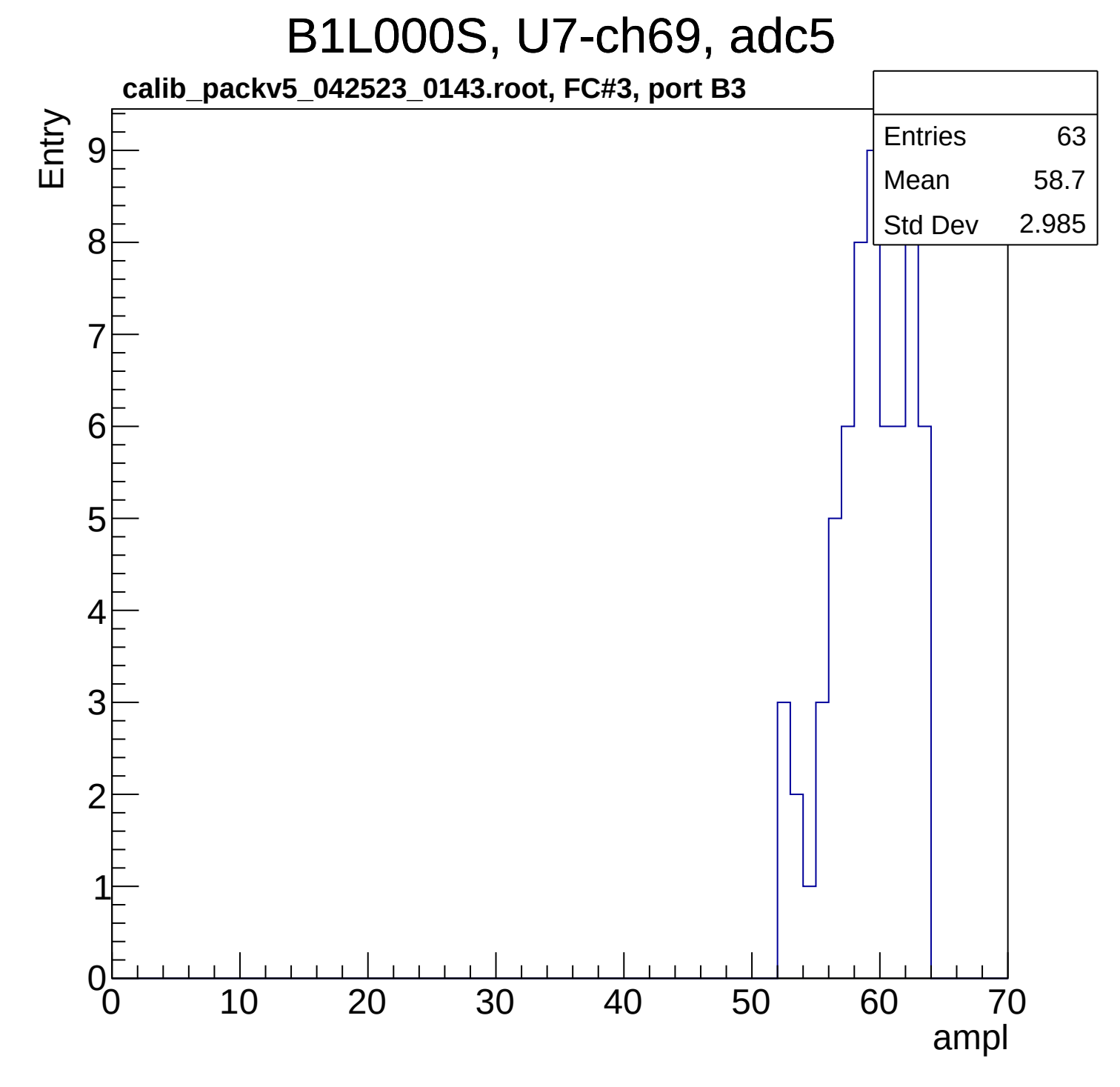
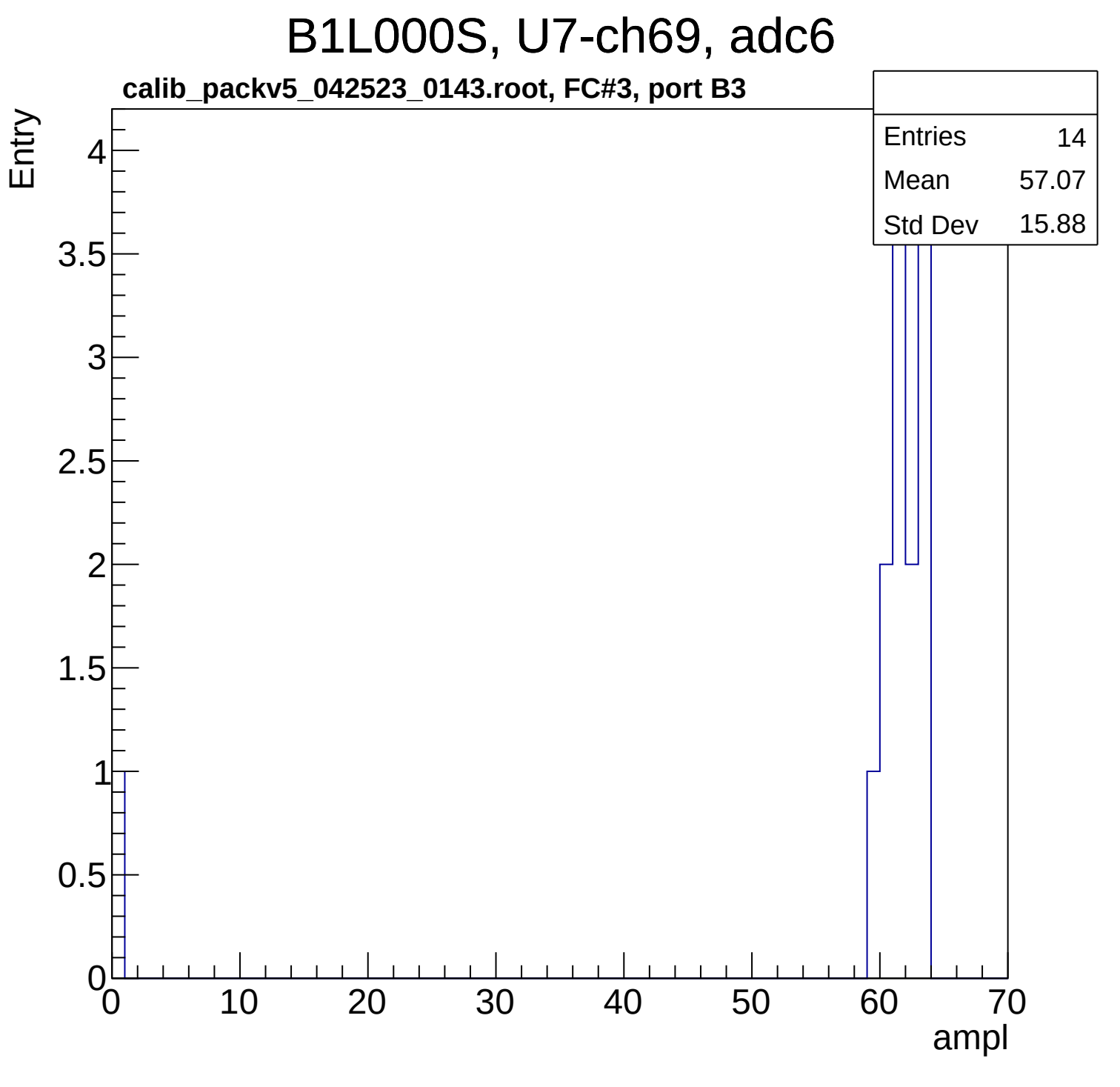

B1L000S, U7-ch69, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 58.7
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch69, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.07
Std Dev 15.88
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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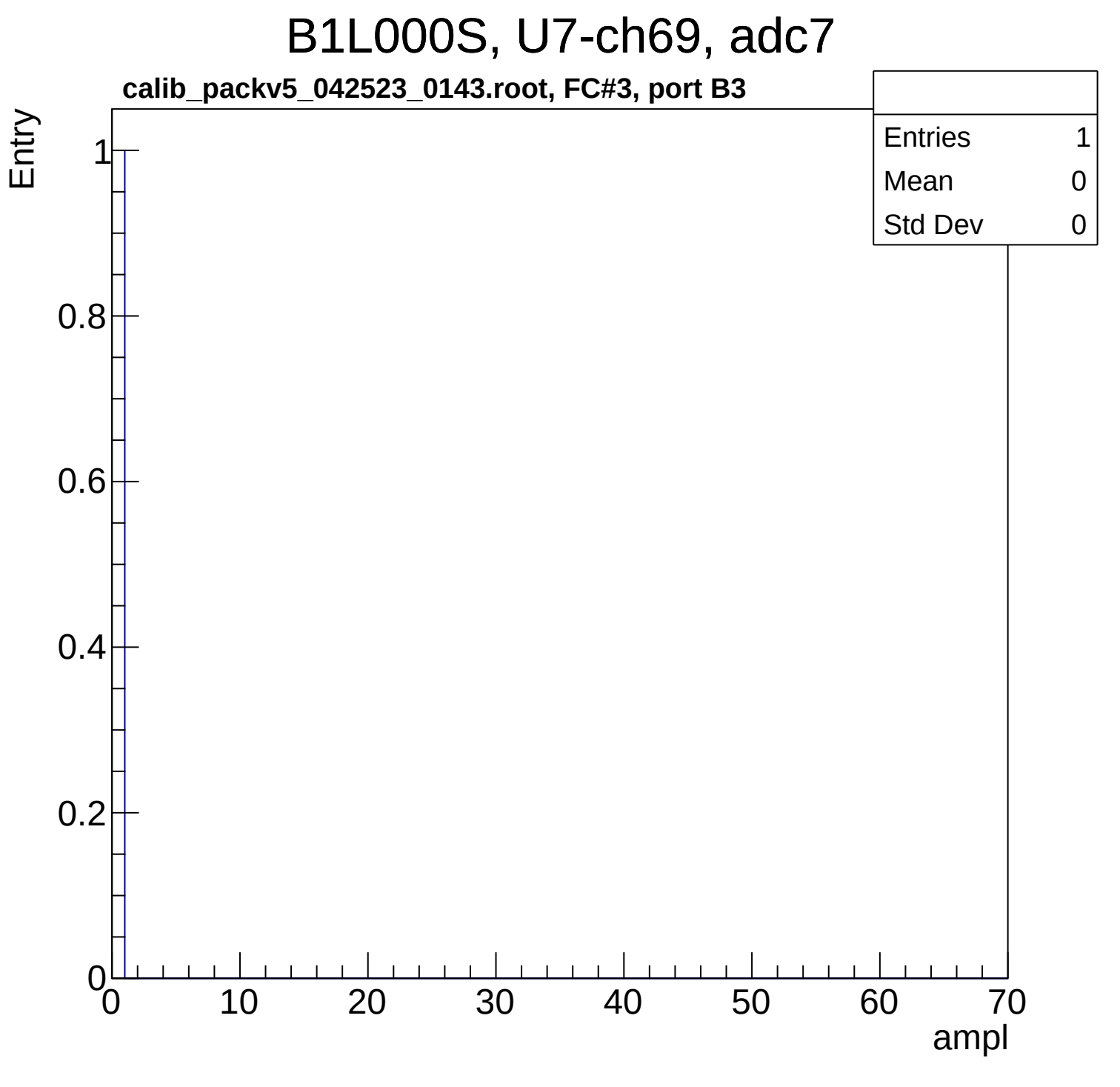
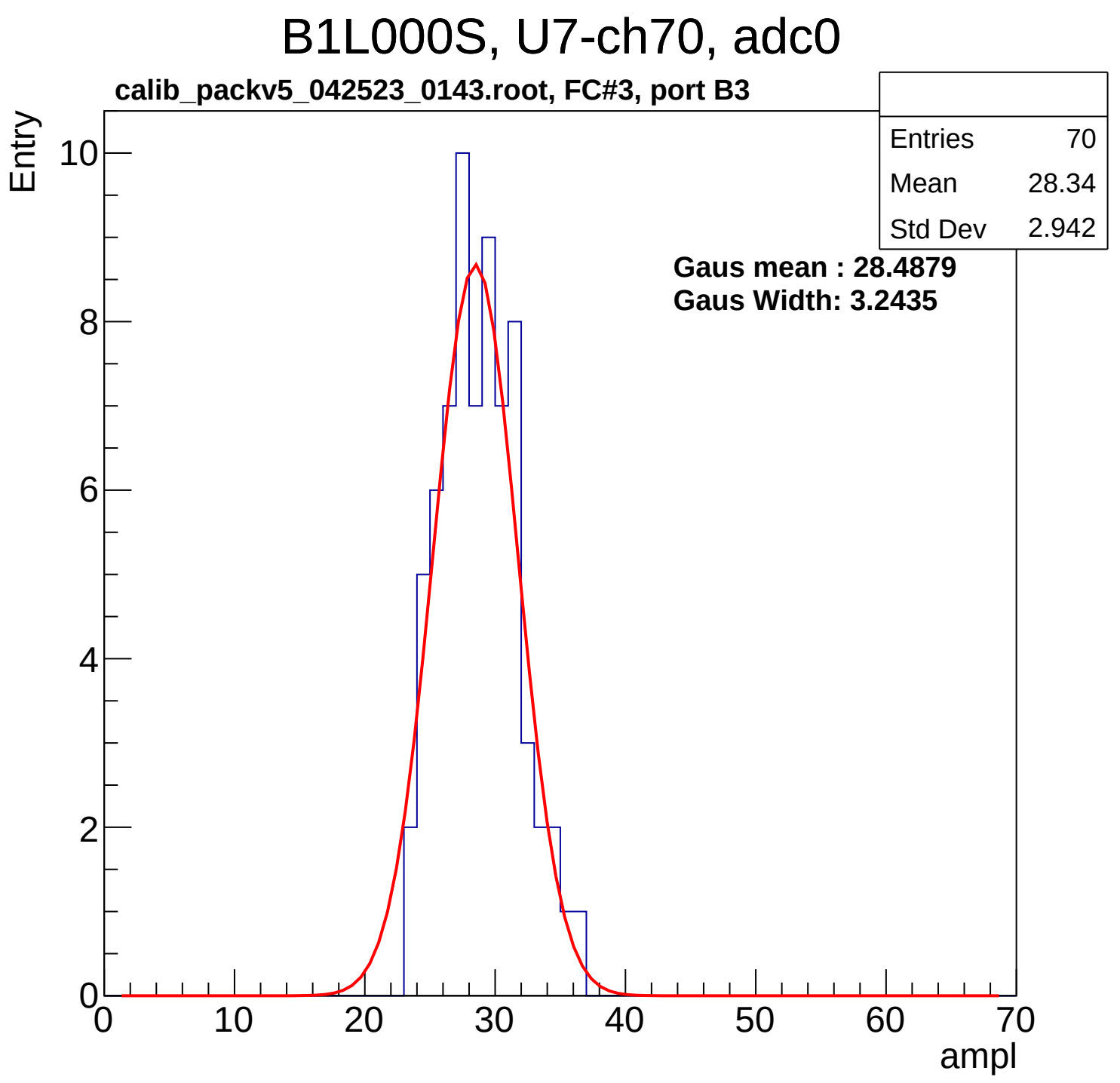

B1L000S, U7-ch69, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch70, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 28.34
Std Dev 2.942
Gaus mean : 28.4879
Gaus Width: 3.2435
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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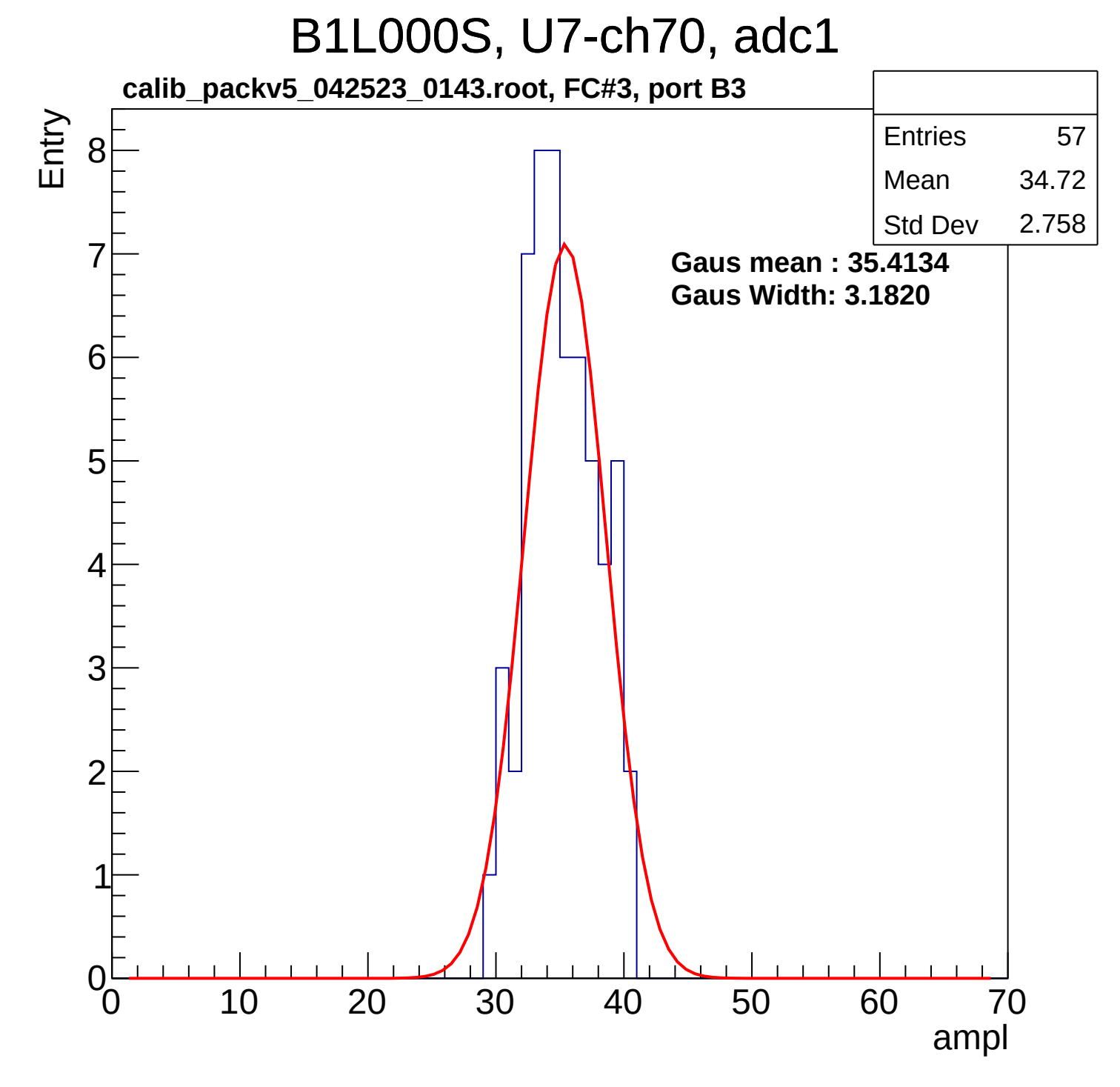
B1L000S, U7-ch70, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 34.72
Std Dev 2.758
Gaus mean : 35.4134
Gaus Width: 3.1820
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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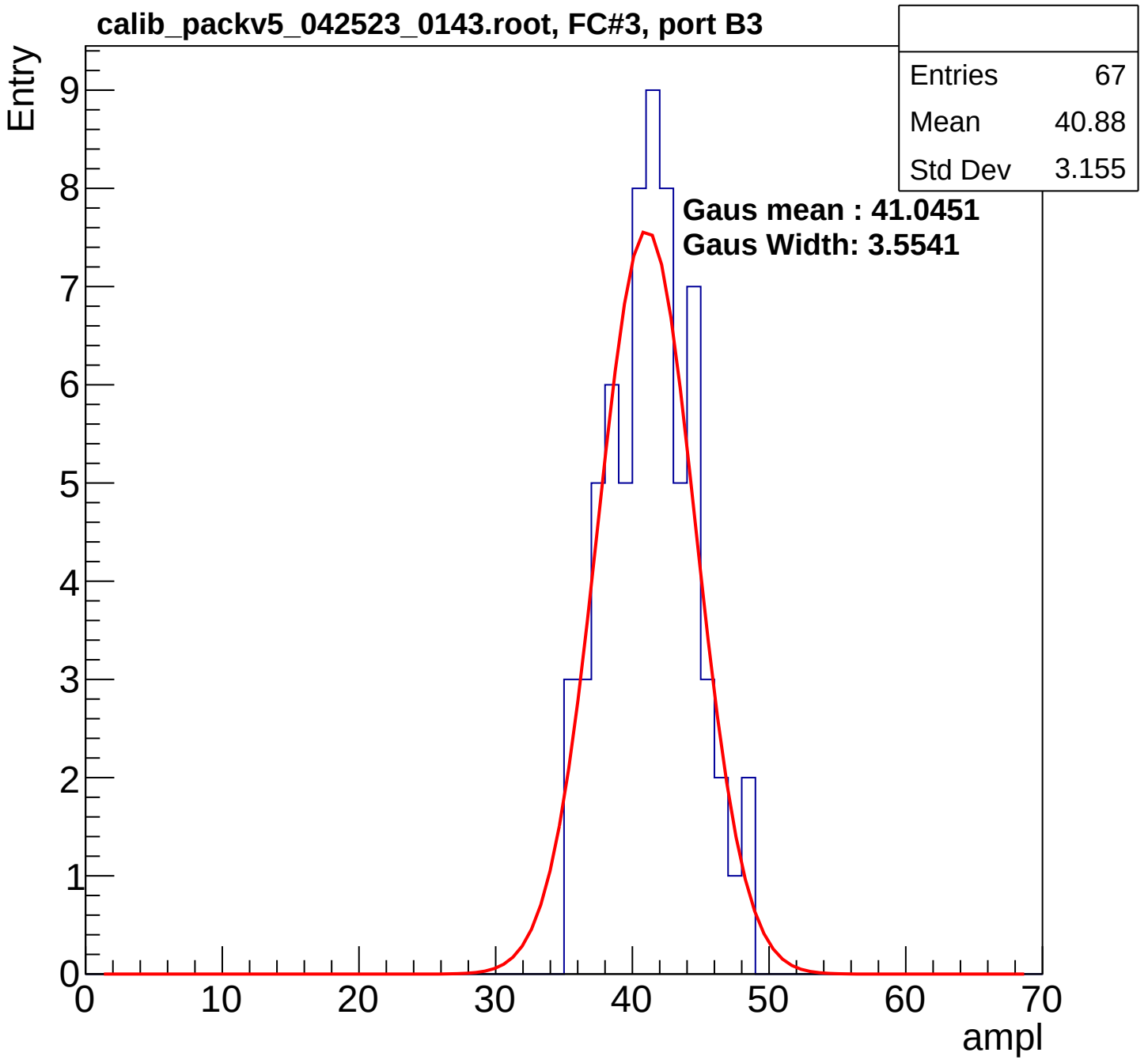
B1L000S, U7-ch70, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.88
Std Dev 3.155
Gaus mean : 41.0451
Gaus Width: 3.5541
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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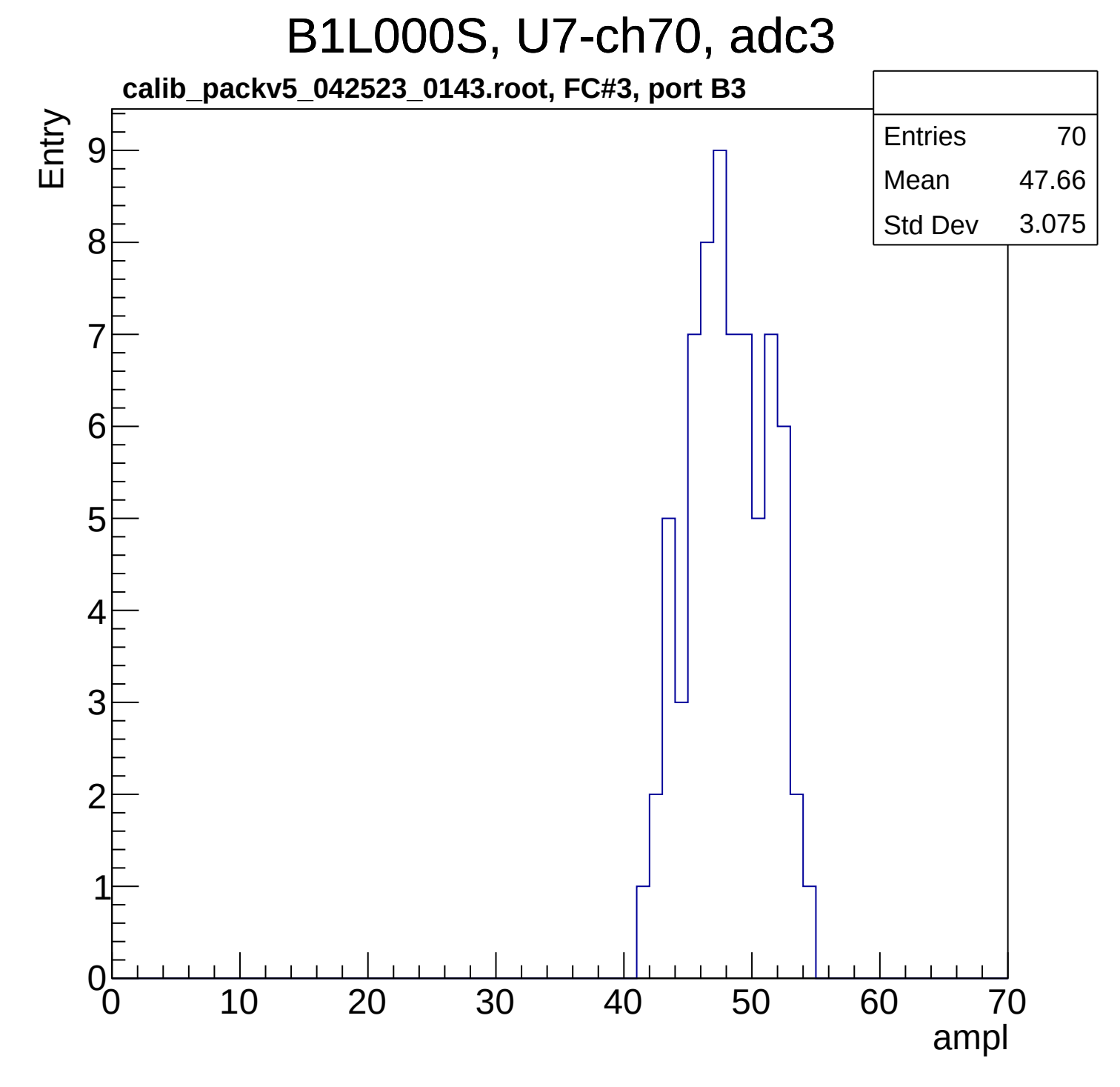
B1L000S, U7-ch70, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 47.66
Std Dev 3.075
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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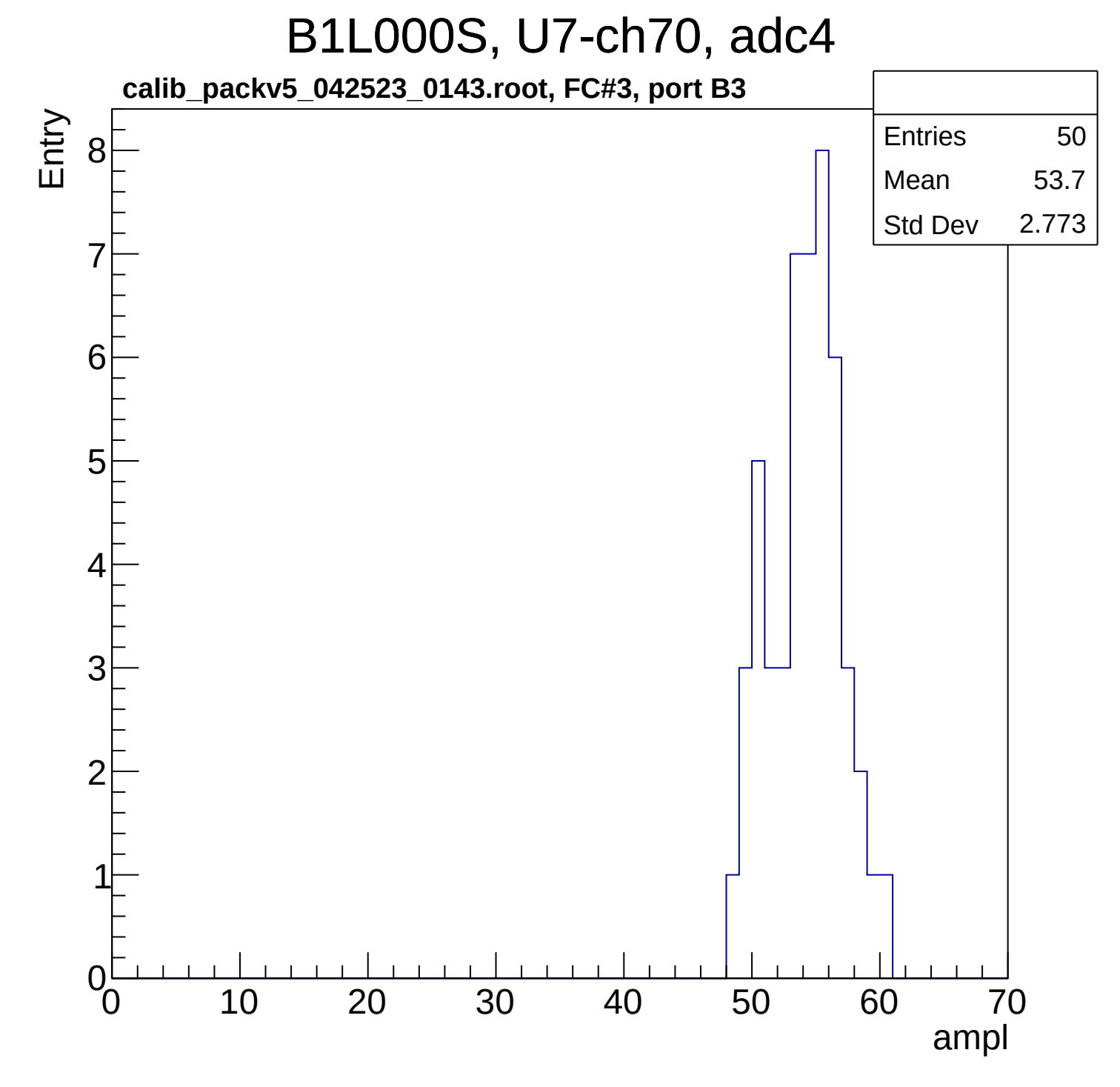
B1L000S, U7-ch70, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 53.7
Std Dev 2.773
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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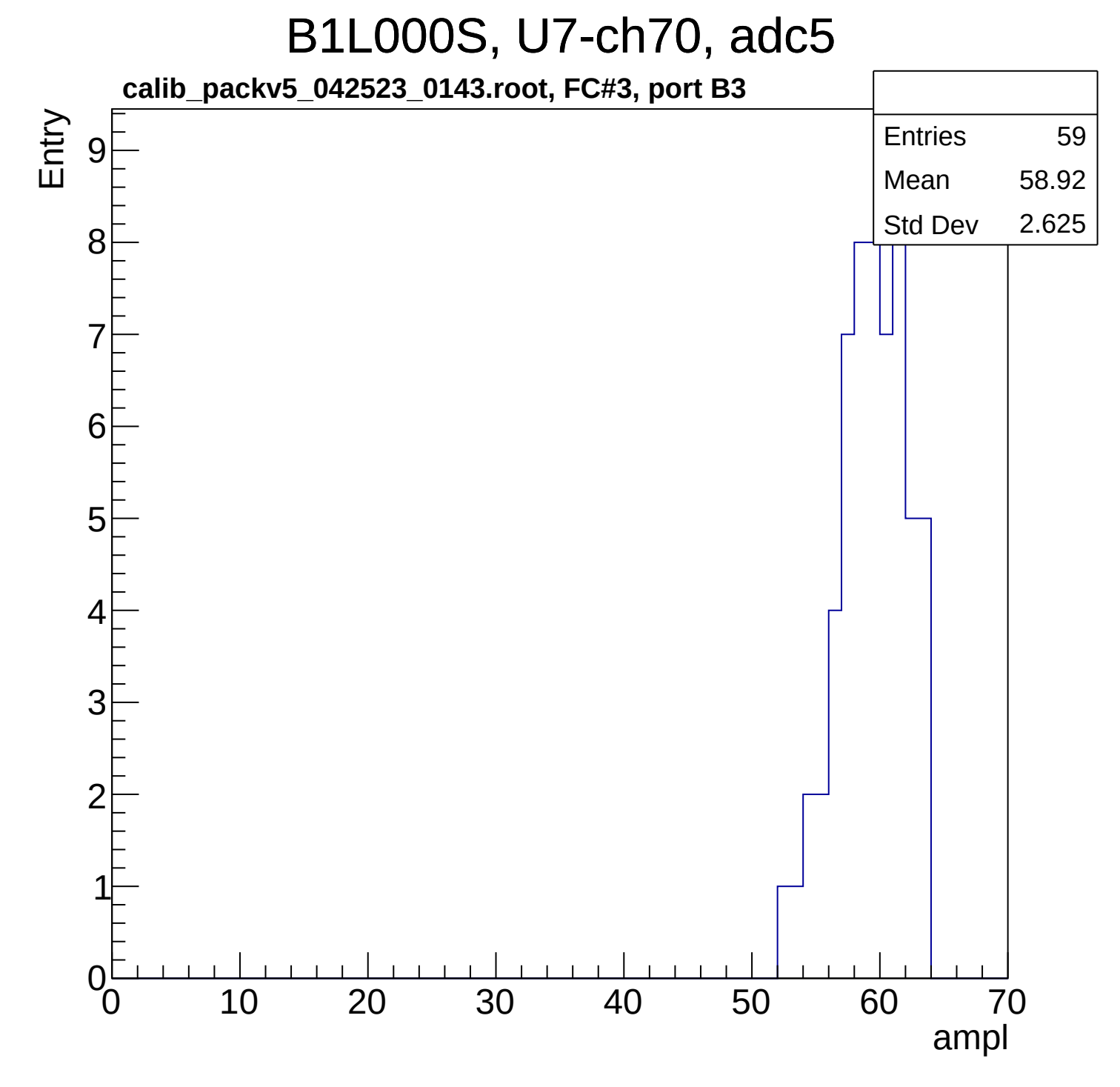
B1L000S, U7-ch70, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 58.92
Std Dev 2.625
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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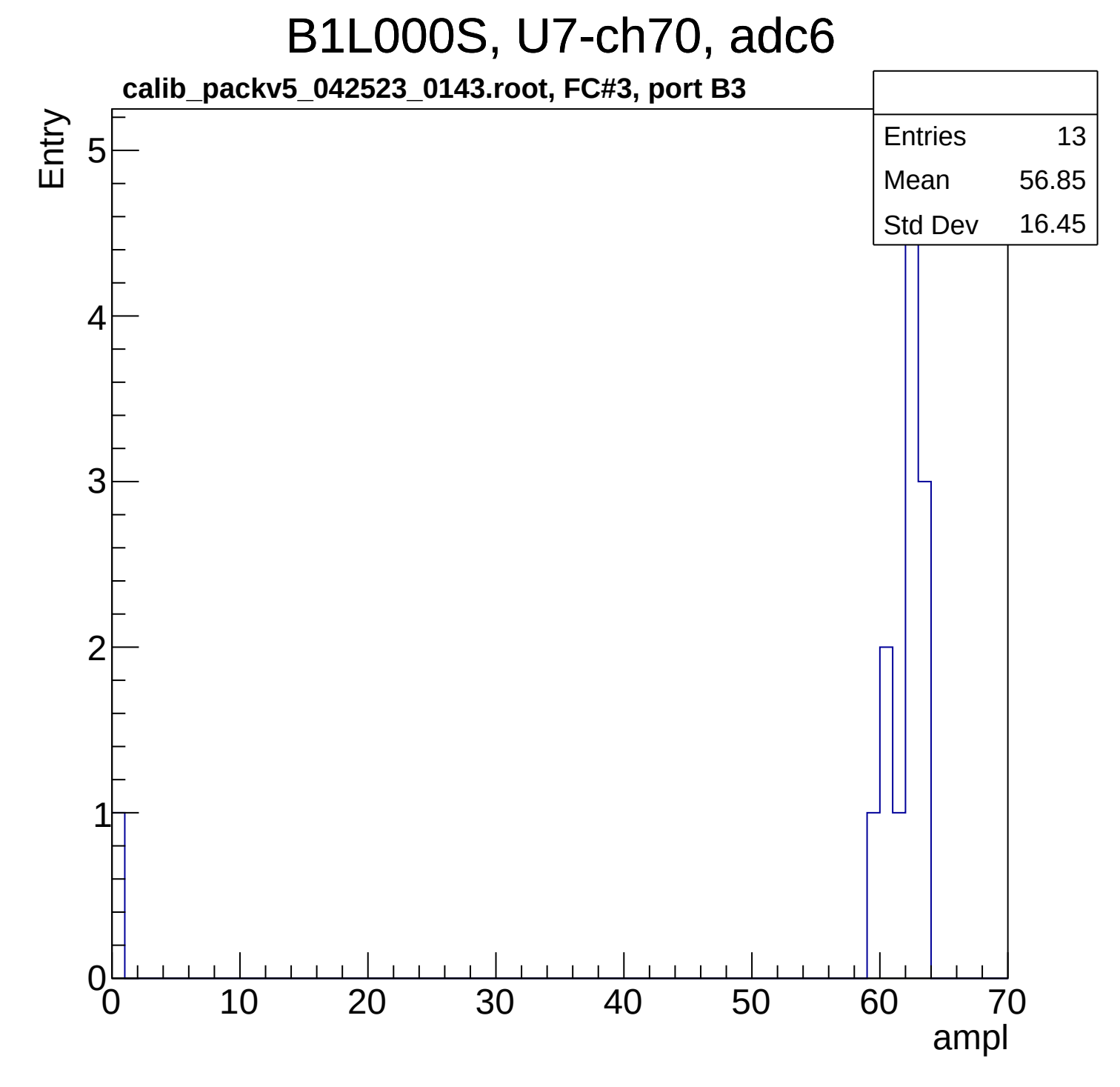
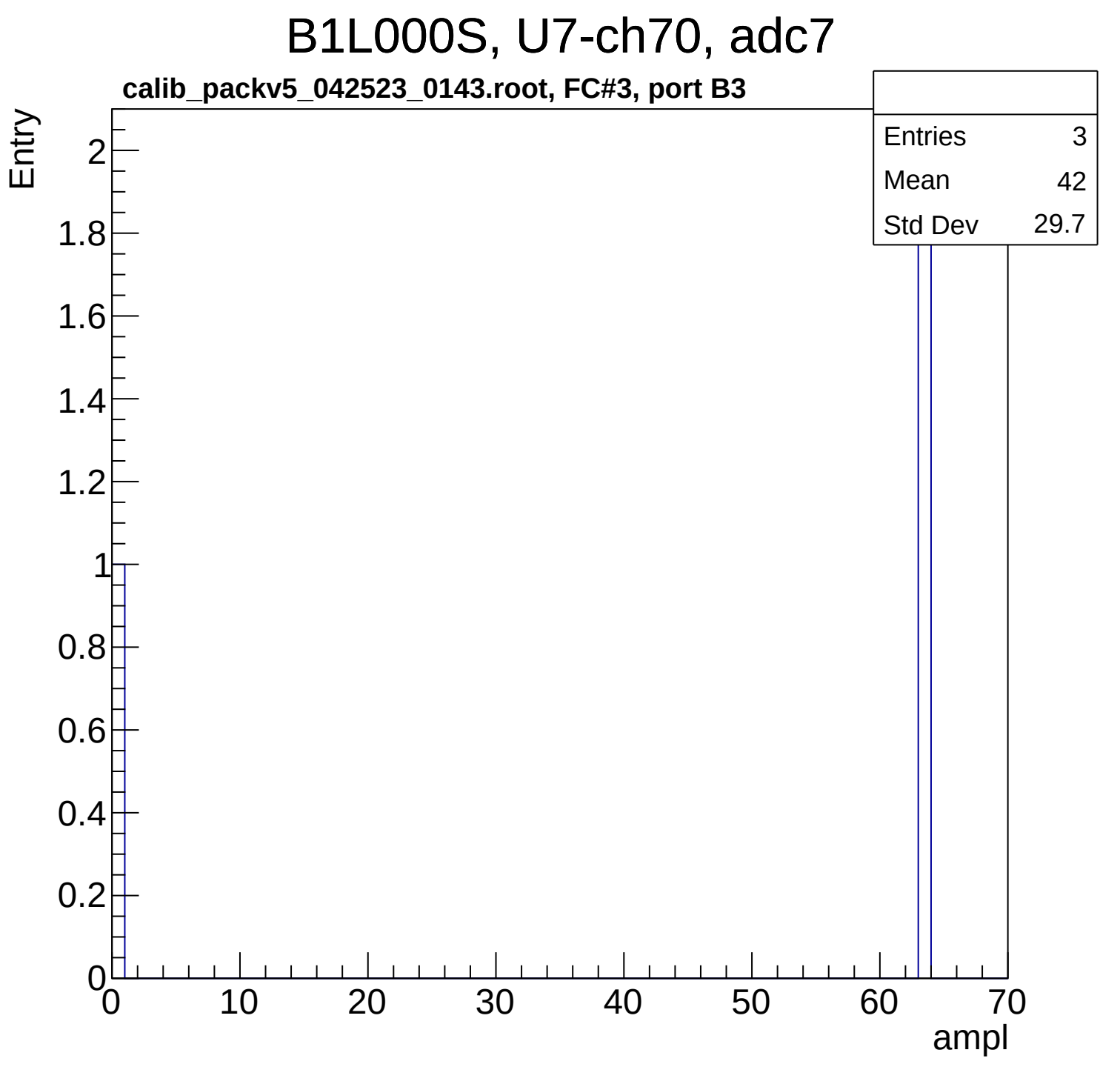
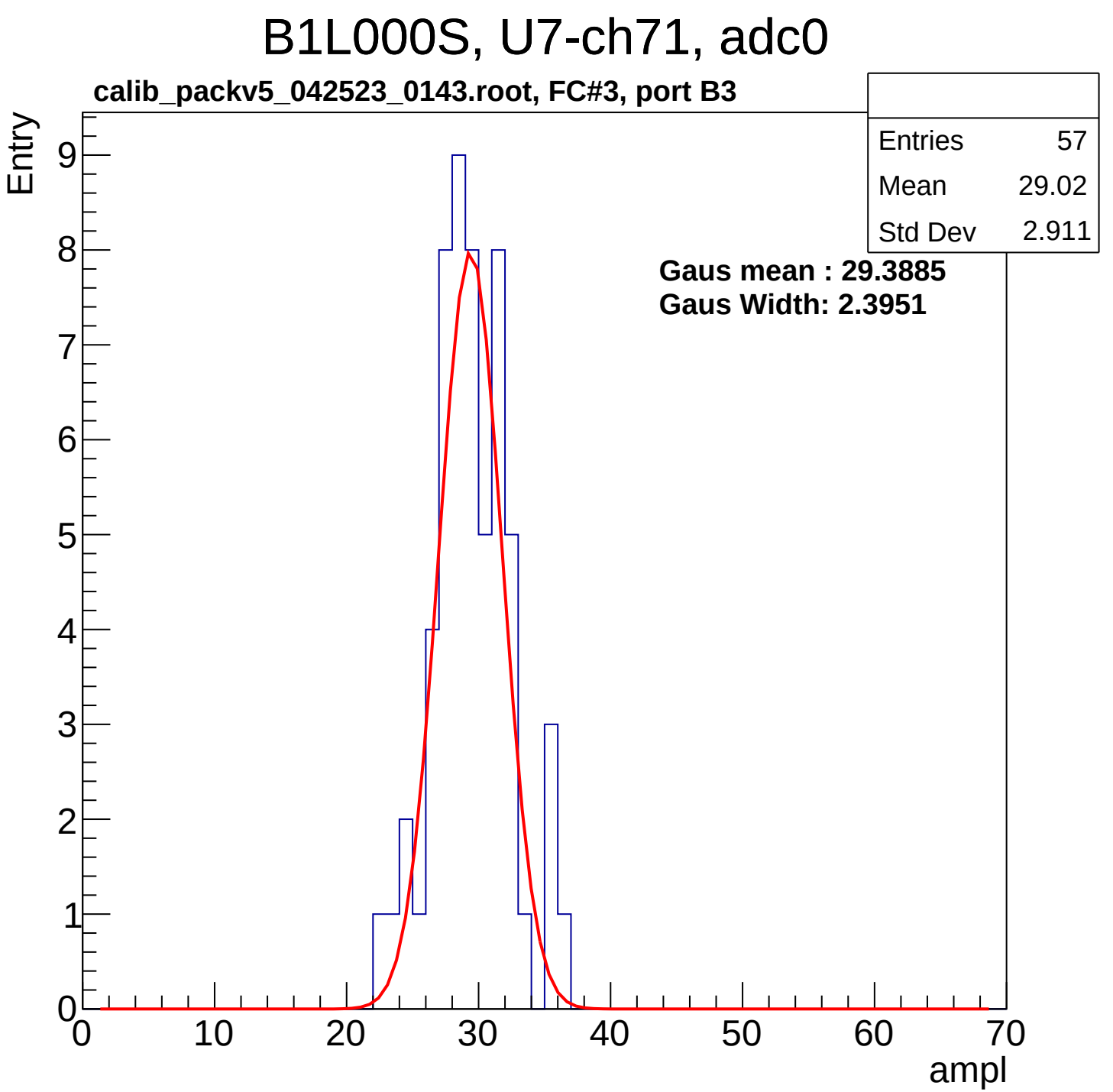

B1L000S, U7-ch70, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.85
Std Dev 16.45
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch70, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 29.02
Std Dev 2.911
Gaus mean : 29.3885
Gaus Width: 2.3951
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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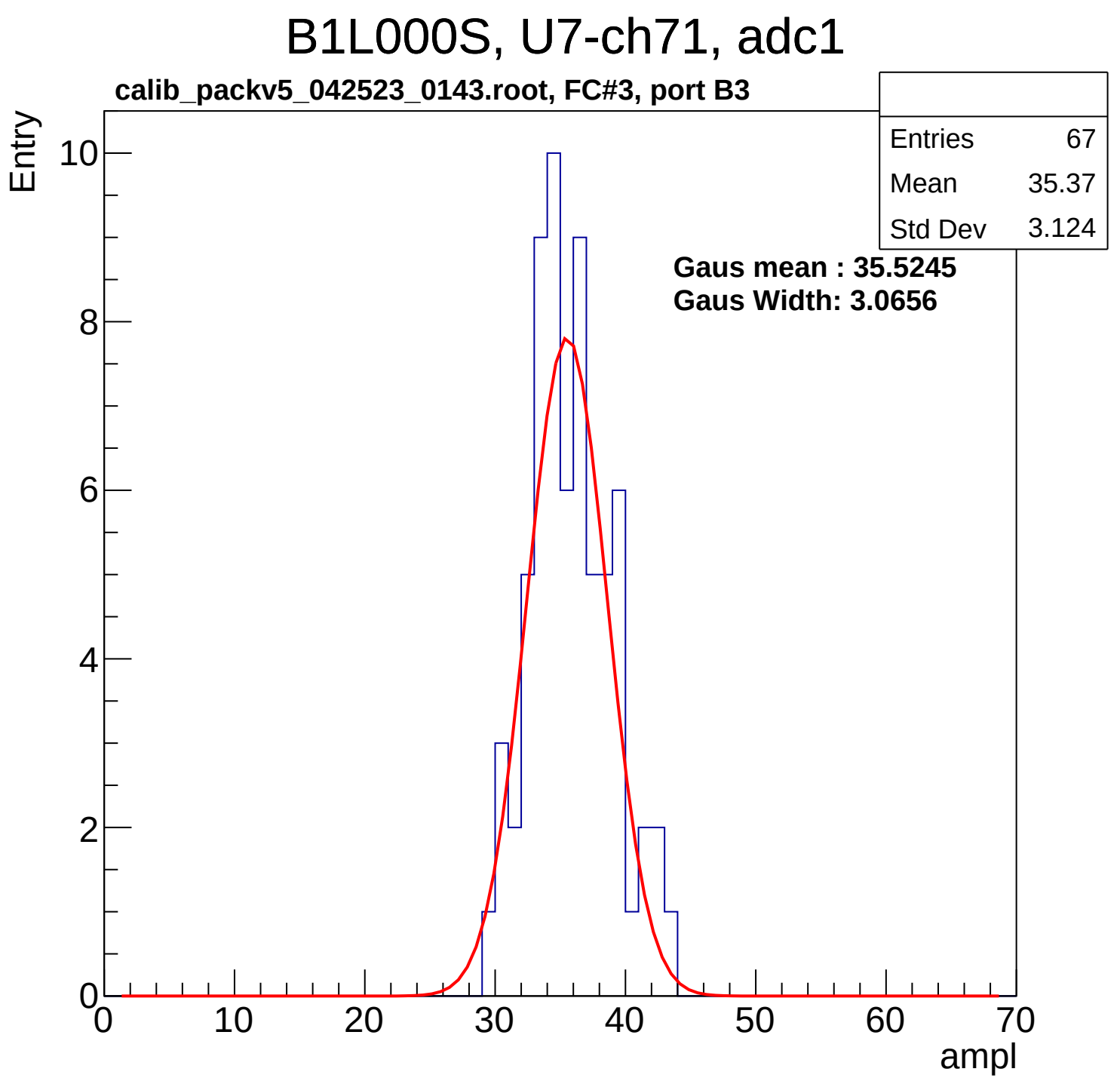
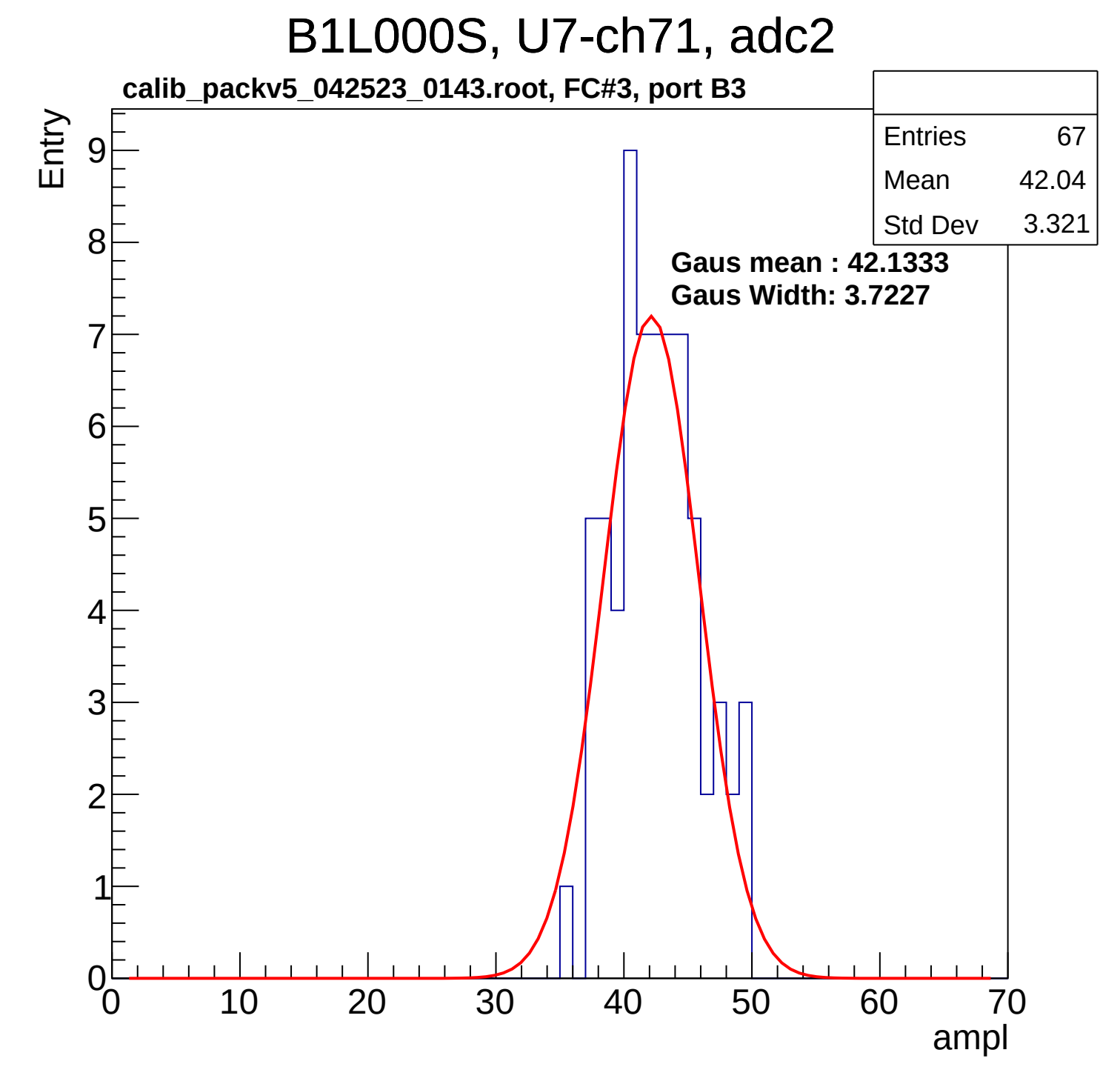
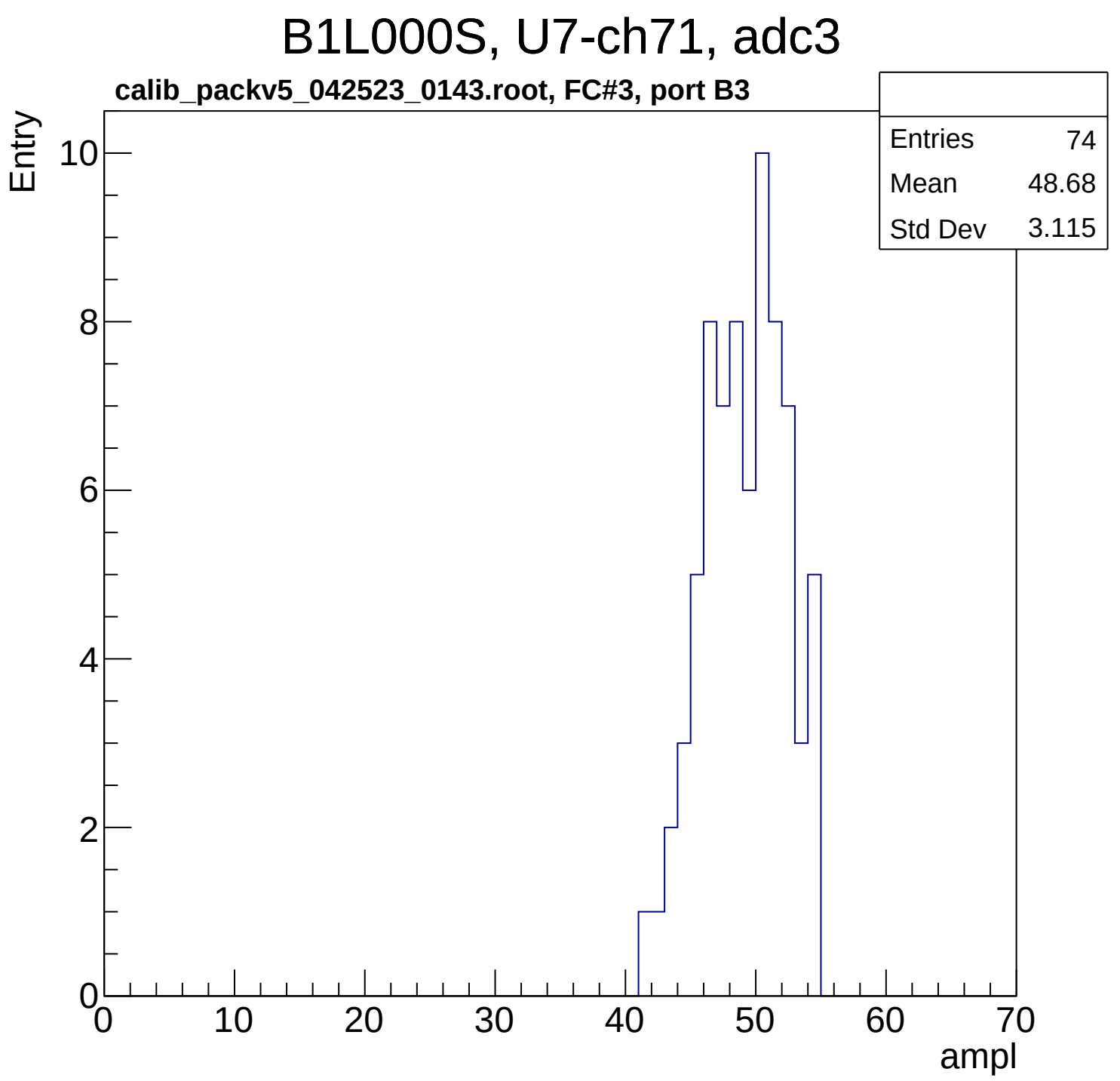
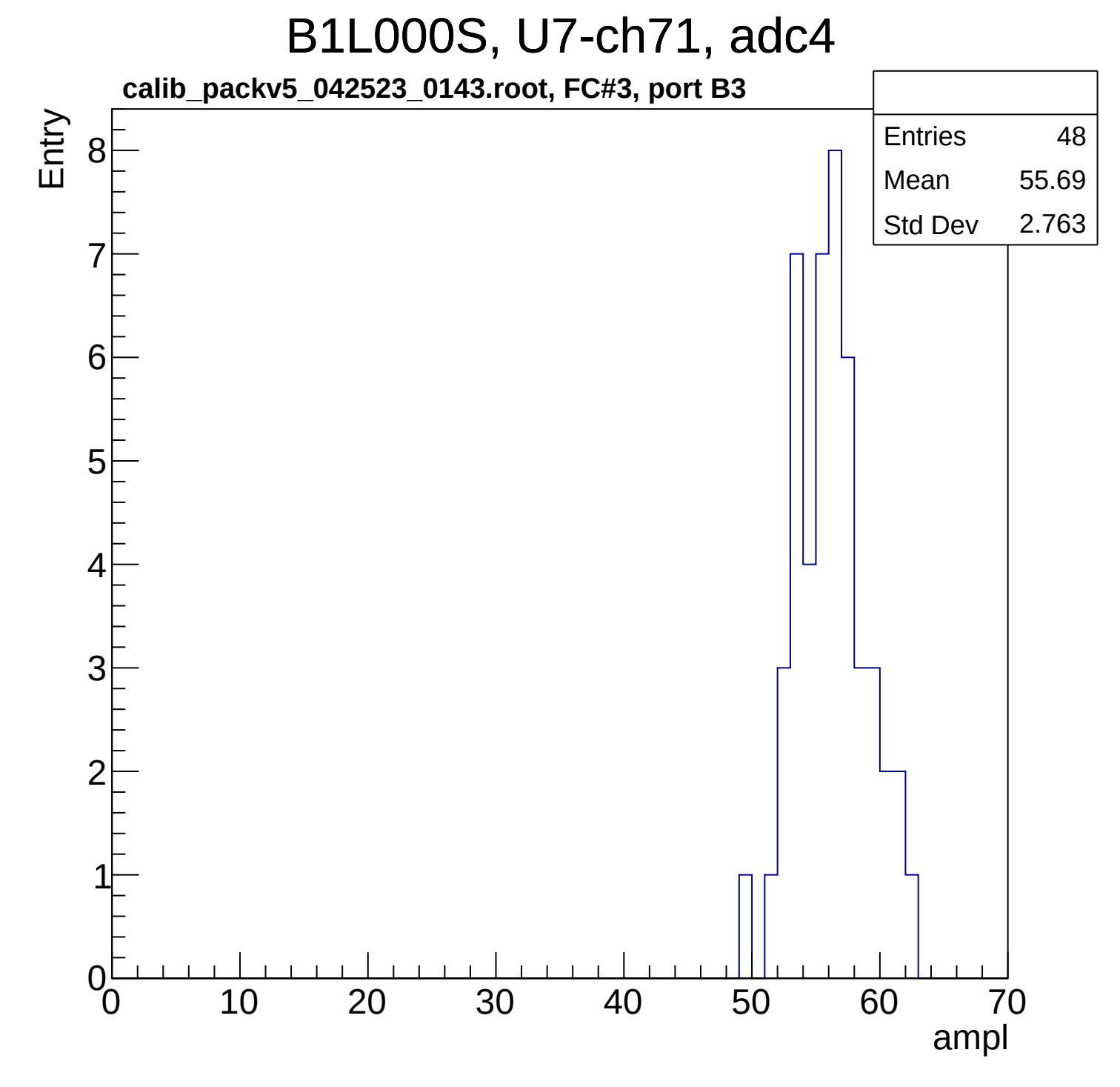

B1L000S, U7-ch71, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.37
Std Dev 3.124
Gaus mean : 35.5245
Gaus Width: 3.0656
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 42.04
Std Dev 3.321
Gaus mean : 42.1333
Gaus Width: 3.7227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 48.68
Std Dev 3.115
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch71, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 55.69
Std Dev 2.763
Entry
ampl
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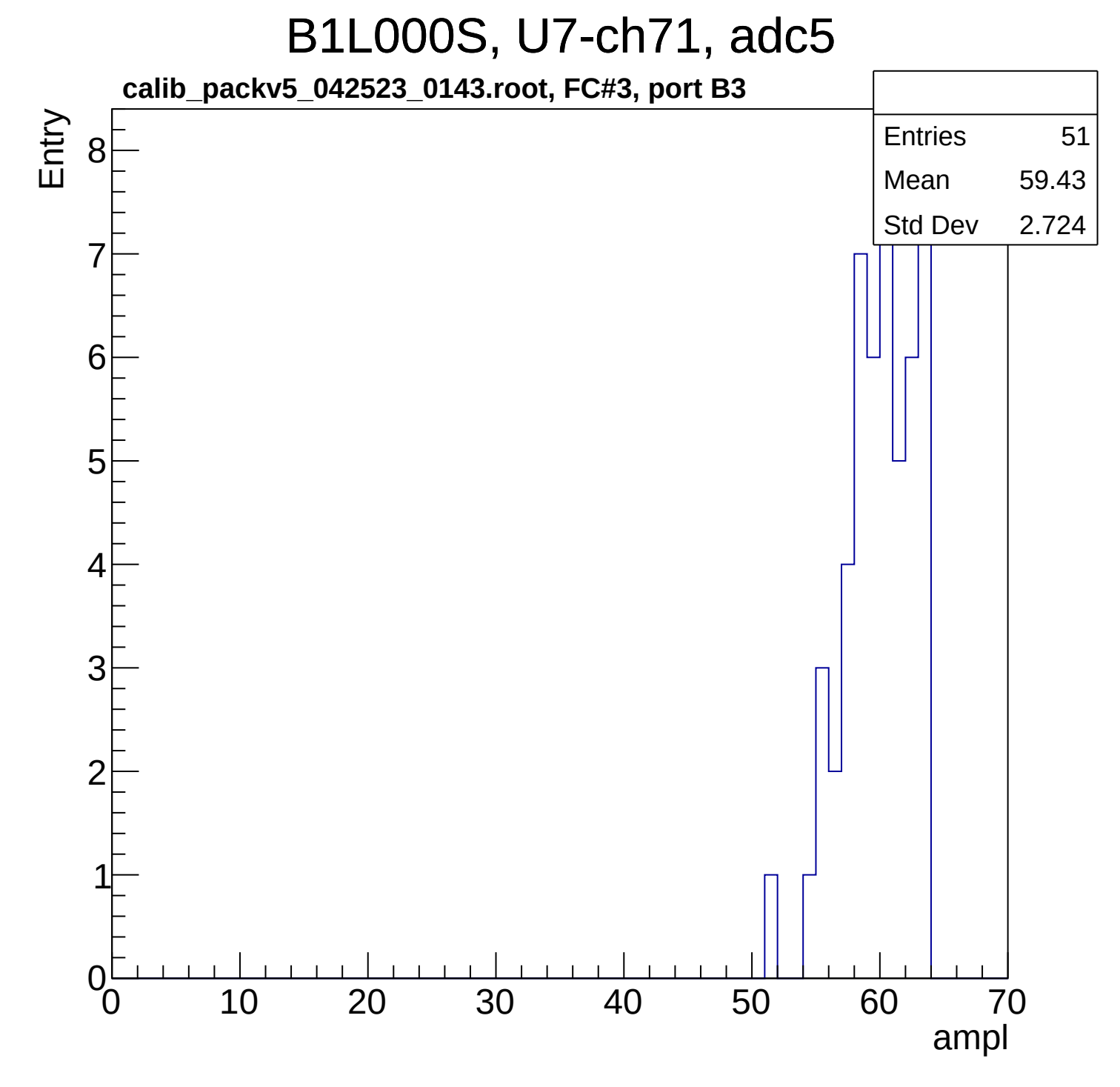
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 59.43
Std Dev 2.724
Entry
ampl
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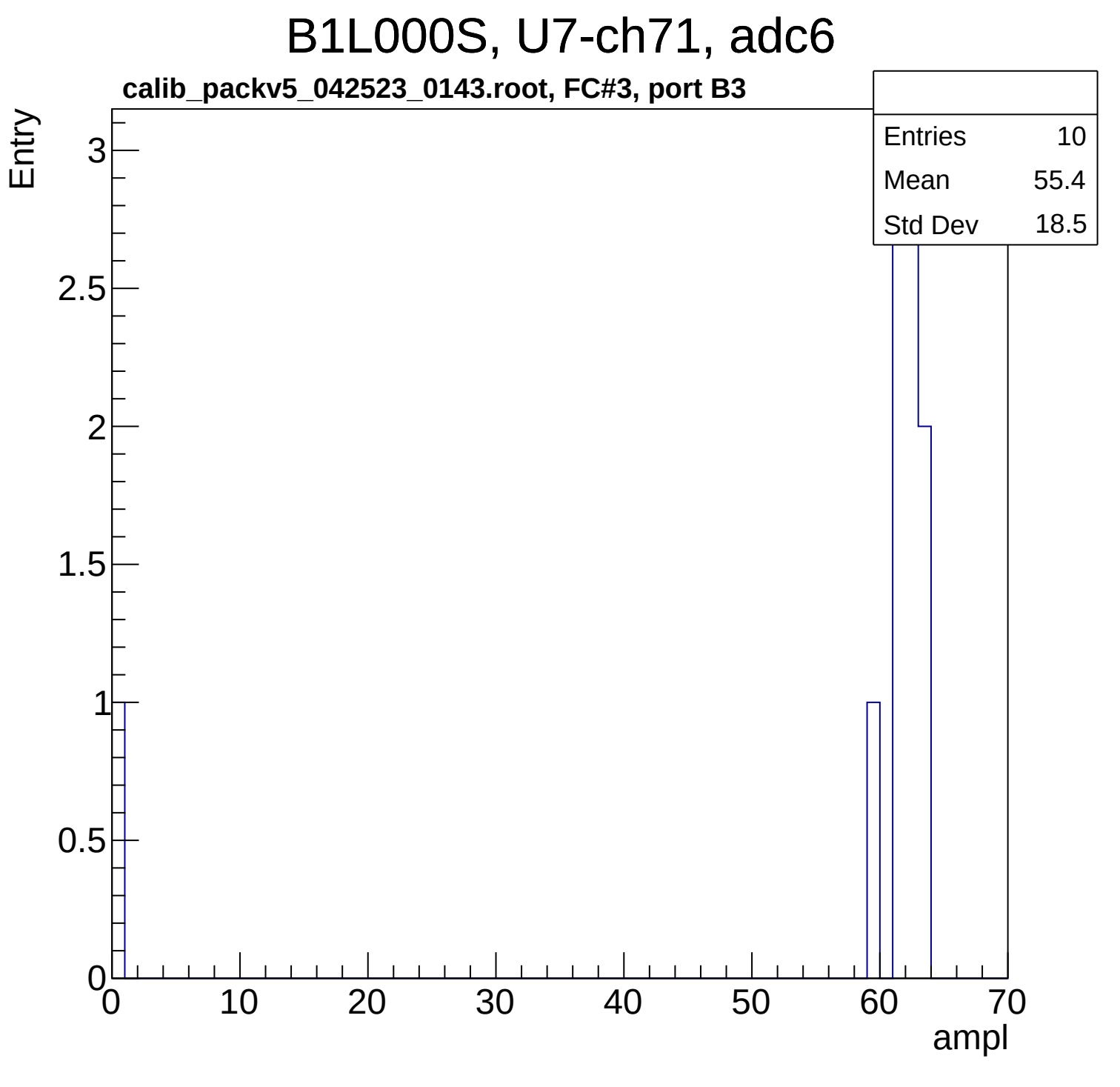
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 55.4
Std Dev 18.5
Entry
ampl
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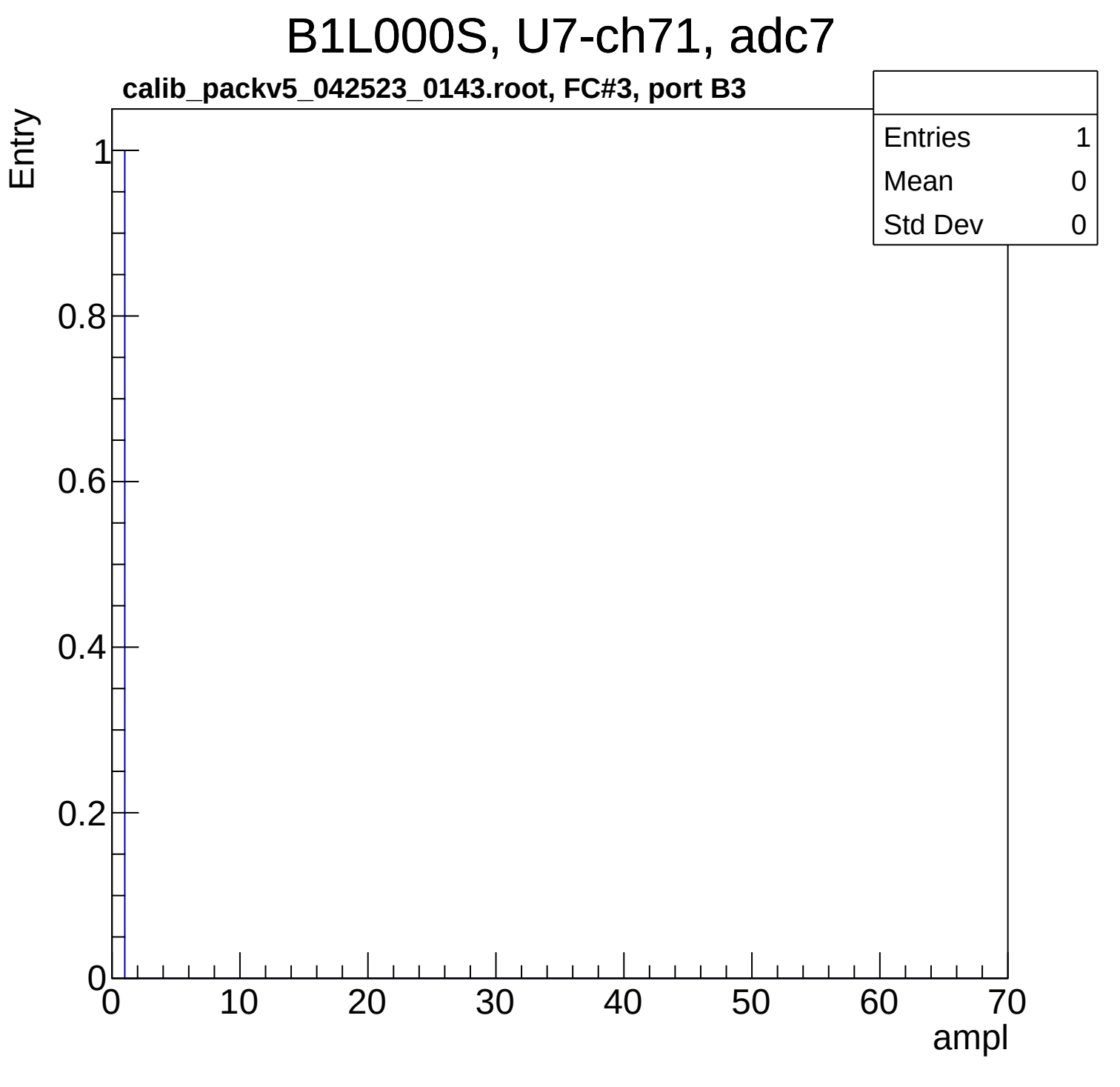
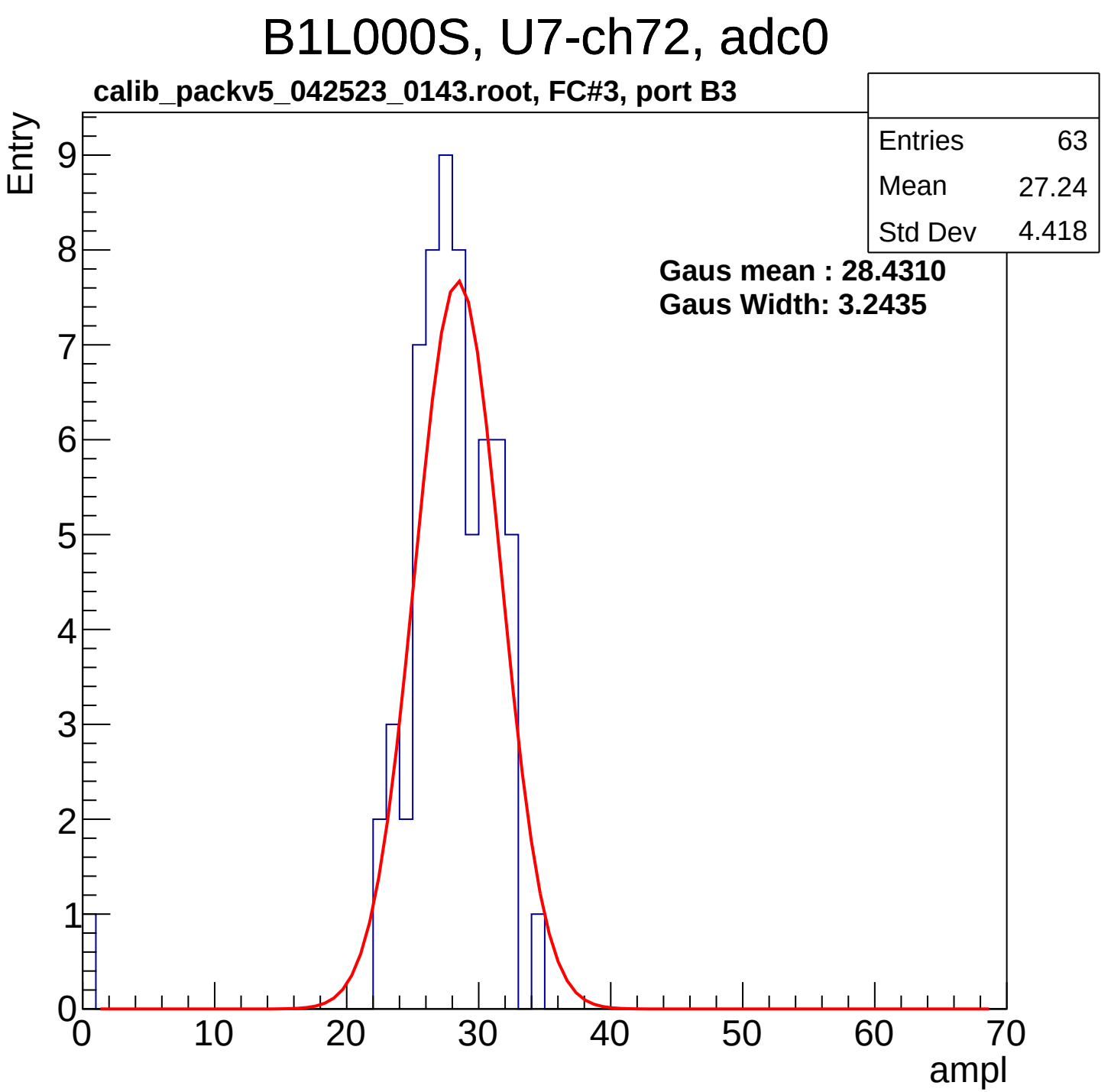
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L000S, U7-ch71, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch72, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 27.24
Std Dev 4.418
Gaus mean : 28.4310
Gaus Width: 3.2435
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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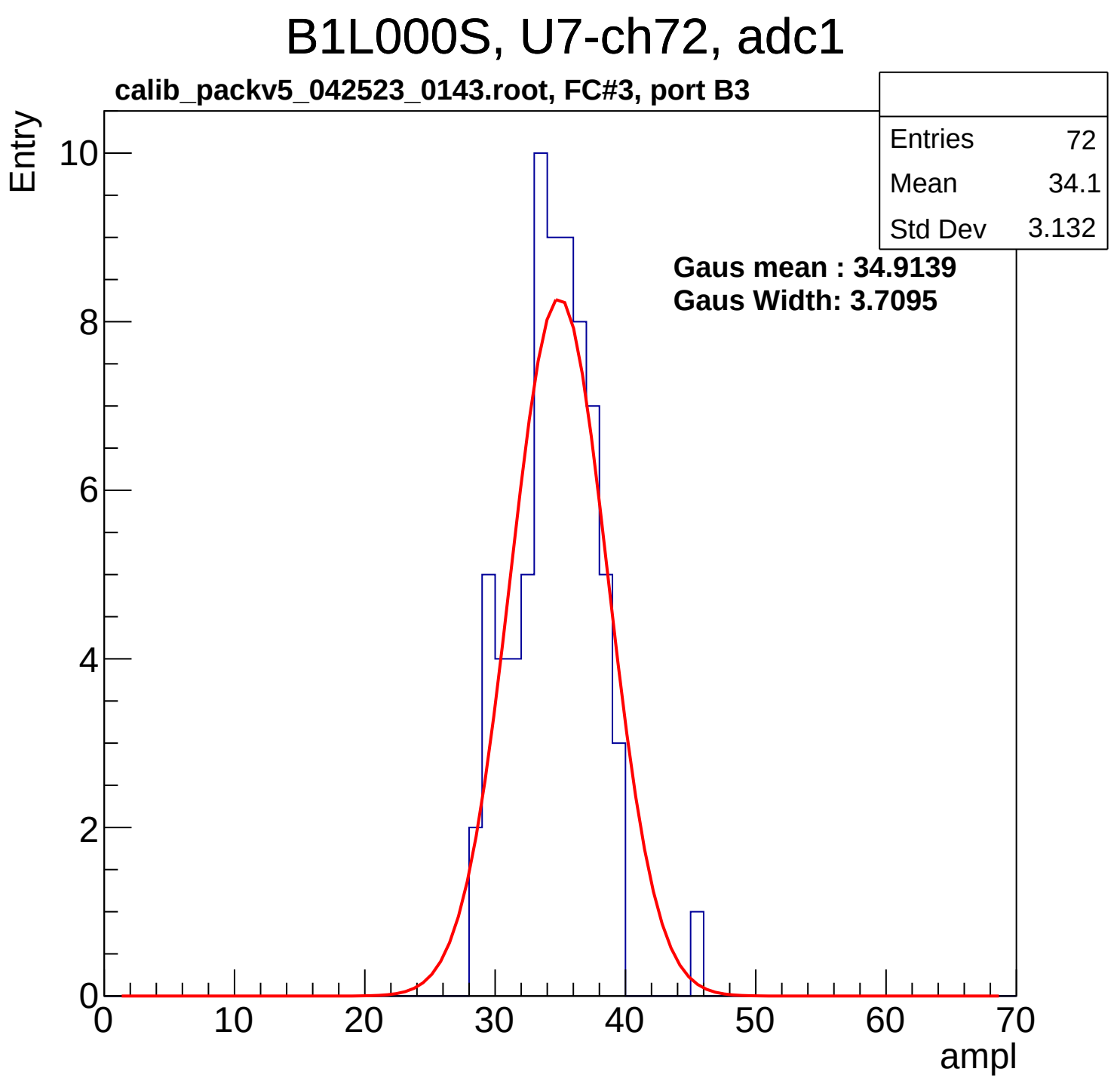
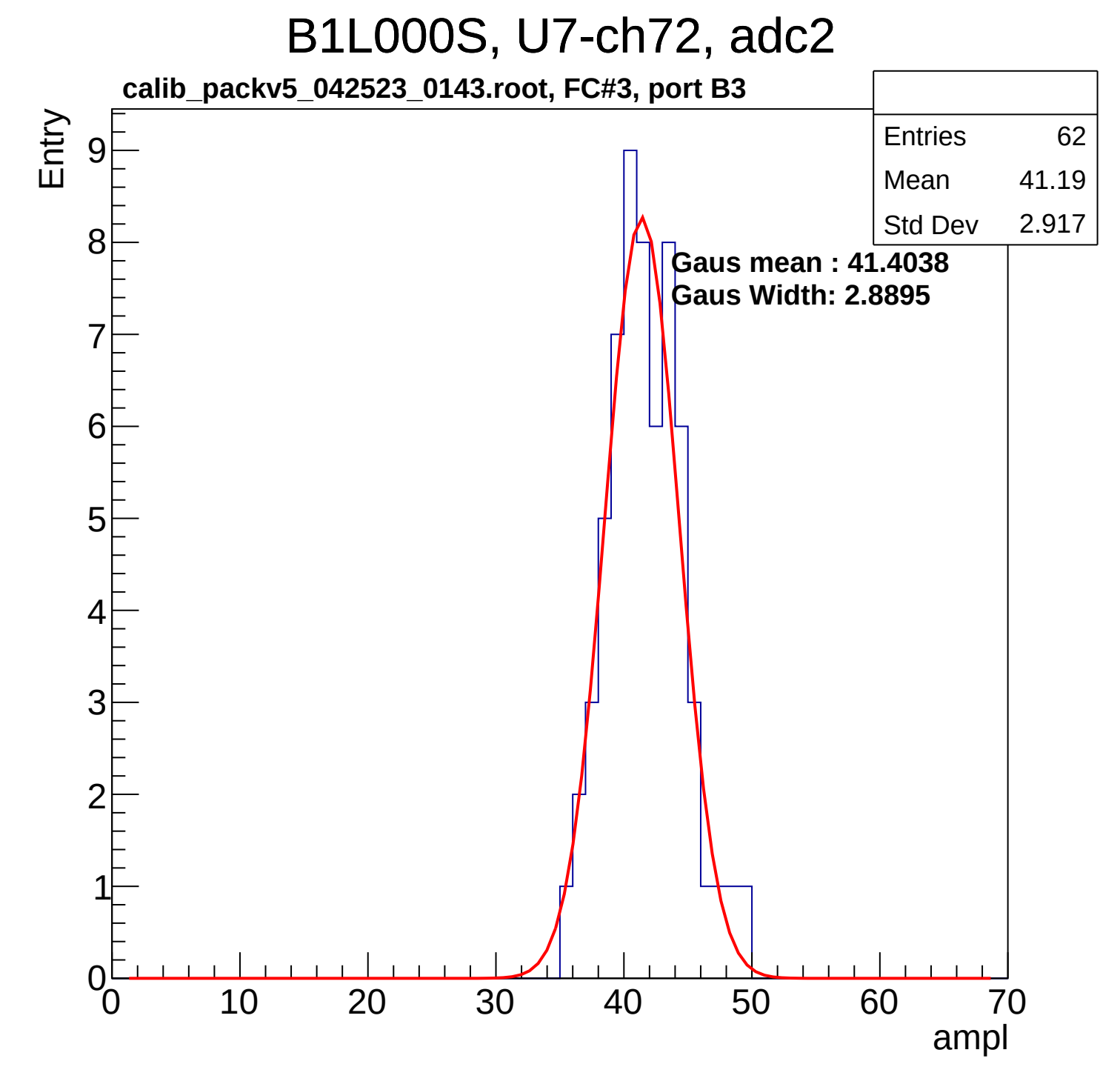
20
30
40
50
60
70


B1L000S, U7-ch72, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 34.1
Std Dev 3.132
Gaus mean : 34.9139
Gaus Width: 3.7095
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch72, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 41.19
Std Dev 2.917
Gaus mean : 41.4038
Gaus Width: 2.8895
Entry
ampl
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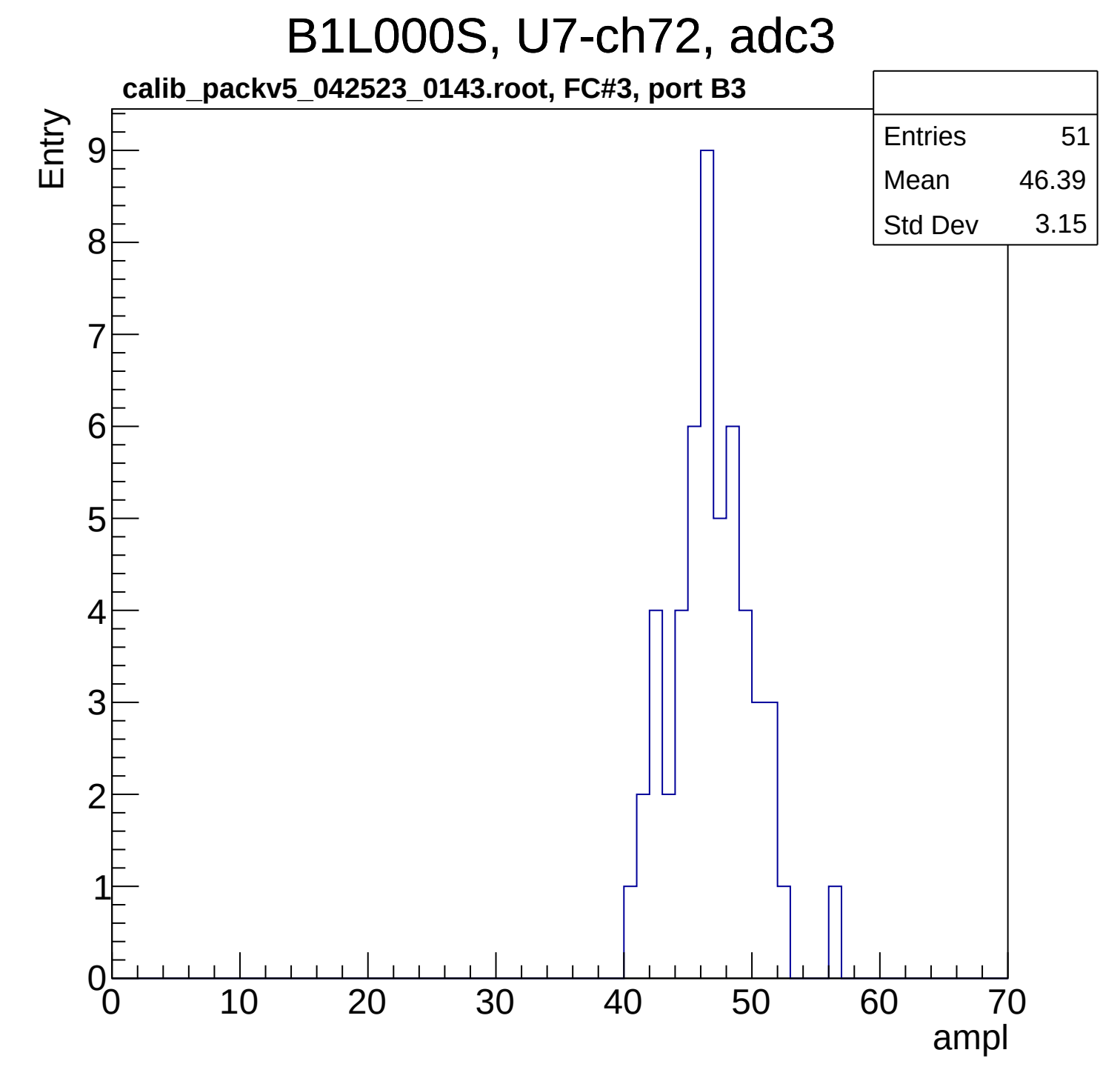
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch72, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 46.39
Std Dev 3.15
Entry
ampl
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2
3
4
5
6
7
8
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0
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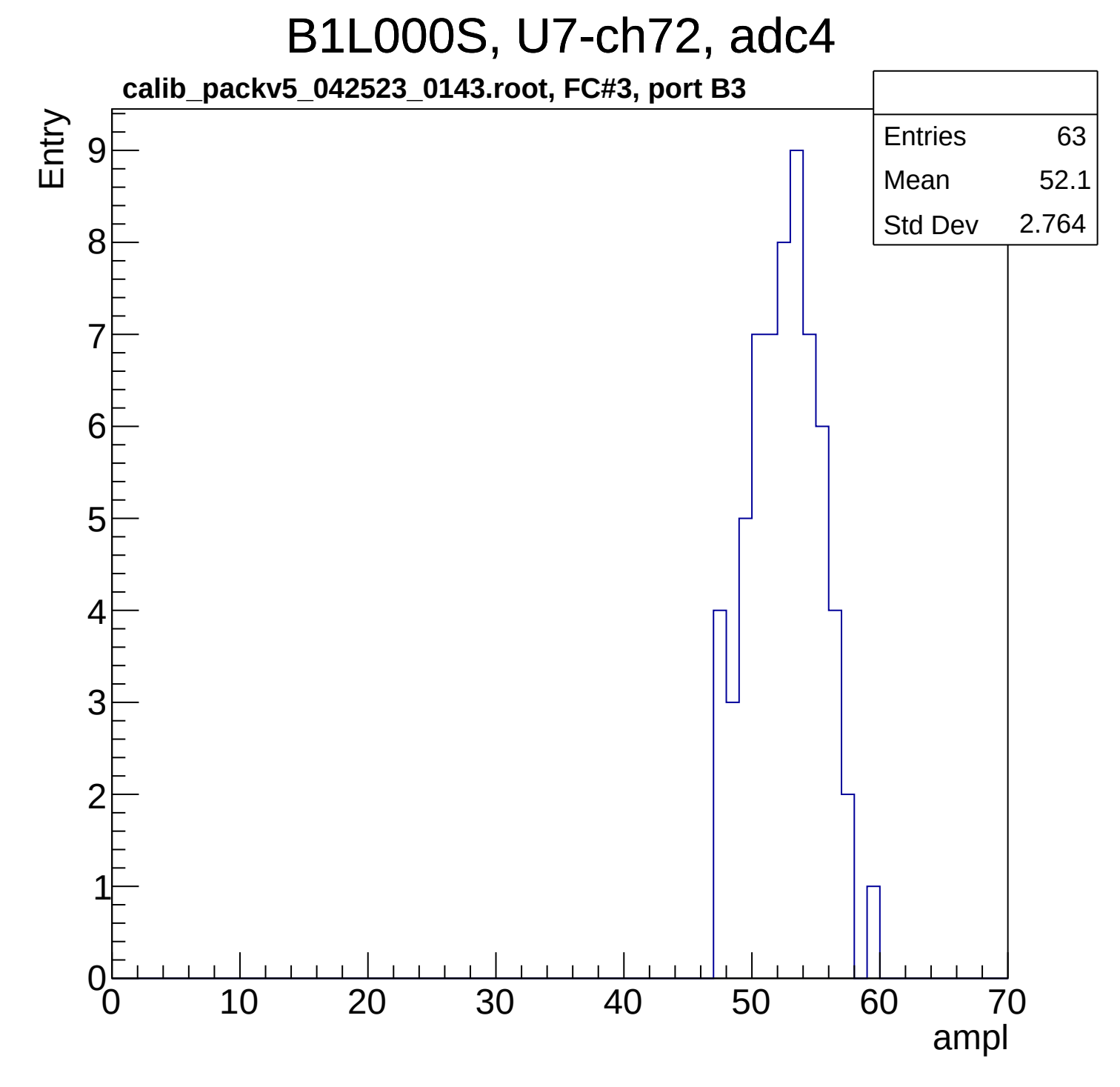
B1L000S, U7-ch72, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 52.1
Std Dev 2.764
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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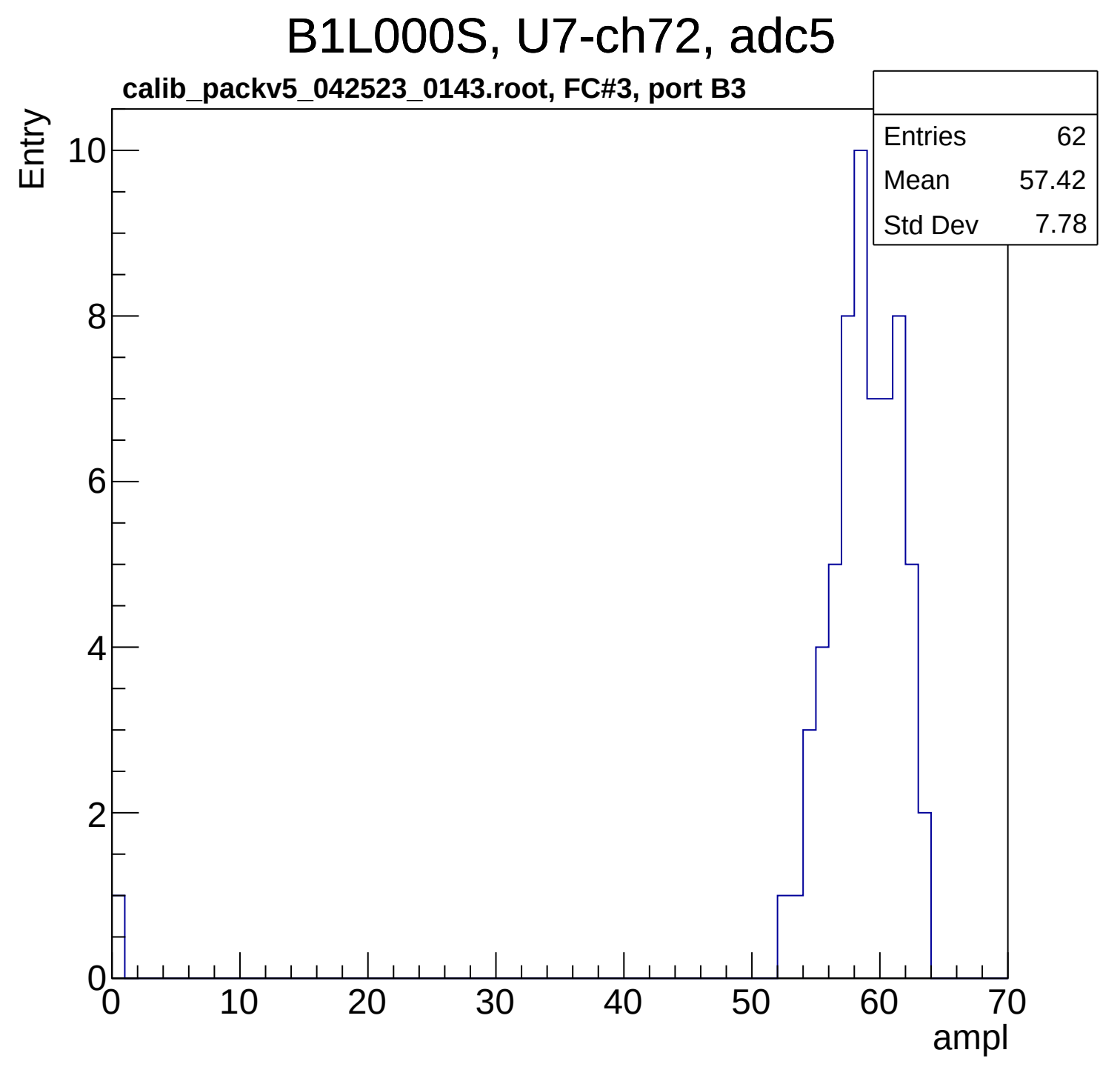
B1L000S, U7-ch72, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.42
Std Dev 7.78
Entry
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4
6
8
10
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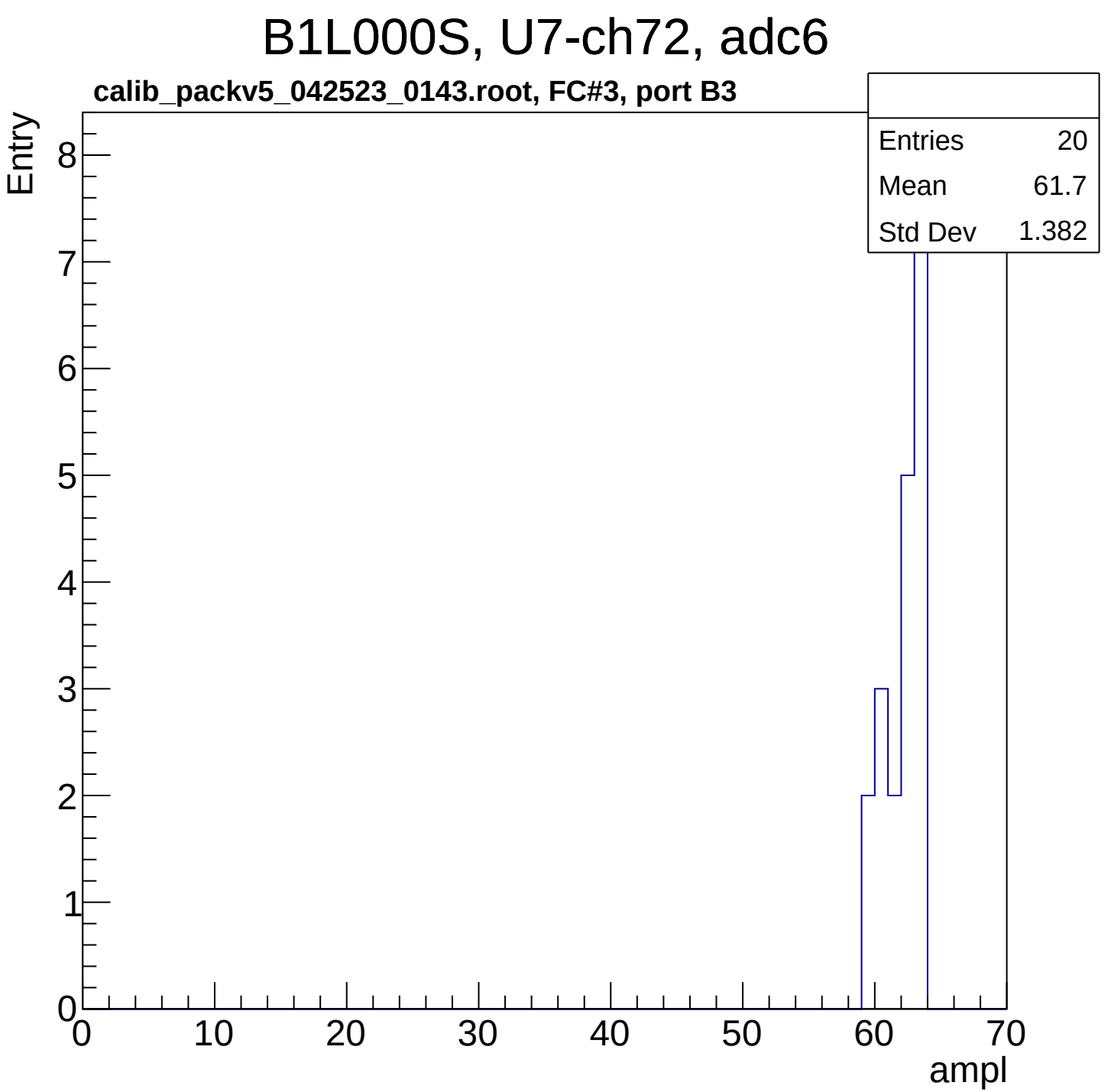
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch72, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 20
Mean 61.7
Std Dev 1.382
Entry
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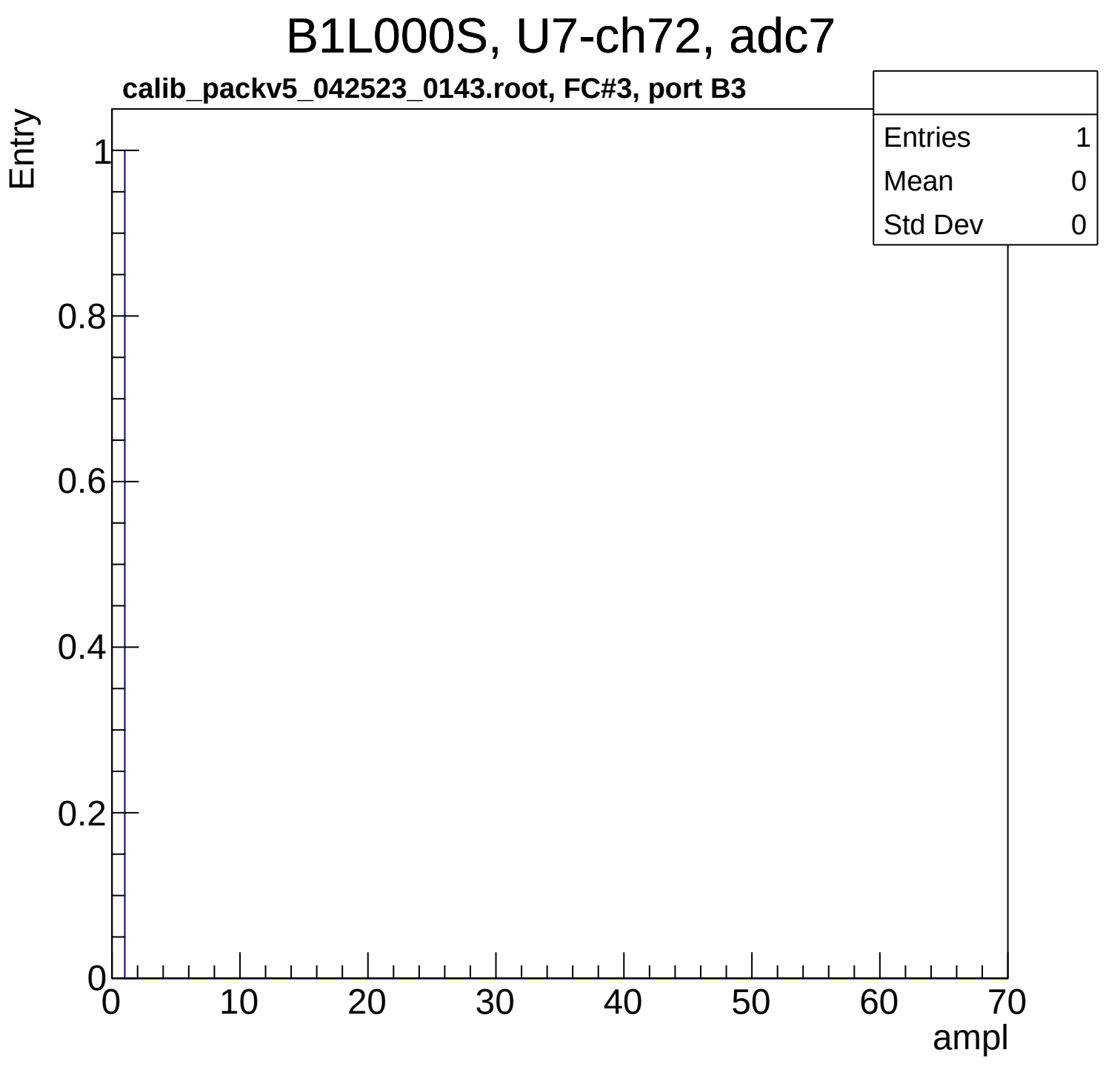
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch72, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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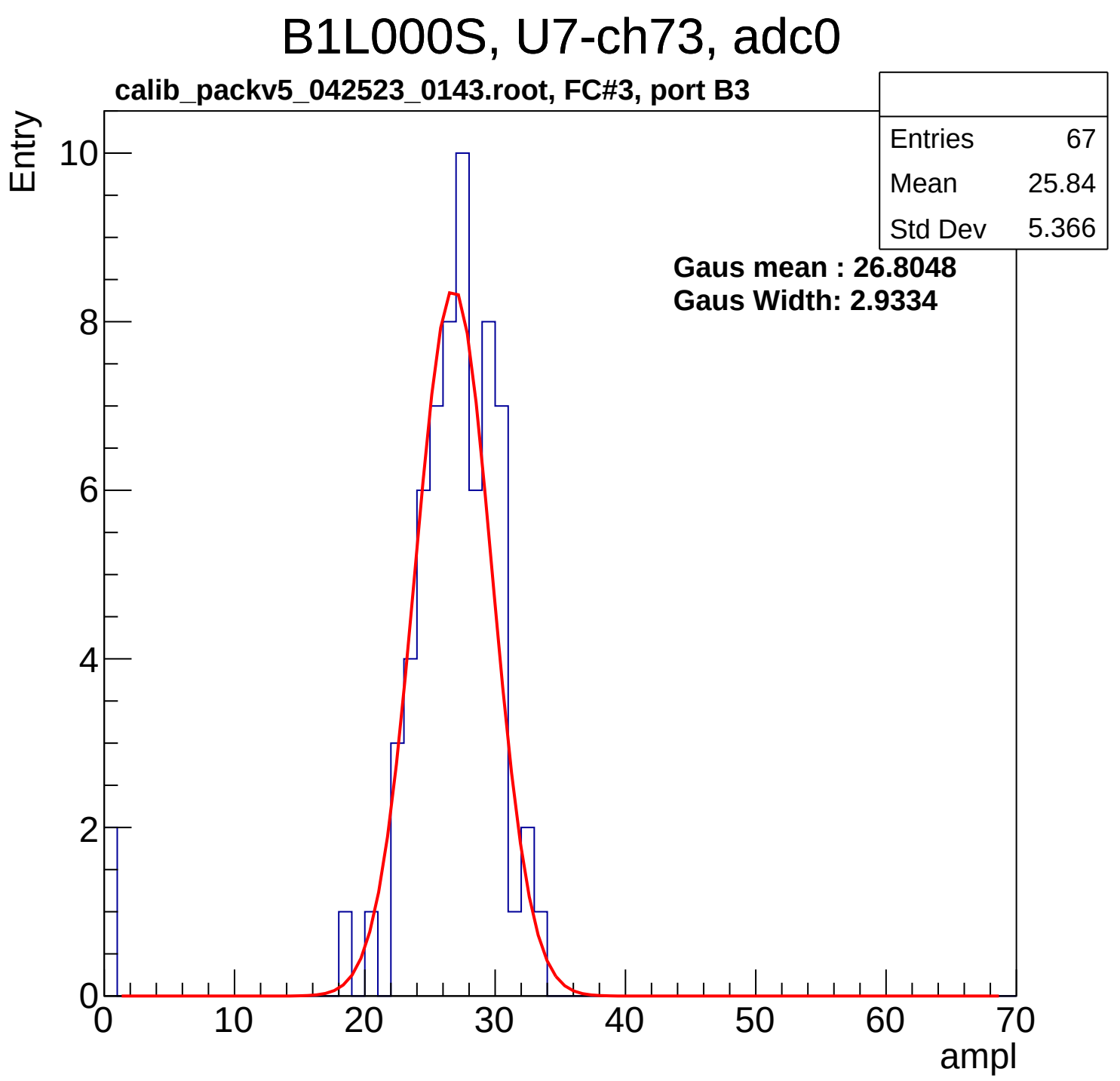
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch73, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 25.84
Std Dev 5.366
Gaus mean : 26.8048
Gaus Width: 2.9334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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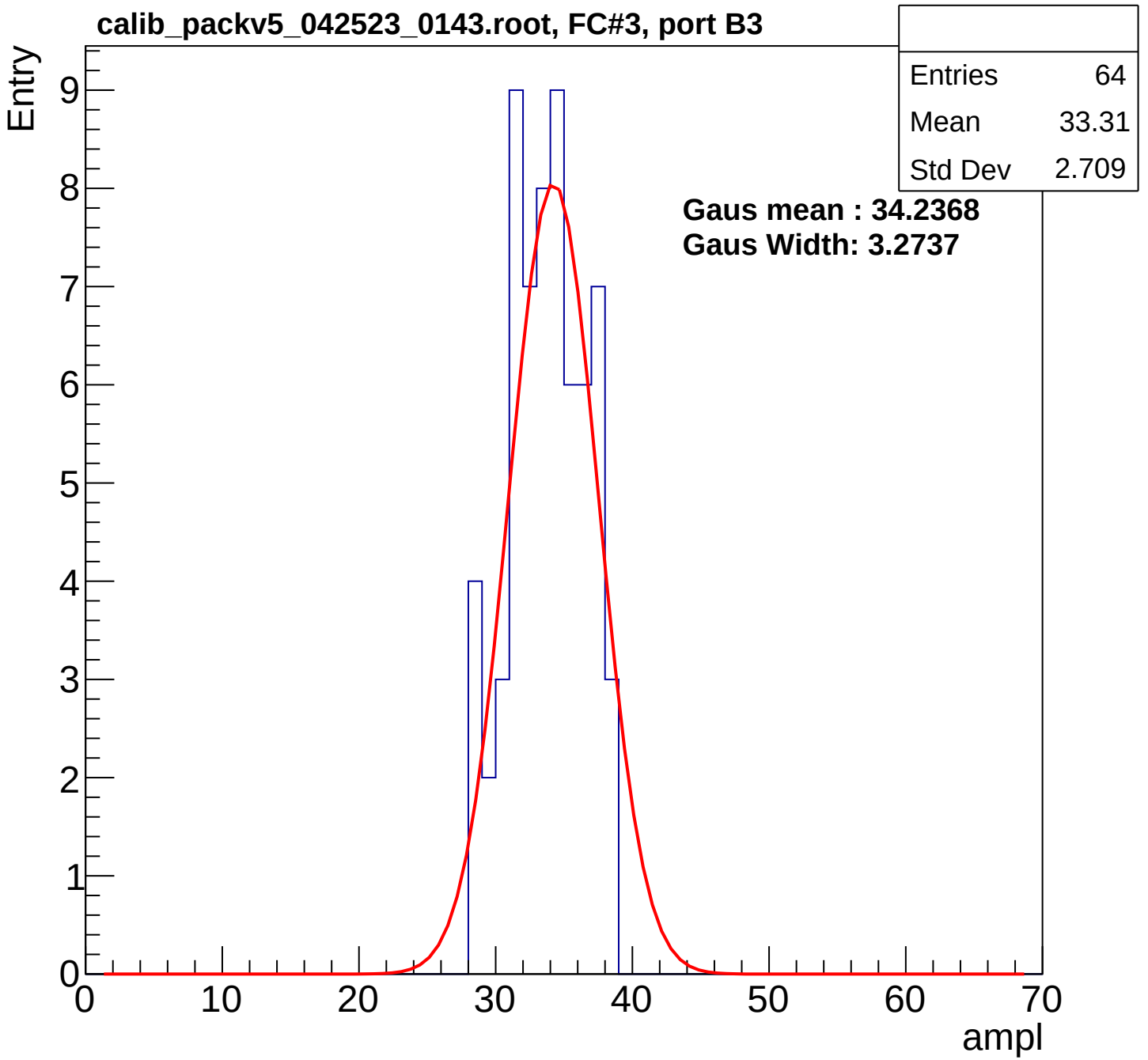
B1L000S, U7-ch73, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 33.31
Std Dev 2.709
Gaus mean : 34.2368
Gaus Width: 3.2737
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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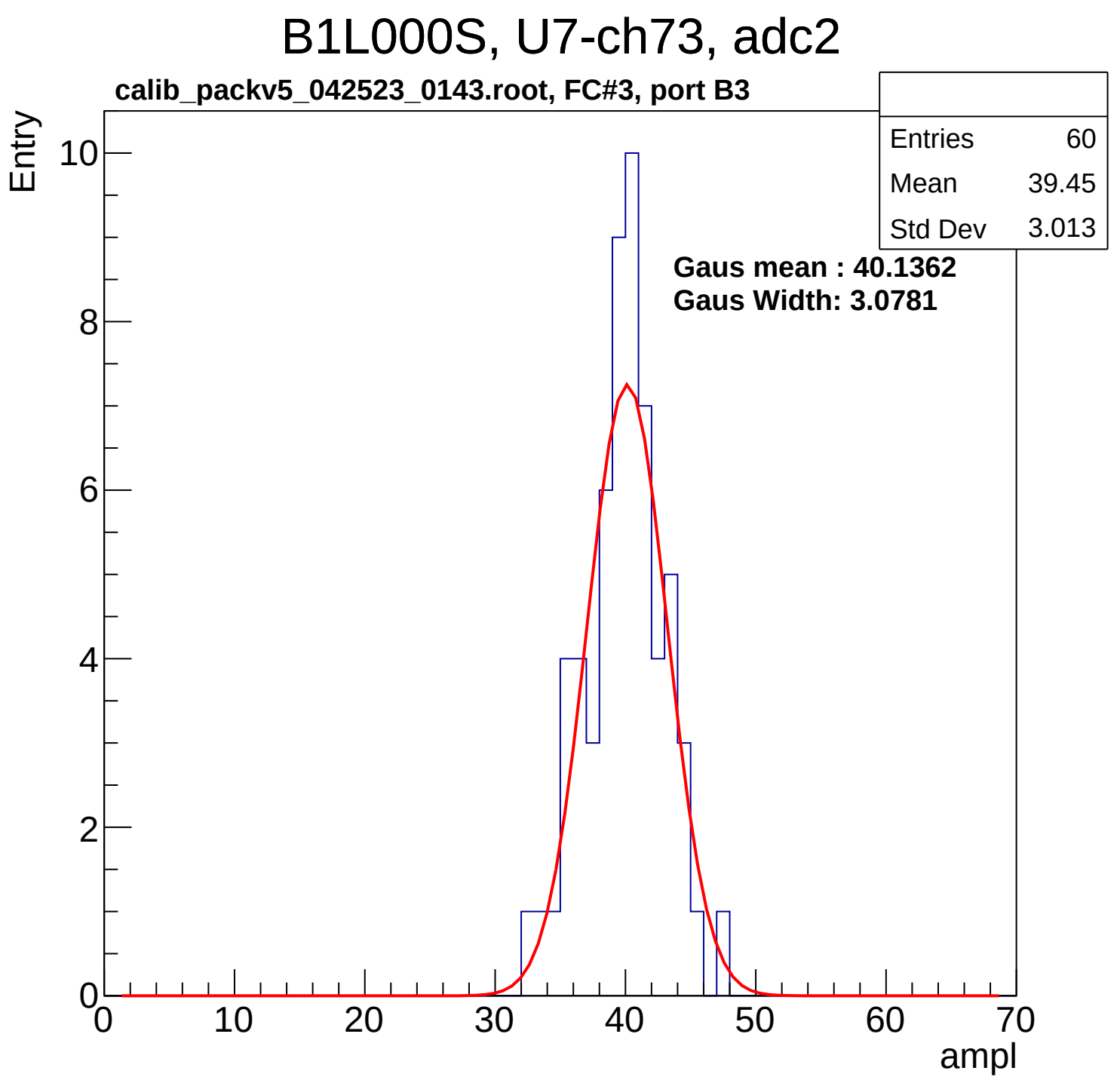
B1L000S, U7-ch73, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 39.45
Std Dev 3.013
Gaus mean : 40.1362
Gaus Width: 3.0781
Entry
ampl
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2
4
6
8
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0
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40
50
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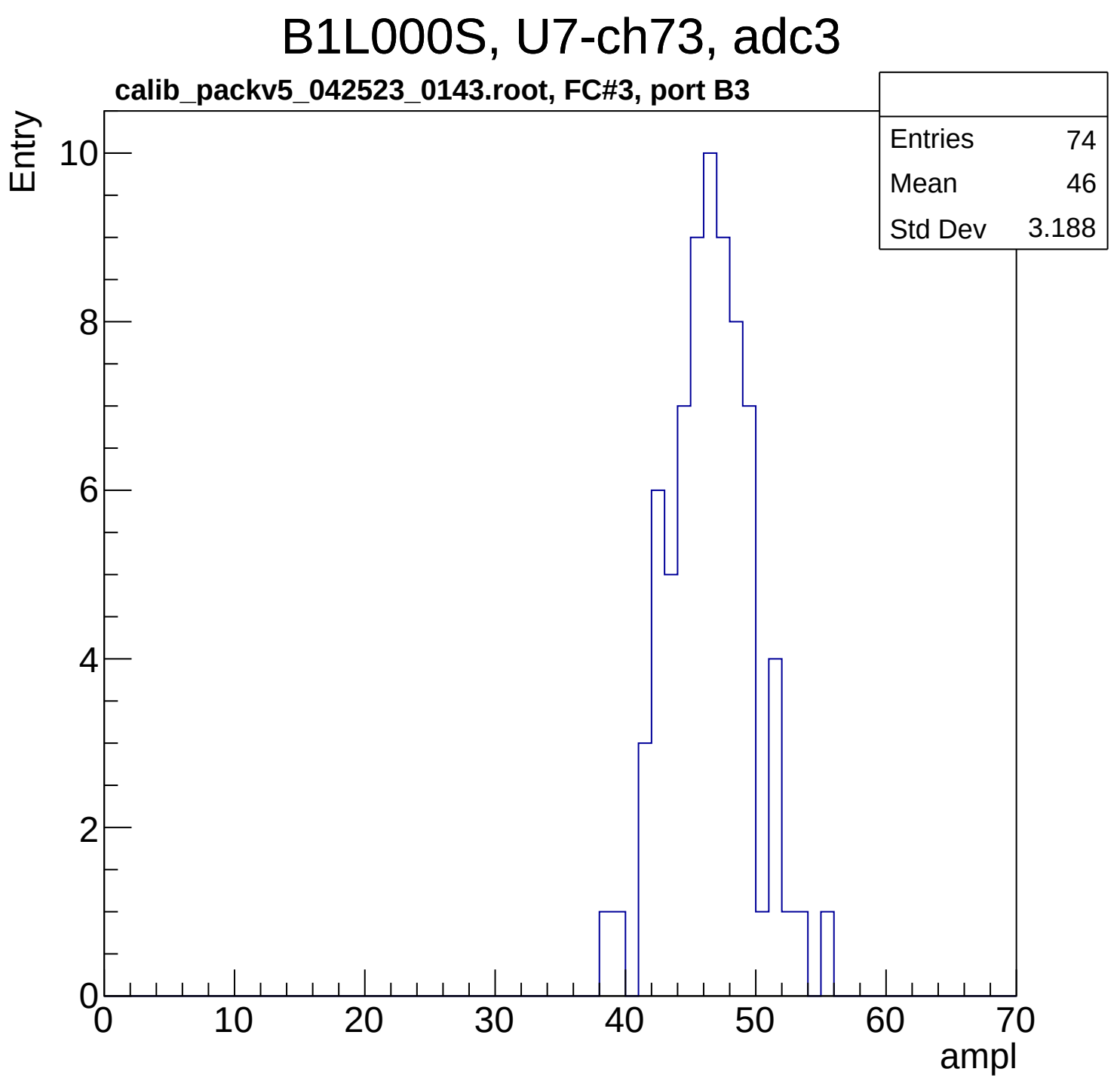
B1L000S, U7-ch73, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 46
Std Dev 3.188
Entry
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10
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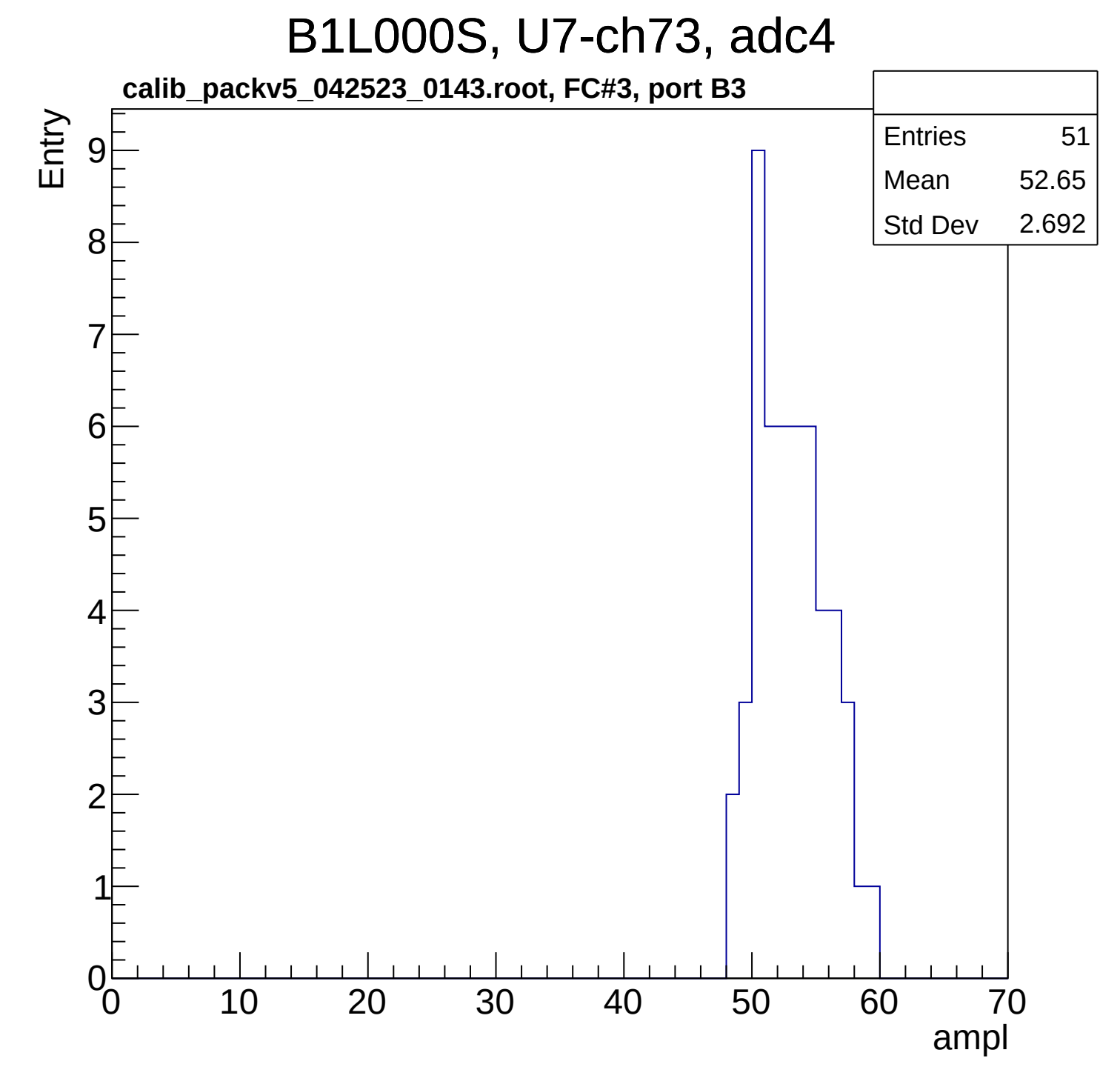
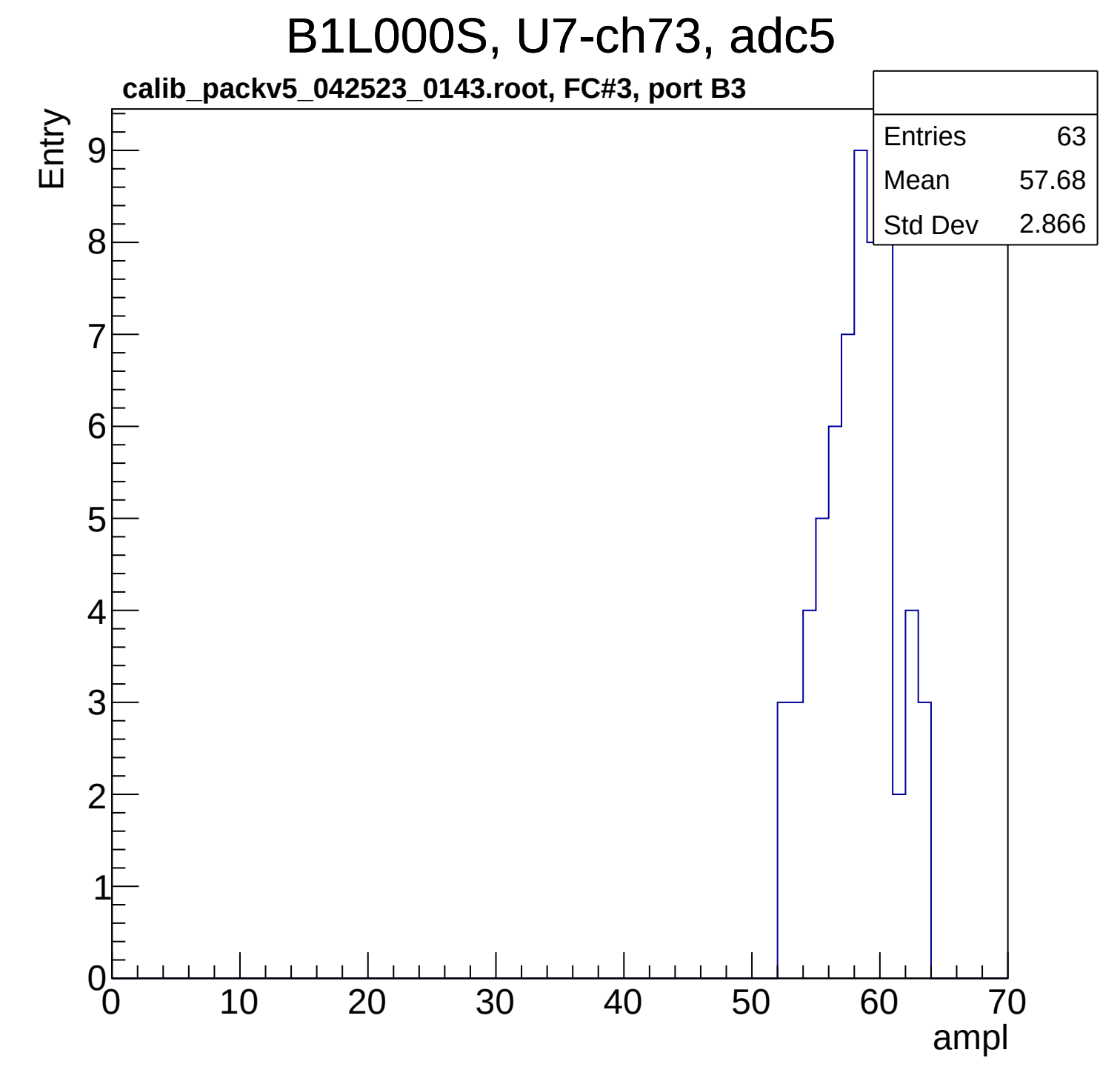
50
60
70
ampl


B1L000S, U7-ch73, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 52.65
Std Dev 2.692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch73, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 57.68
Std Dev 2.866
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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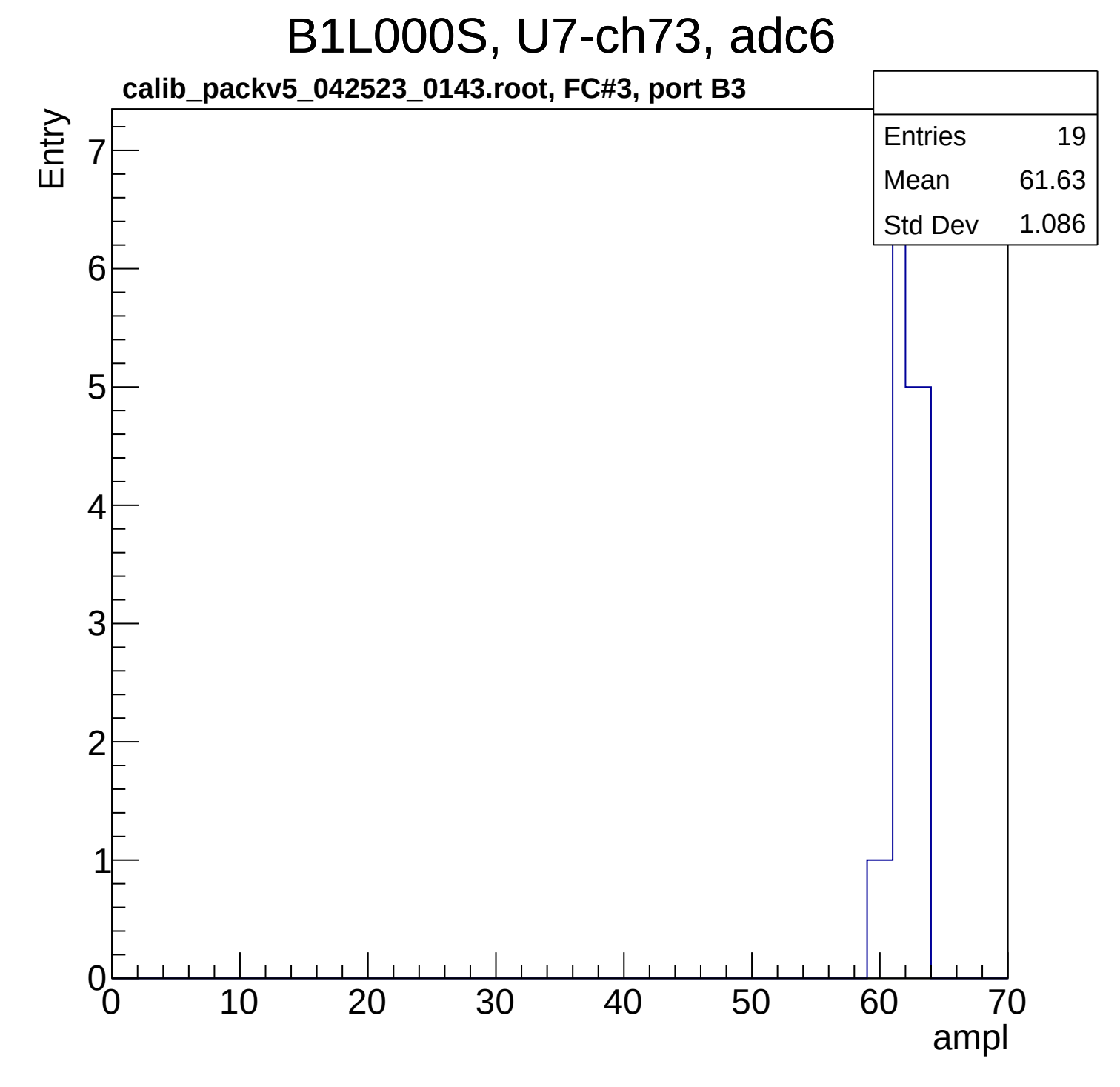
B1L000S, U7-ch73, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 61.63
Std Dev 1.086
Entry
0
1
2
3
4
5
6
7
0
10
20
30
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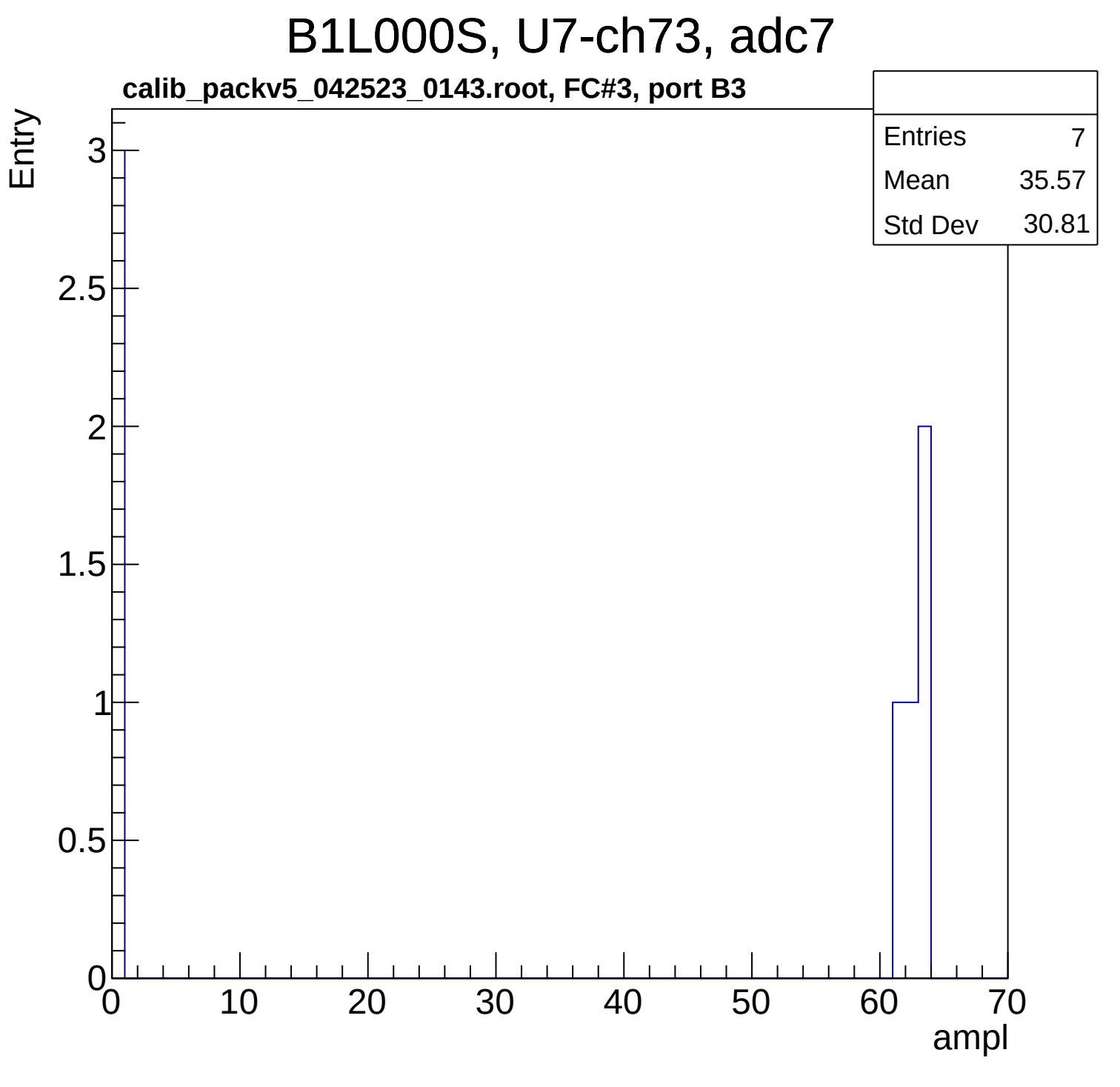
50
60
70
ampl


B1L000S, U7-ch73, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 35.57
Std Dev 30.81
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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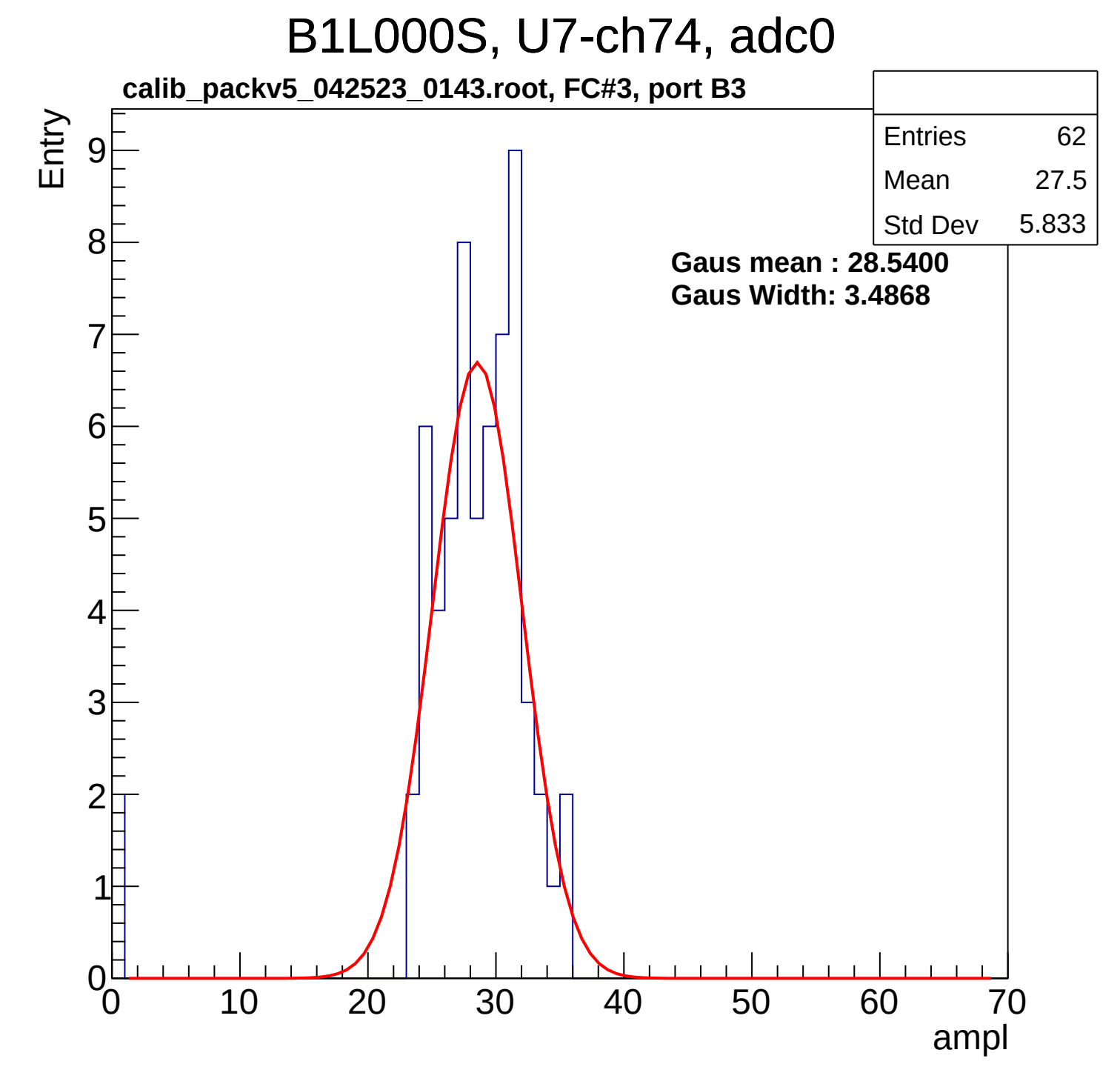
B1L000S, U7-ch74, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.5
Std Dev 5.833
Gaus mean : 28.5400
Gaus Width: 3.4868
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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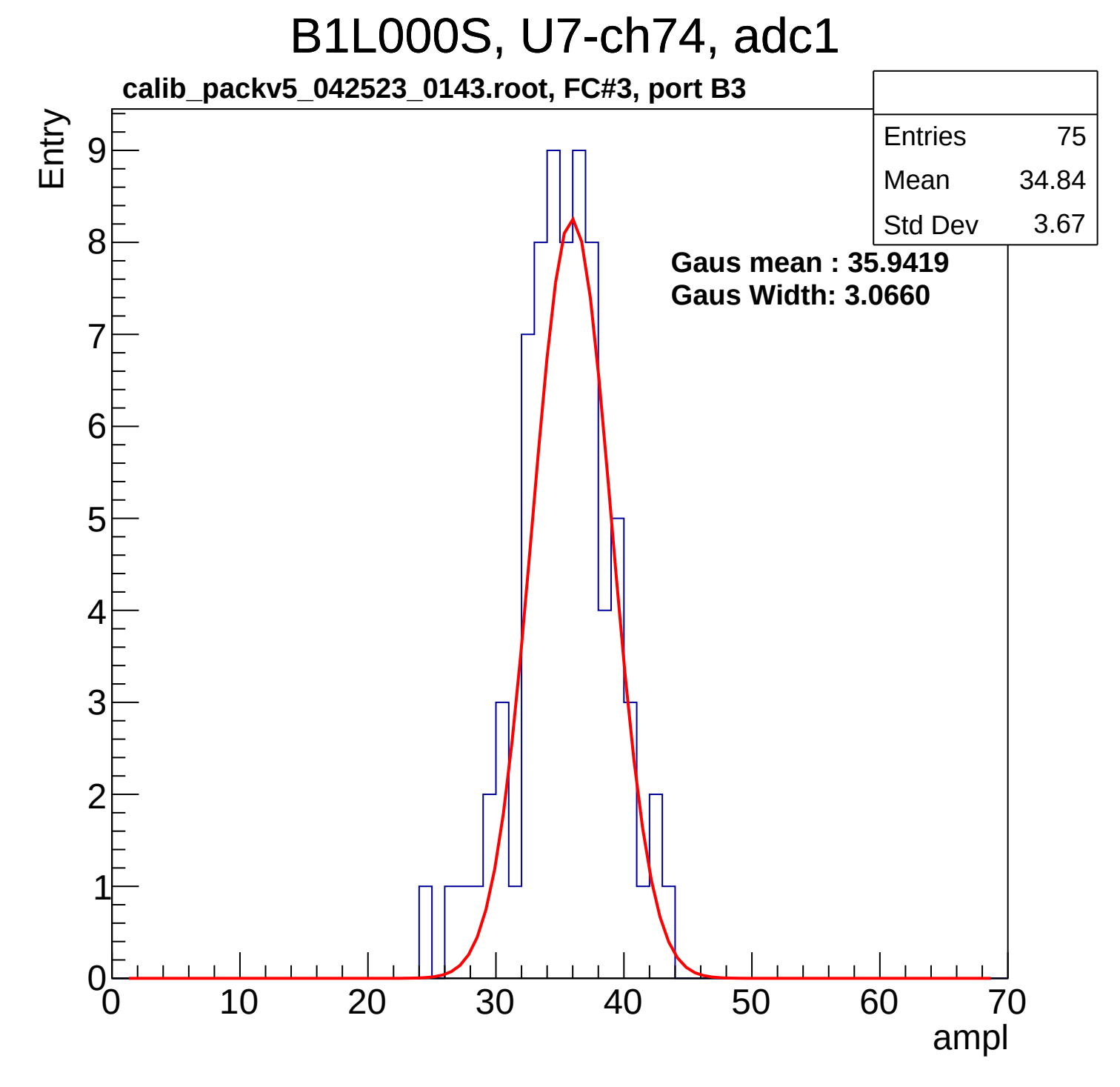
B1L000S, U7-ch74, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 34.84
Std Dev 3.67
Gaus mean : 35.9419
Gaus Width: 3.0660
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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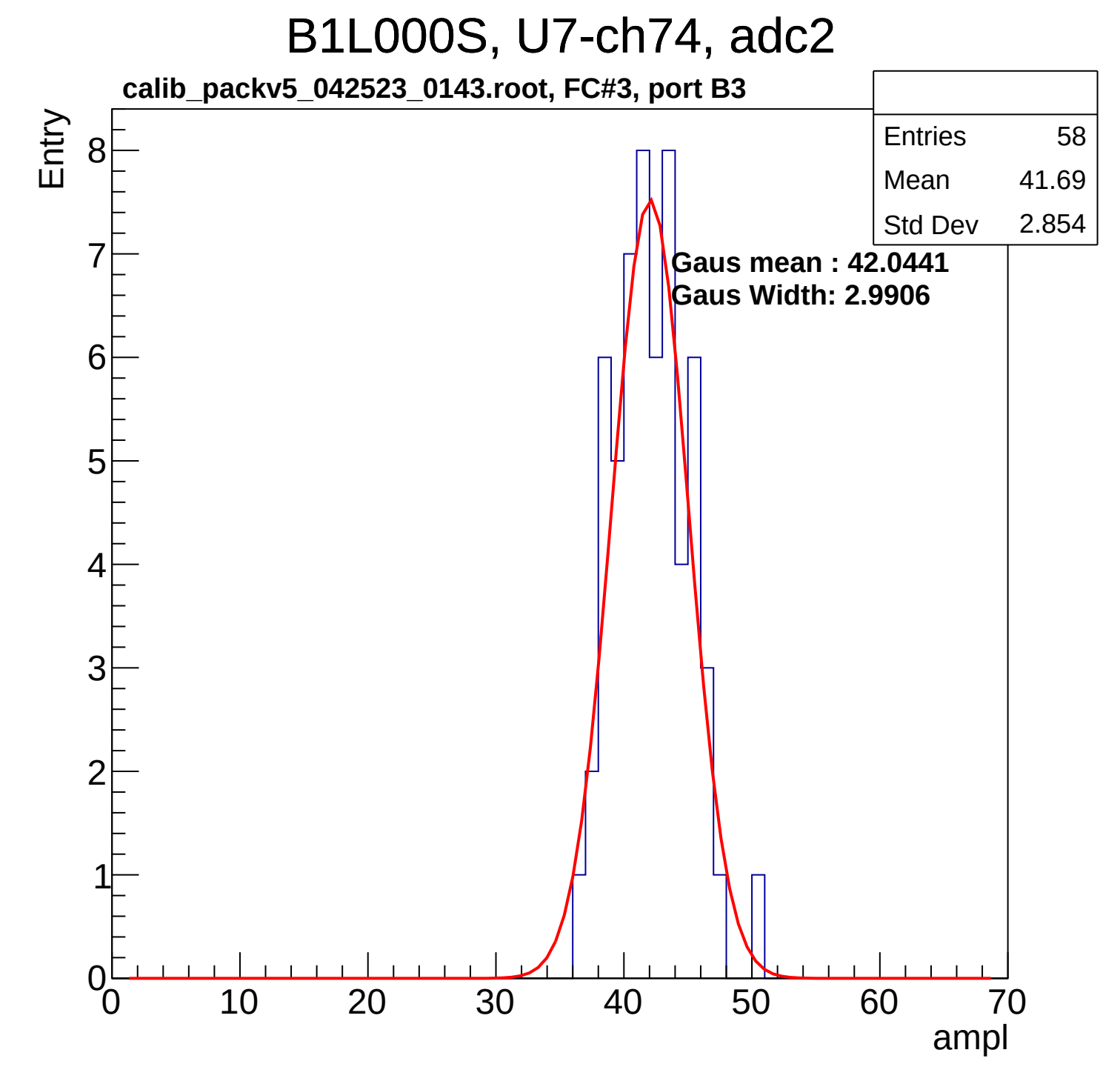
B1L000S, U7-ch74, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.69
Std Dev 2.854
Gaus mean : 42.0441
Gaus Width: 2.9906
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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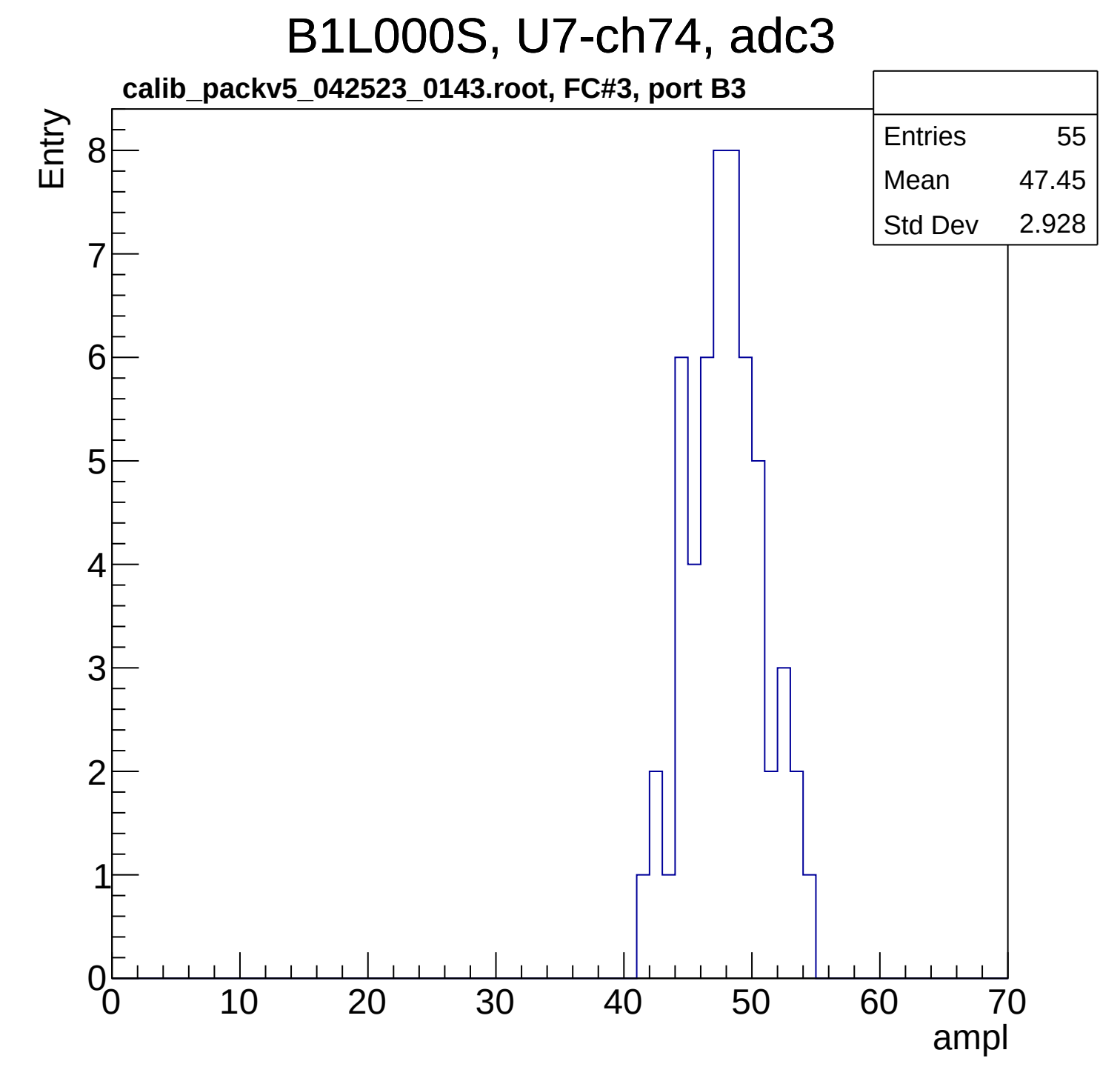
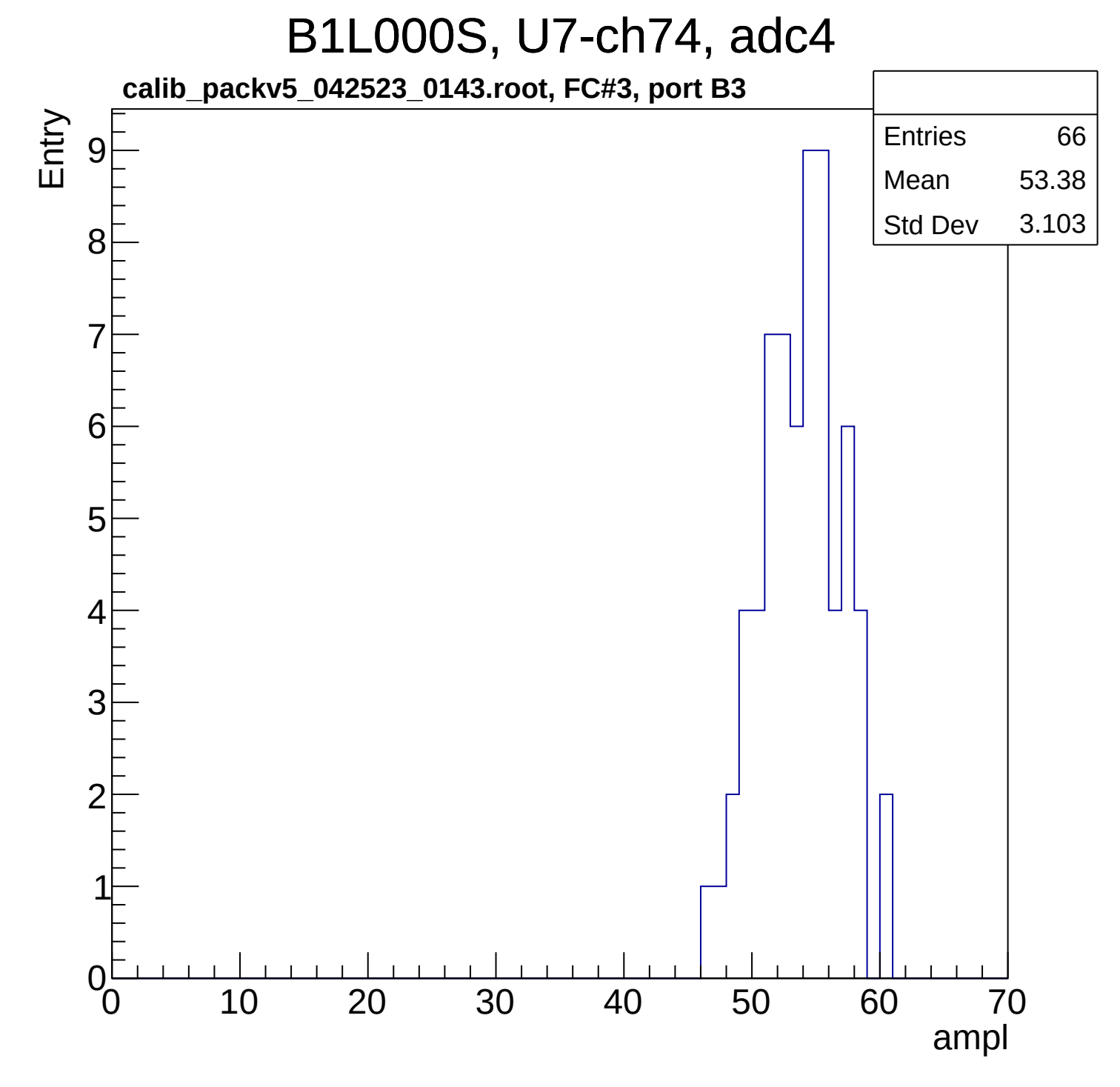

B1L000S, U7-ch74, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 47.45
Std Dev 2.928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch74, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 53.38
Std Dev 3.103
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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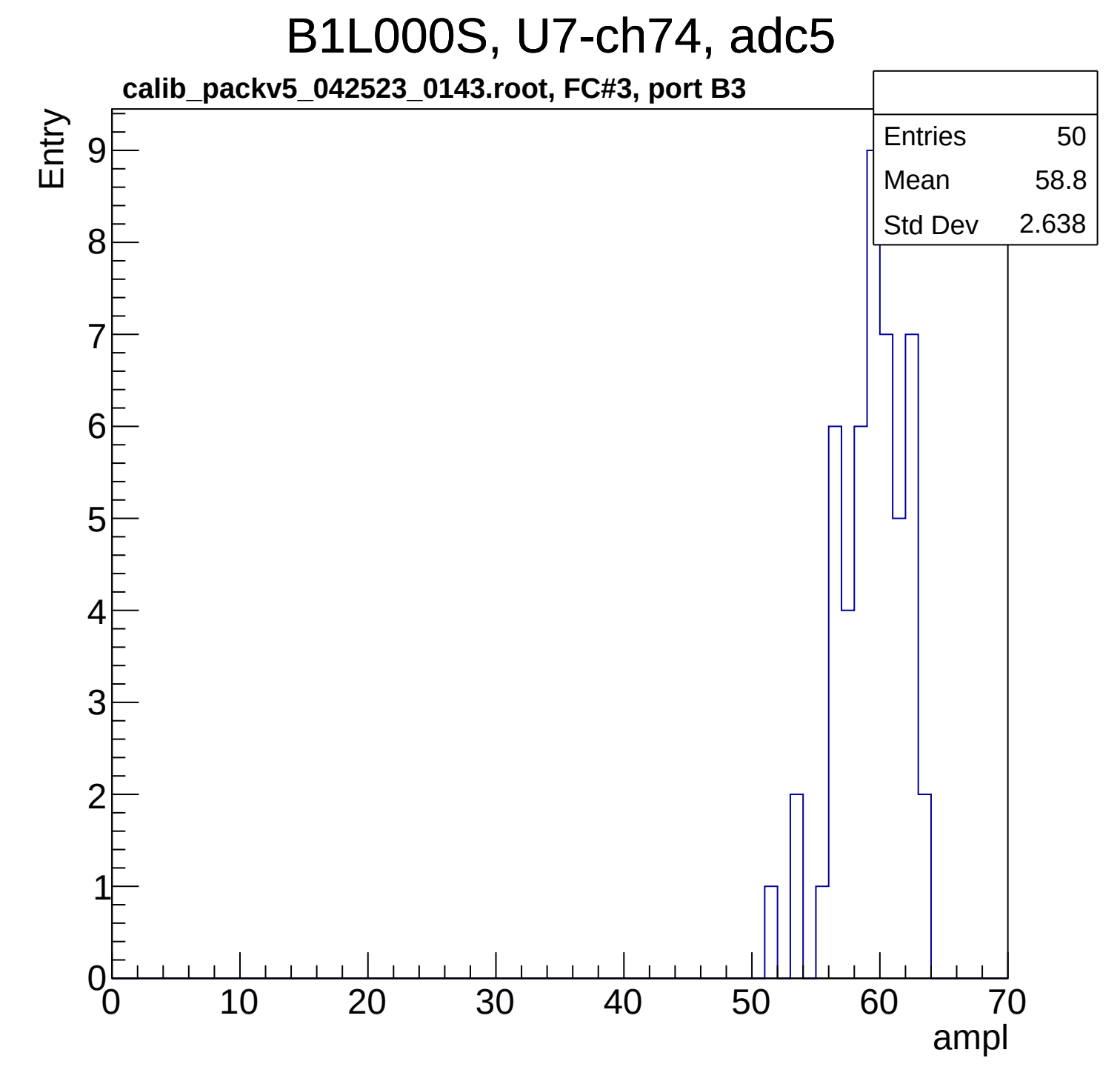
B1L000S, U7-ch74, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 58.8
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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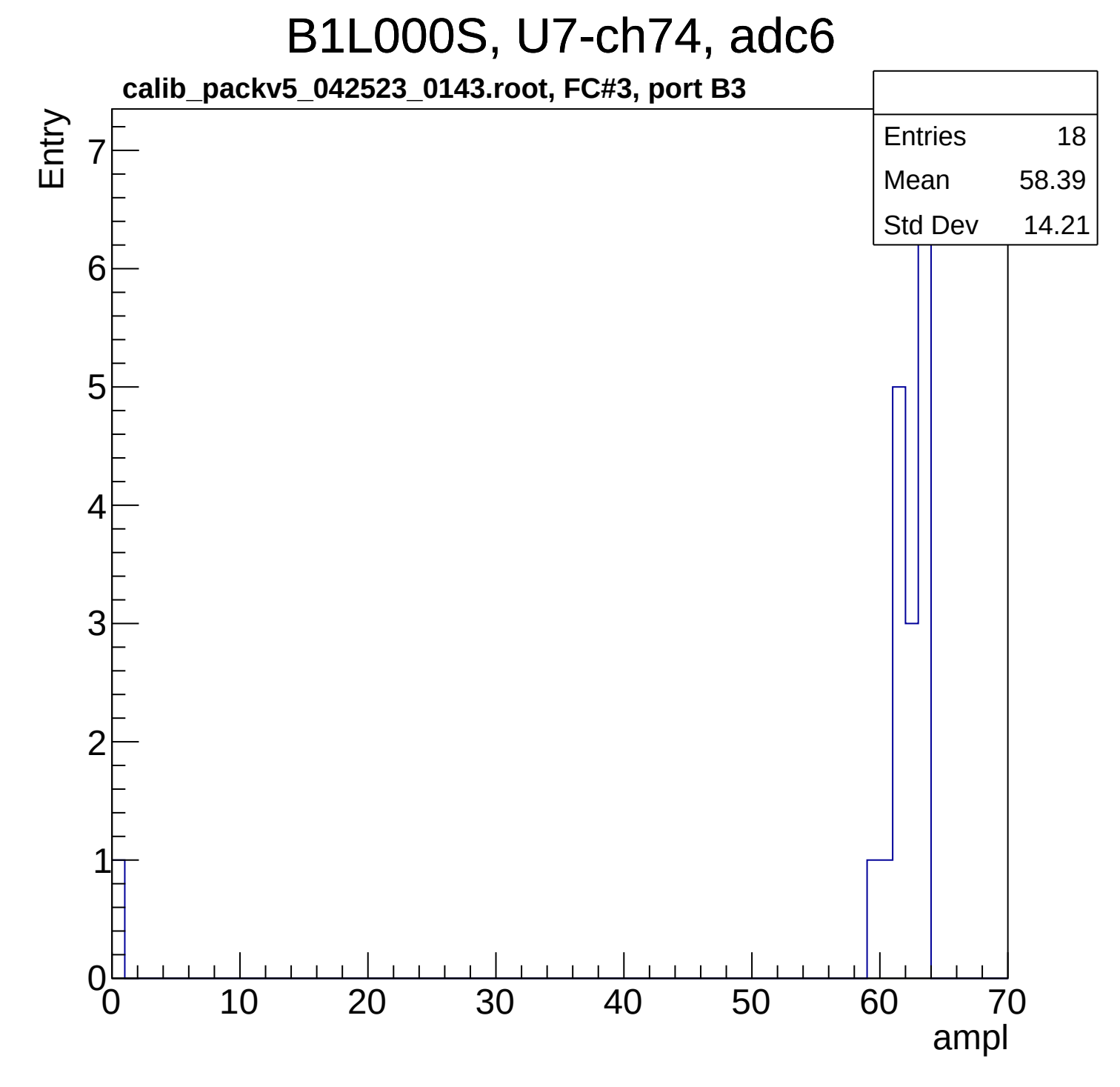
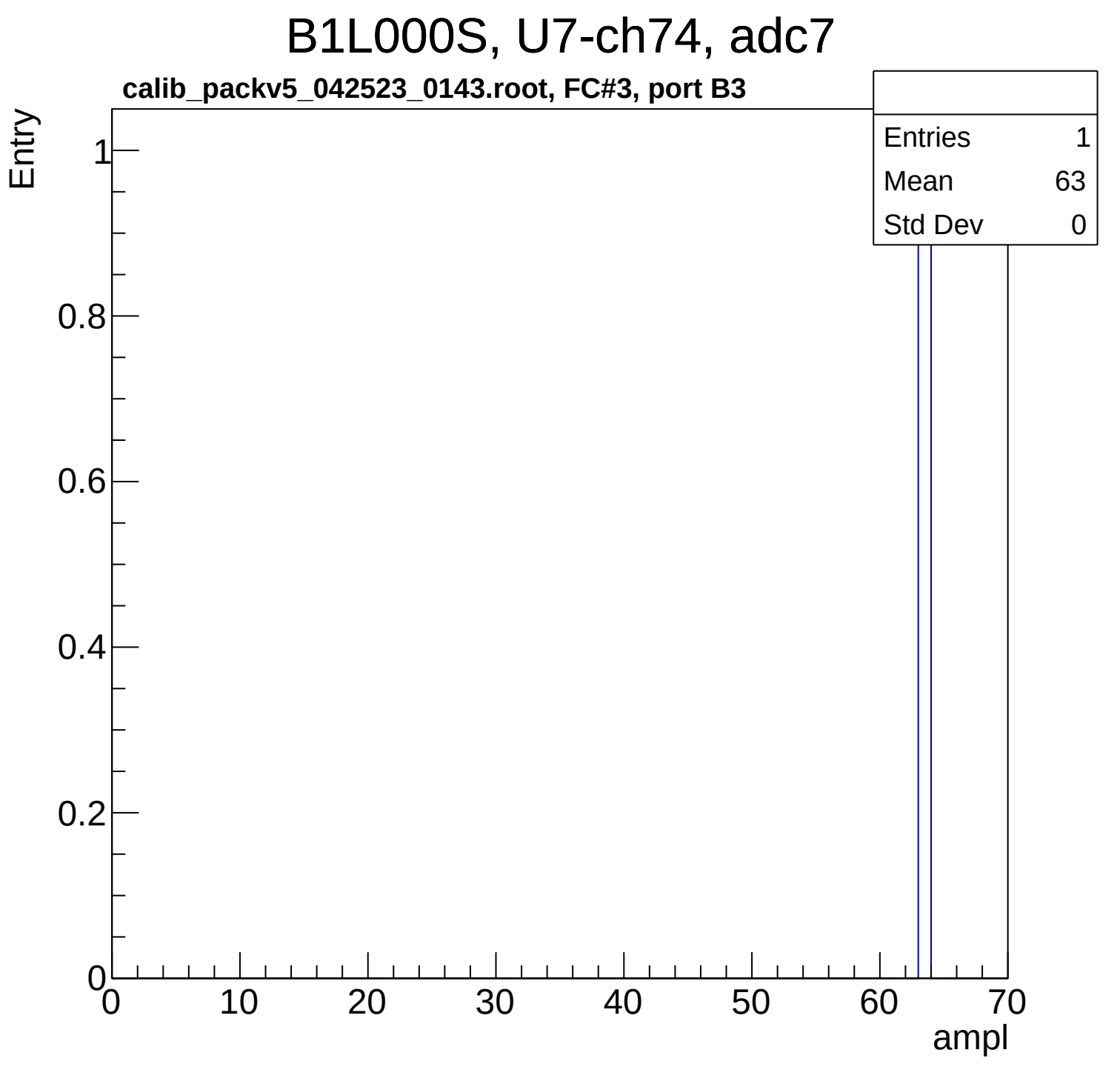
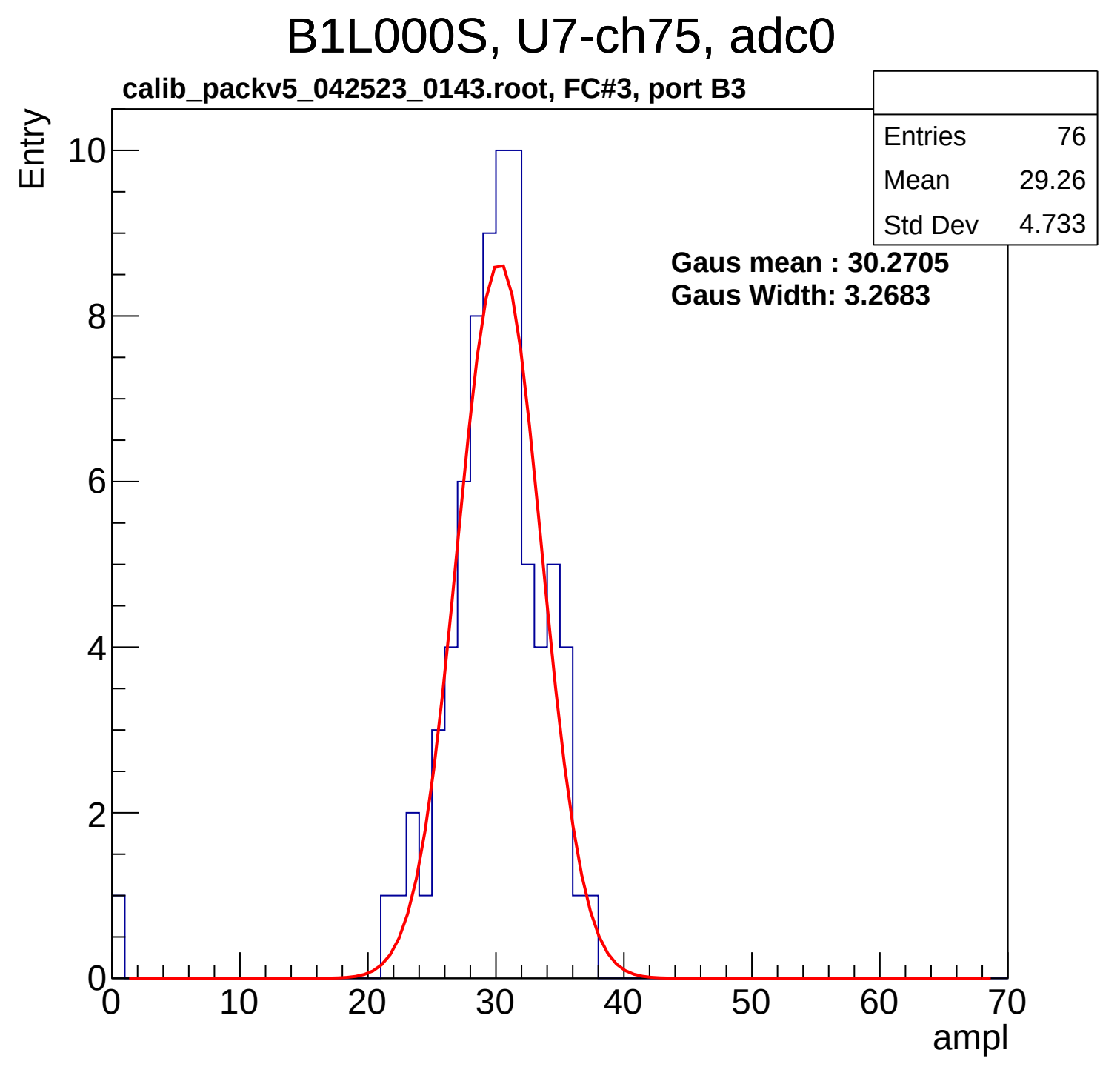
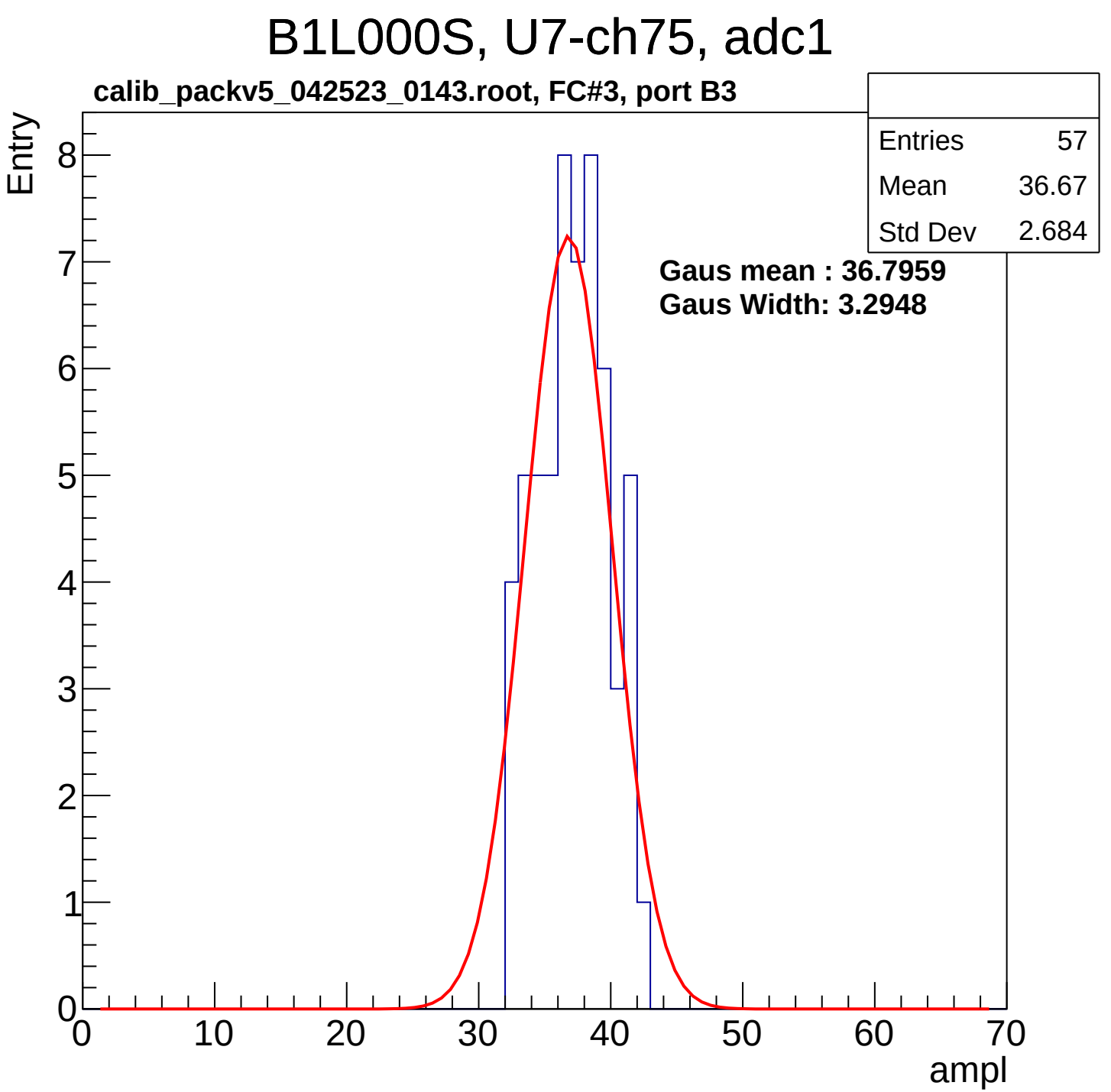
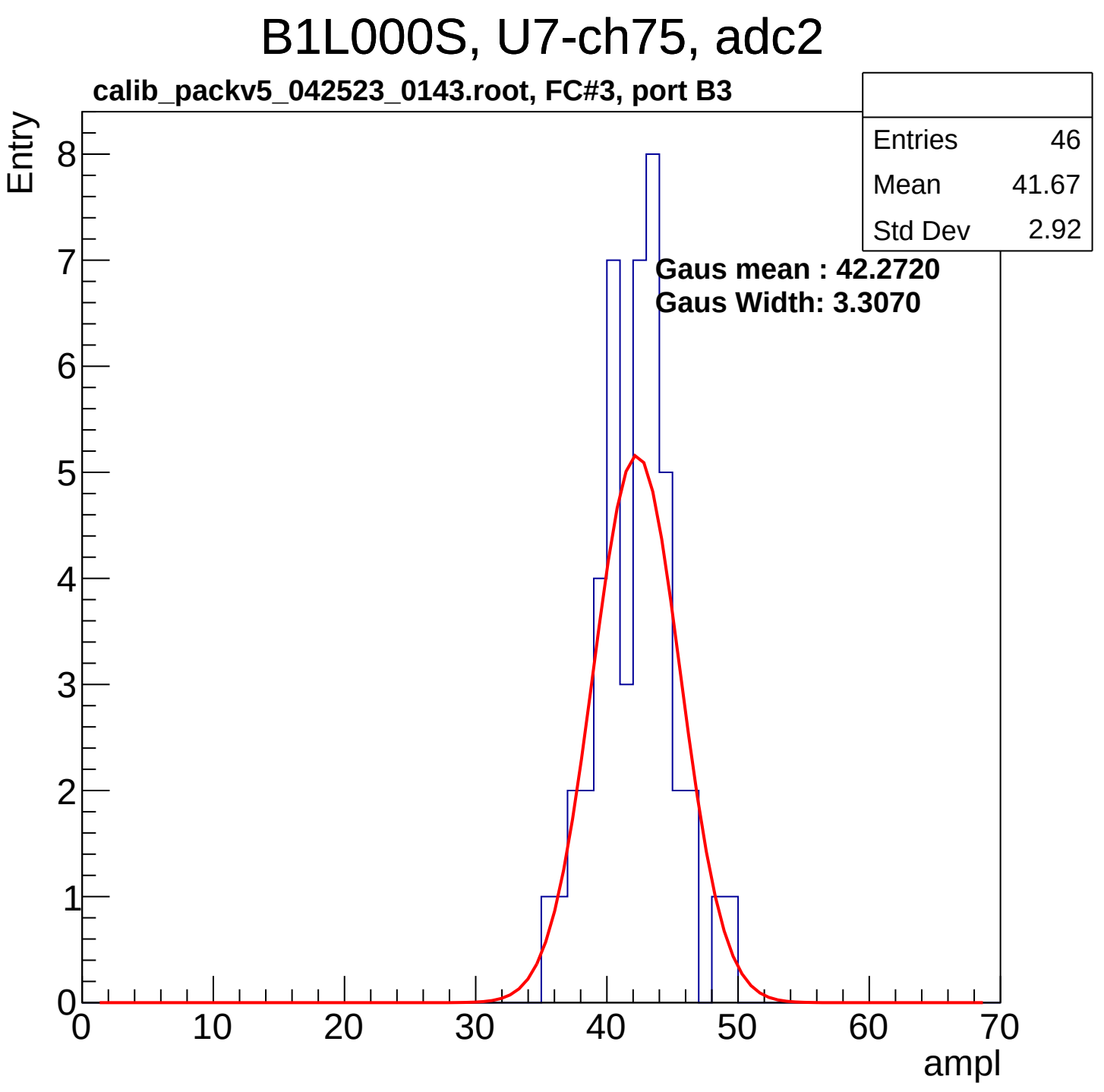

B1L000S, U7-ch74, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 58.39
Std Dev 14.21
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch74, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch75, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 29.26
Std Dev 4.733
Gaus mean : 30.2705
Gaus Width: 3.2683
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch75, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 36.67
Std Dev 2.684
Gaus mean : 36.7959
Gaus Width: 3.2948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch75, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 41.67
Std Dev 2.92
Gaus mean : 42.2720
Gaus Width: 3.3070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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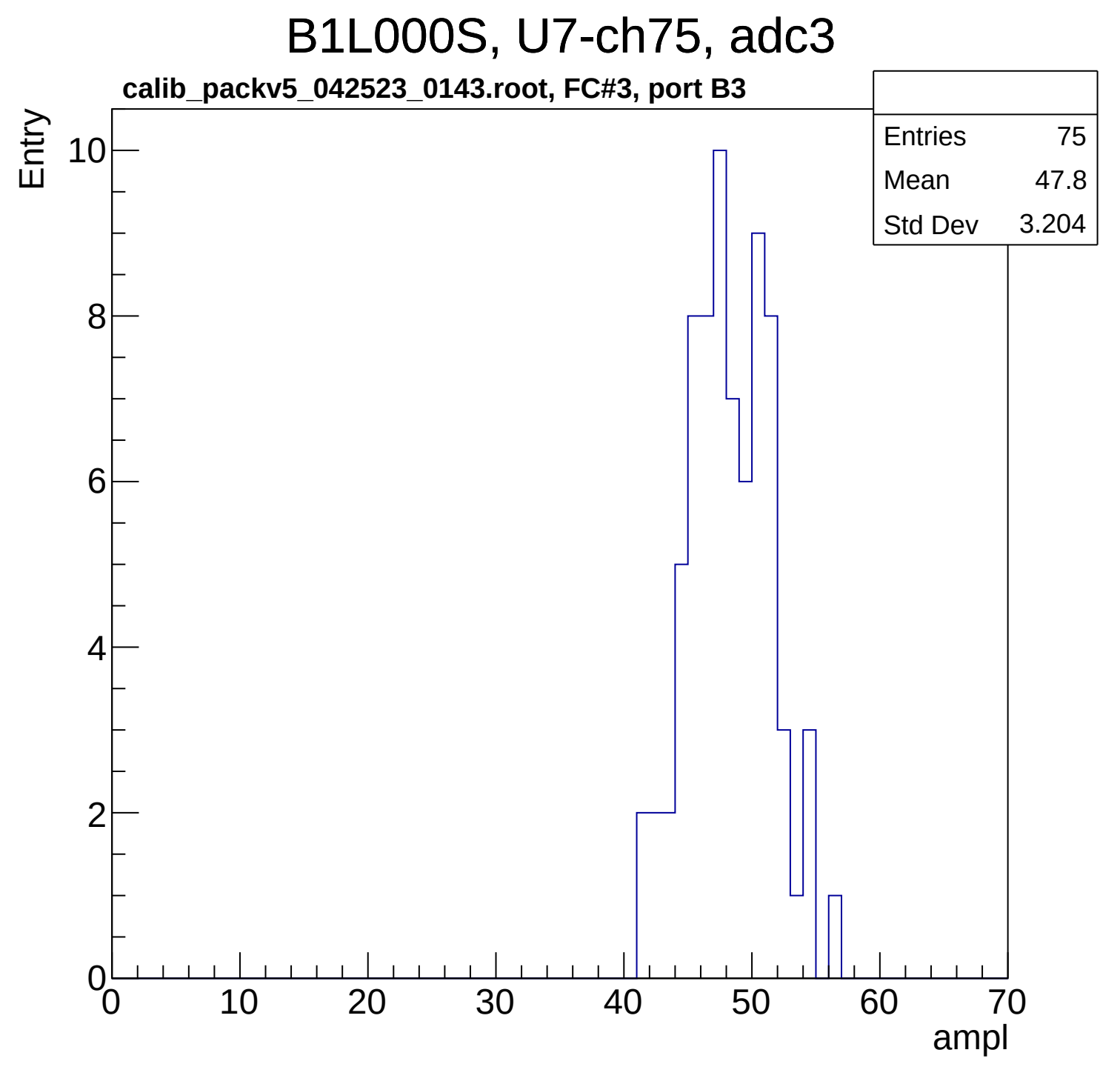
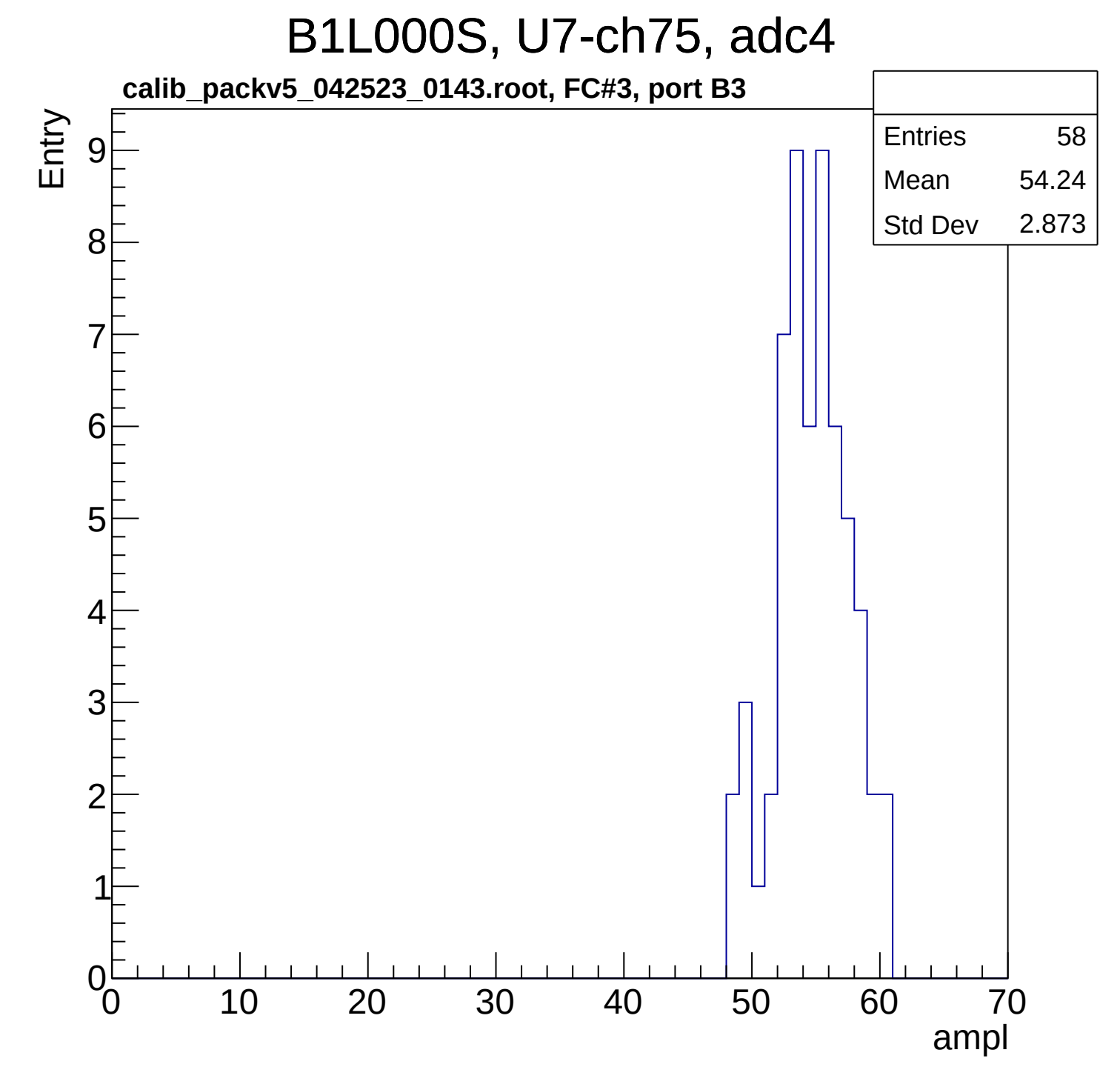
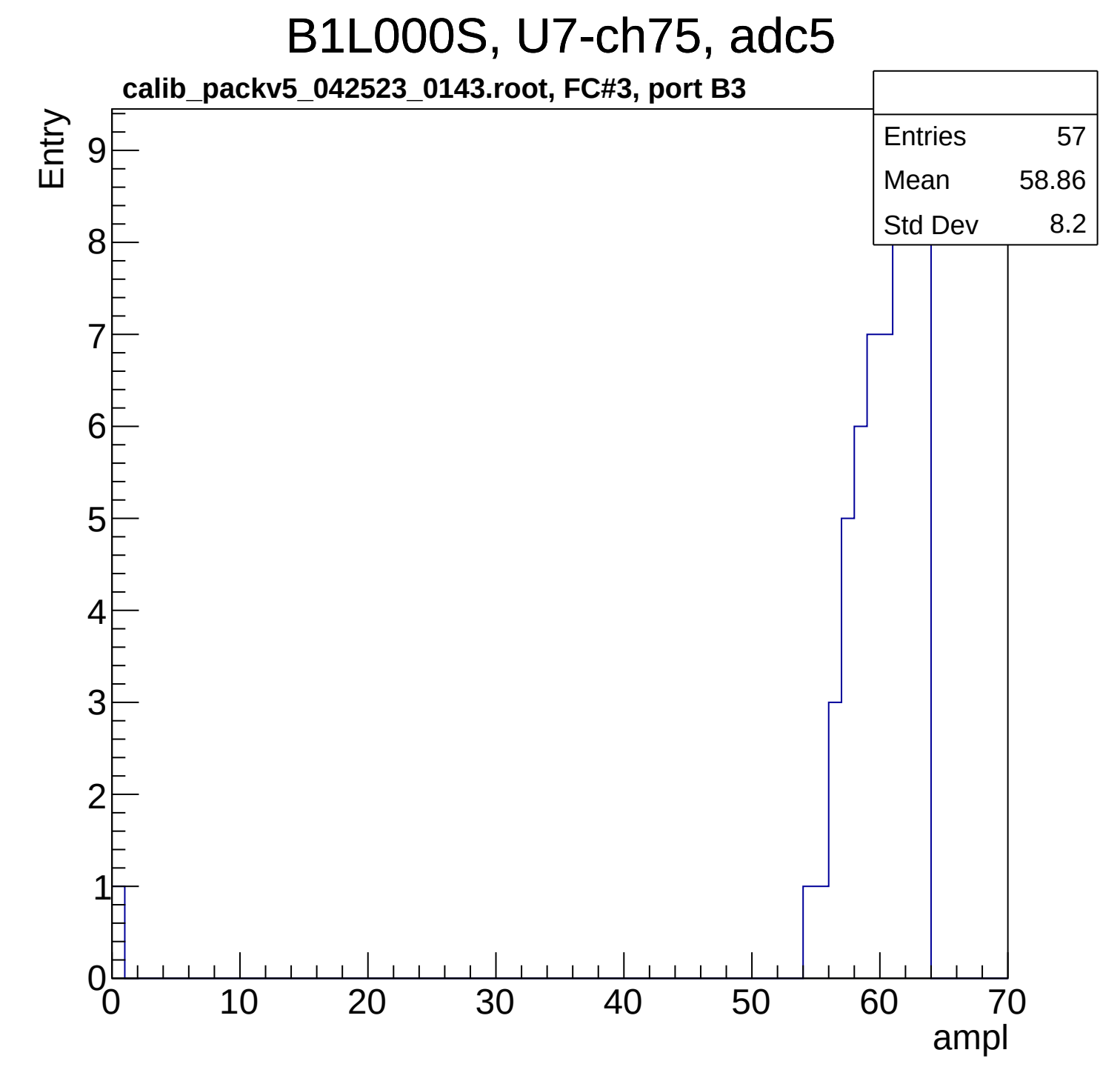

B1L000S, U7-ch75, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 47.8
Std Dev 3.204
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch75, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 54.24
Std Dev 2.873
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch75, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 58.86
Std Dev 8.2
Entry
ampl
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2
3
4
5
6
7
8
9
0
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50
60
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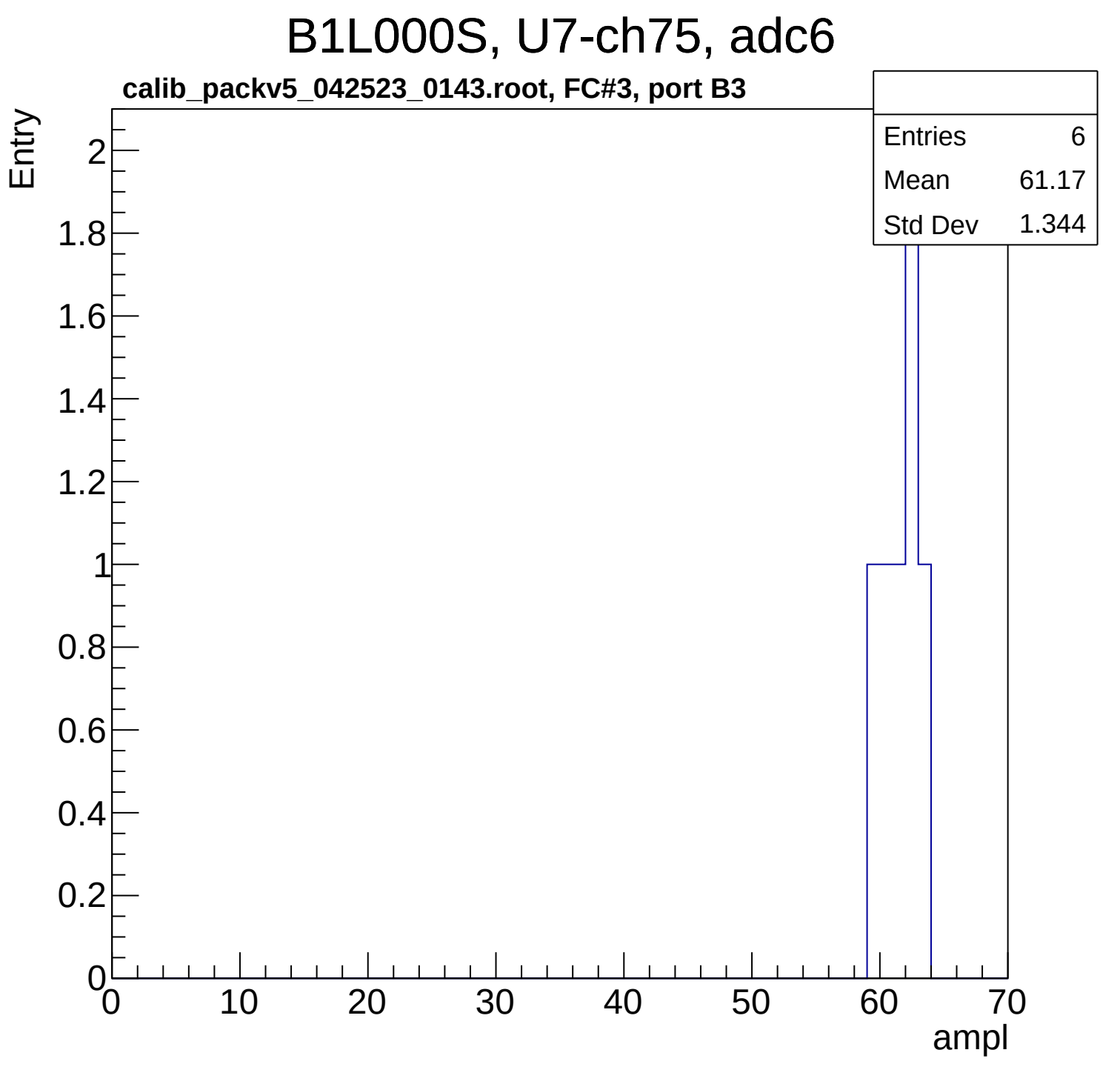
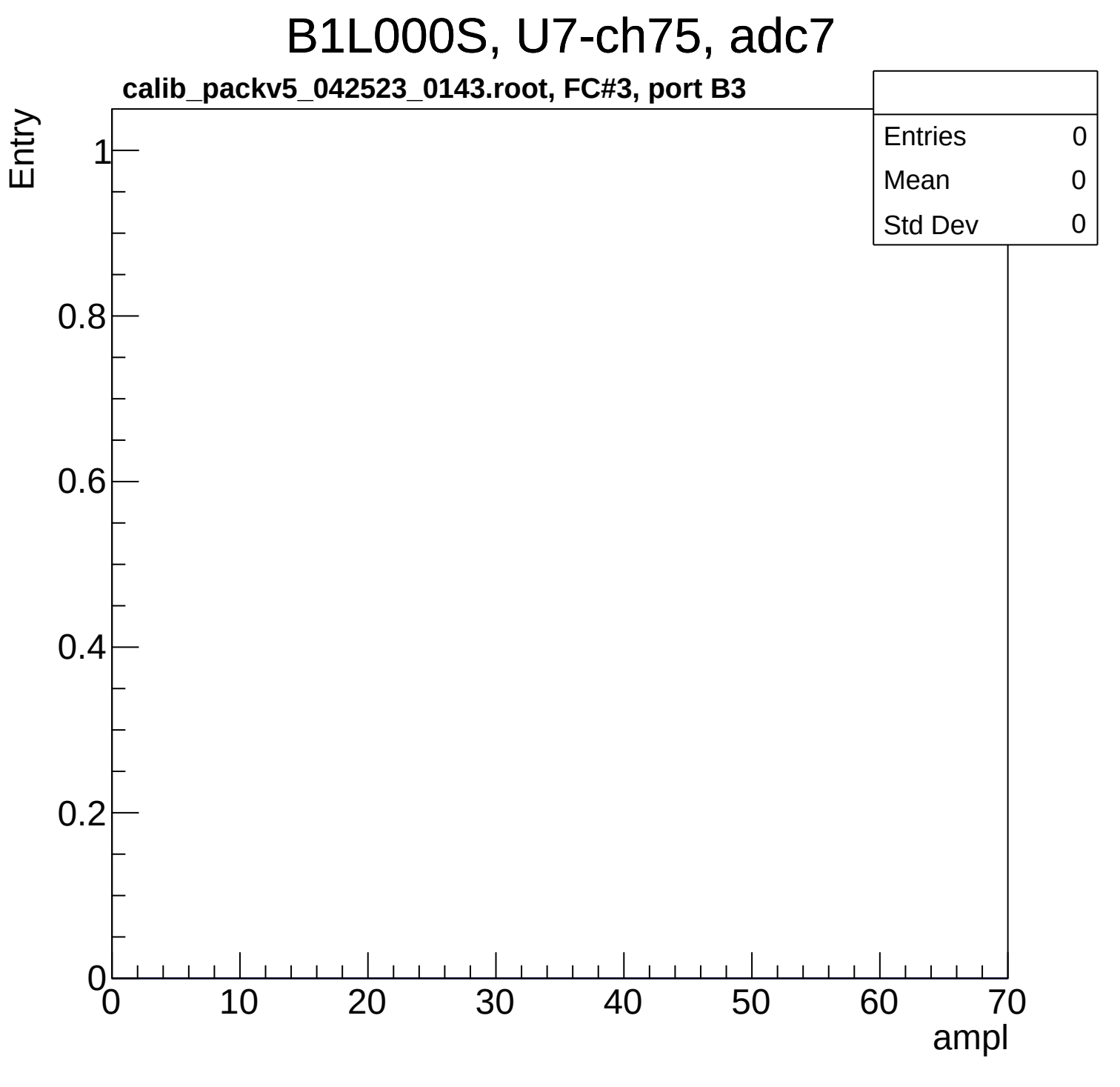
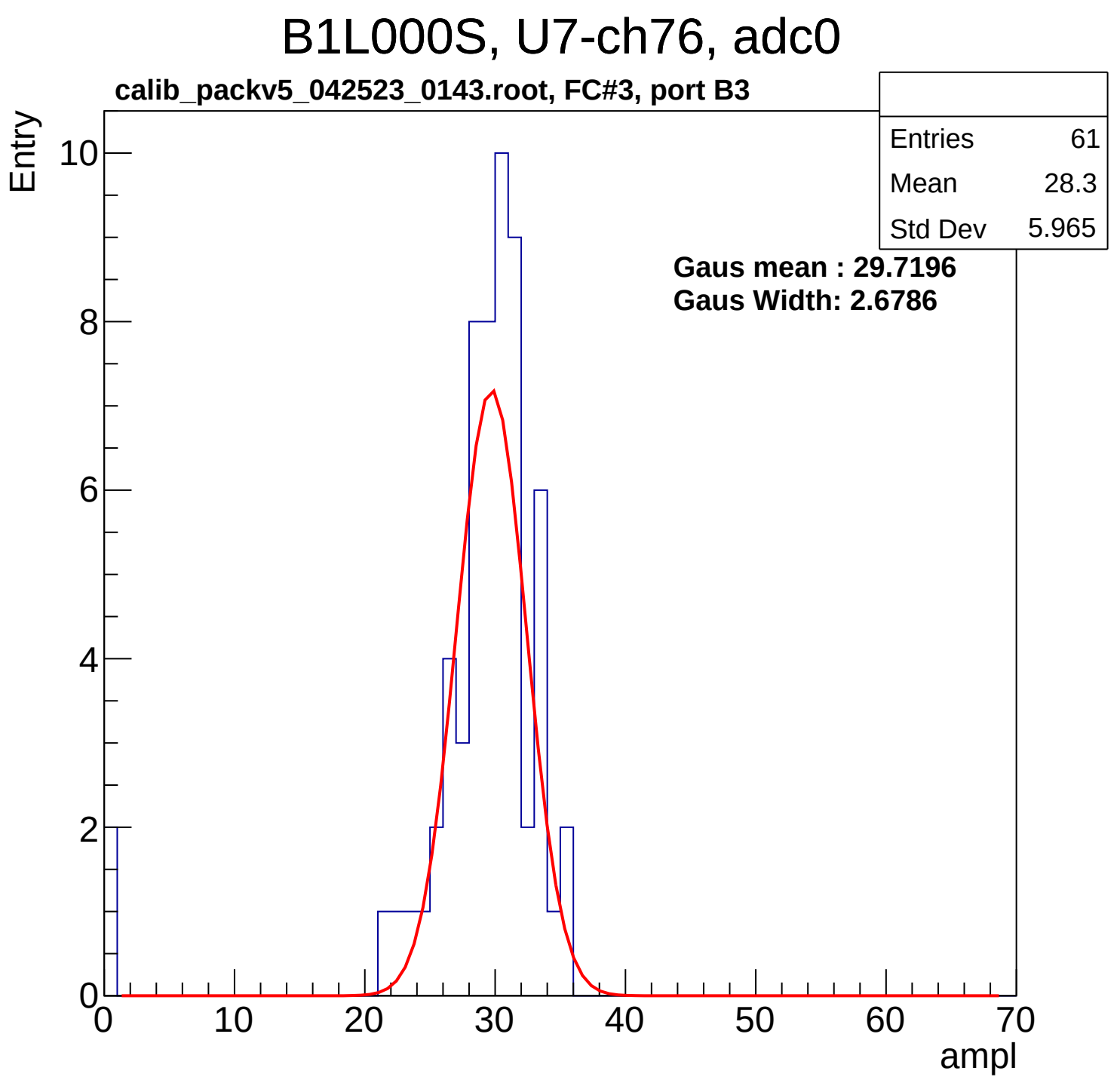
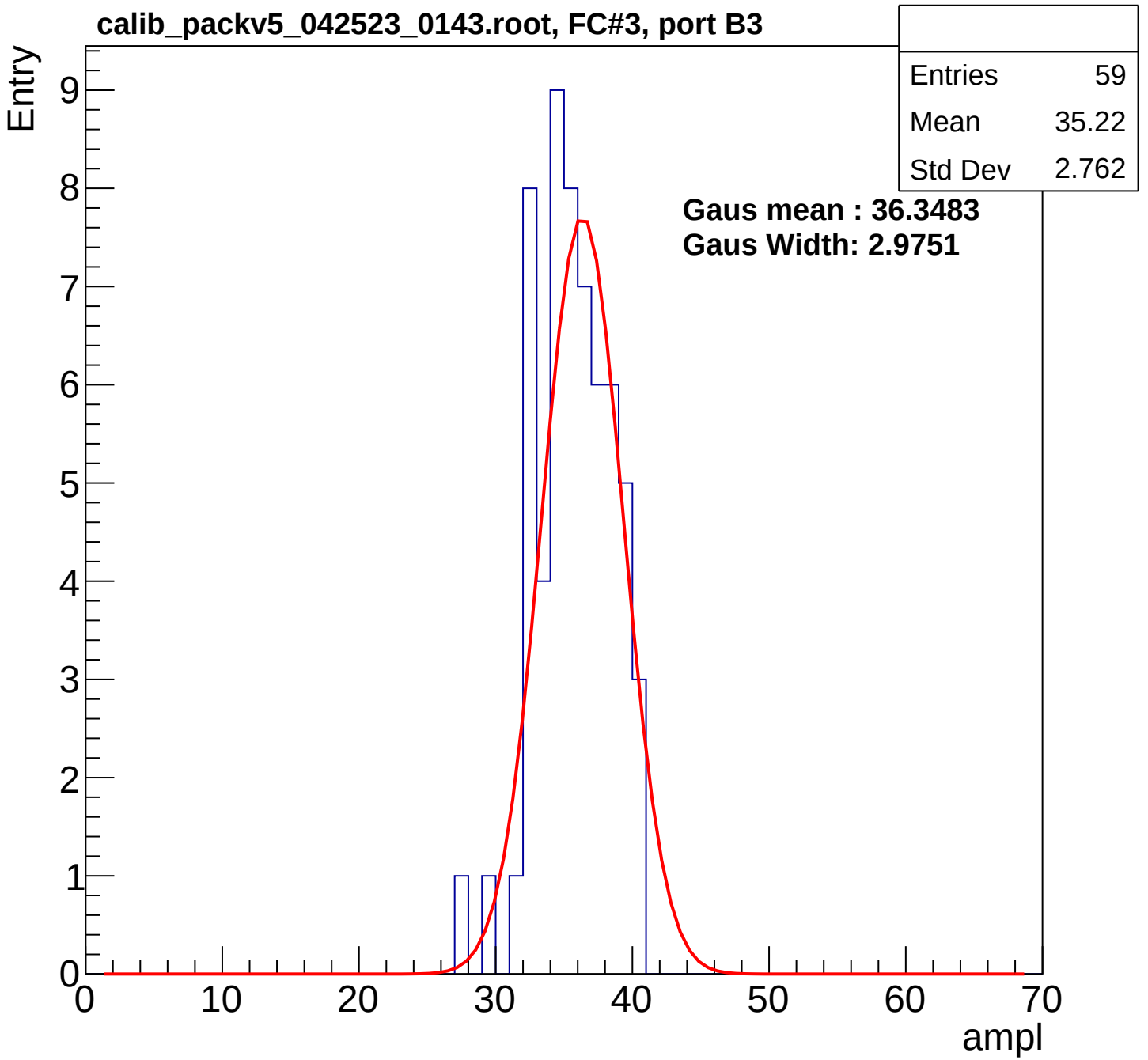
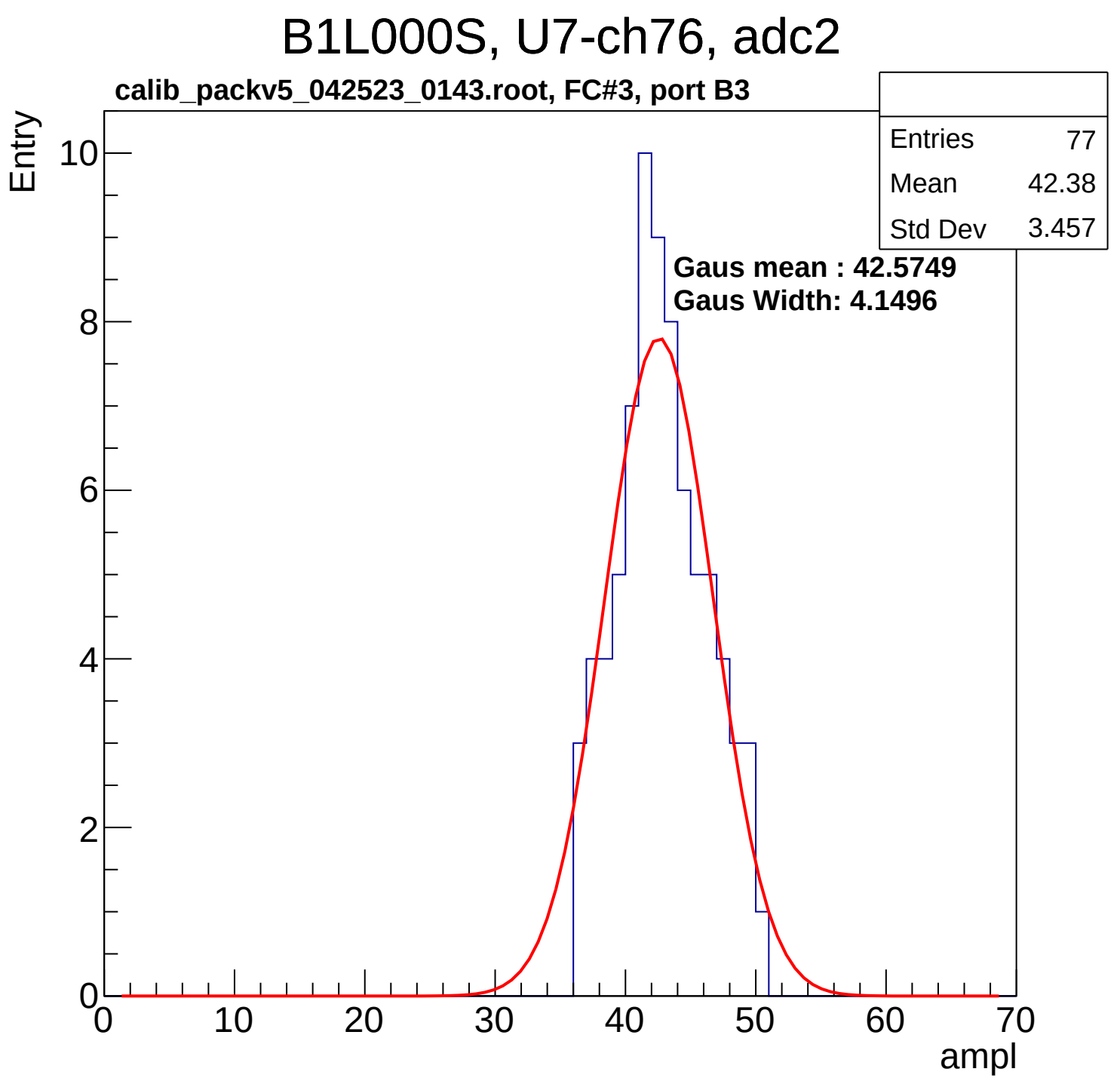

B1L000S, U7-ch75, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 61.17
Std Dev 1.344
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch75, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch76, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 28.3
Std Dev 5.965
Gaus mean : 29.7196
Gaus Width: 2.6786
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch76, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.22
Std Dev 2.762
Gaus mean : 36.3483
Gaus Width: 2.9751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch76, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 42.38
Std Dev 3.457
Gaus mean : 42.5749
Gaus Width: 4.1496
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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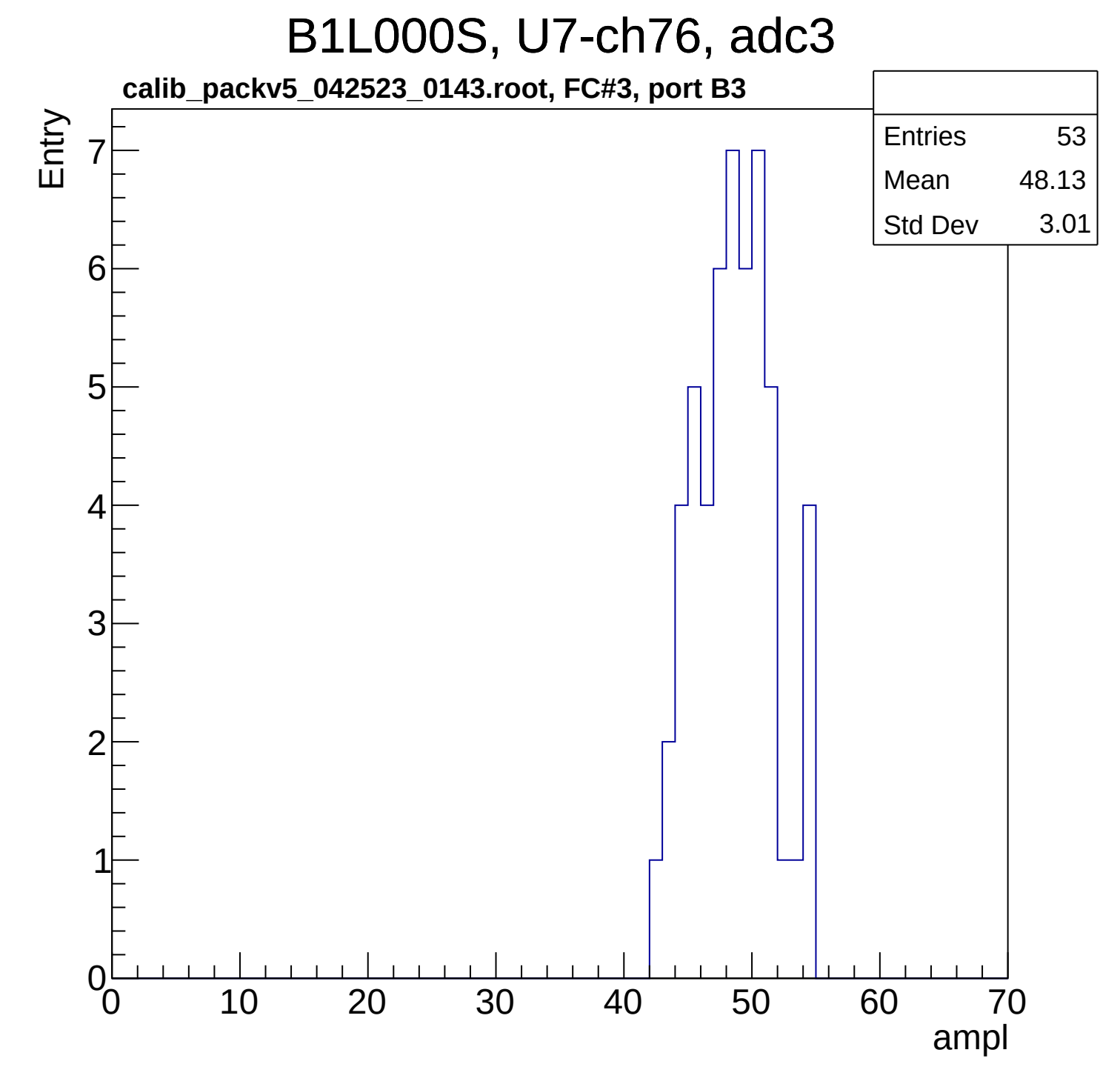
B1L000S, U7-ch76, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 48.13
Std Dev 3.01
Entry
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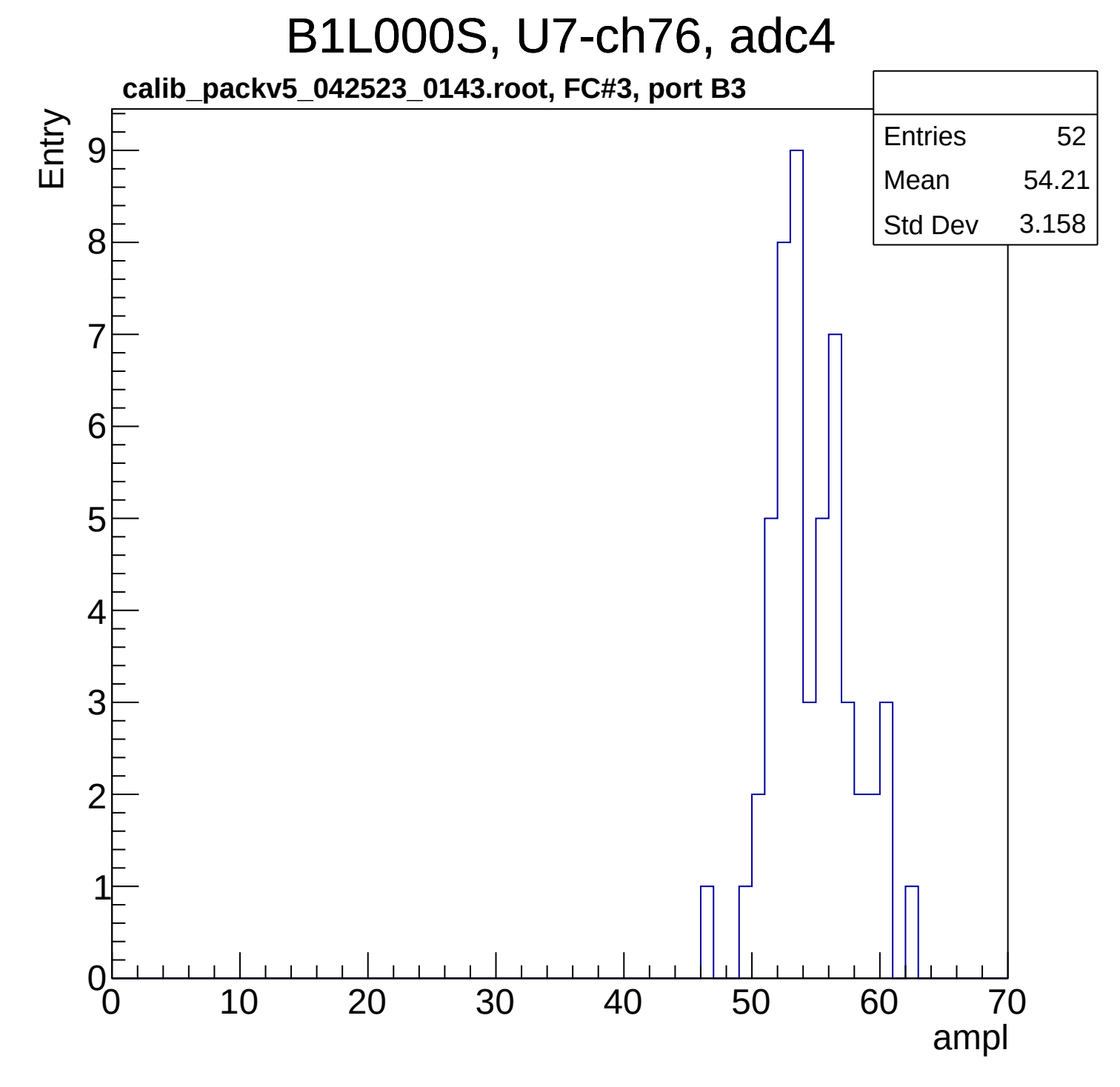
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch76, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 54.21
Std Dev 3.158
Entry
ampl
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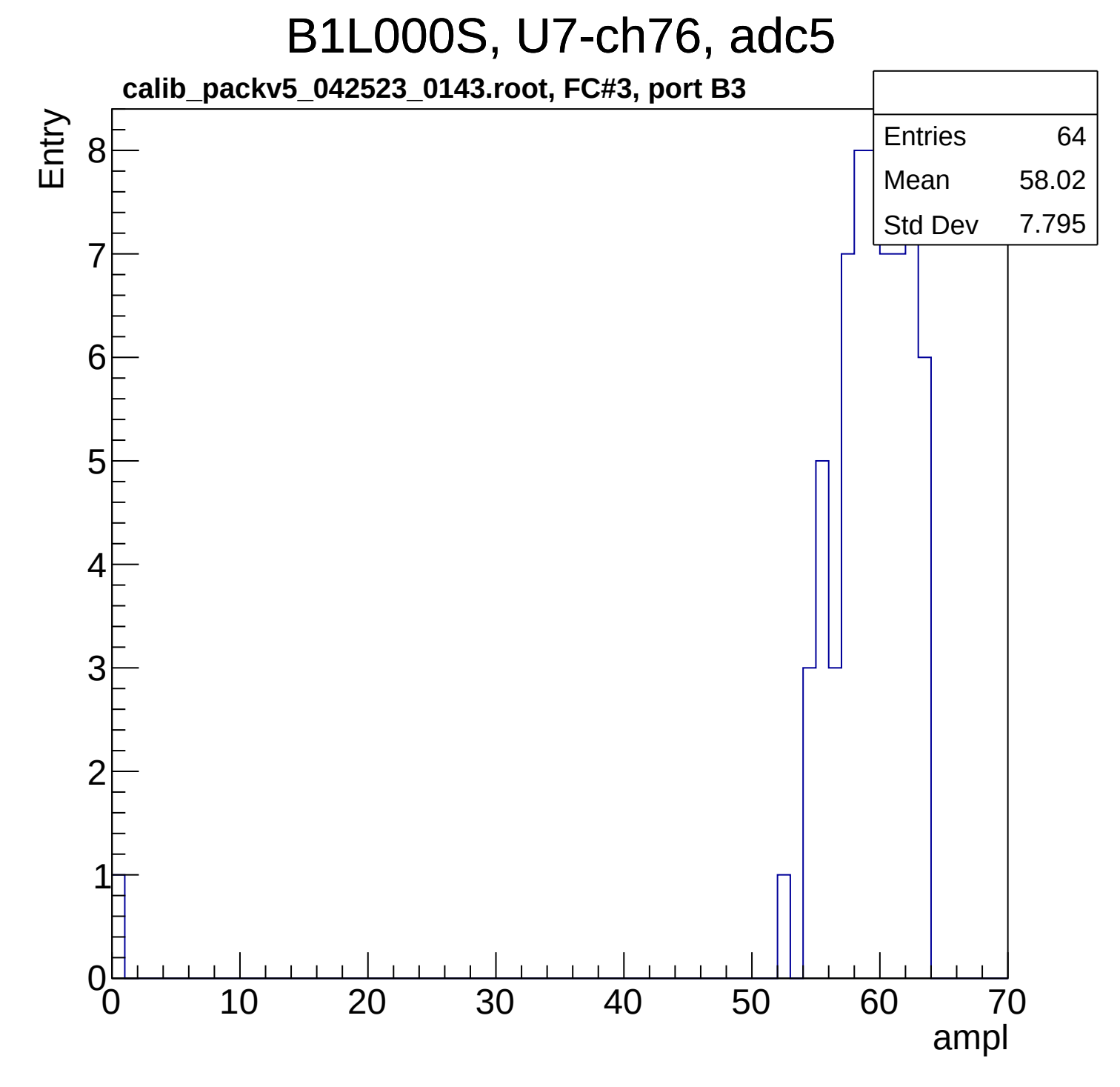
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch76, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 58.02
Std Dev 7.795
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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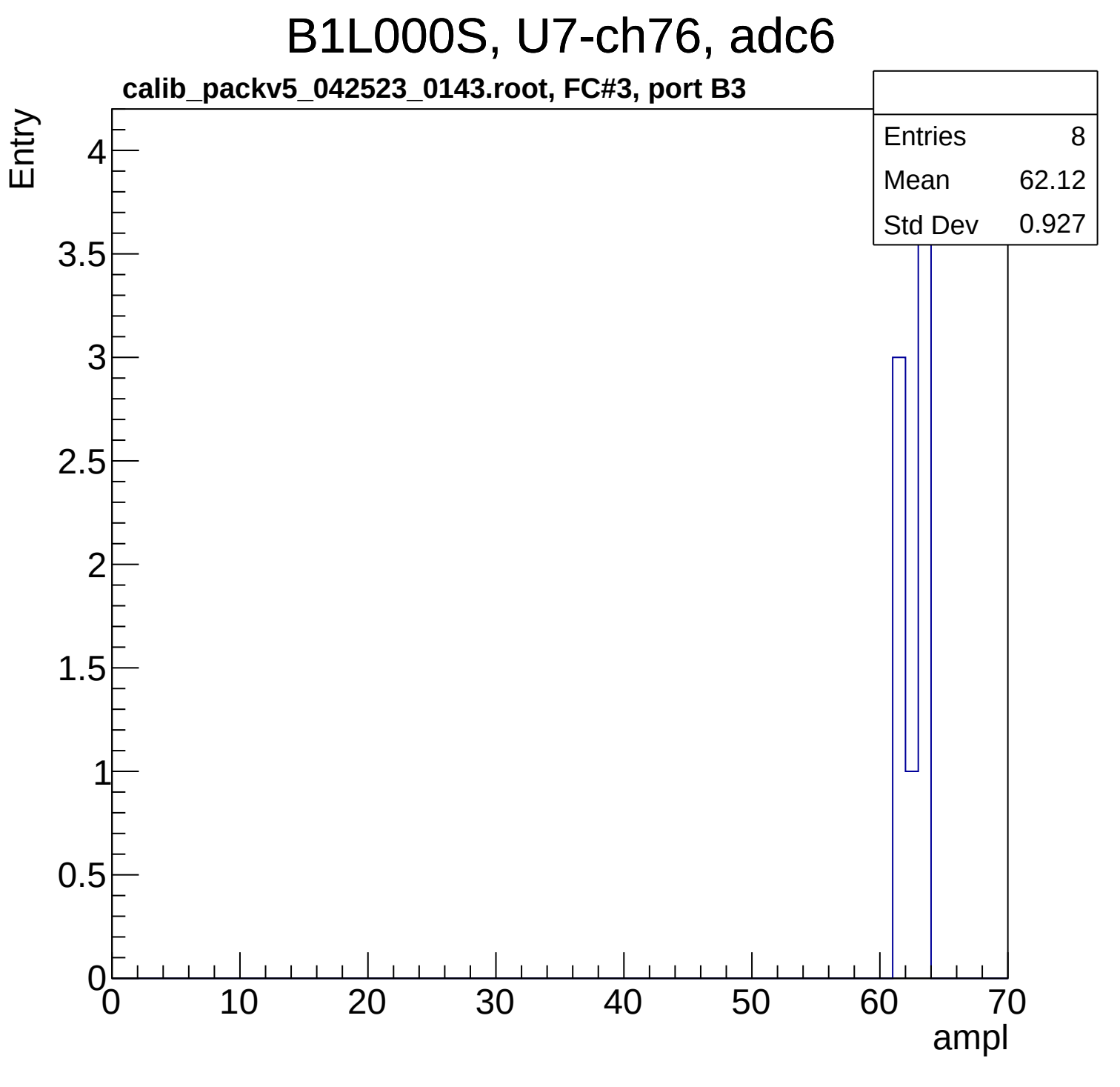
B1L000S, U7-ch76, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 62.12
Std Dev 0.927
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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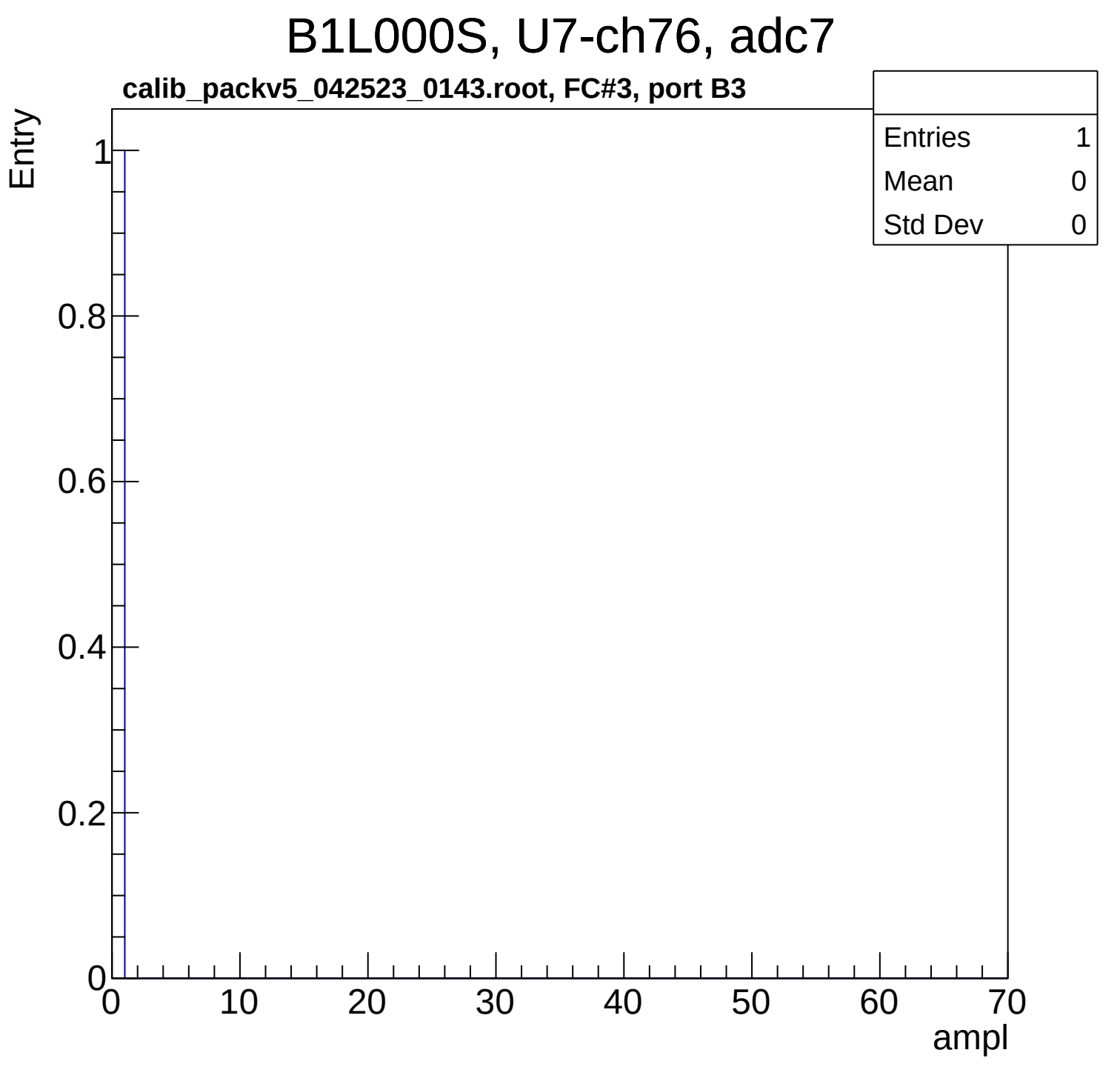
B1L000S, U7-ch76, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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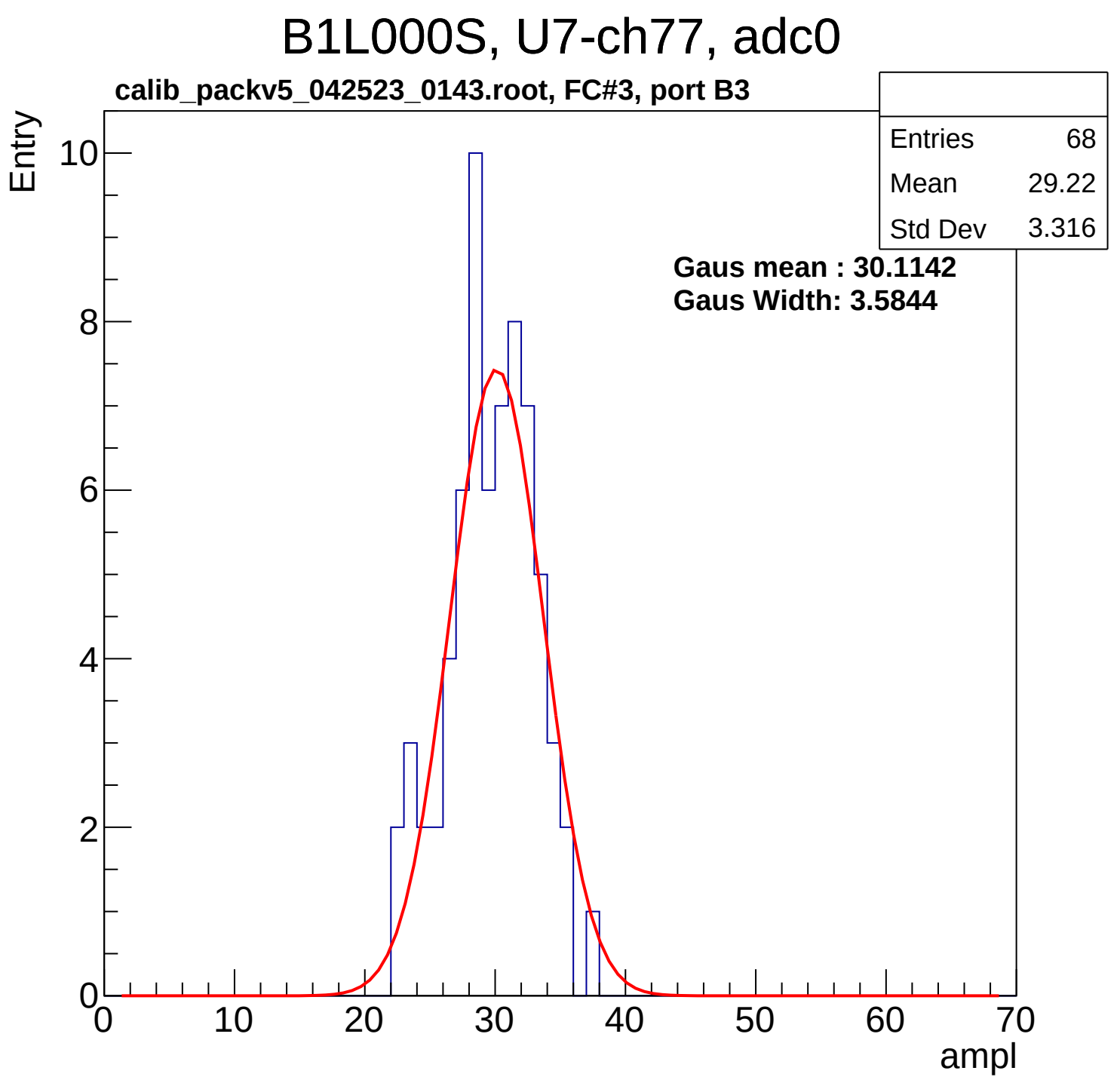
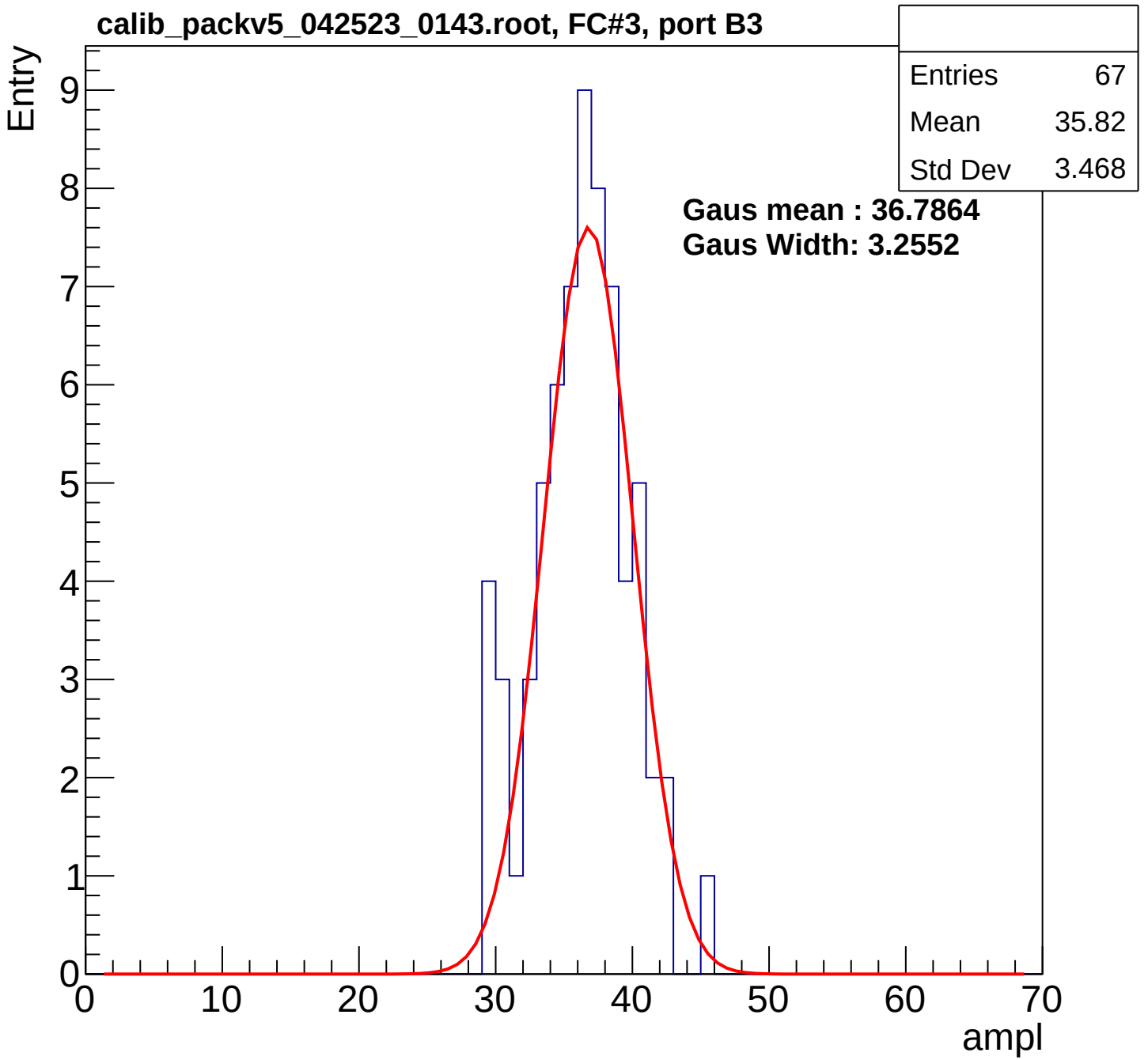
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch77, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 29.22
Std Dev 3.316
Gaus mean : 30.1142
Gaus Width: 3.5844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch77, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.82
Std Dev 3.468
Gaus mean : 36.7864
Gaus Width: 3.2552
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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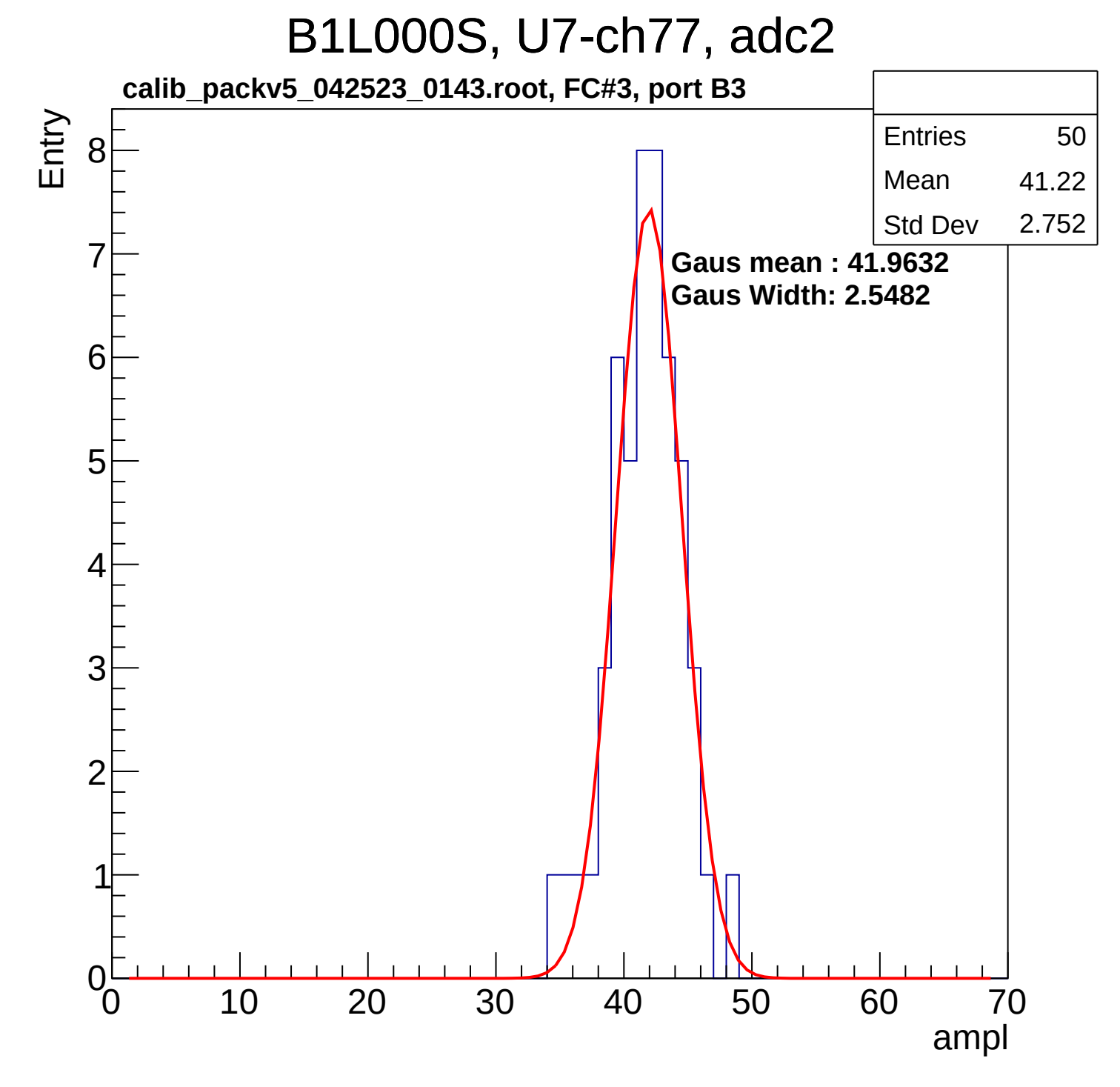
B1L000S, U7-ch77, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 41.22
Std Dev 2.752
Gaus mean : 41.9632
Gaus Width: 2.5482
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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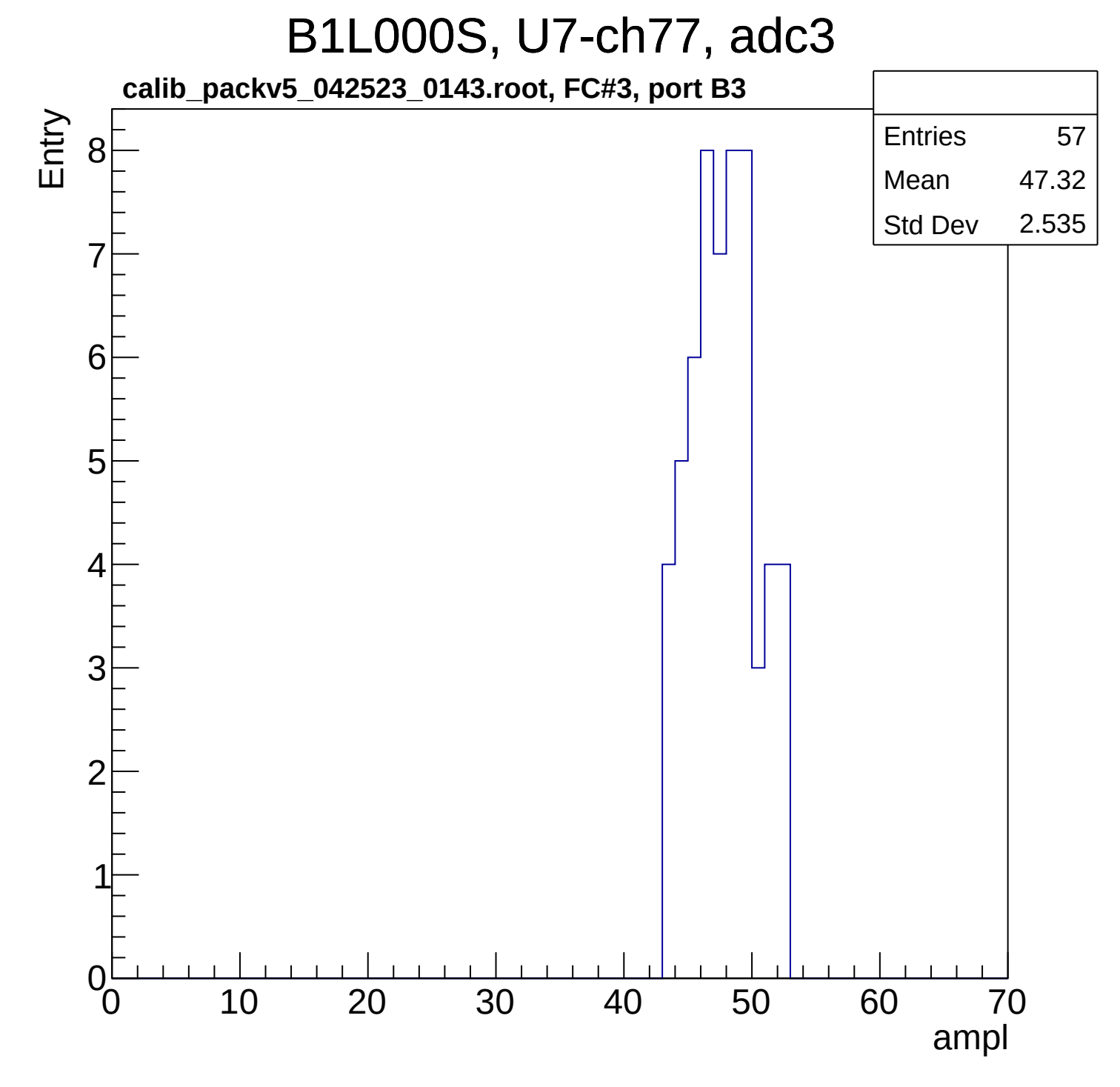
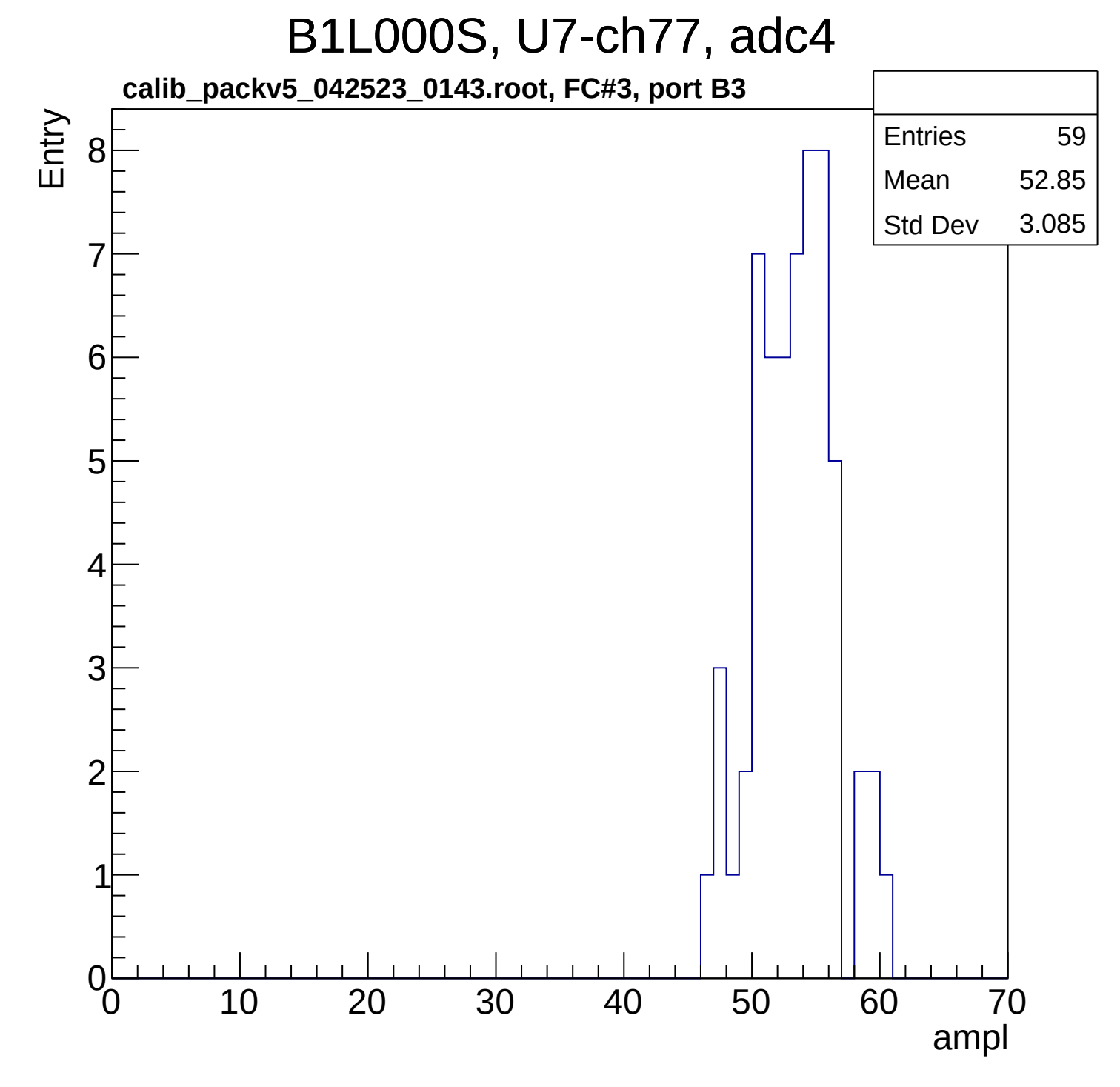
40
50
60
70


B1L000S, U7-ch77, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.32
Std Dev 2.535
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch77, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 52.85
Std Dev 3.085
Entry
ampl
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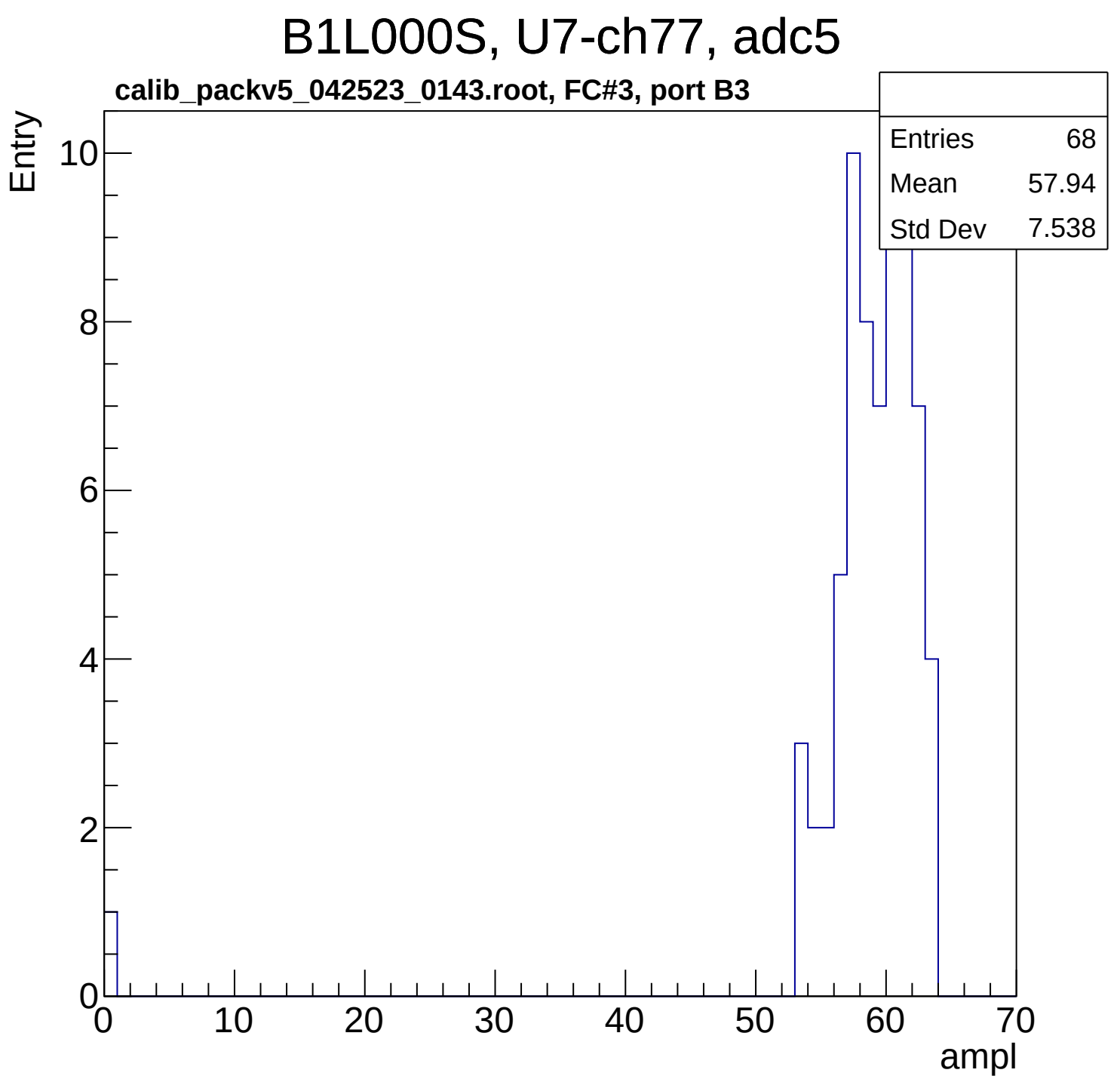
B1L000S, U7-ch77, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 57.94
Std Dev 7.538
Entry
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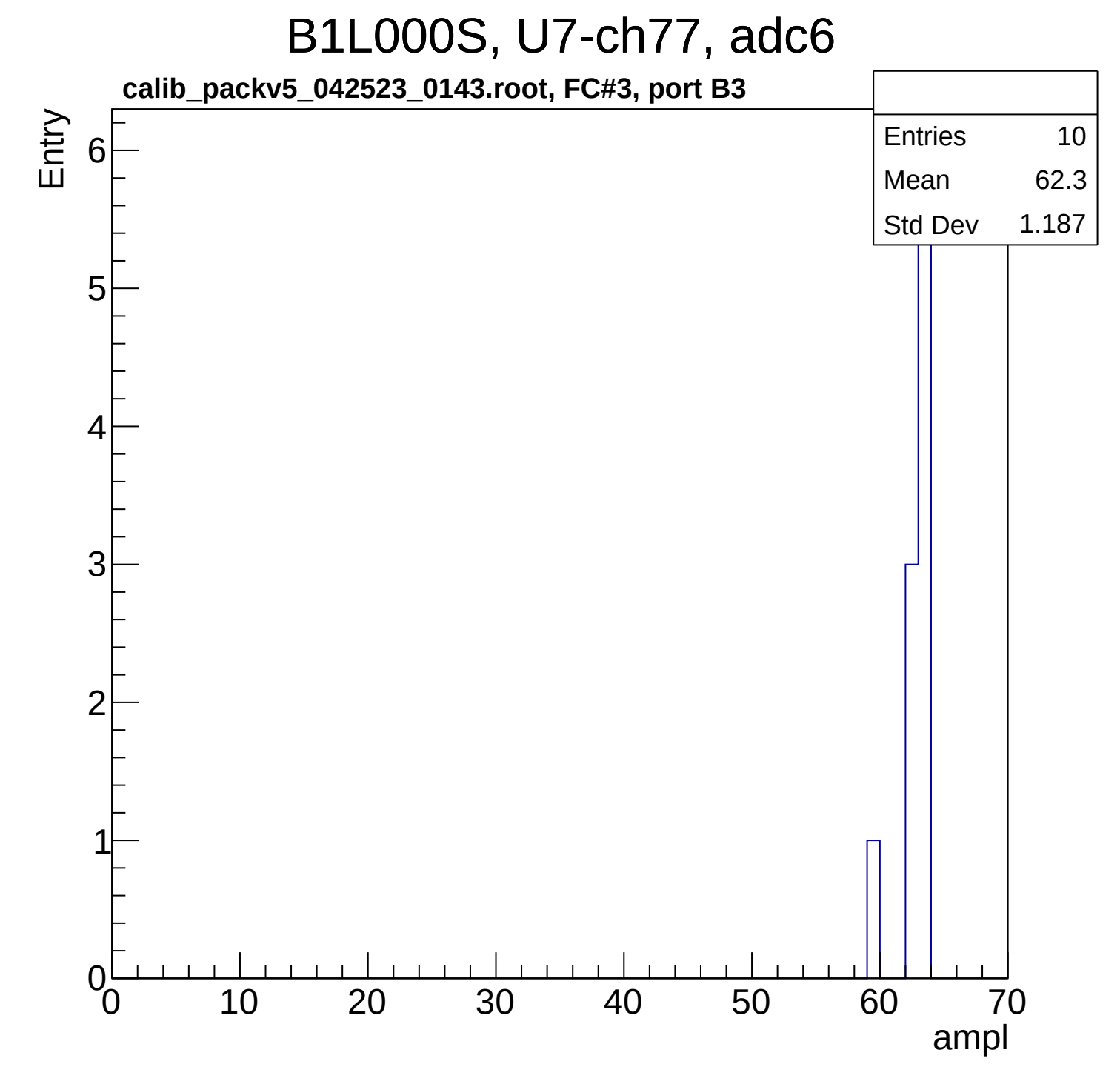
70
ampl


B1L000S, U7-ch77, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.3
Std Dev 1.187
Entry
ampl
0
1
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3
4
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40
50
60
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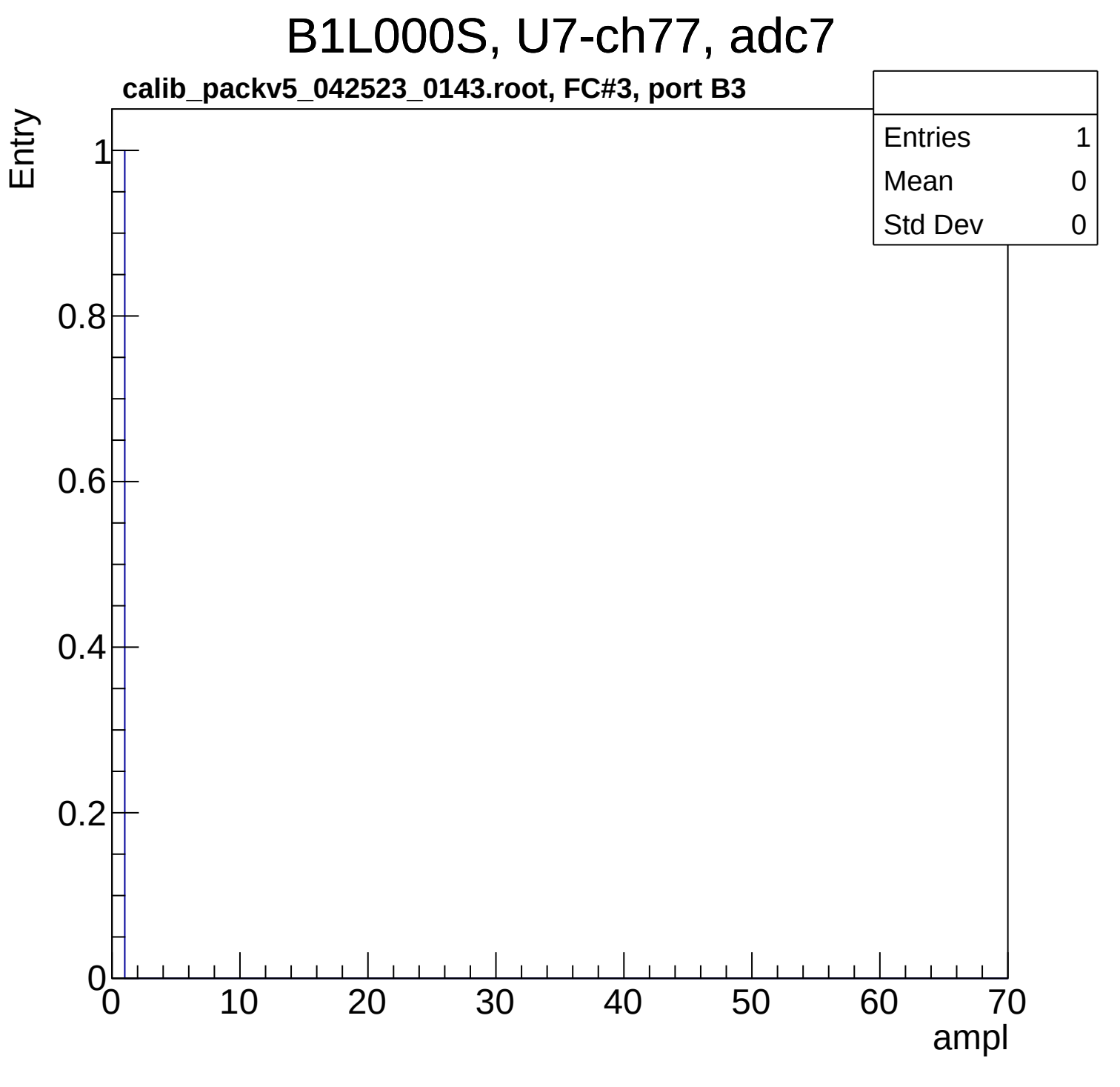
B1L000S, U7-ch77, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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10
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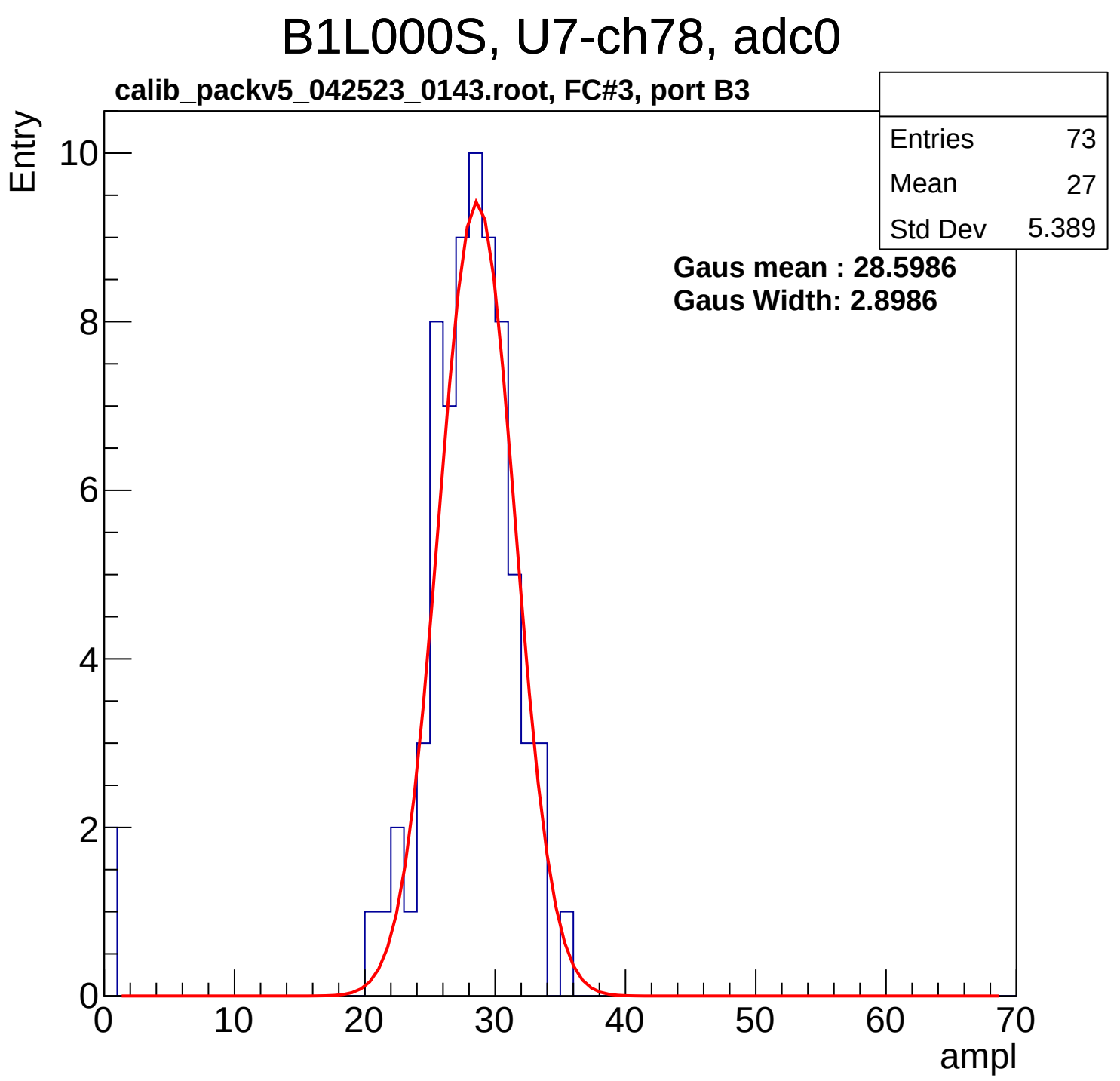
50
60
70
ampl


B1L000S, U7-ch78, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 27
Std Dev 5.389
Gaus mean : 28.5986
Gaus Width: 2.8986
Entry
ampl
0
2
4
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8
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0
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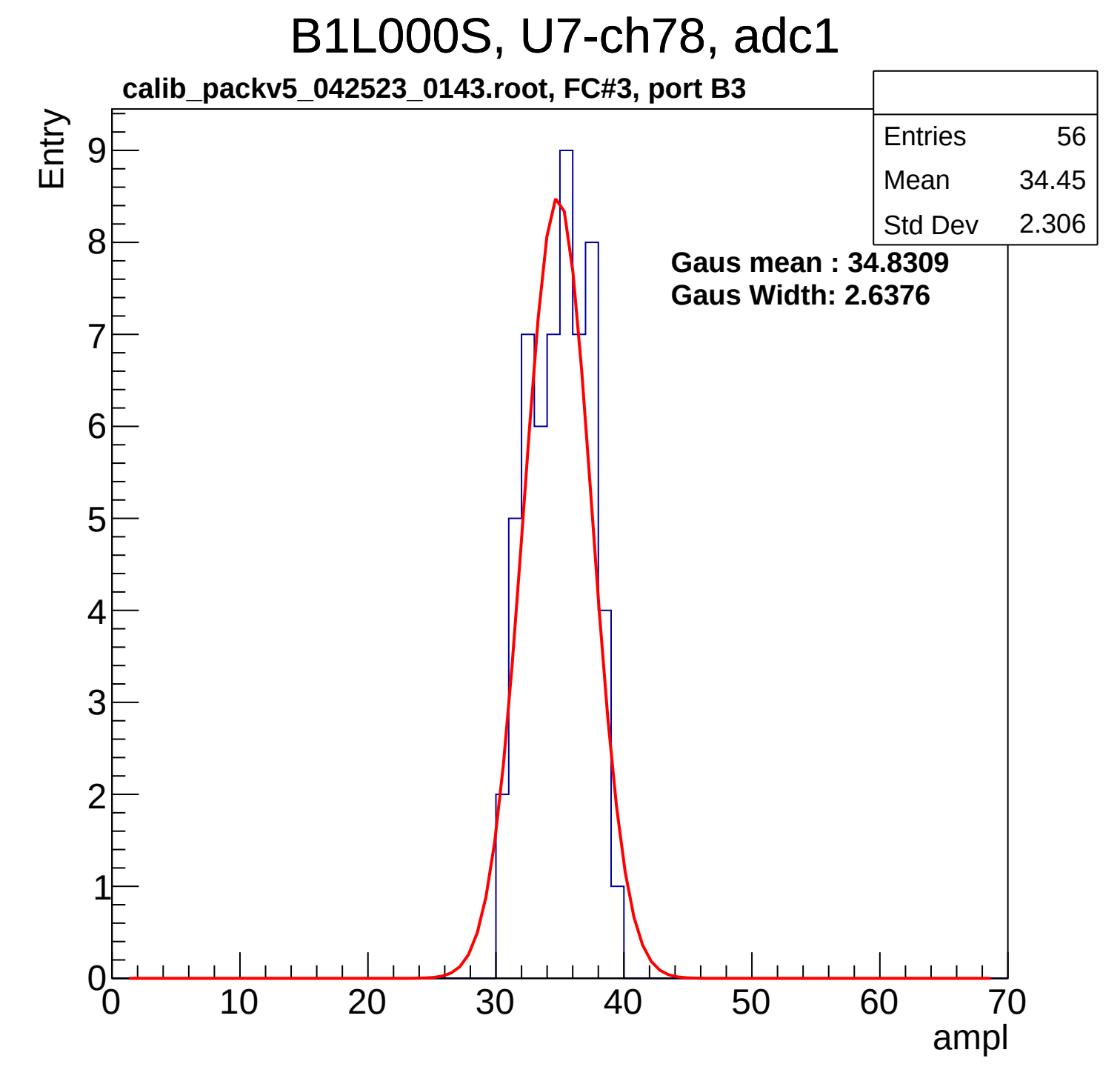
B1L000S, U7-ch78, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 34.45
Std Dev 2.306
Gaus mean : 34.8309
Gaus Width: 2.6376
Entry
ampl
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2
3
4
5
6
7
8
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0
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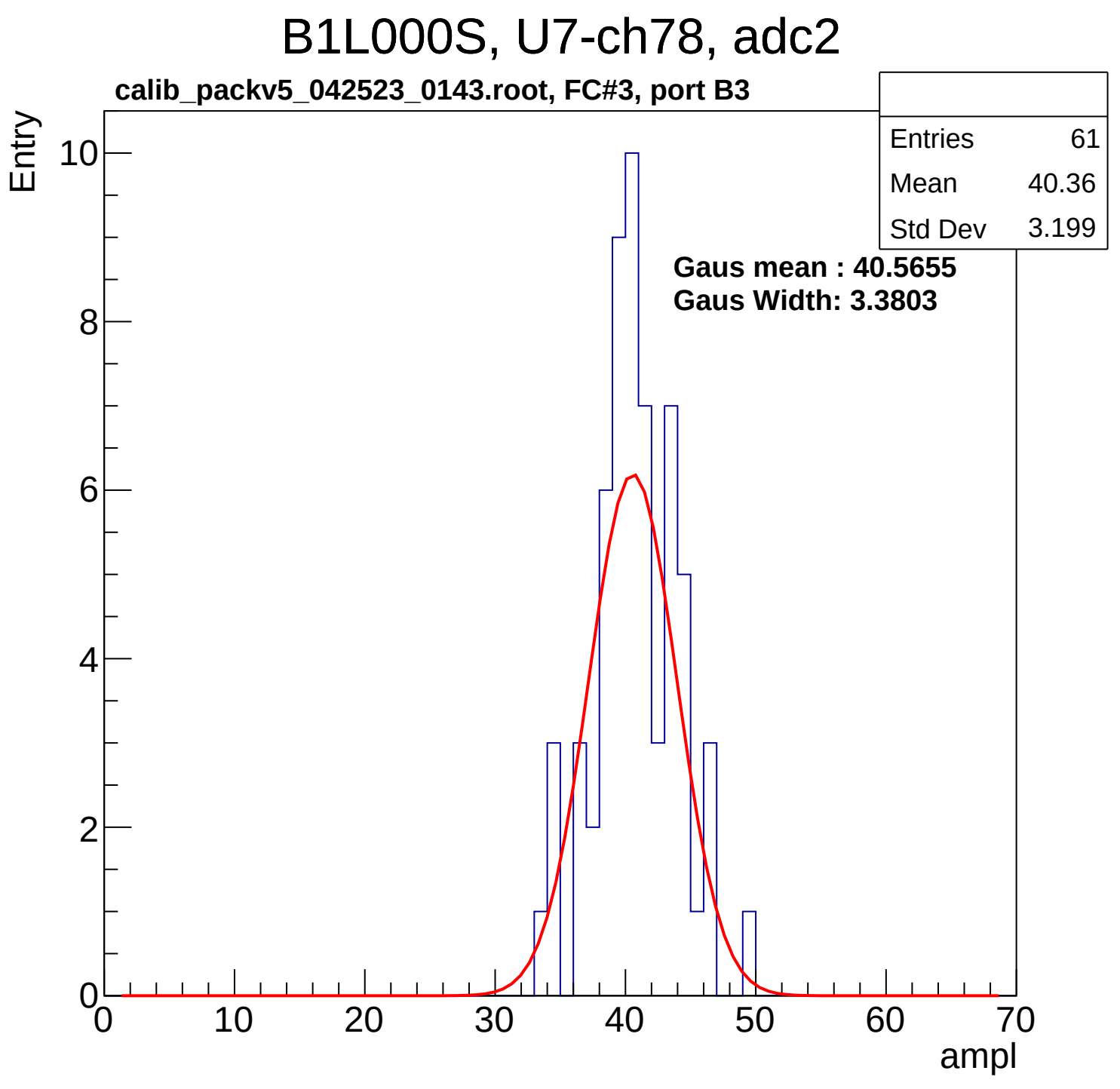
B1L000S, U7-ch78, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 40.36
Std Dev 3.199
Gaus mean : 40.5655
Gaus Width: 3.3803
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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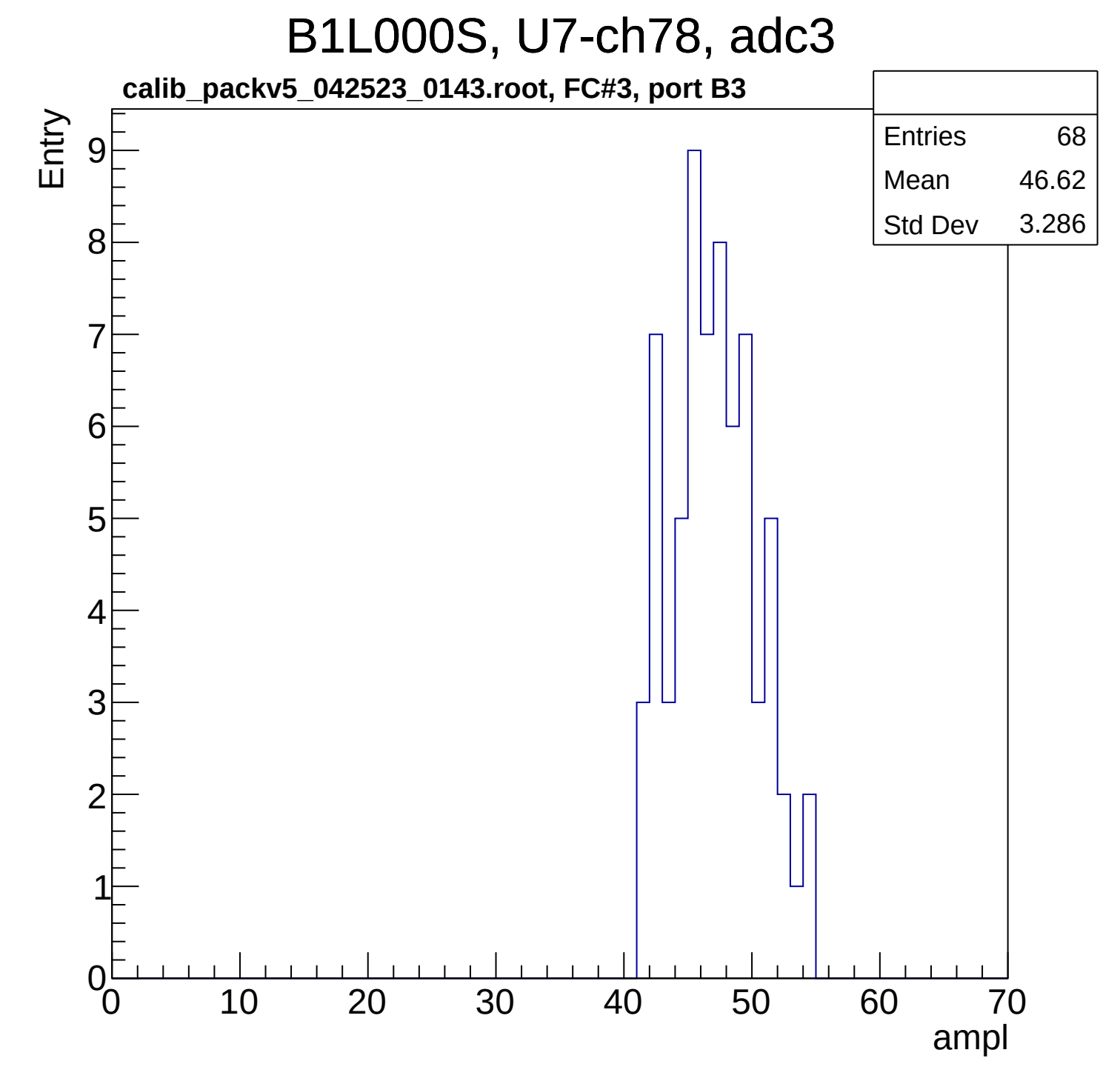
B1L000S, U7-ch78, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 46.62
Std Dev 3.286
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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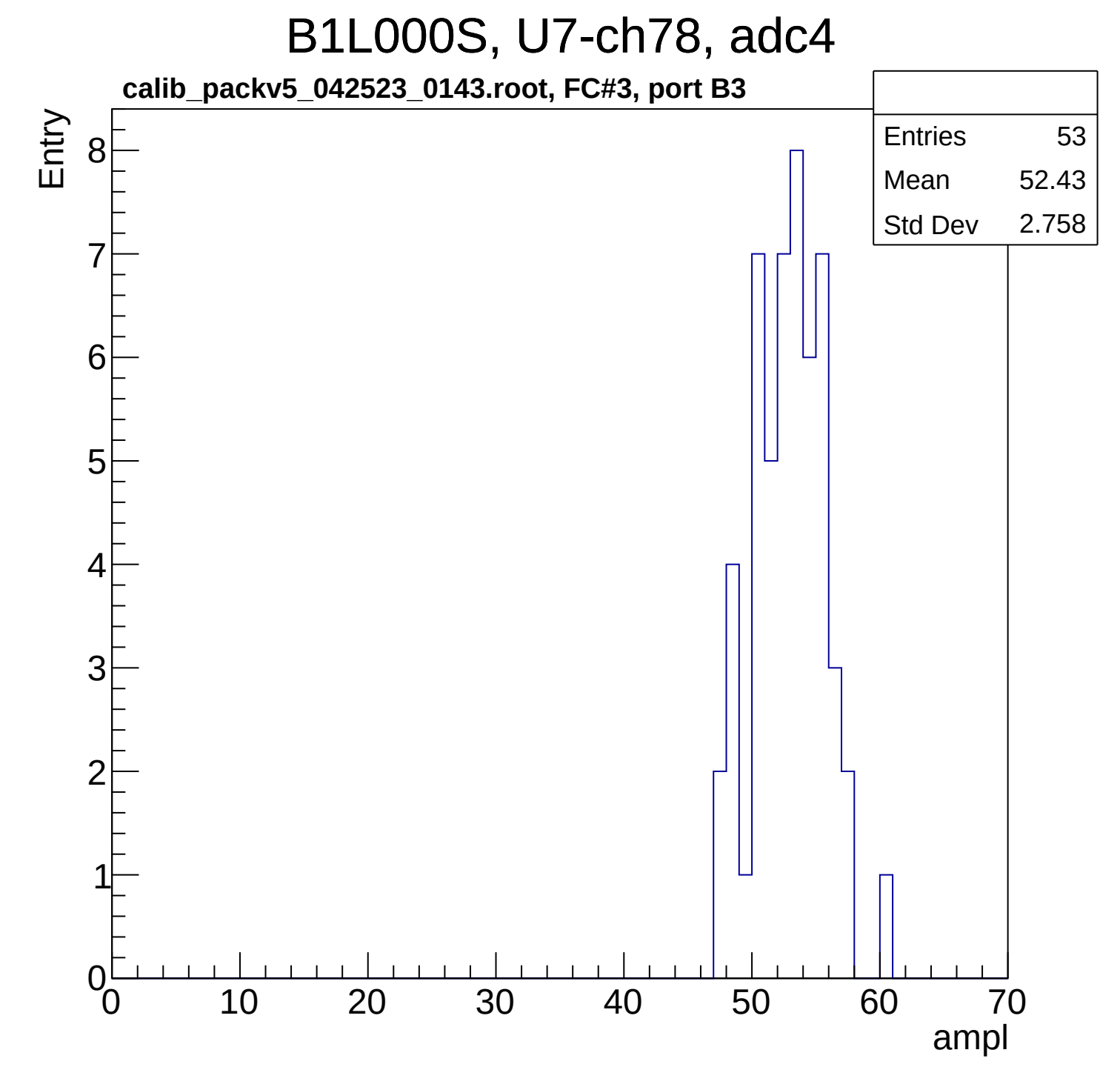
B1L000S, U7-ch78, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 52.43
Std Dev 2.758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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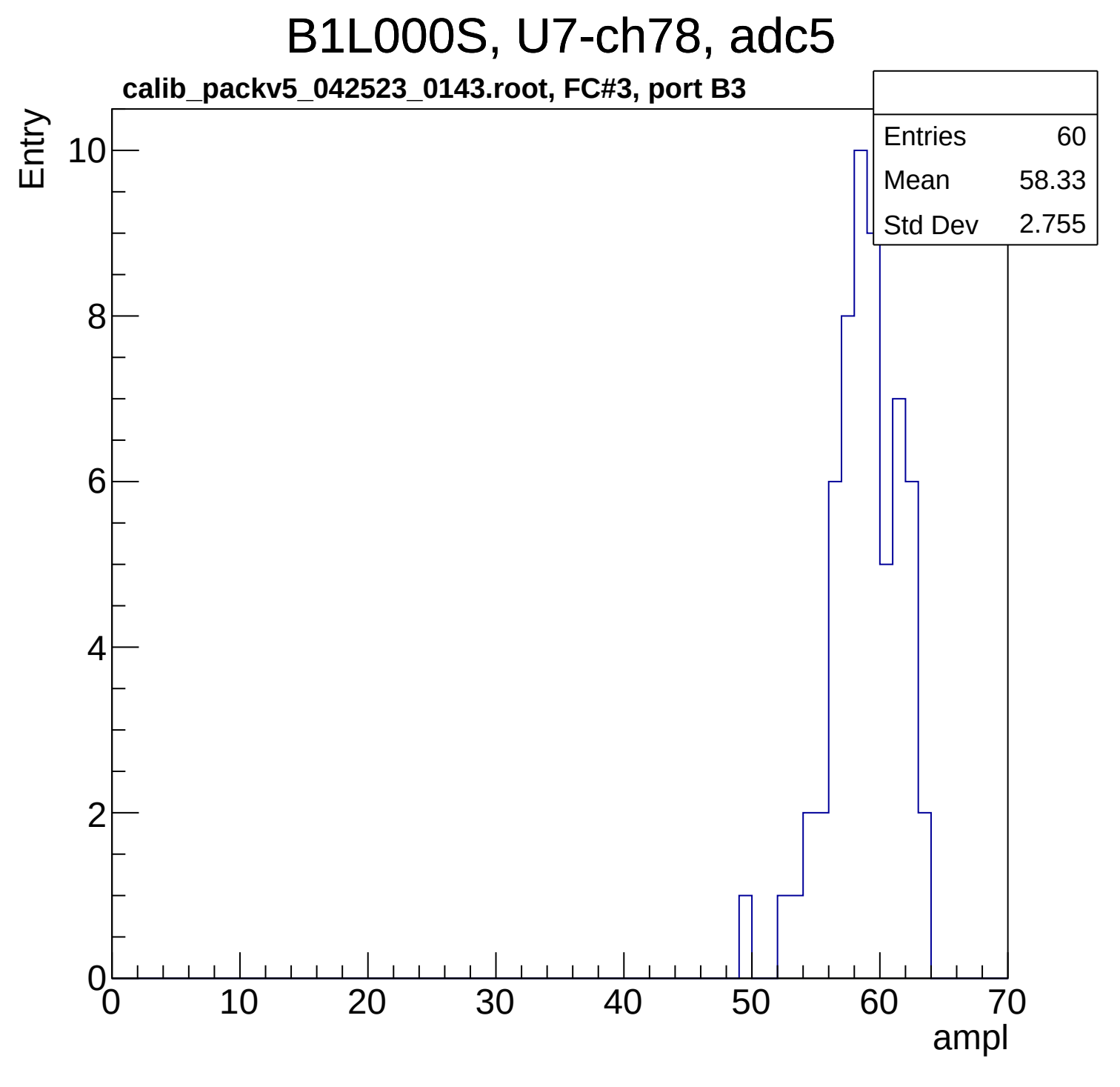
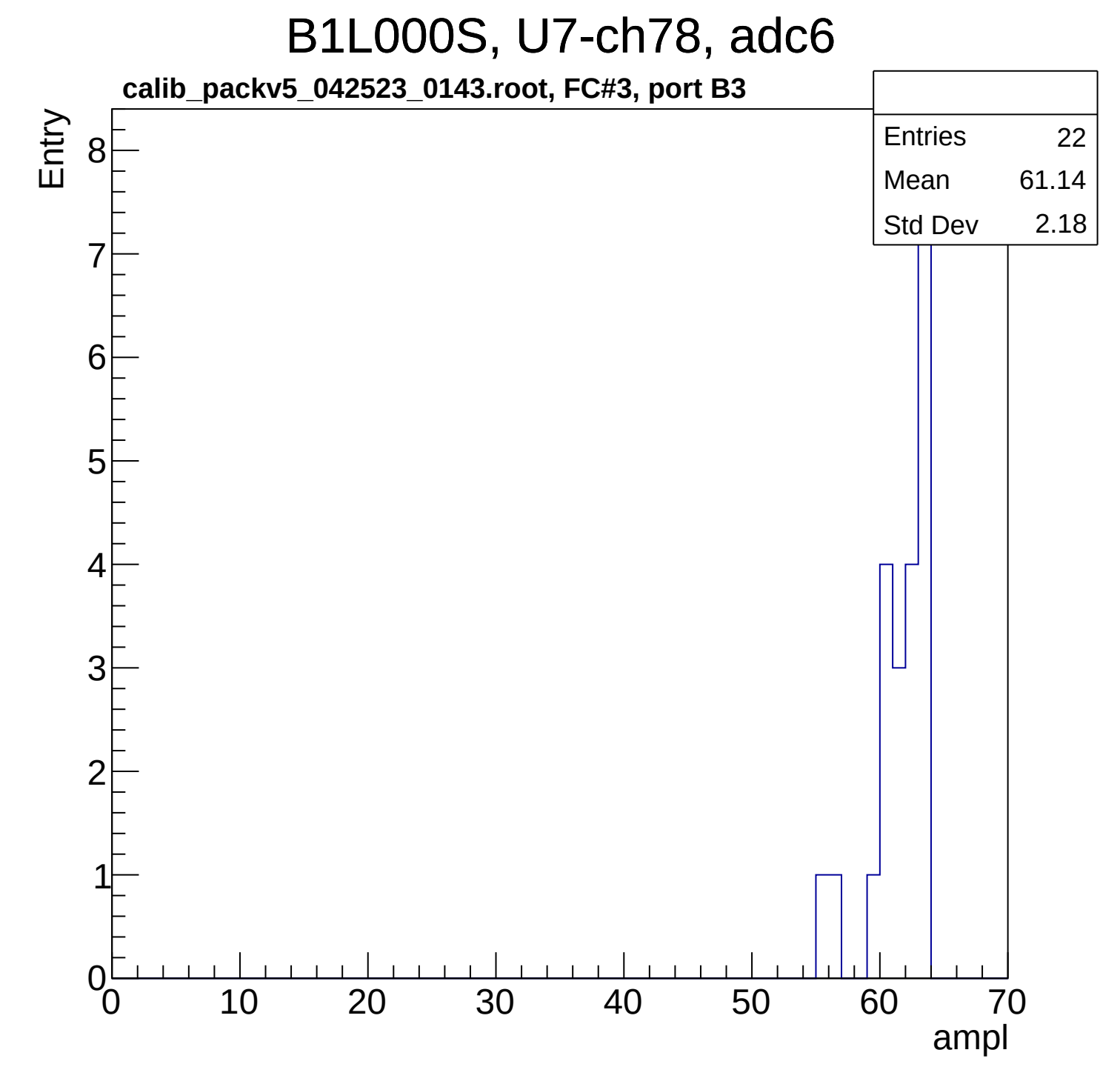

B1L000S, U7-ch78, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.33
Std Dev 2.755
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch78, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 61.14
Std Dev 2.18
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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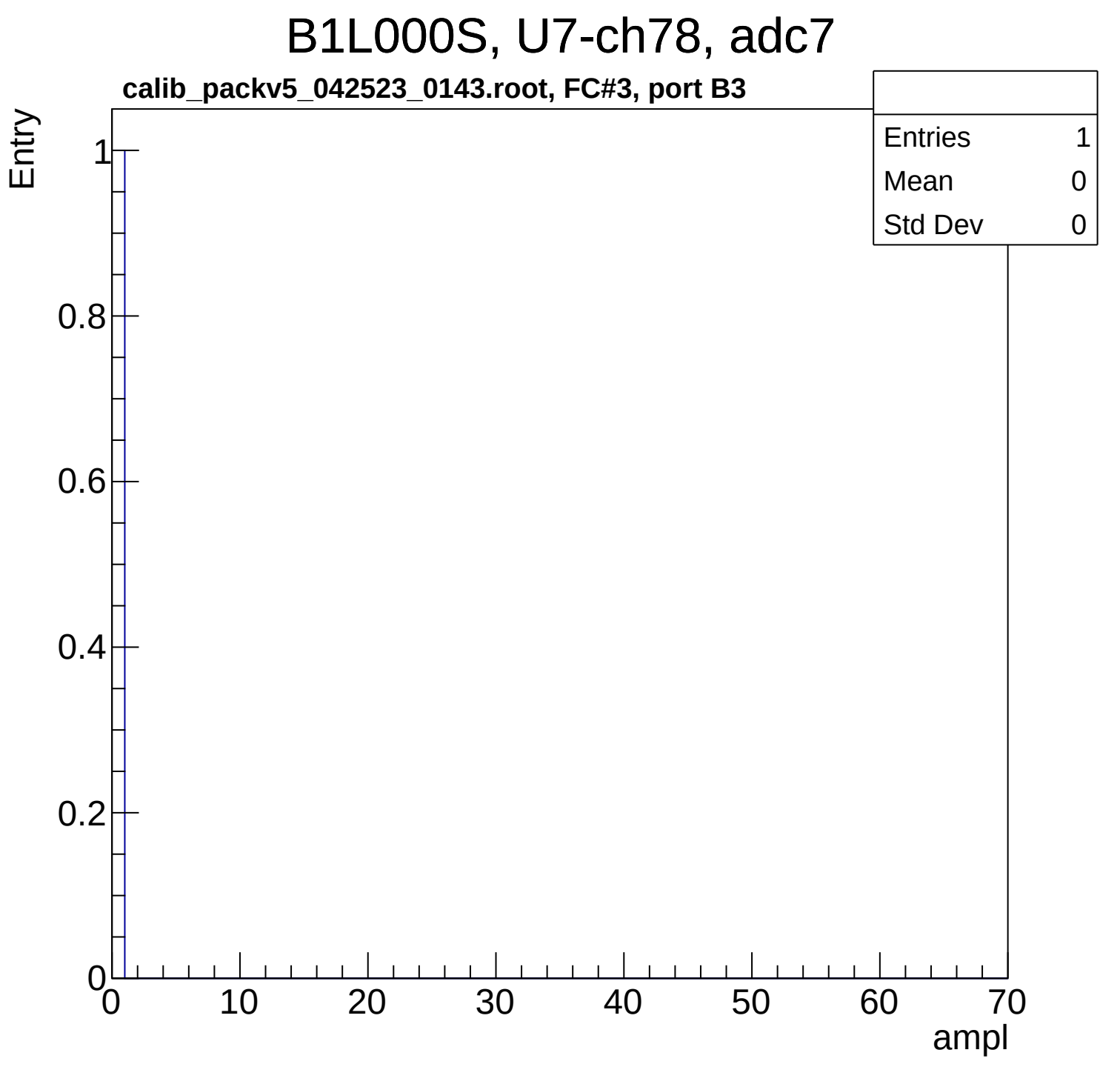
B1L000S, U7-ch78, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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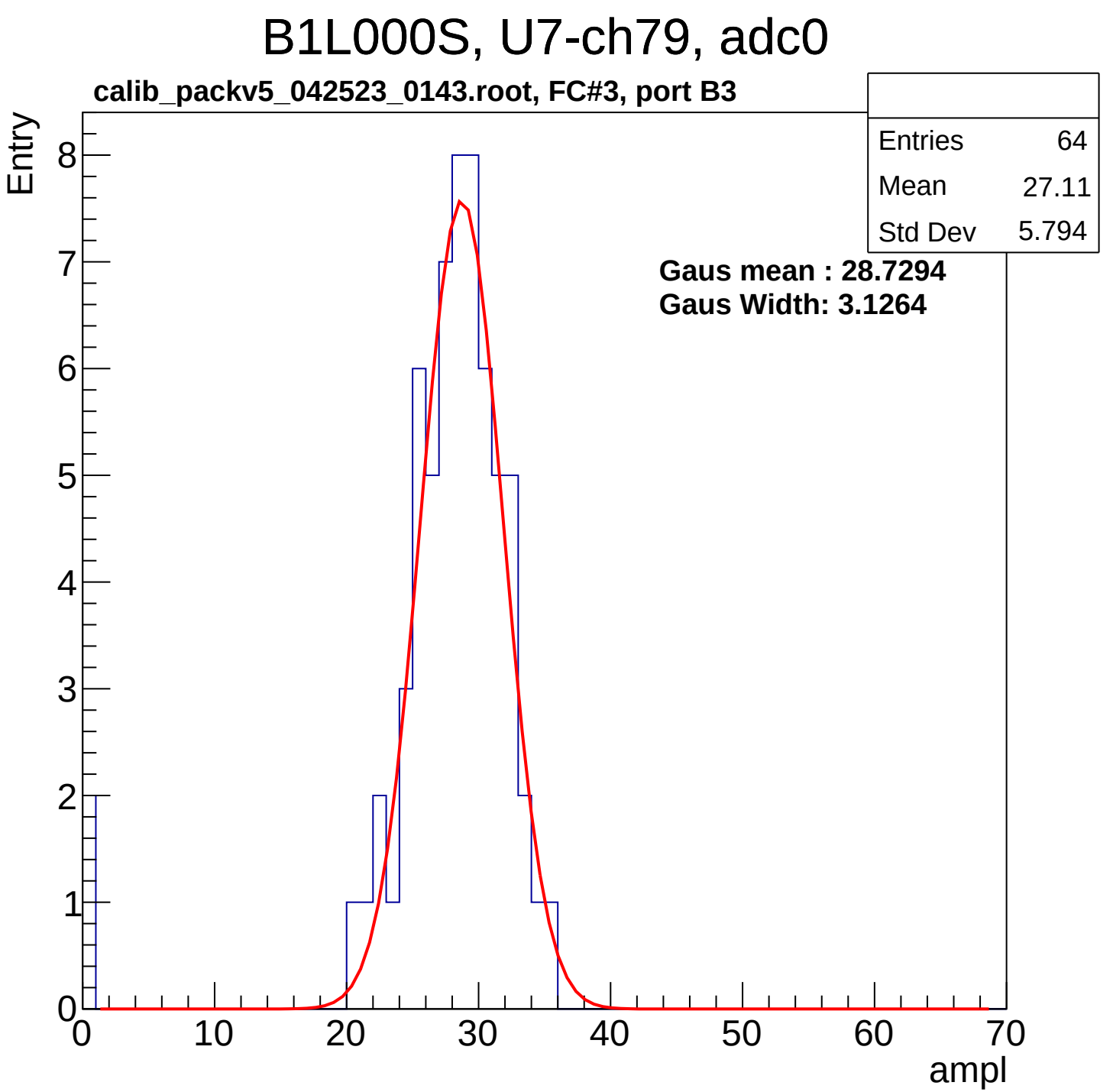
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch79, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 27.11
Std Dev 5.794
Gaus mean : 28.7294
Gaus Width: 3.1264
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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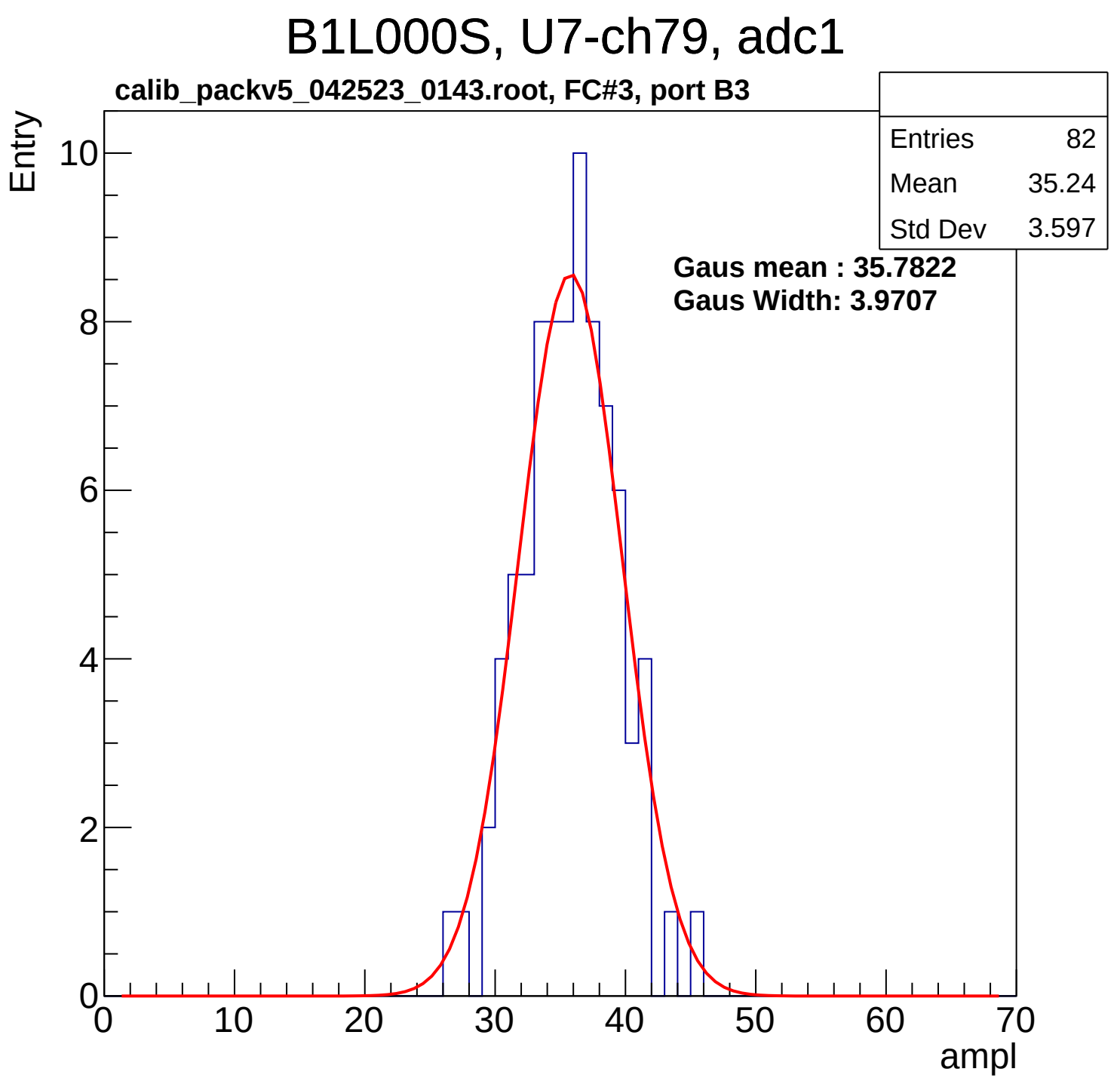
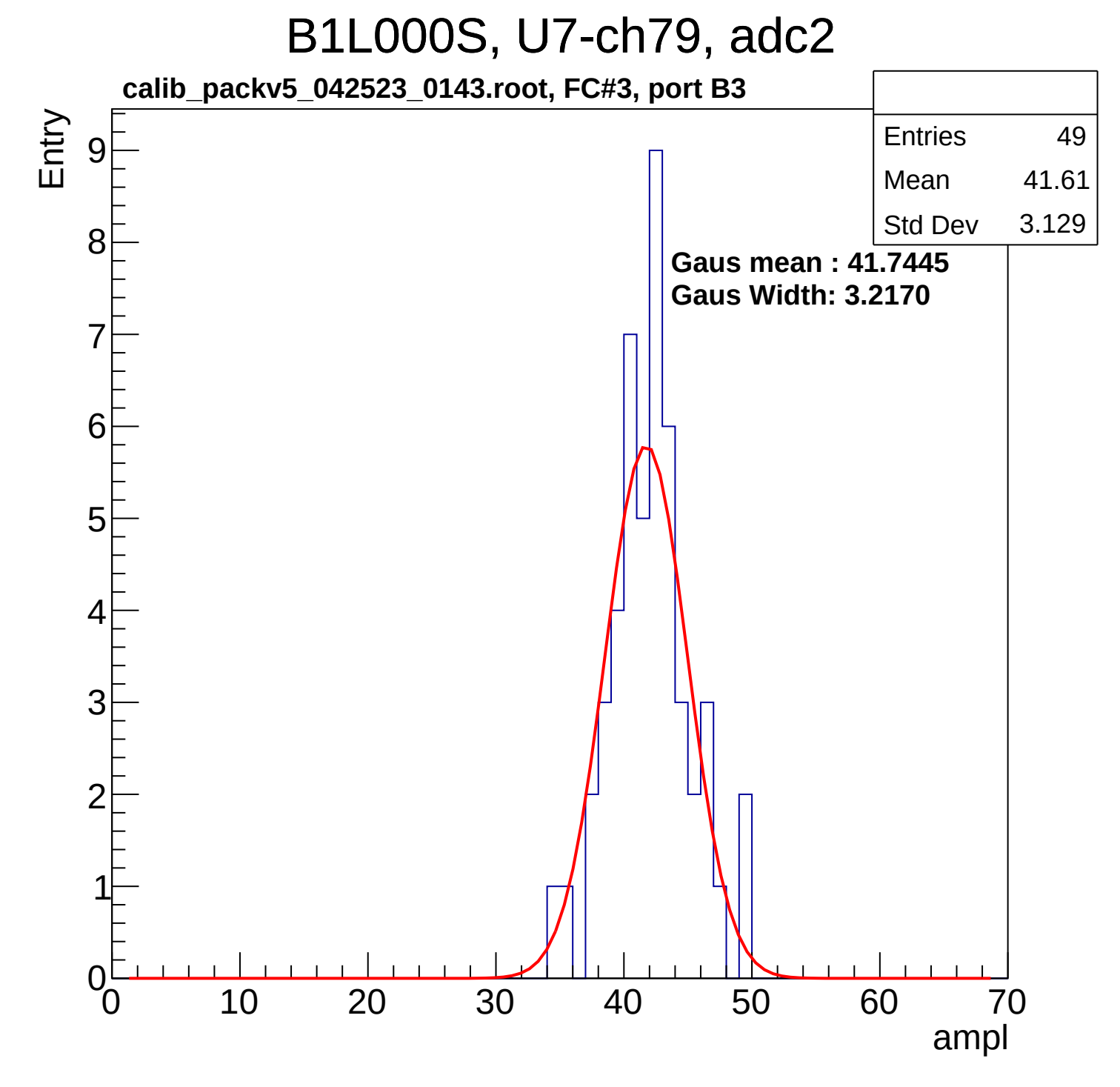

B1L000S, U7-ch79, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 82
Mean 35.24
Std Dev 3.597
Gaus mean : 35.7822
Gaus Width: 3.9707
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch79, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 41.61
Std Dev 3.129
Gaus mean : 41.7445
Gaus Width: 3.2170
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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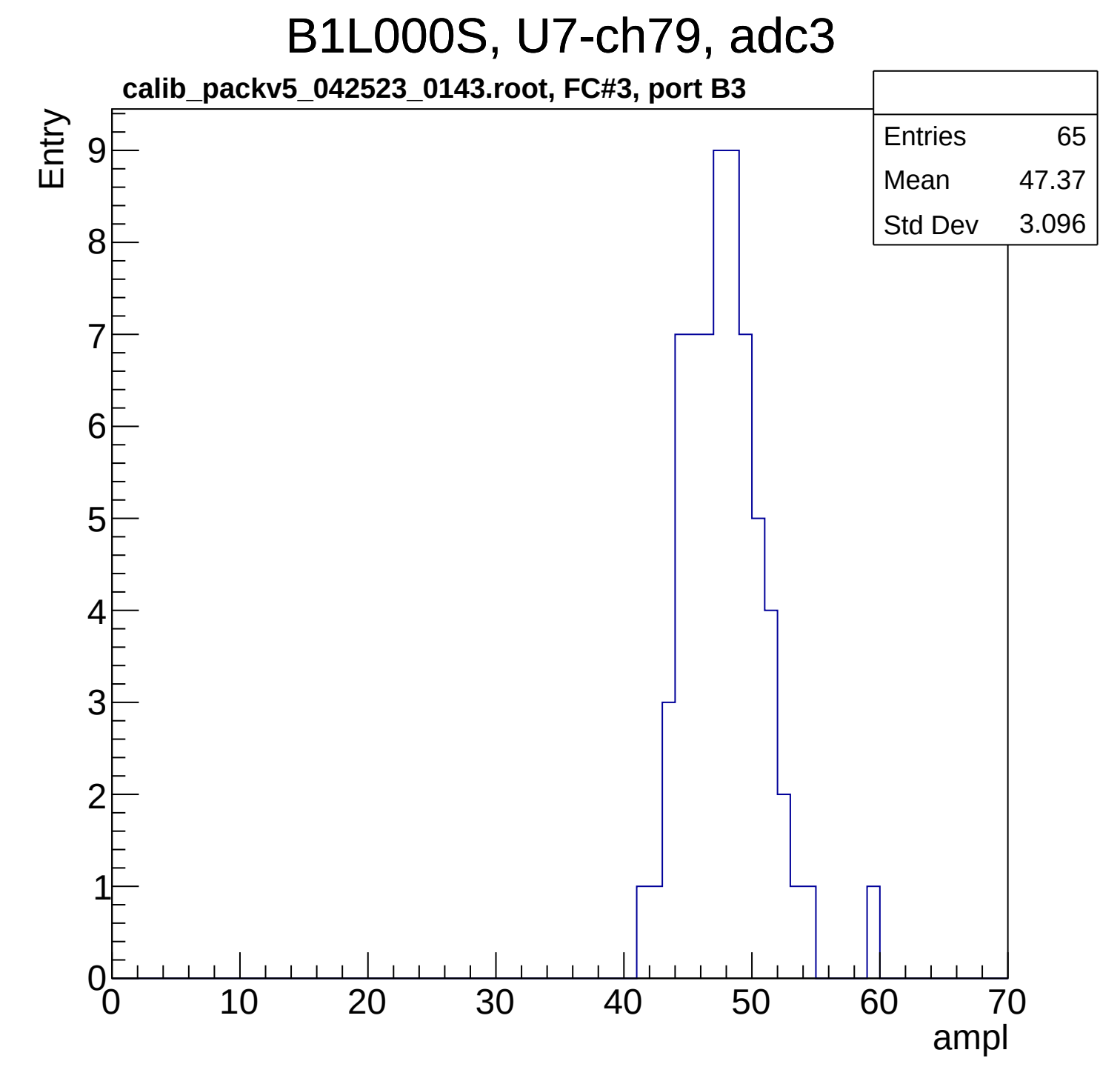
B1L000S, U7-ch79, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 47.37
Std Dev 3.096
Entry
ampl
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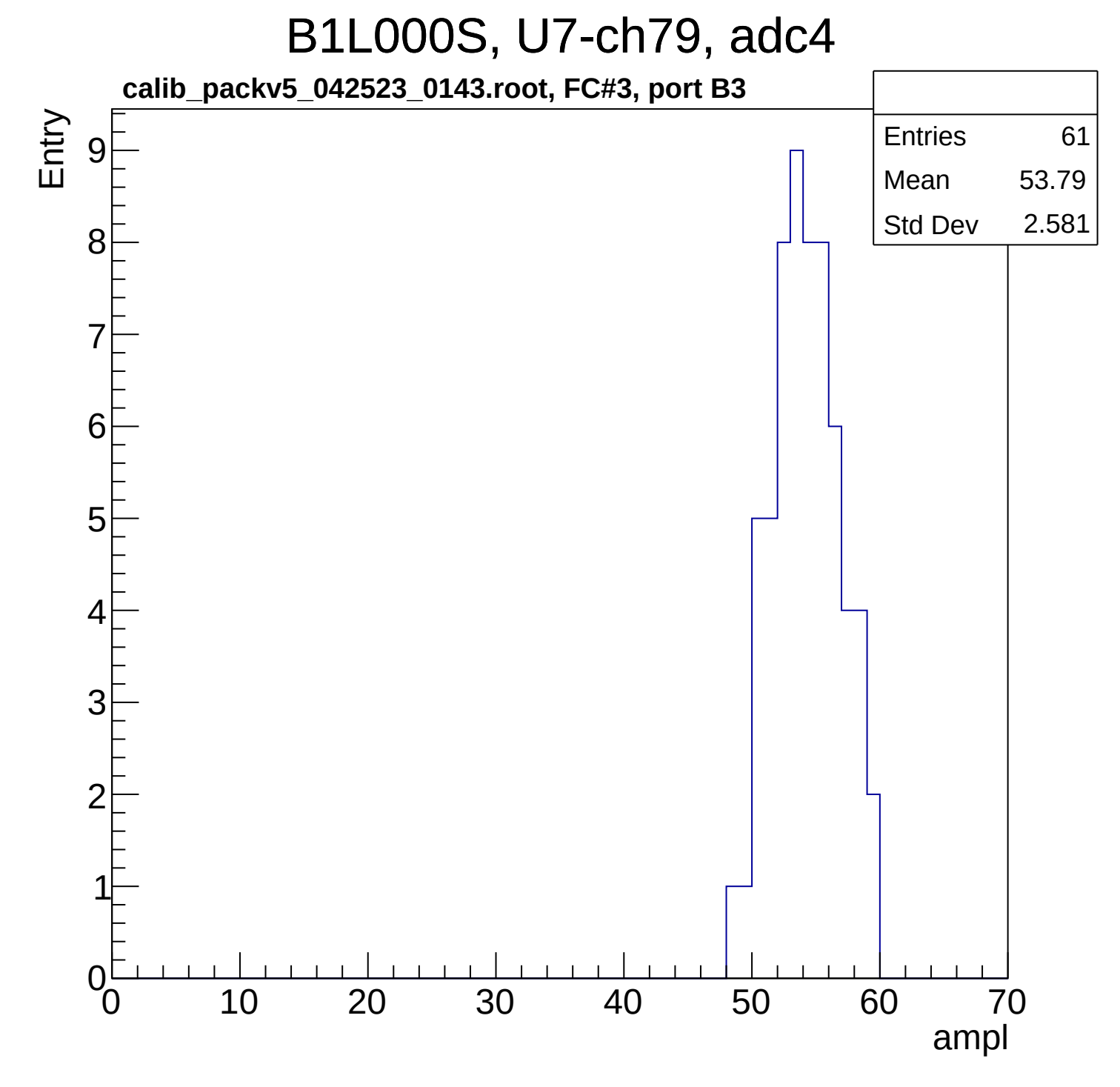
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch79, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 53.79
Std Dev 2.581
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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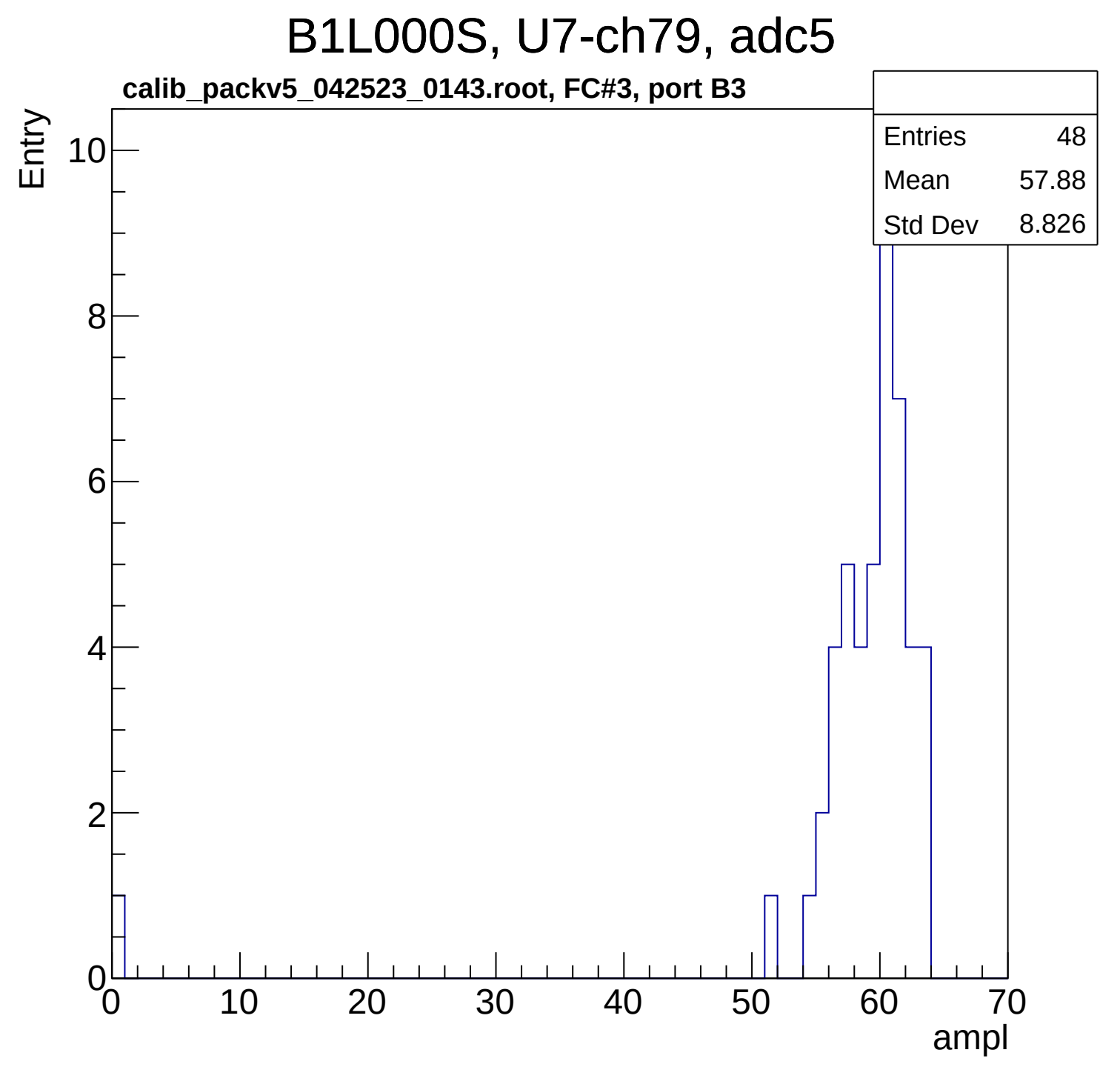
B1L000S, U7-ch79, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 57.88
Std Dev 8.826
Entry
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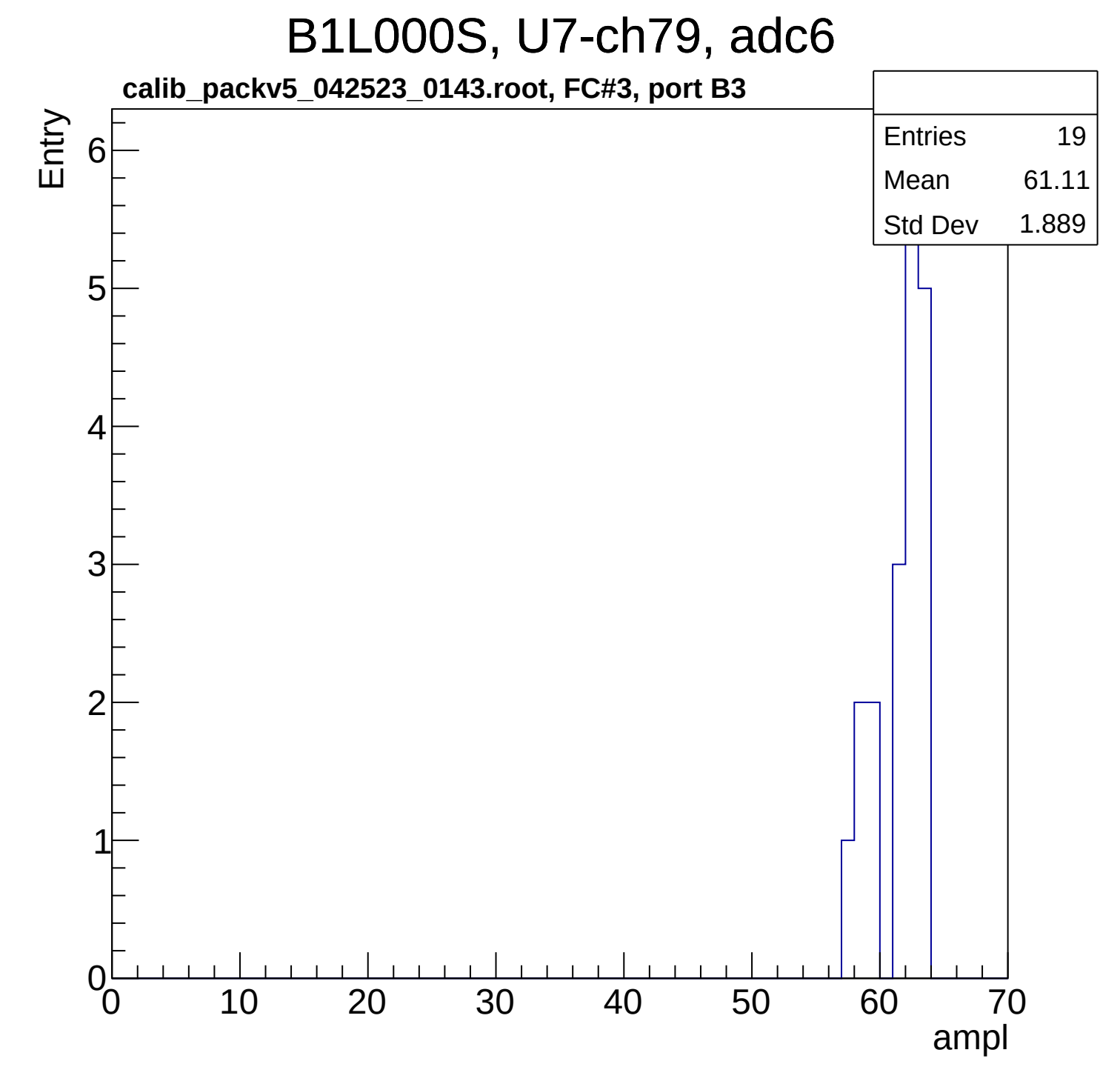
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch79, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 61.11
Std Dev 1.889
Entry
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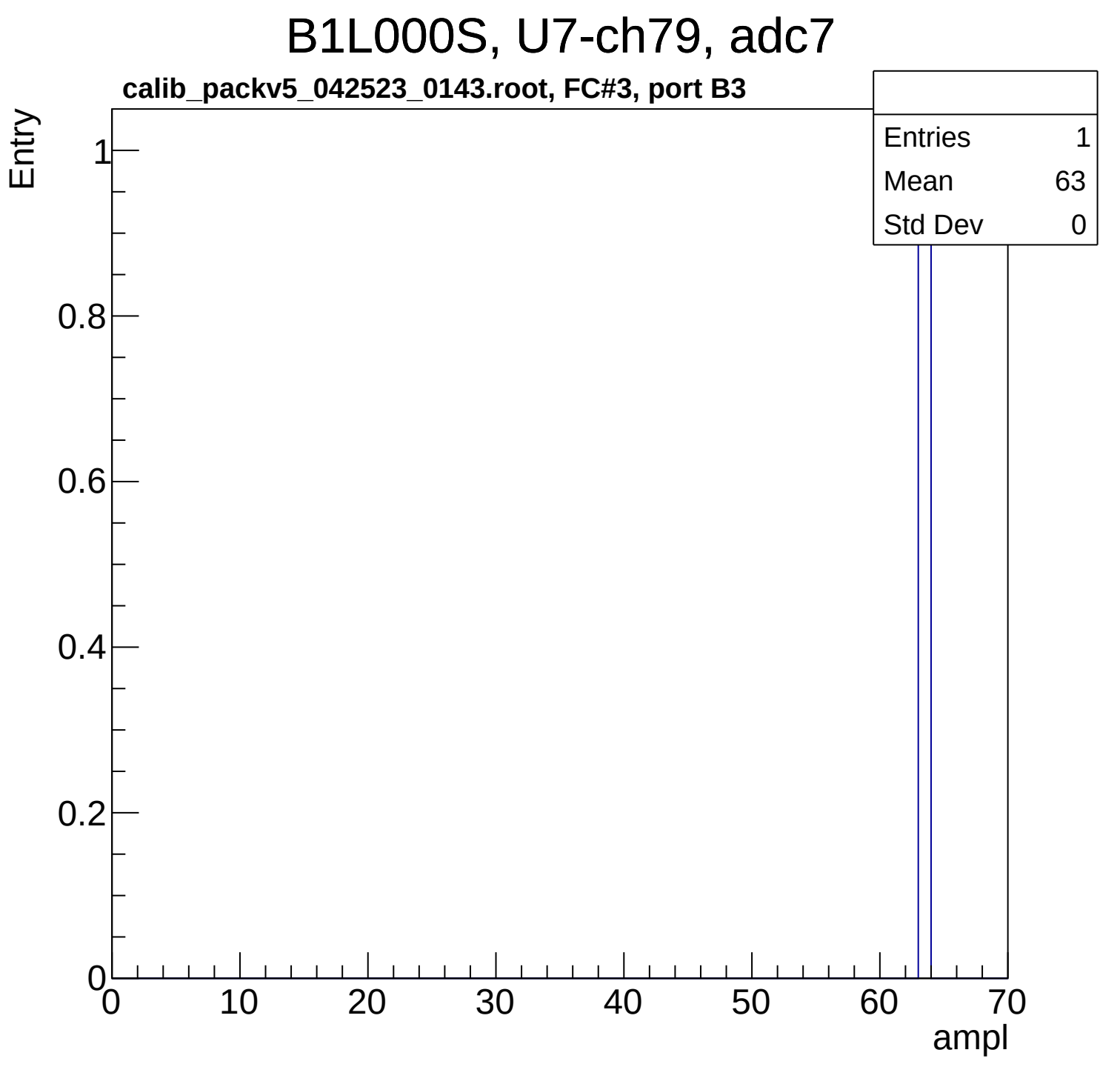
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch79, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
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10
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50
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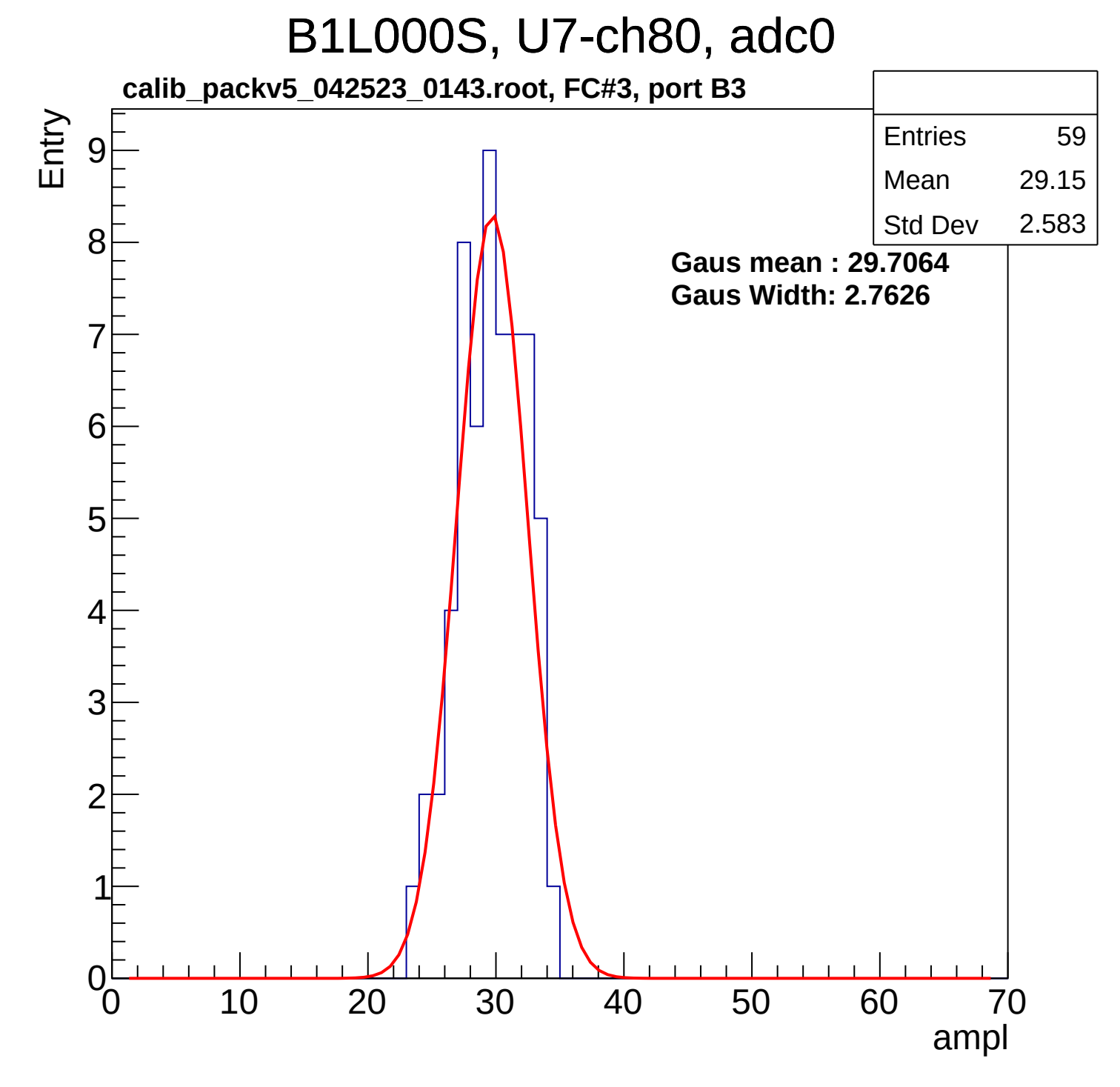
70
ampl


B1L000S, U7-ch80, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 29.15
Std Dev 2.583
Gaus mean : 29.7064
Gaus Width: 2.7626
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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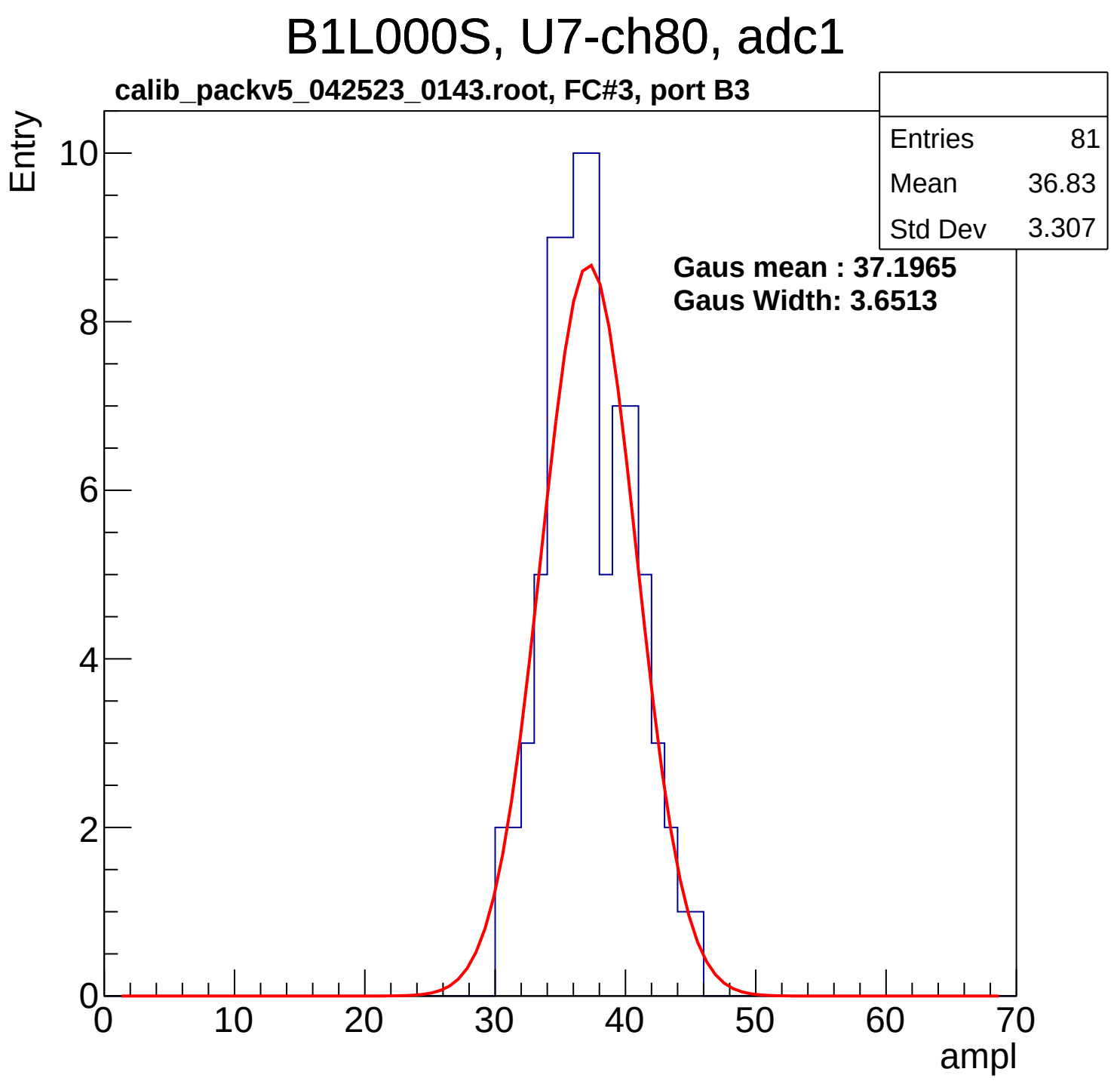
B1L000S, U7-ch80, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 36.83
Std Dev 3.307
Gaus mean : 37.1965
Gaus Width: 3.6513
Entry
ampl
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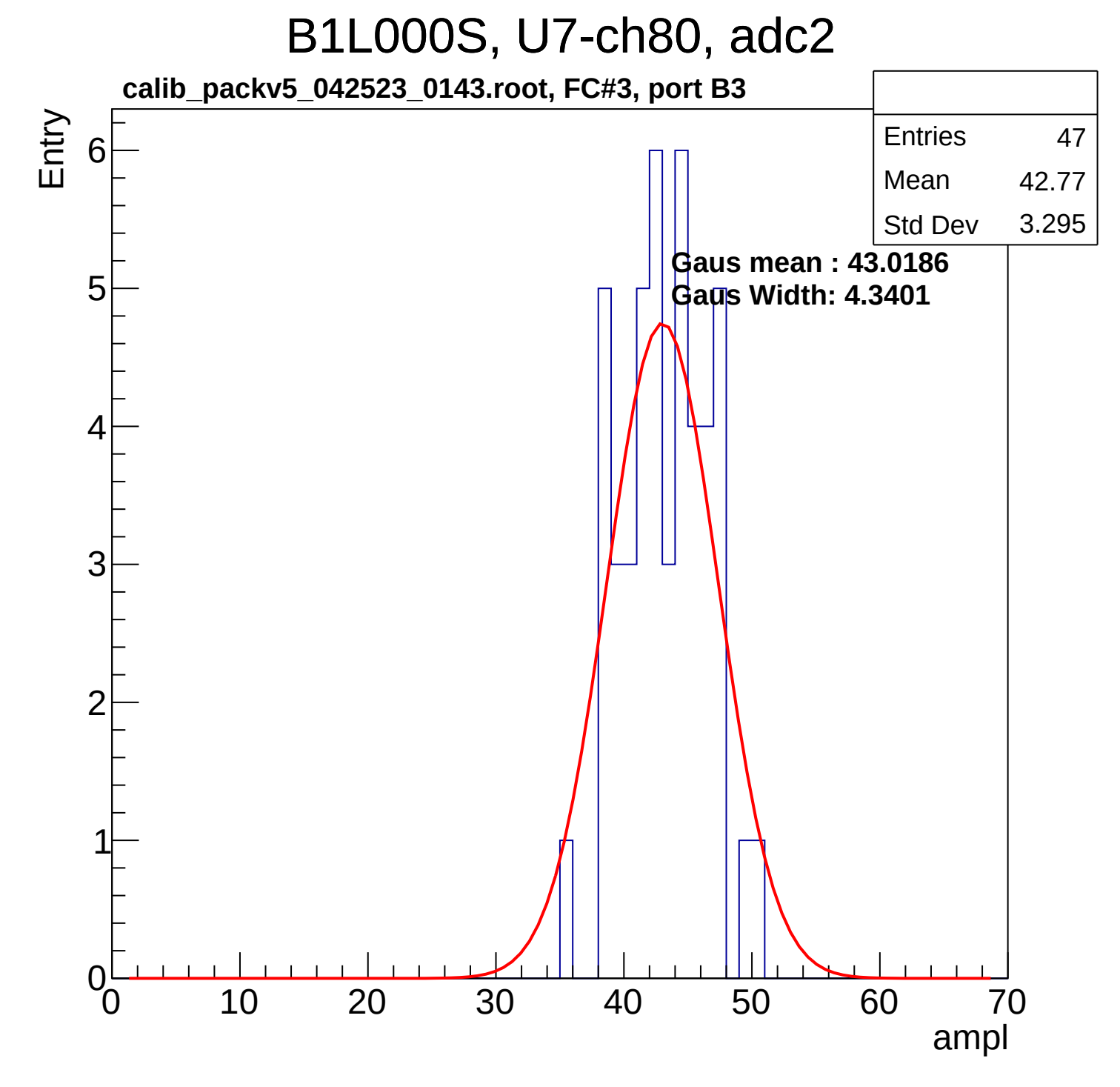
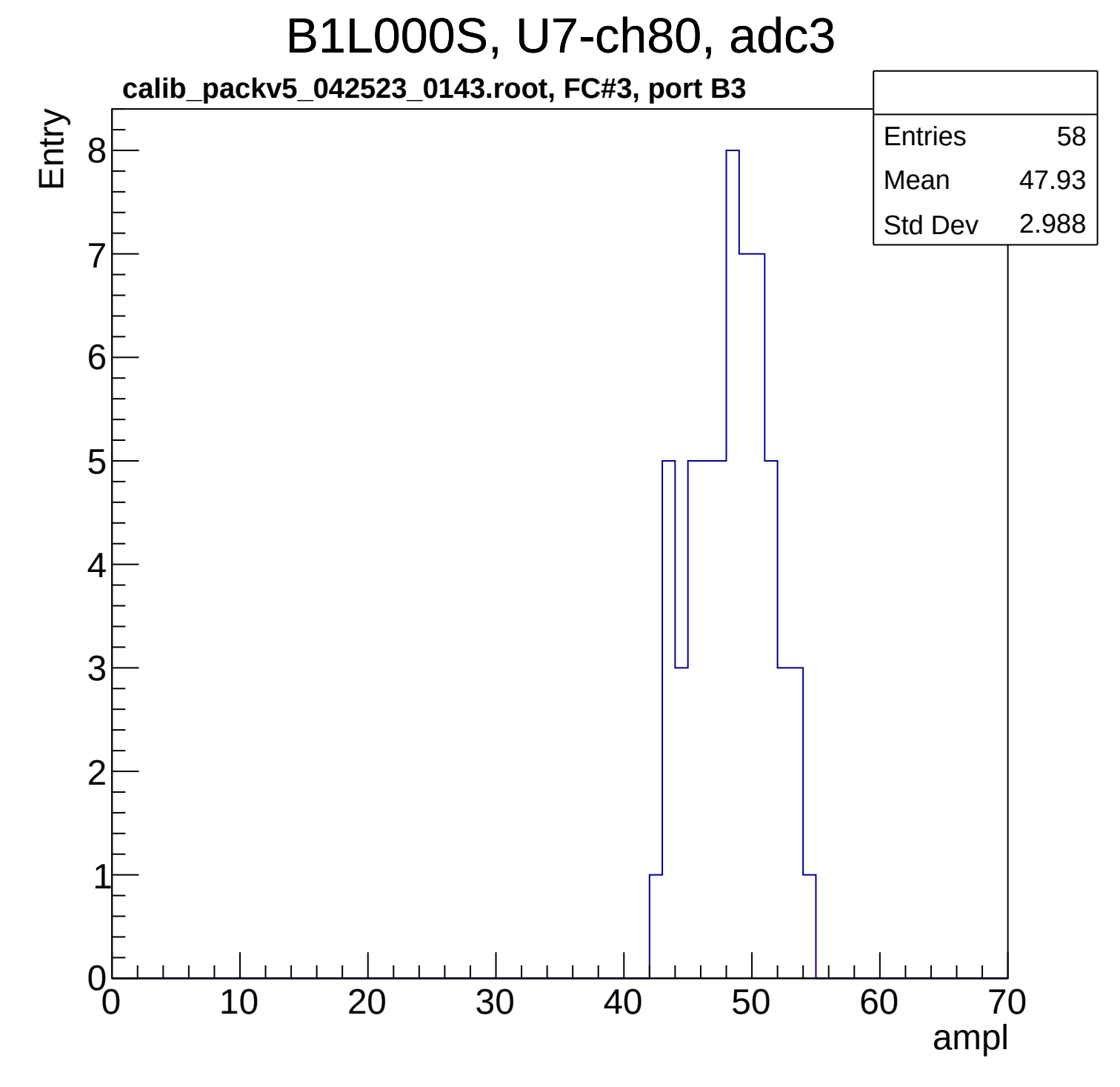
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch80, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 42.77
Std Dev 3.295
Gaus mean : 43.0186
Gaus Width: 4.3401
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U7-ch80, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 47.93
Std Dev 2.988
Entry
ampl
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3
4
5
6
7
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0
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60
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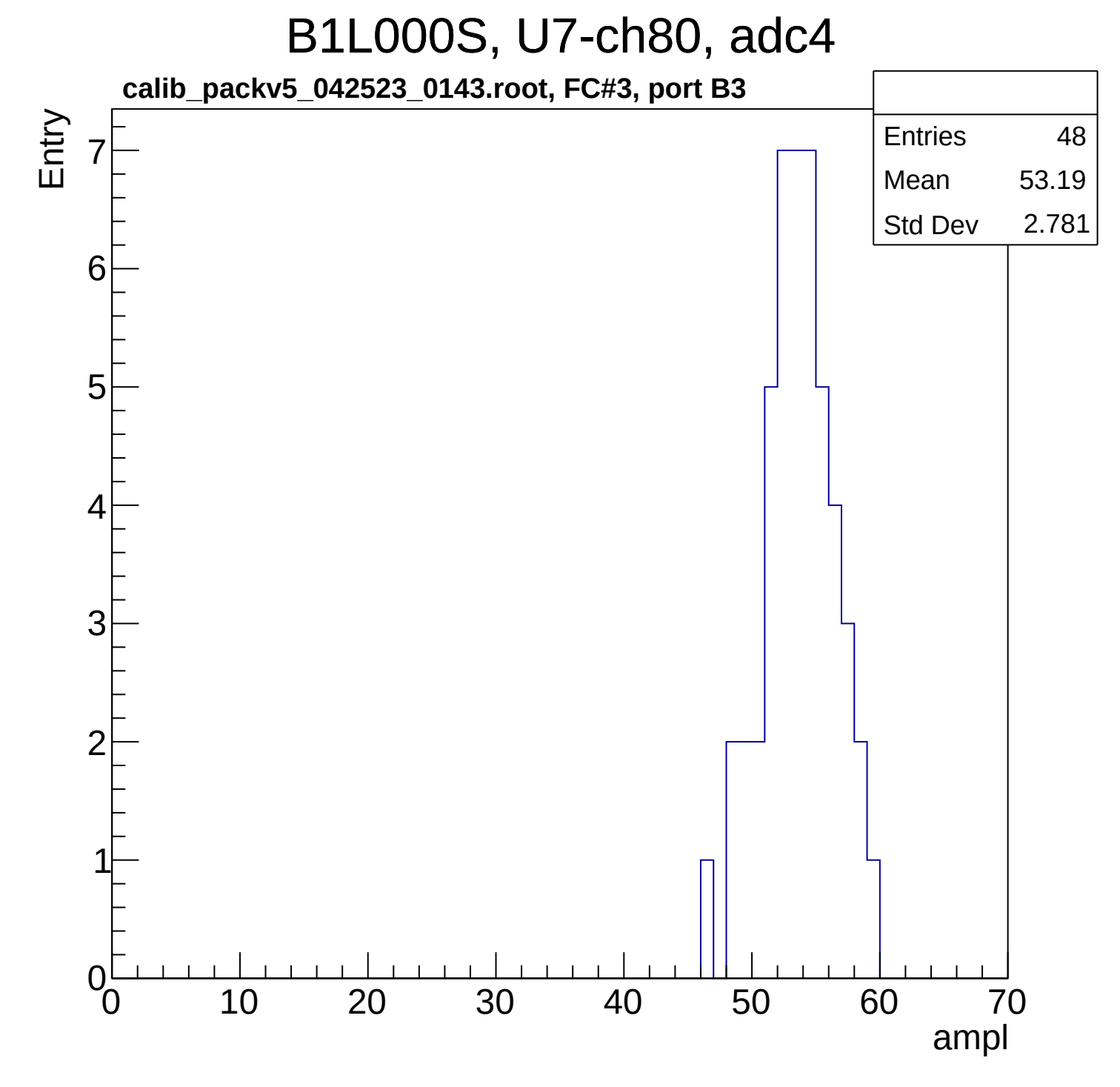
B1L000S, U7-ch80, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 53.19
Std Dev 2.781
Entry
ampl
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0
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60
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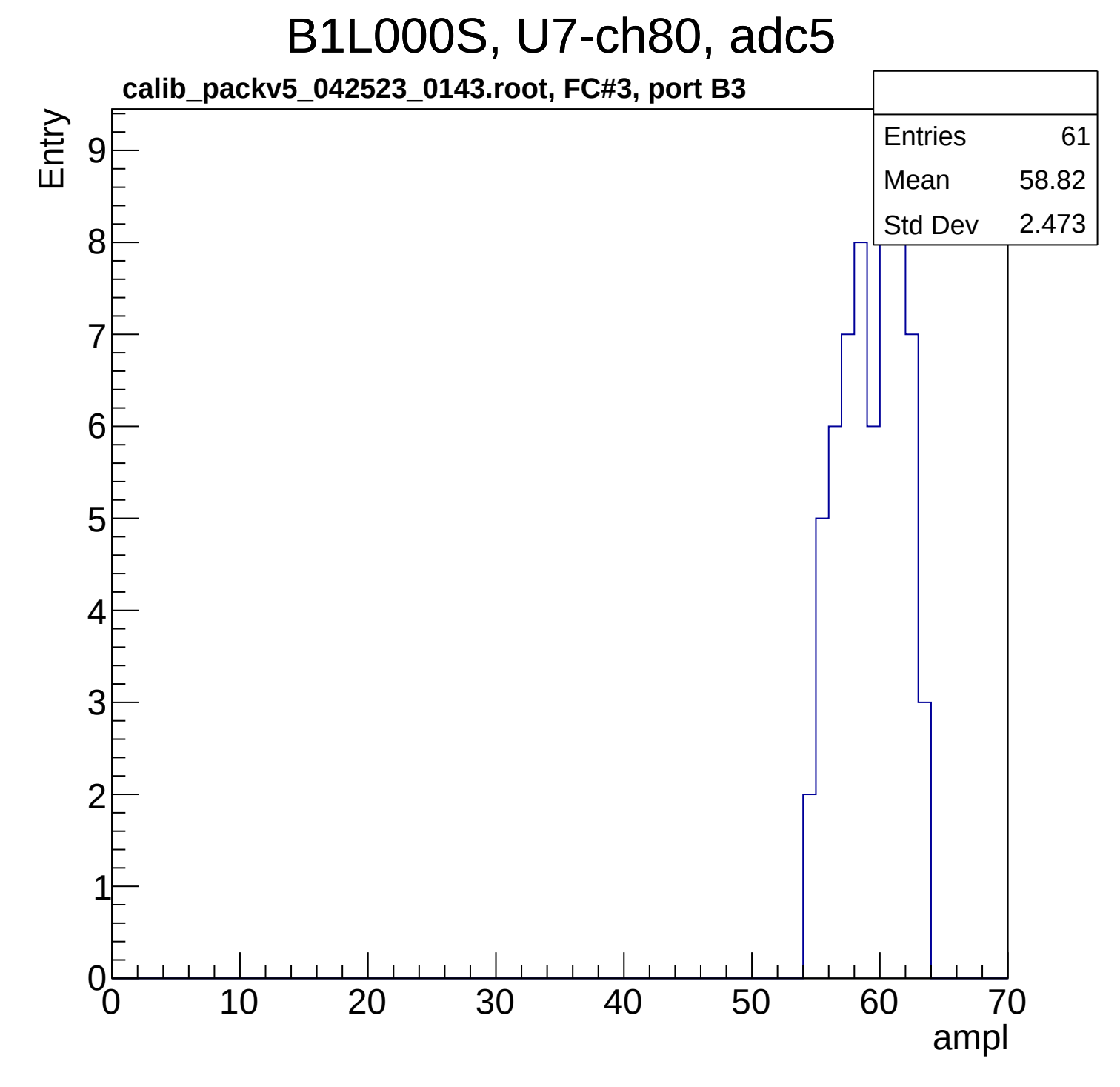
B1L000S, U7-ch80, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.82
Std Dev 2.473
Entry
ampl
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4
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7
8
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0
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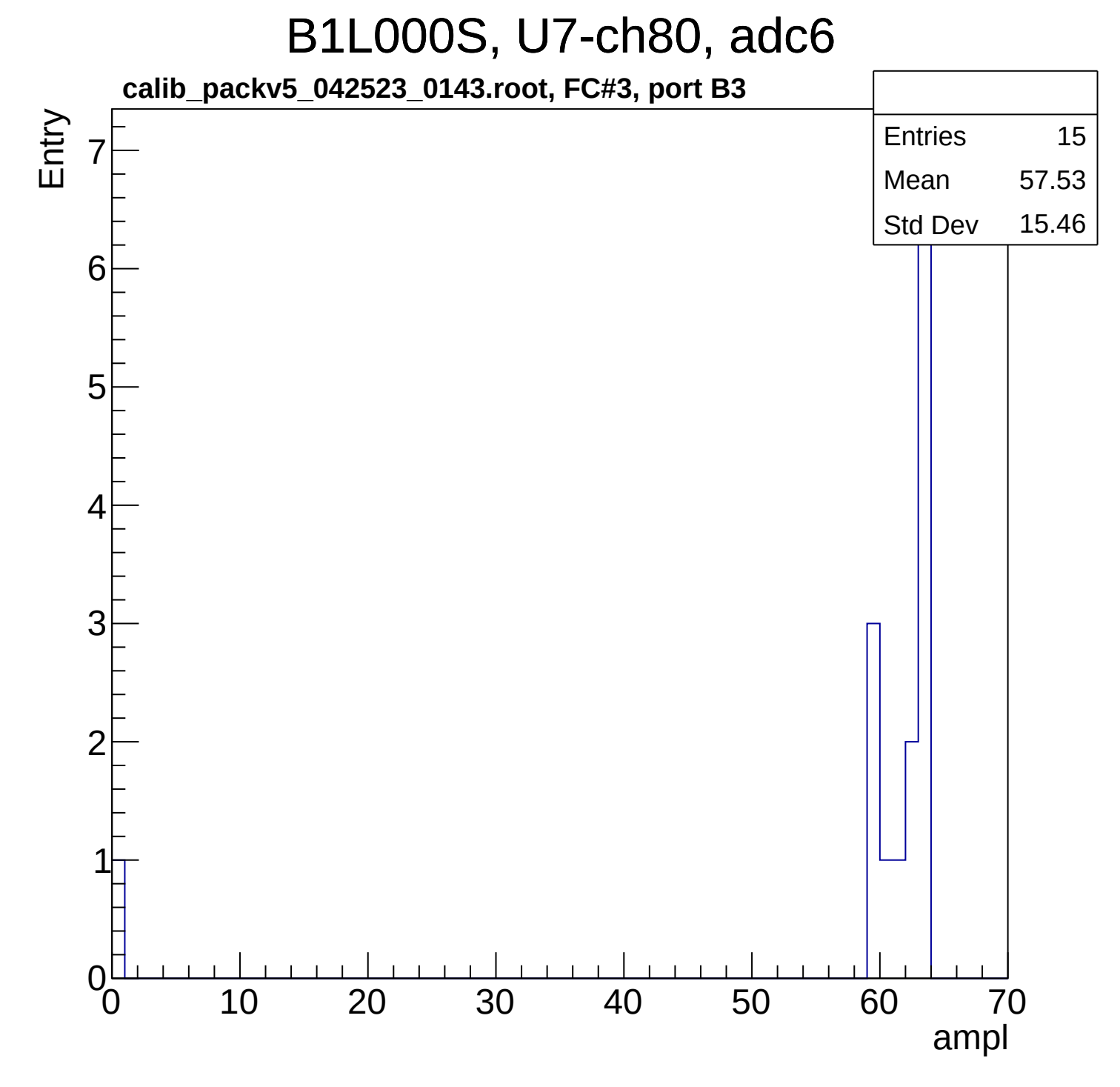
B1L000S, U7-ch80, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.53
Std Dev 15.46
Entry
ampl
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4
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7
0
10
20
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40
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60
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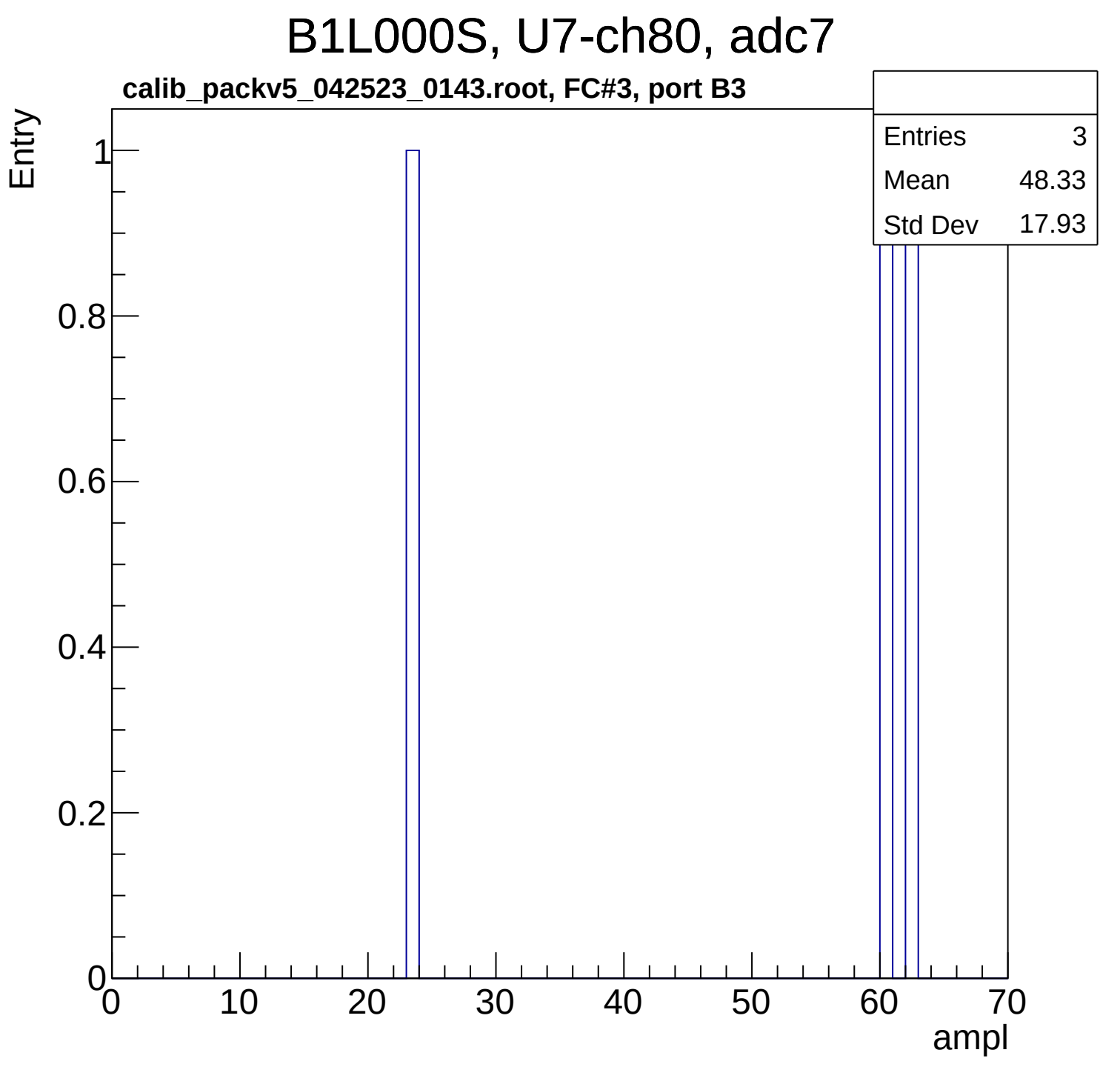
B1L000S, U7-ch80, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 48.33
Std Dev 17.93
Entry
ampl
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0.4
0.6
0.8
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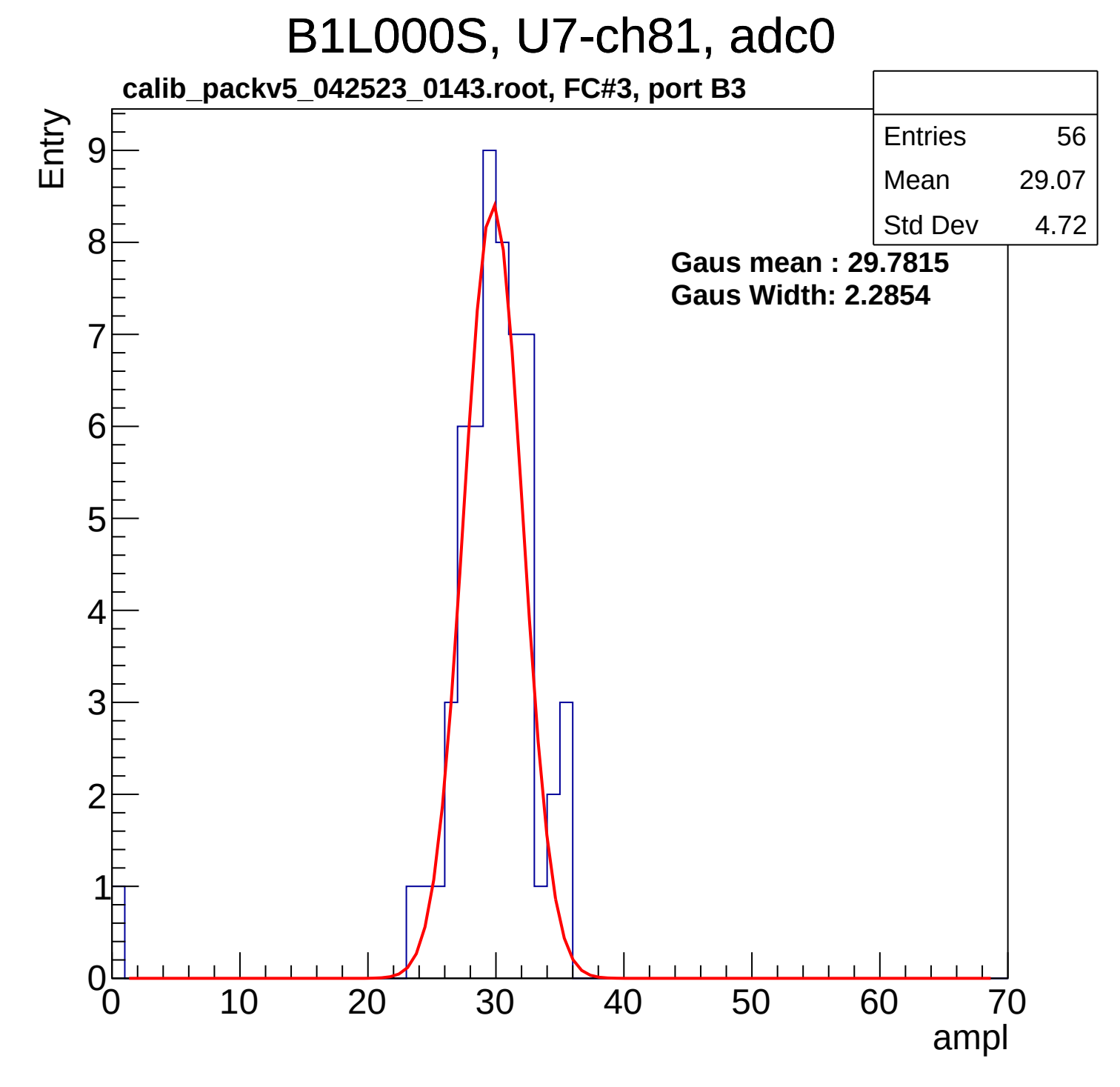
0
10
20
30
40
50
60
70


B1L000S, U7-ch81, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 29.07
Std Dev 4.72
Gaus mean : 29.7815
Gaus Width: 2.2854
Entry
ampl
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2
3
4
5
6
7
8
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0
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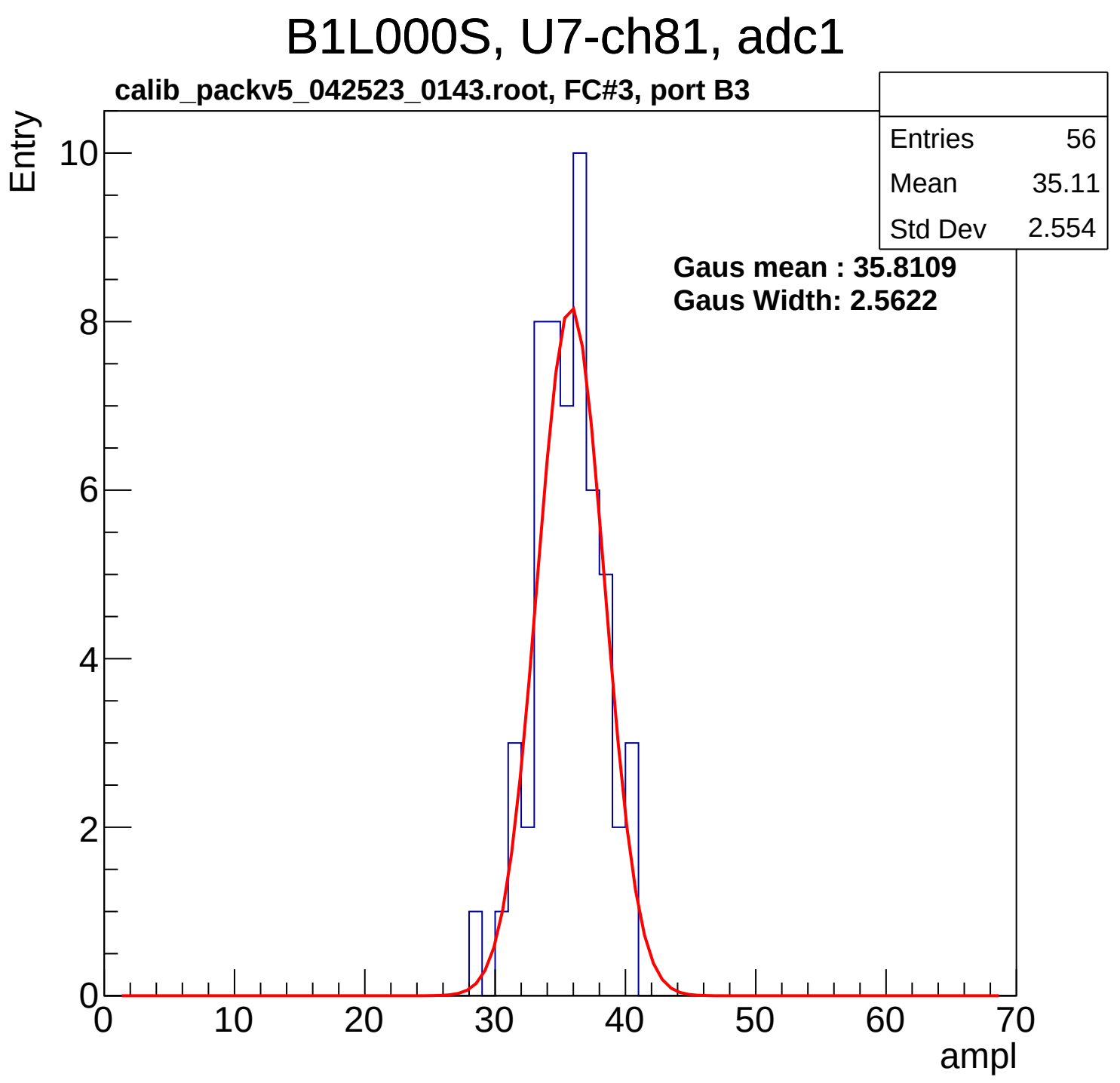
B1L000S, U7-ch81, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.11
Std Dev 2.554
Gaus mean : 35.8109
Gaus Width: 2.5622
Entry
ampl
0
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4
6
8
10
0
10
20
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40
50
60
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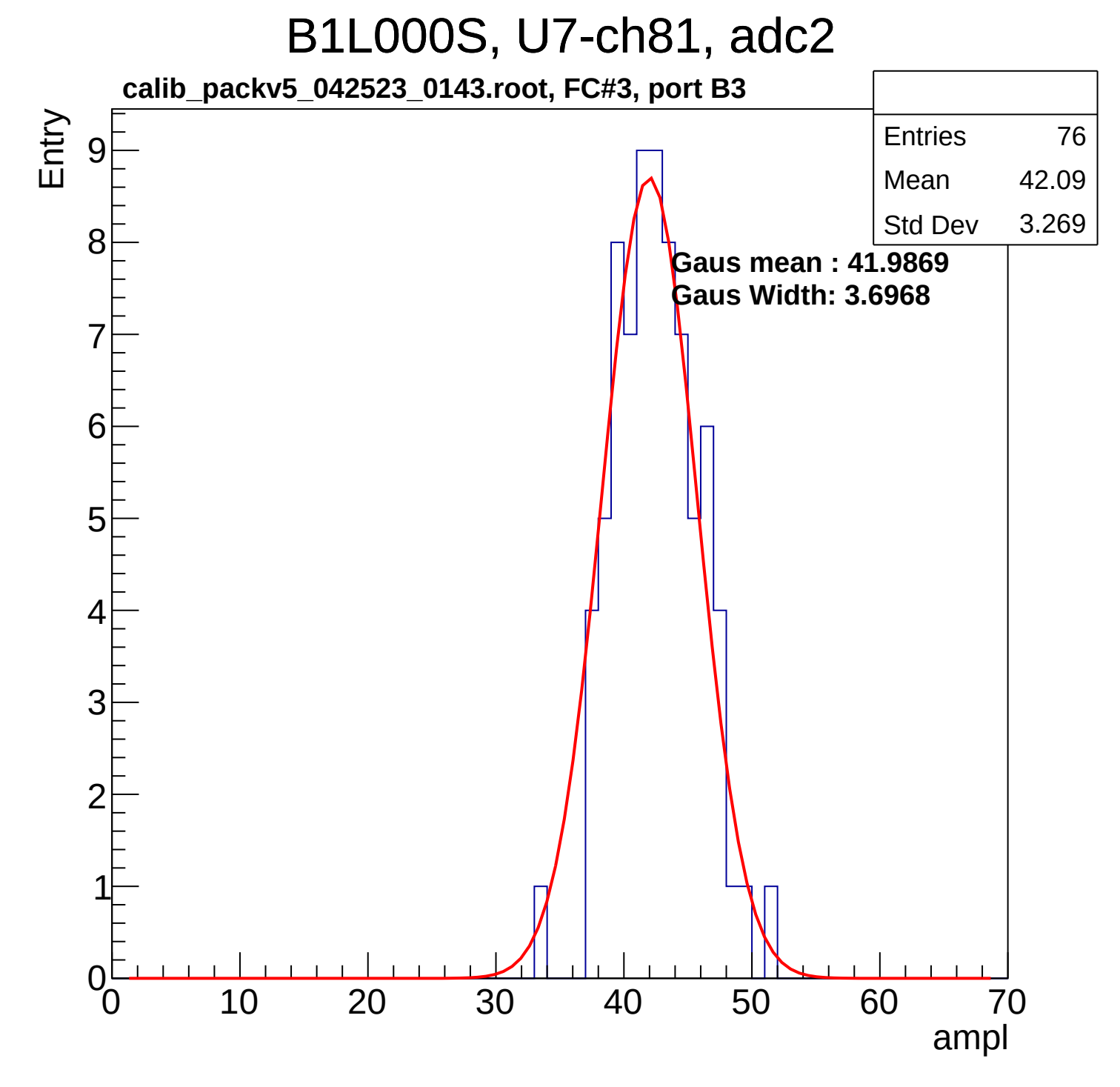
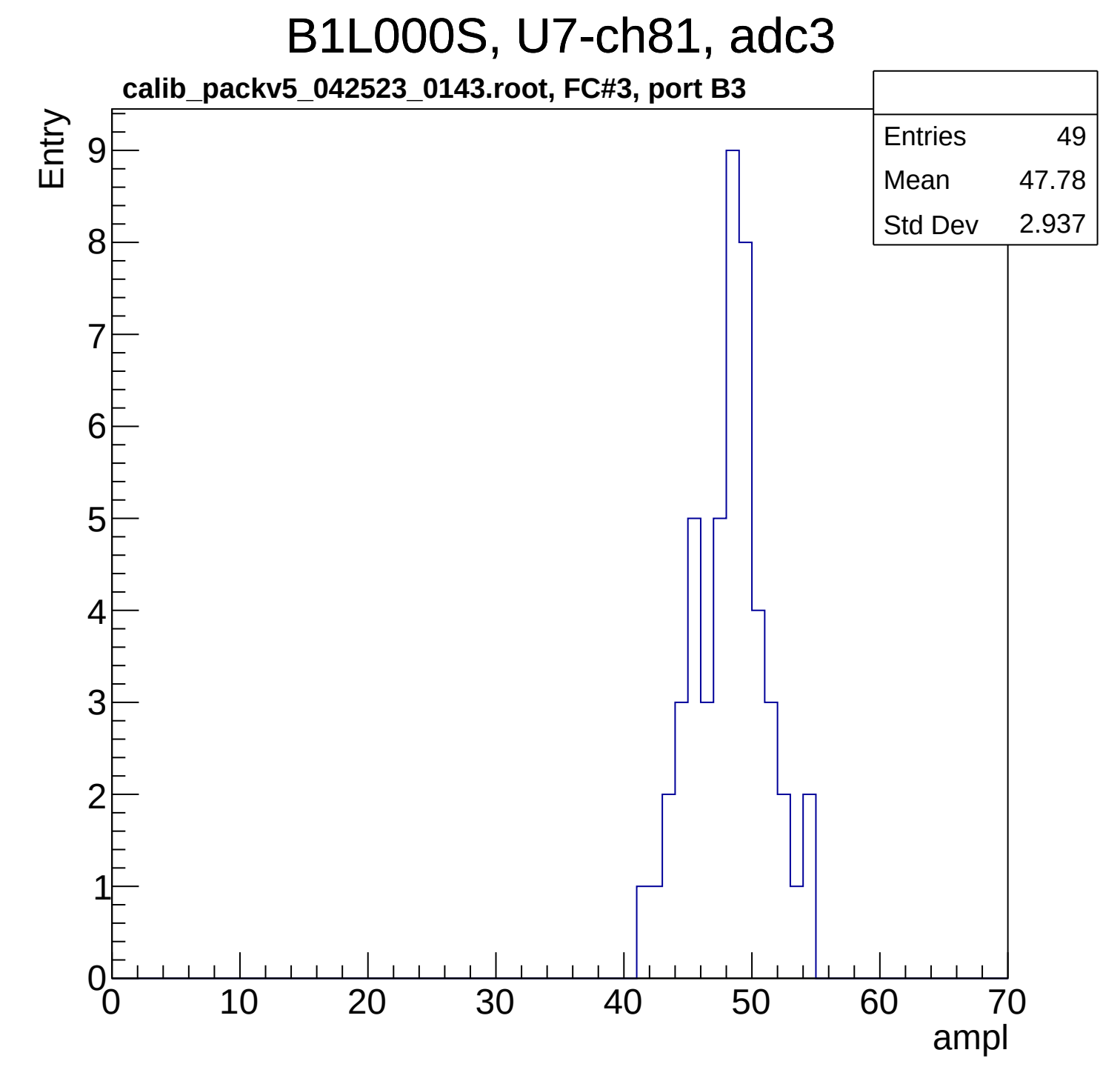

B1L000S, U7-ch81, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 42.09
Std Dev 3.269
Gaus mean : 41.9869
Gaus Width: 3.6968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch81, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 47.78
Std Dev 2.937
Entry
ampl
0
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2
3
4
5
6
7
8
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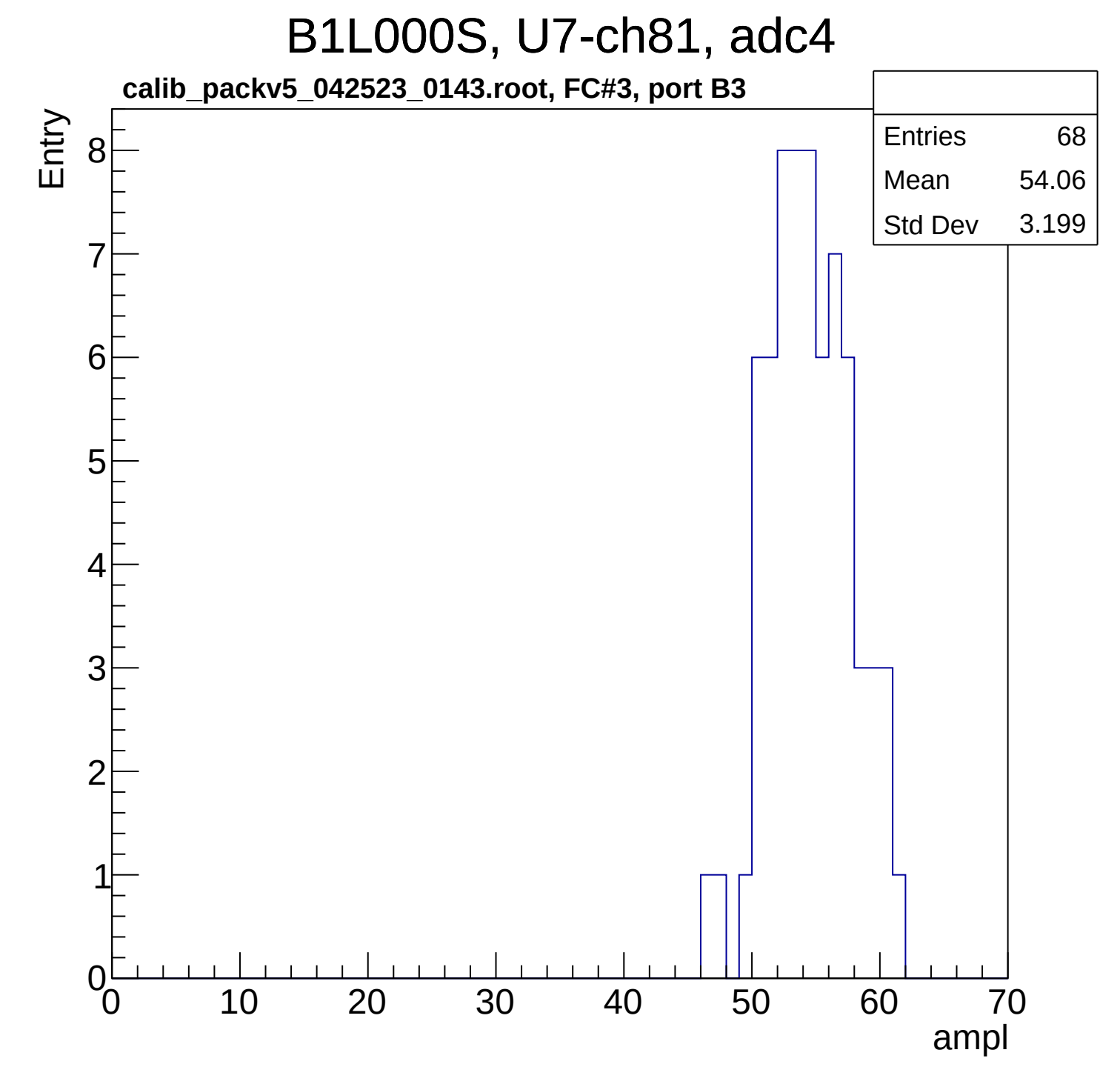
B1L000S, U7-ch81, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 54.06
Std Dev 3.199
Entry
ampl
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2
3
4
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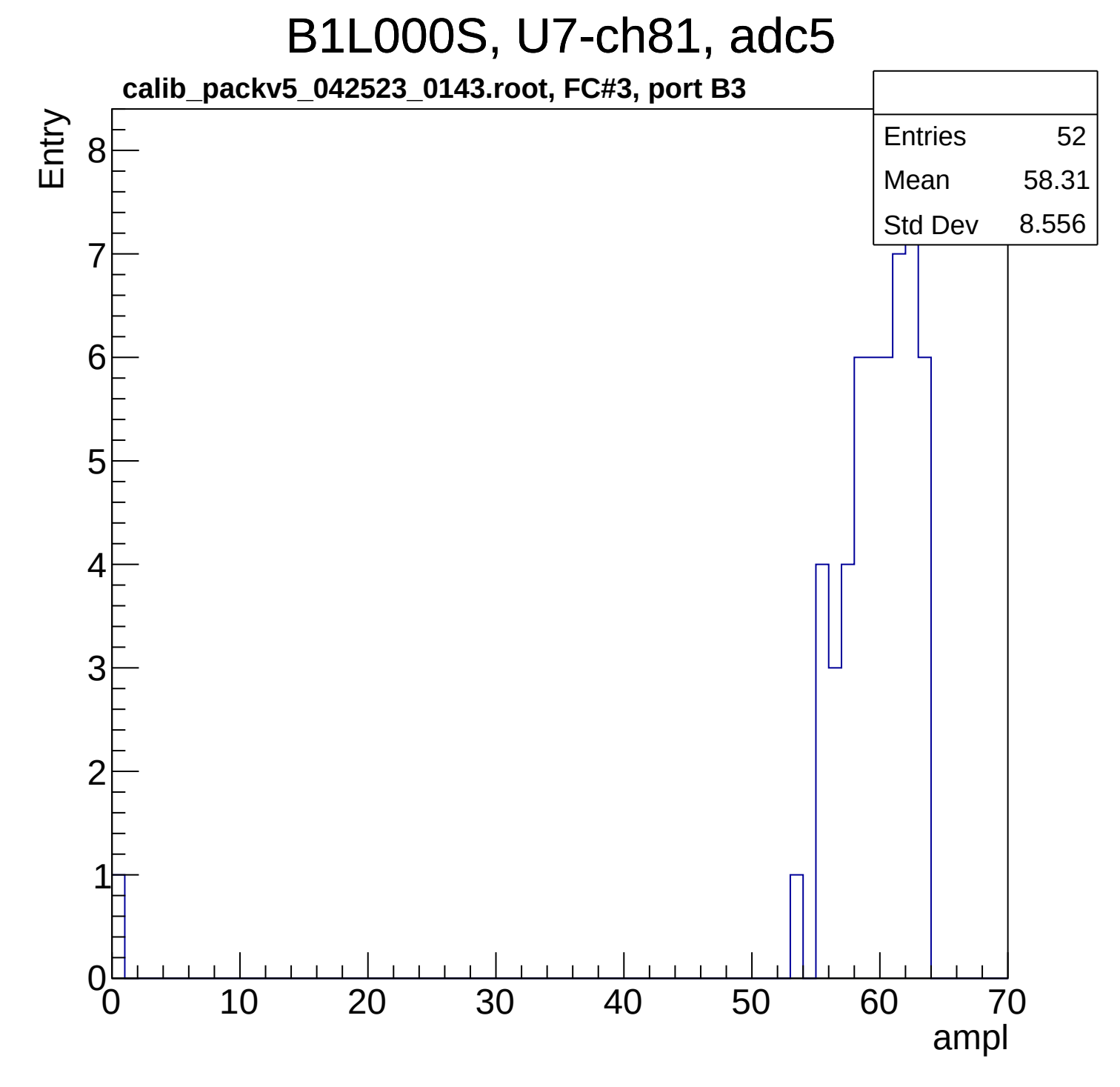
B1L000S, U7-ch81, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 58.31
Std Dev 8.556
Entry
ampl
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1
2
3
4
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0
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60
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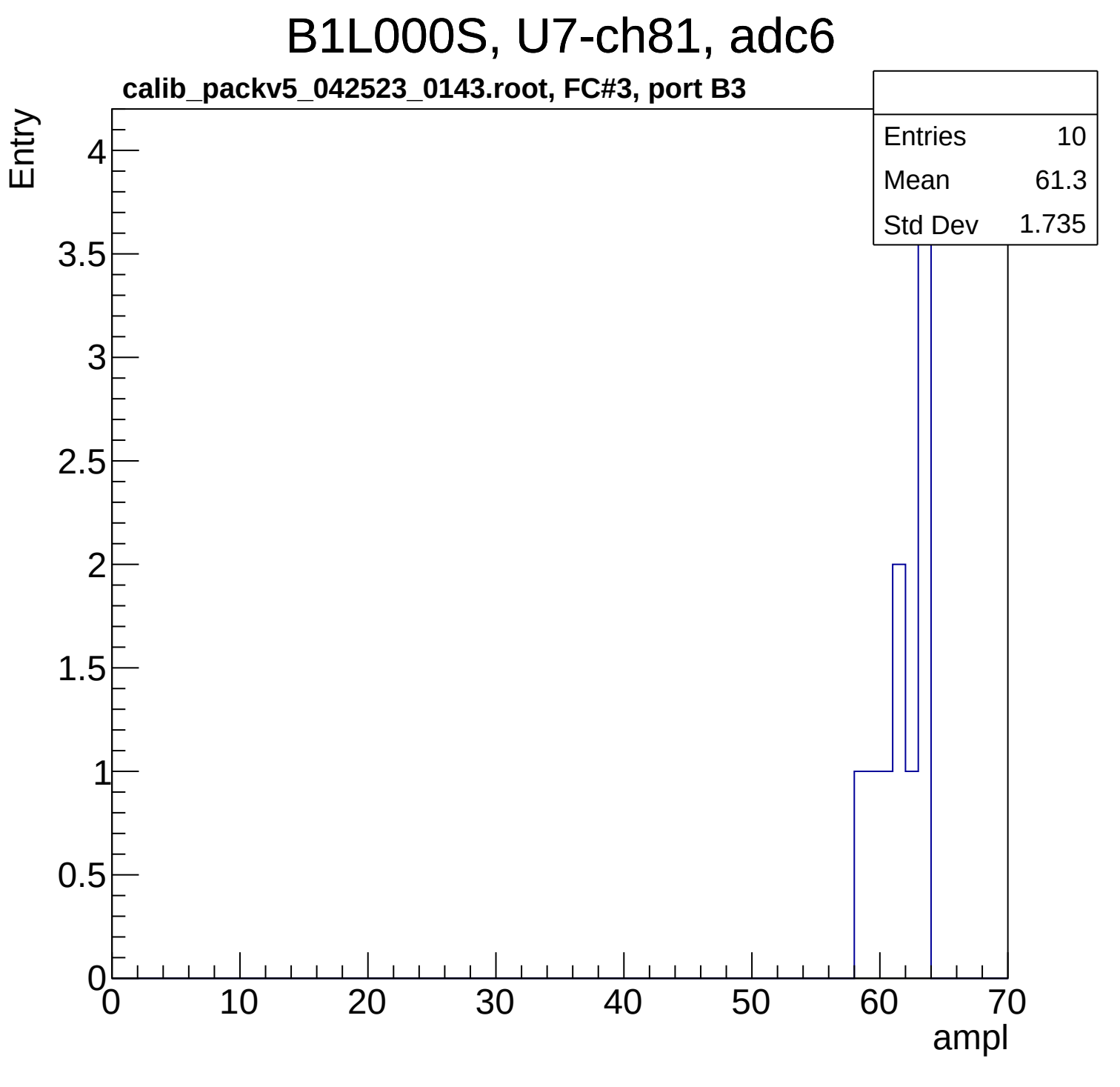
B1L000S, U7-ch81, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.3
Std Dev 1.735
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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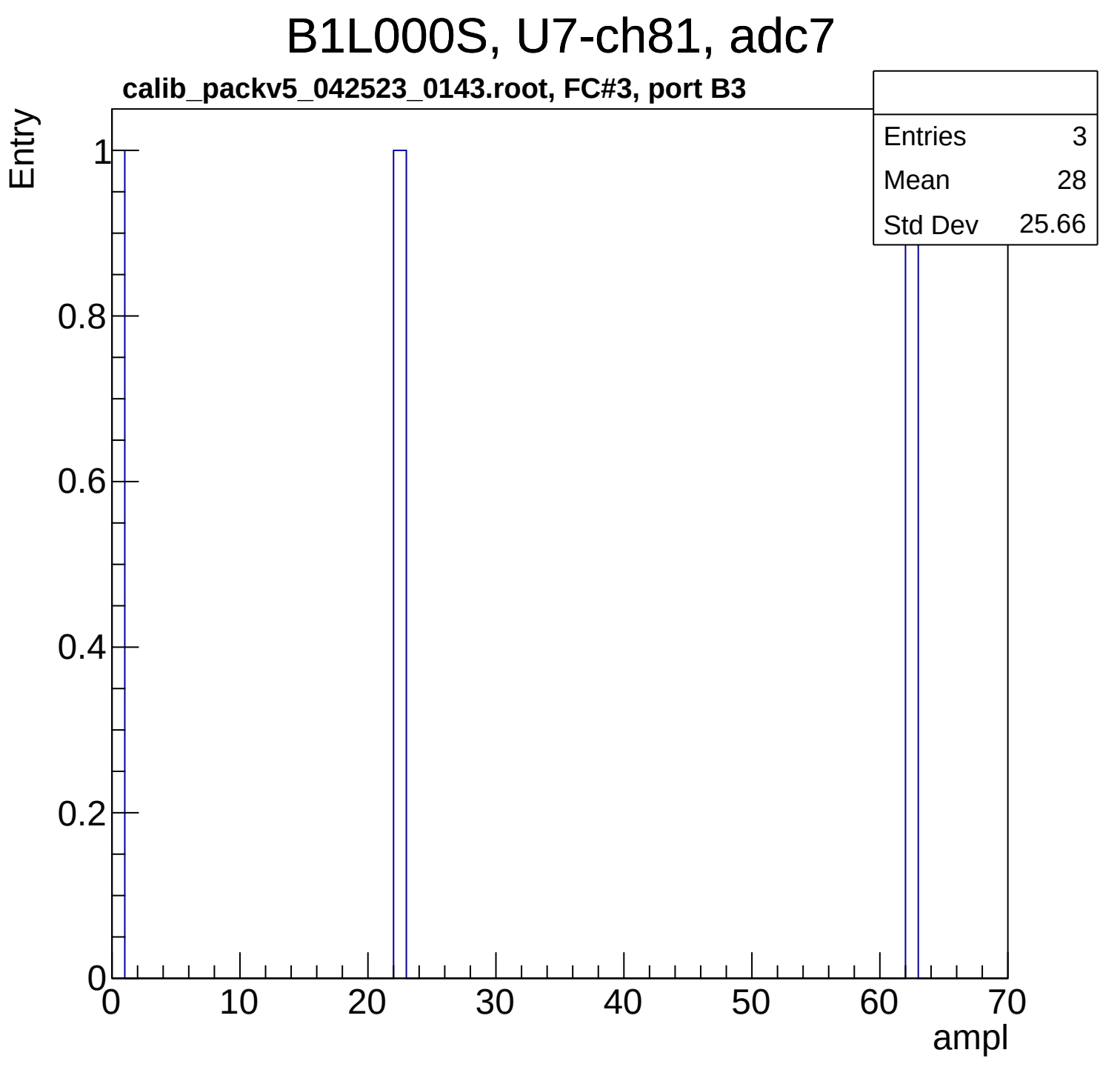
B1L000S, U7-ch81, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 28
Std Dev 25.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
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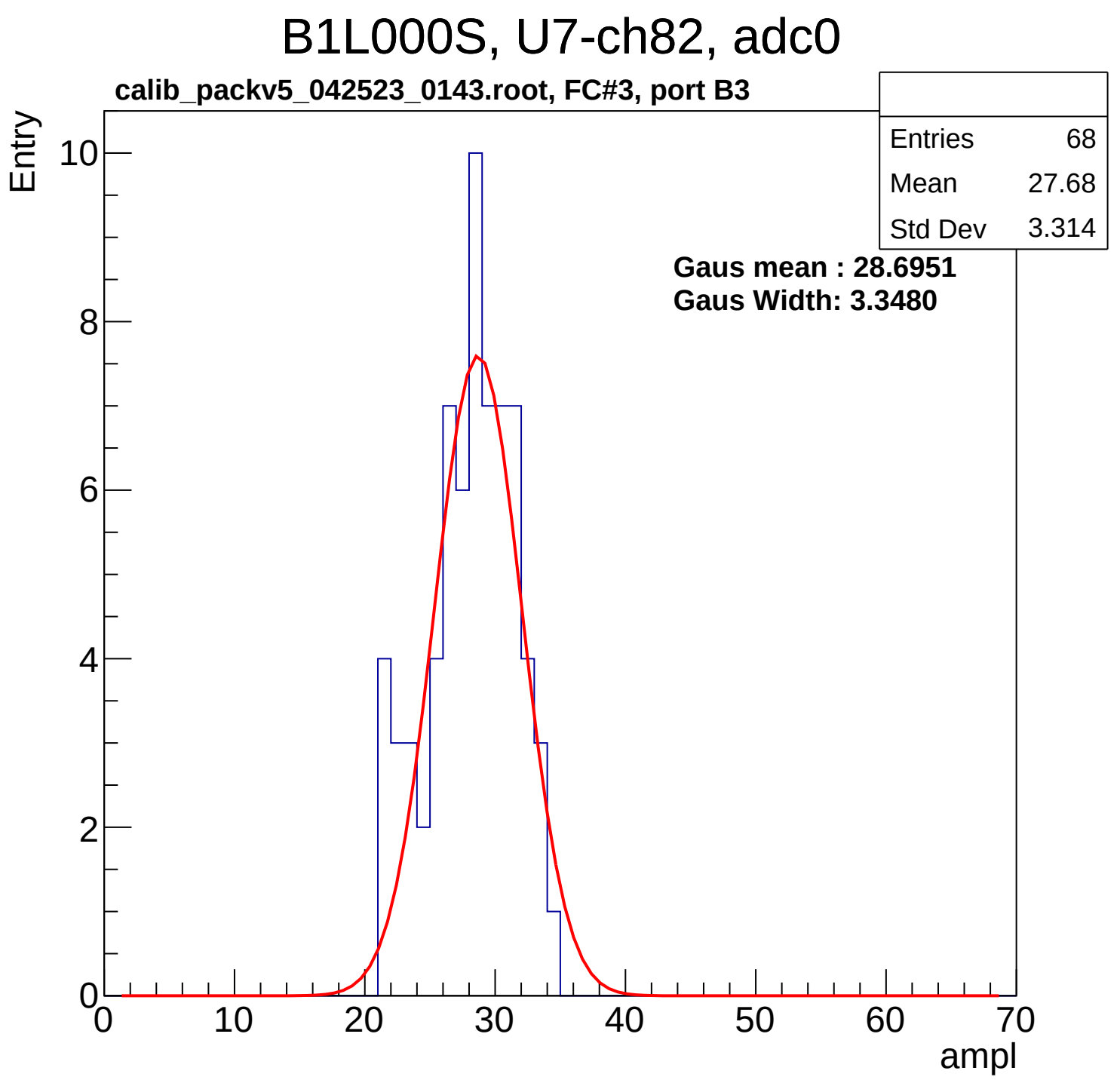
B1L000S, U7-ch82, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 27.68
Std Dev 3.314
Gaus mean : 28.6951
Gaus Width: 3.3480
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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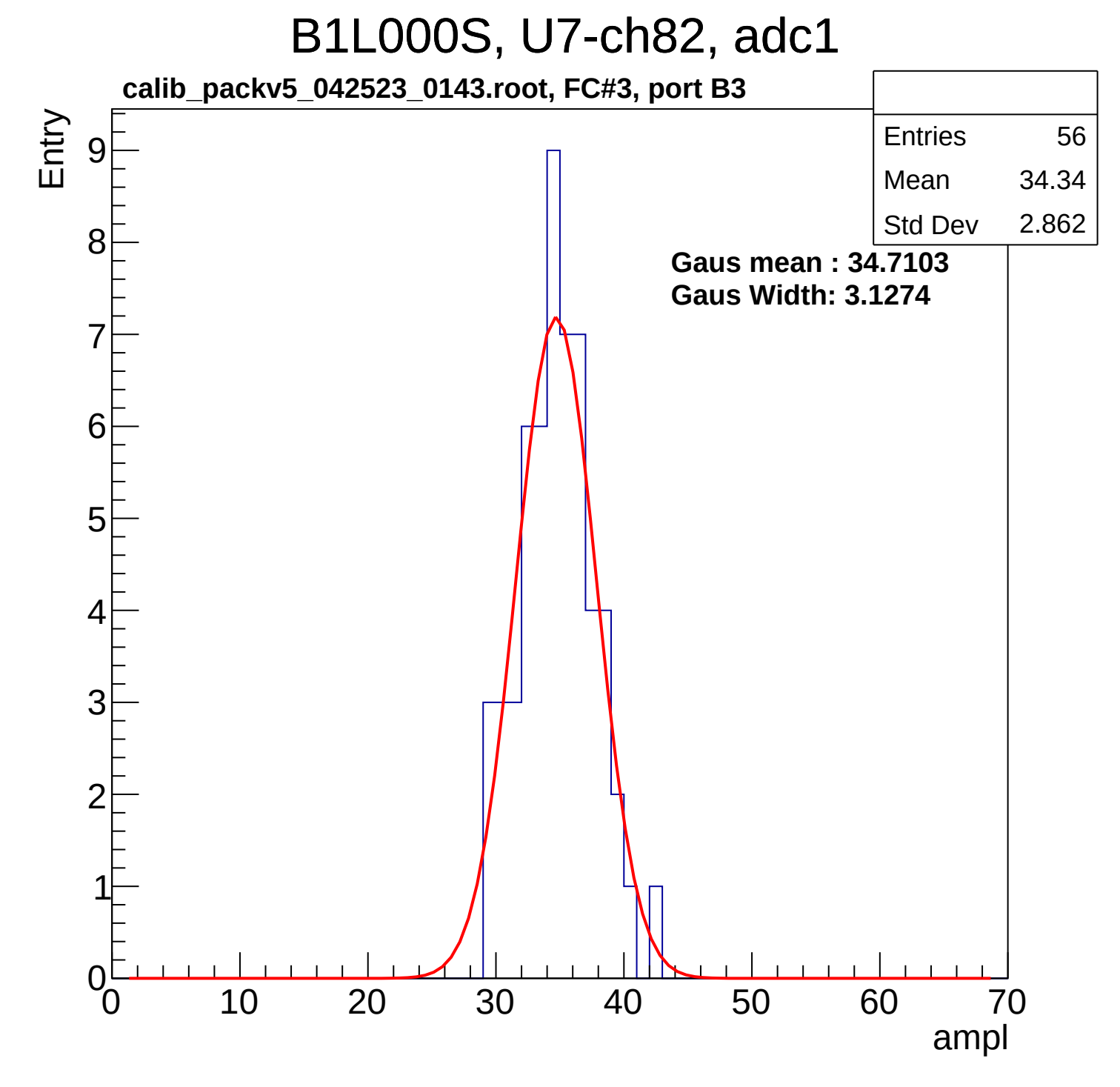
B1L000S, U7-ch82, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 34.34
Std Dev 2.862
Gaus mean : 34.7103
Gaus Width: 3.1274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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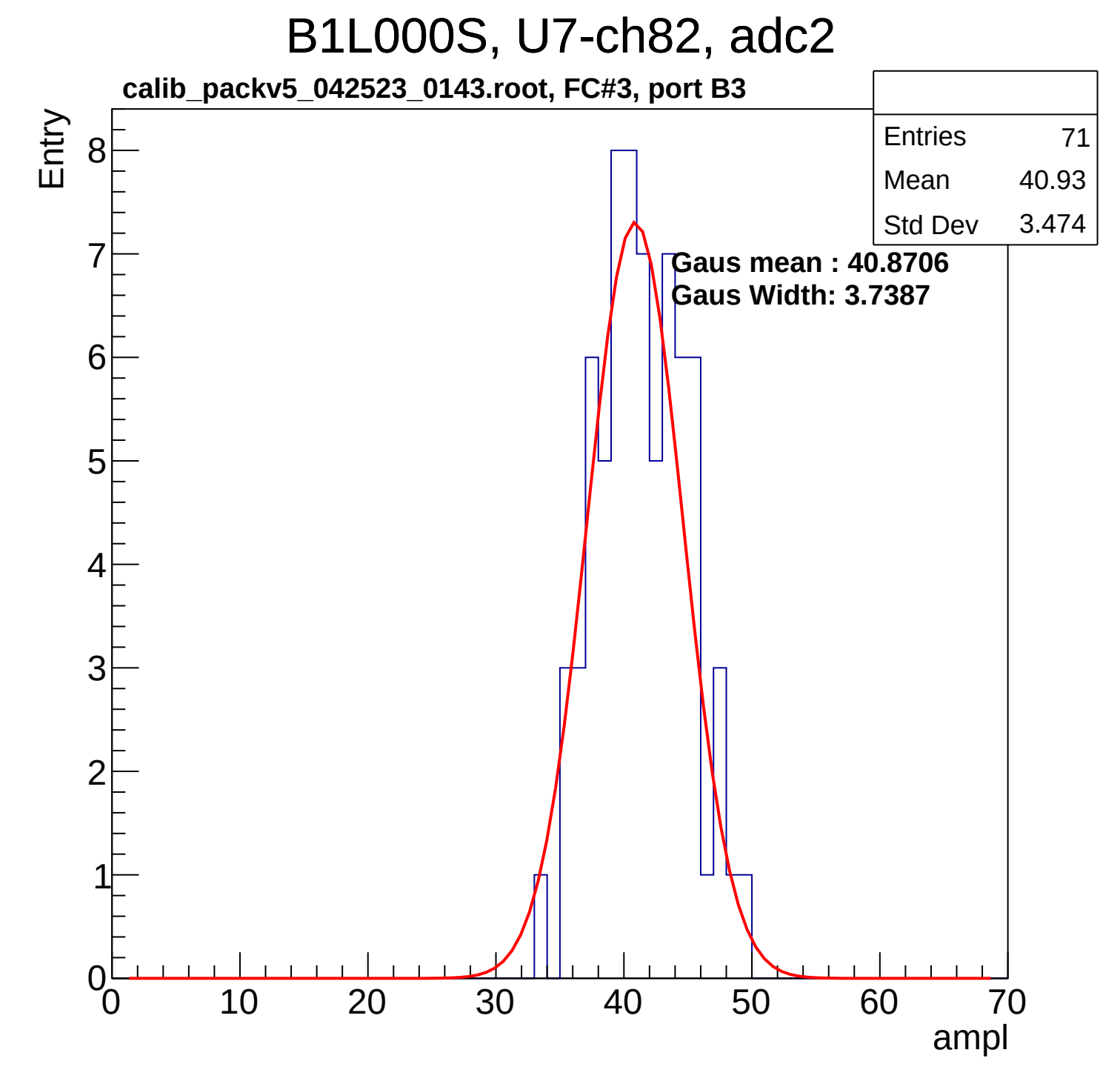
B1L000S, U7-ch82, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 40.93
Std Dev 3.474
Gaus mean : 40.8706
Gaus Width: 3.7387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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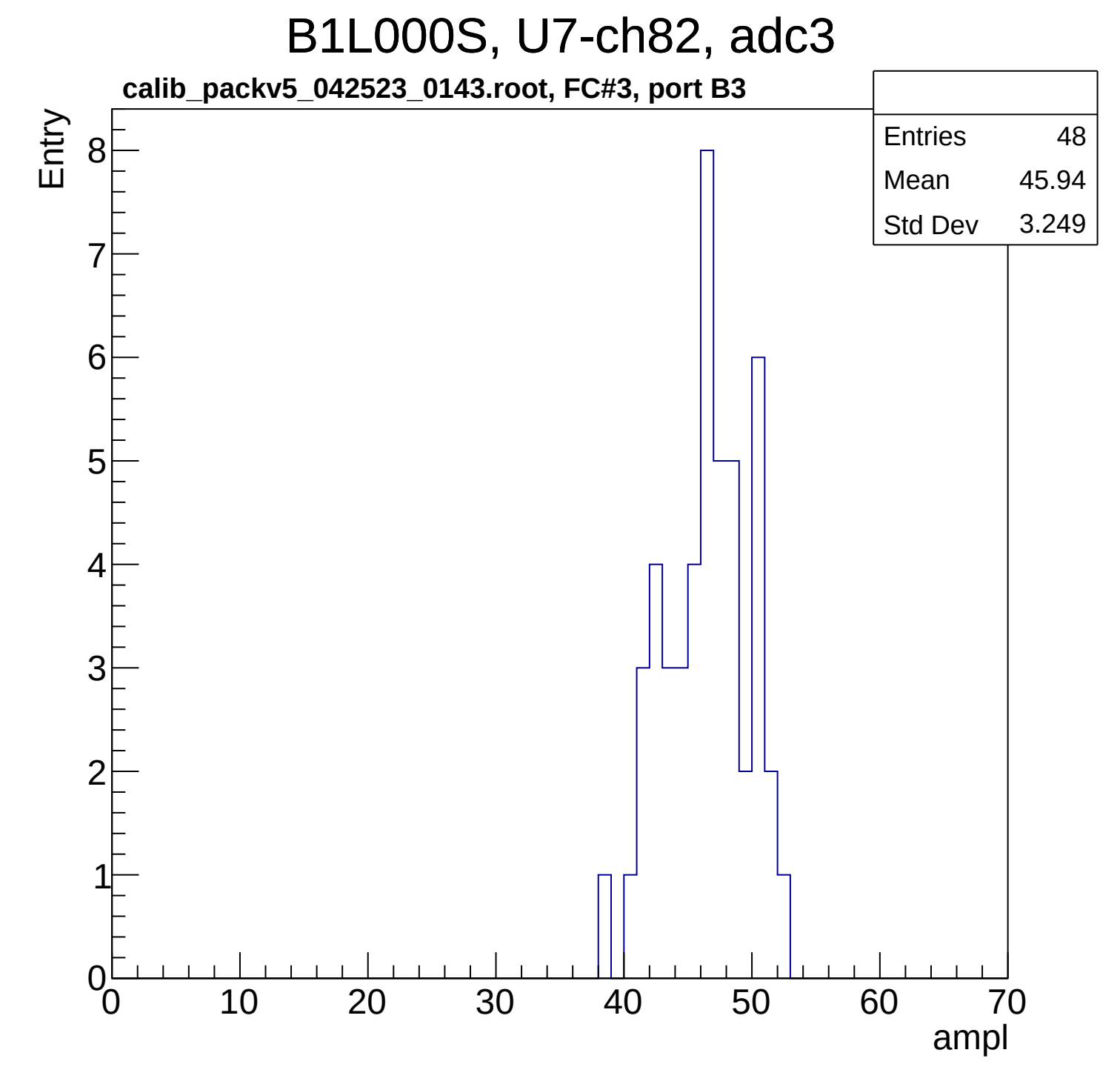
B1L000S, U7-ch82, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 45.94
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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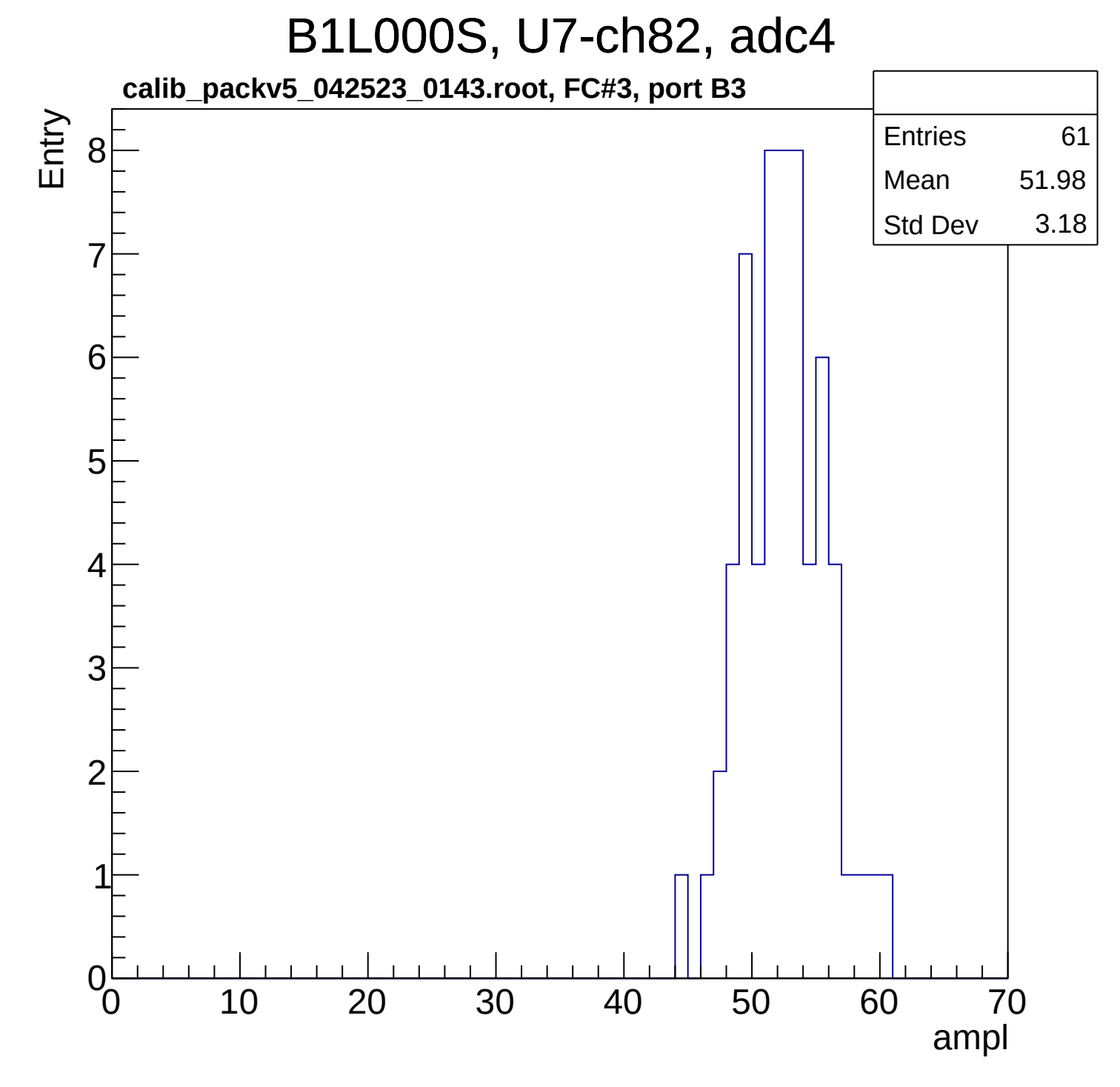
B1L000S, U7-ch82, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 51.98
Std Dev 3.18
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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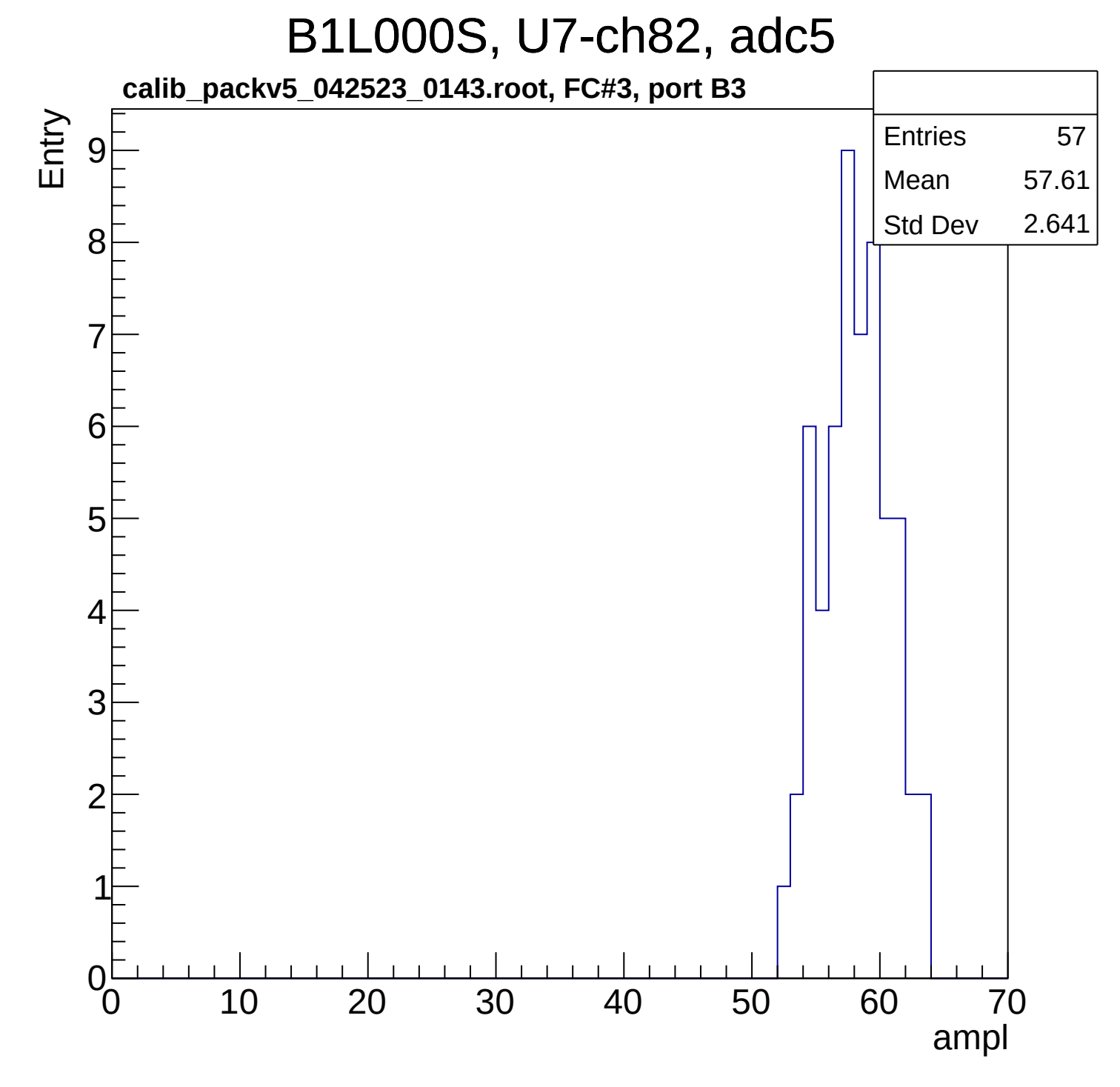
B1L000S, U7-ch82, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.61
Std Dev 2.641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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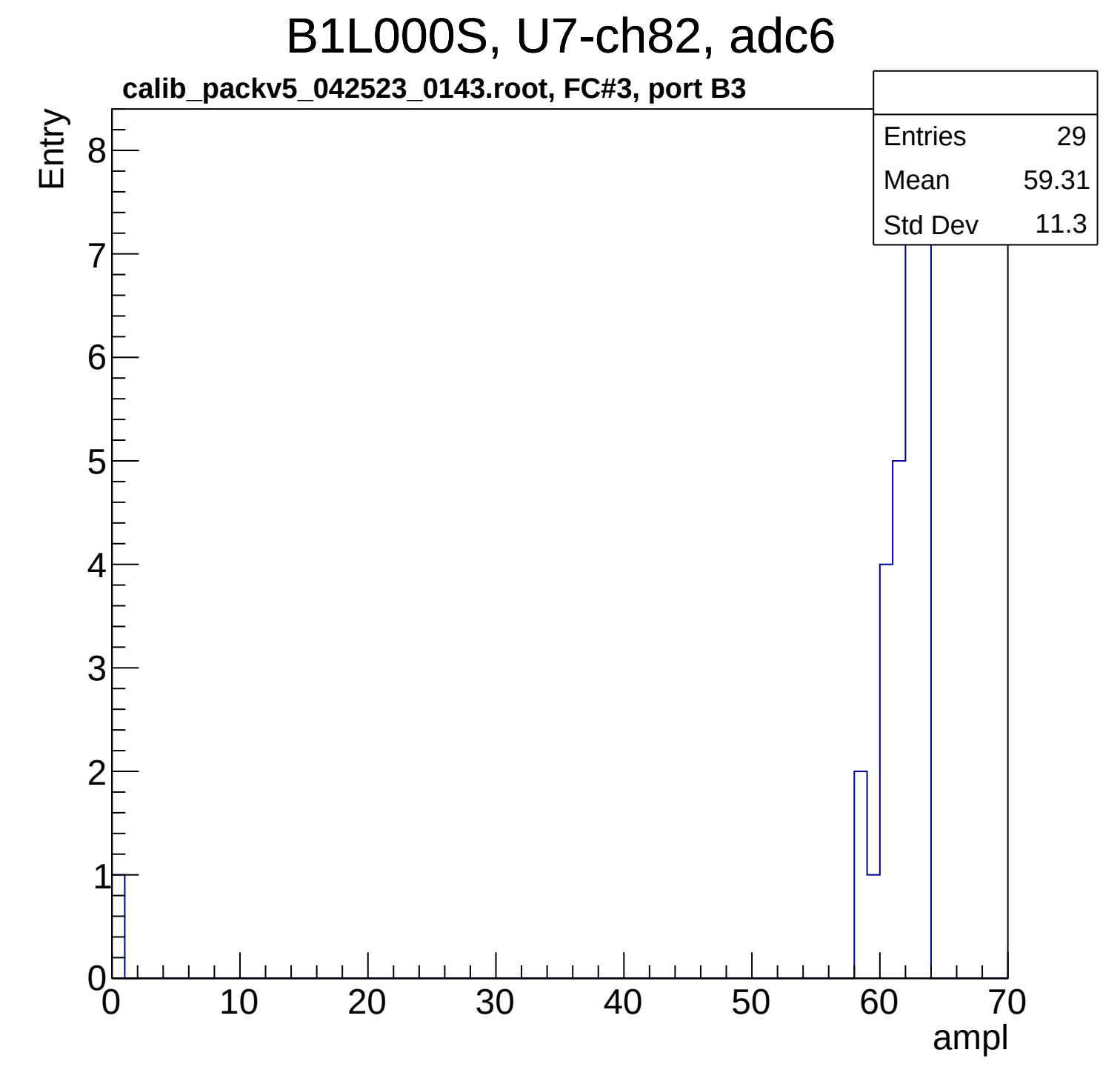
B1L000S, U7-ch82, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 29
Mean 59.31
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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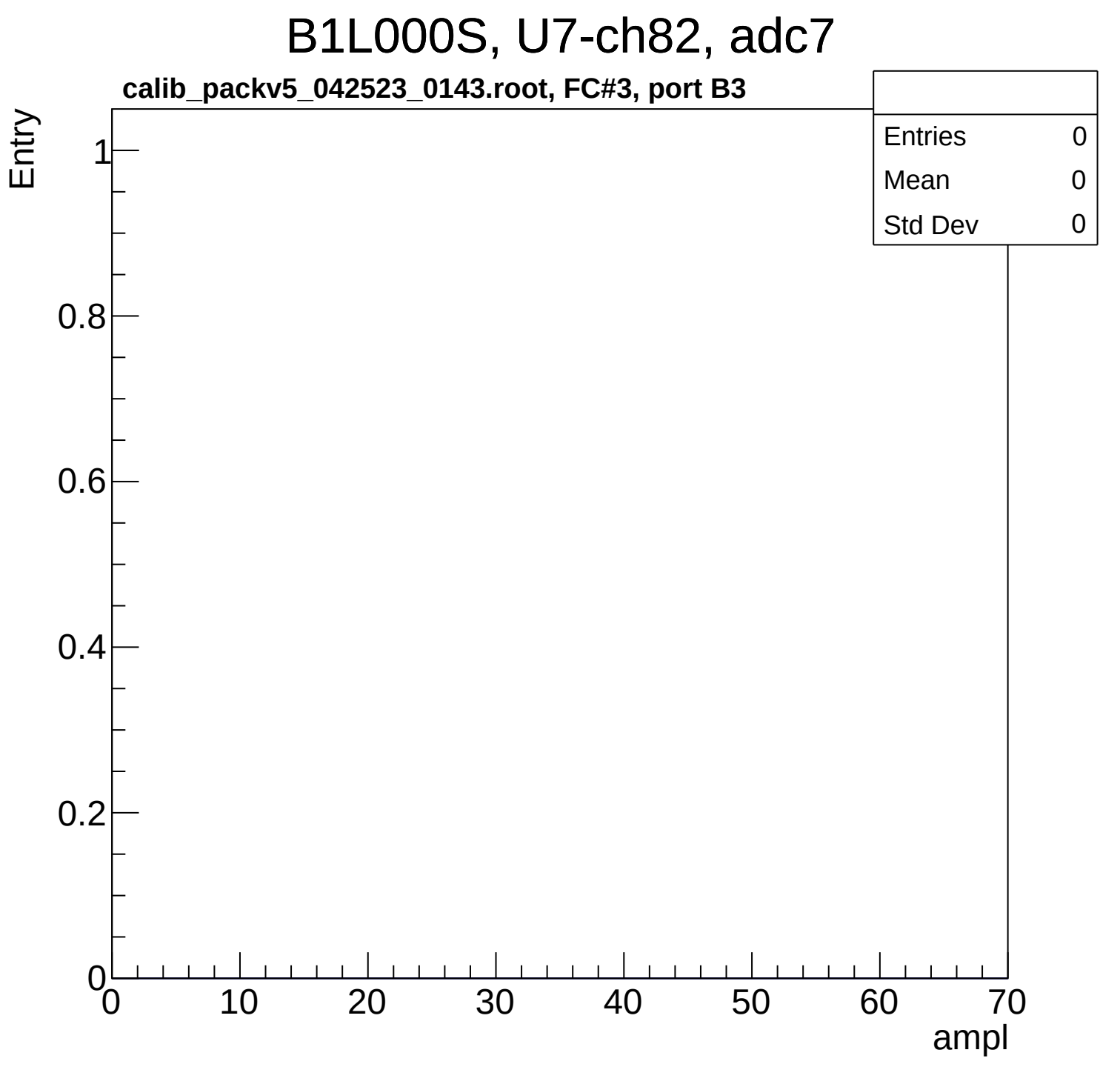
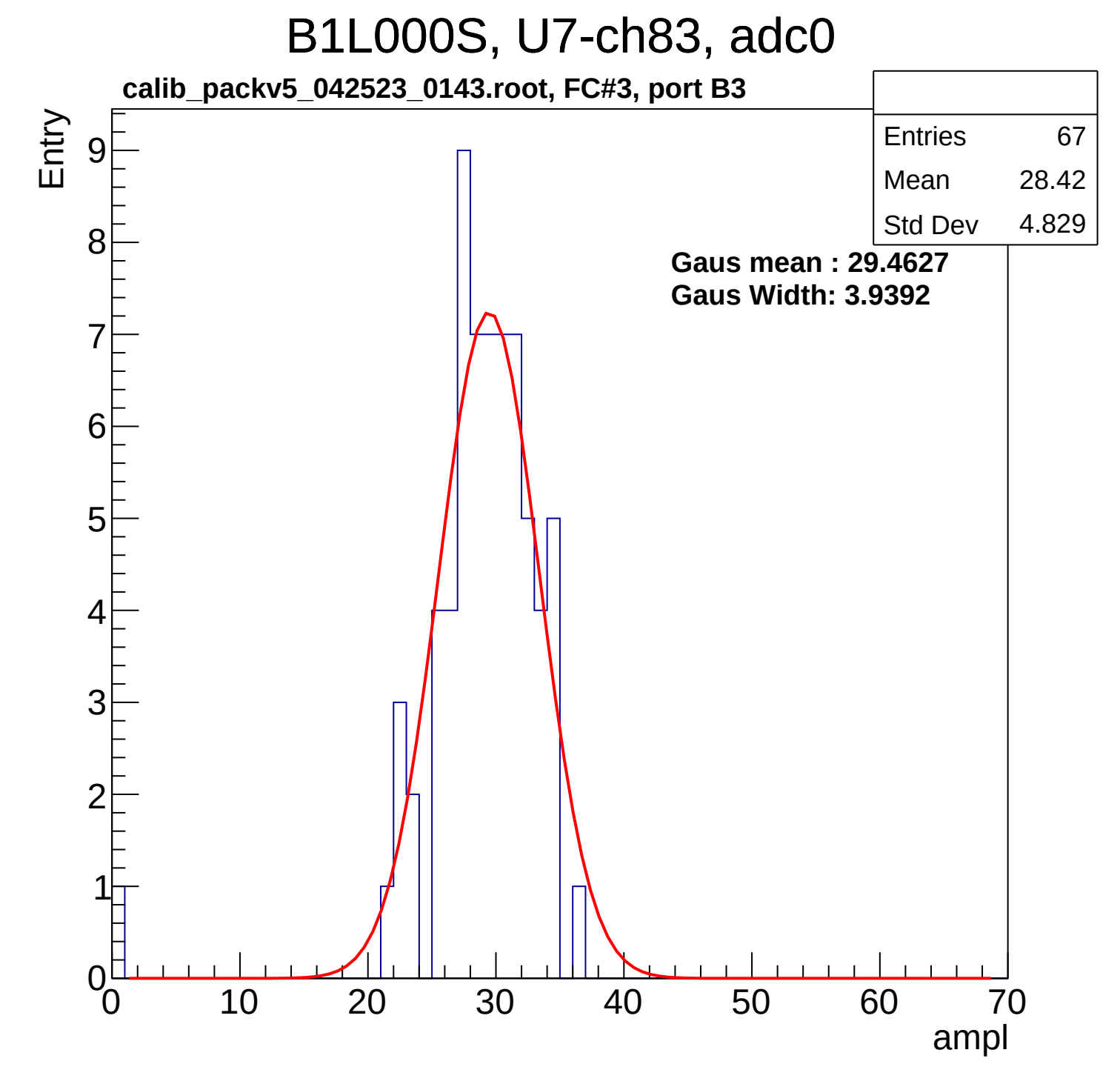
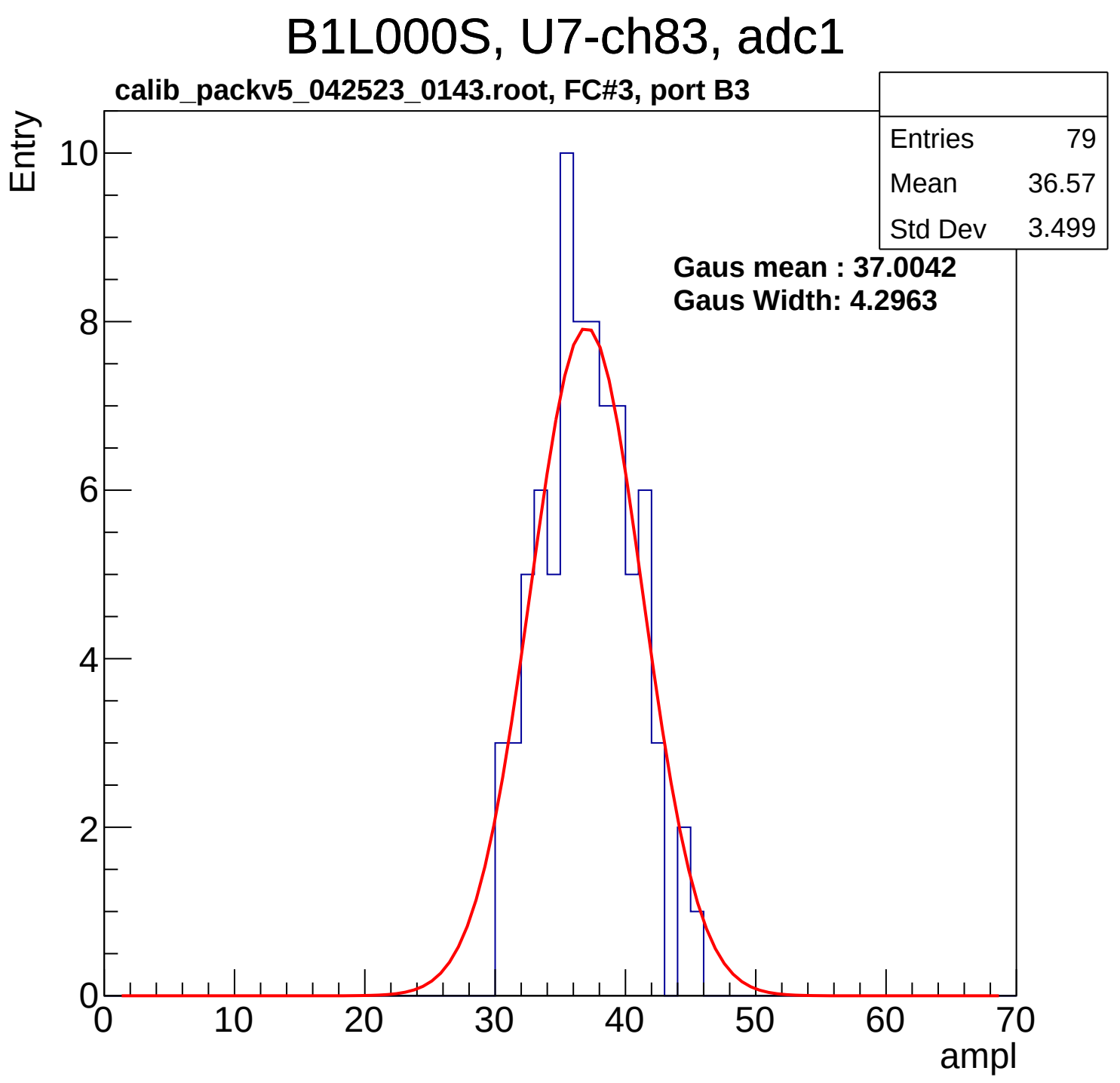
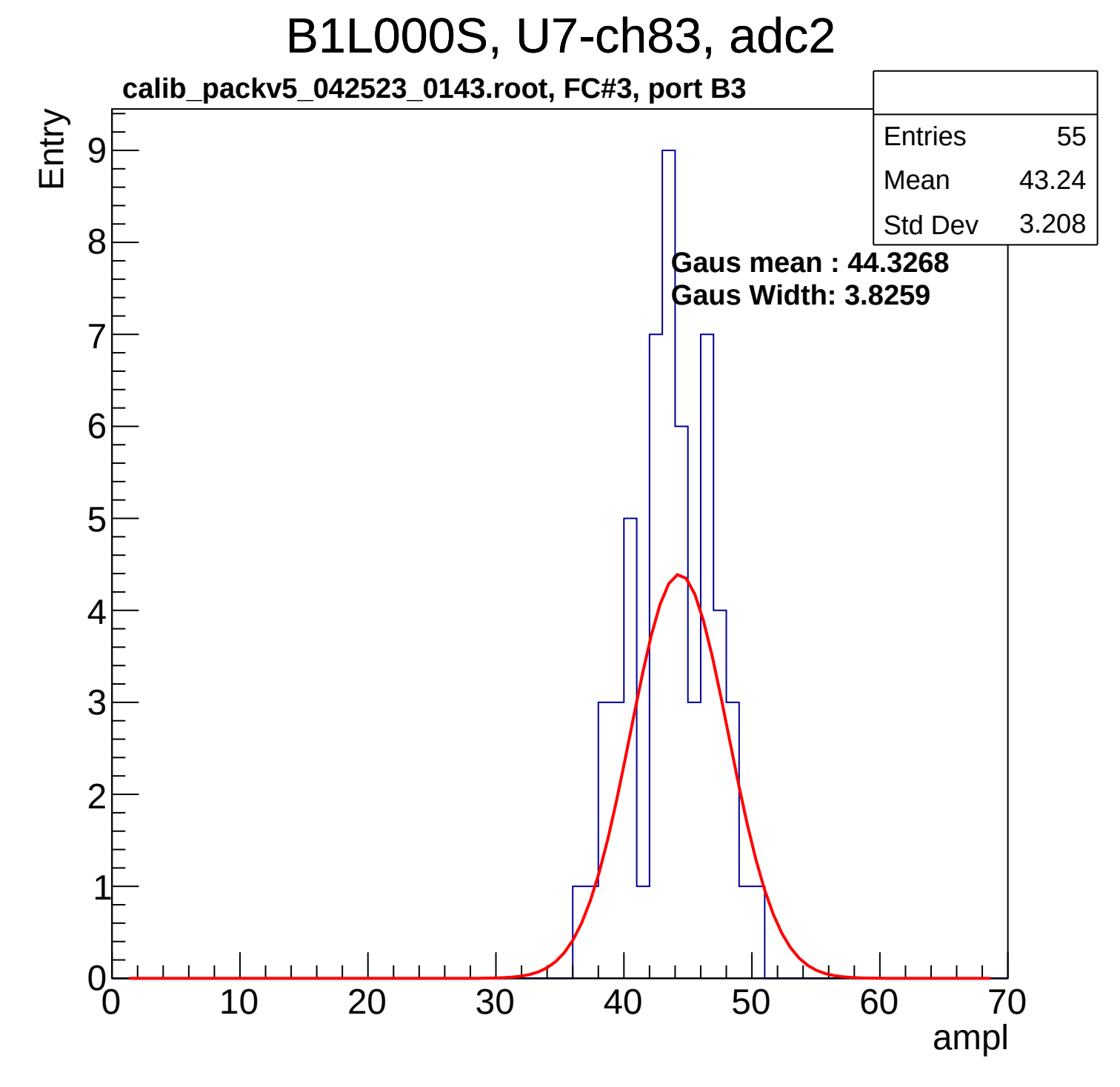

B1L000S, U7-ch82, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch83, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 28.42
Std Dev 4.829
Gaus mean : 29.4627
Gaus Width: 3.9392
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch83, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 36.57
Std Dev 3.499
Gaus mean : 37.0042
Gaus Width: 4.2963
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch83, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 43.24
Std Dev 3.208
Gaus mean : 44.3268
Gaus Width: 3.8259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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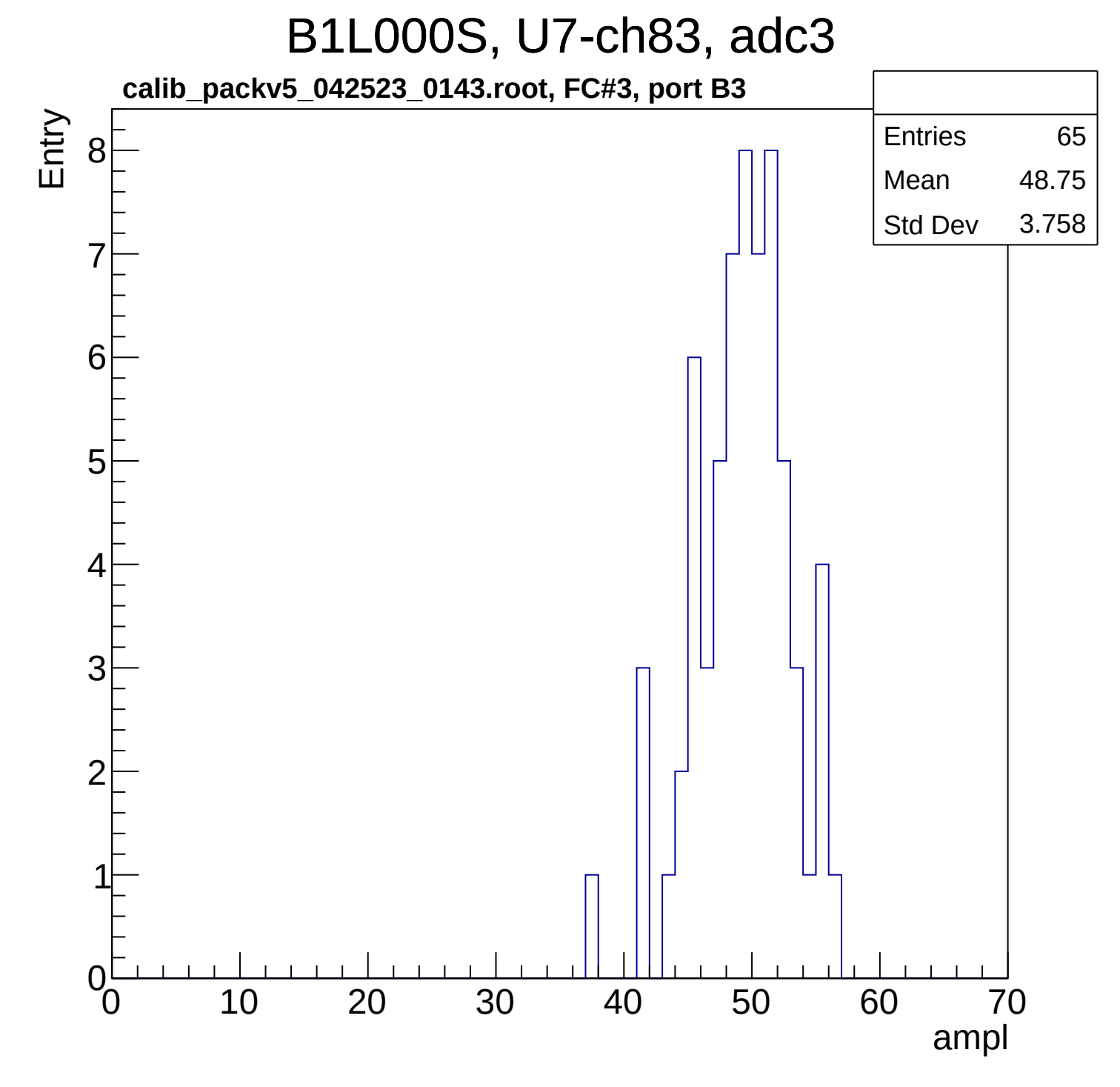
B1L000S, U7-ch83, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.75
Std Dev 3.758
Entry
ampl
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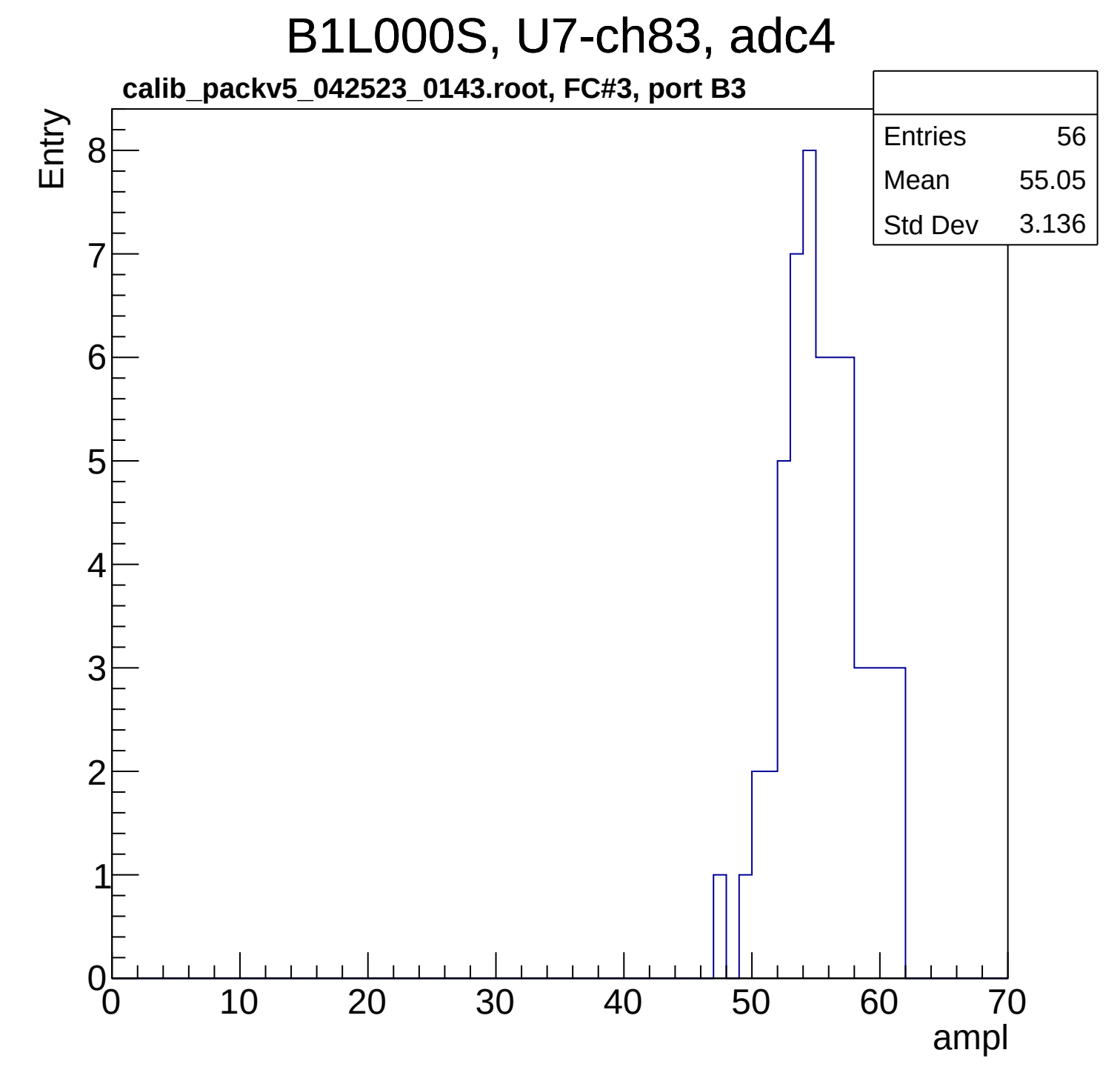
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch83, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 55.05
Std Dev 3.136
Entry
ampl
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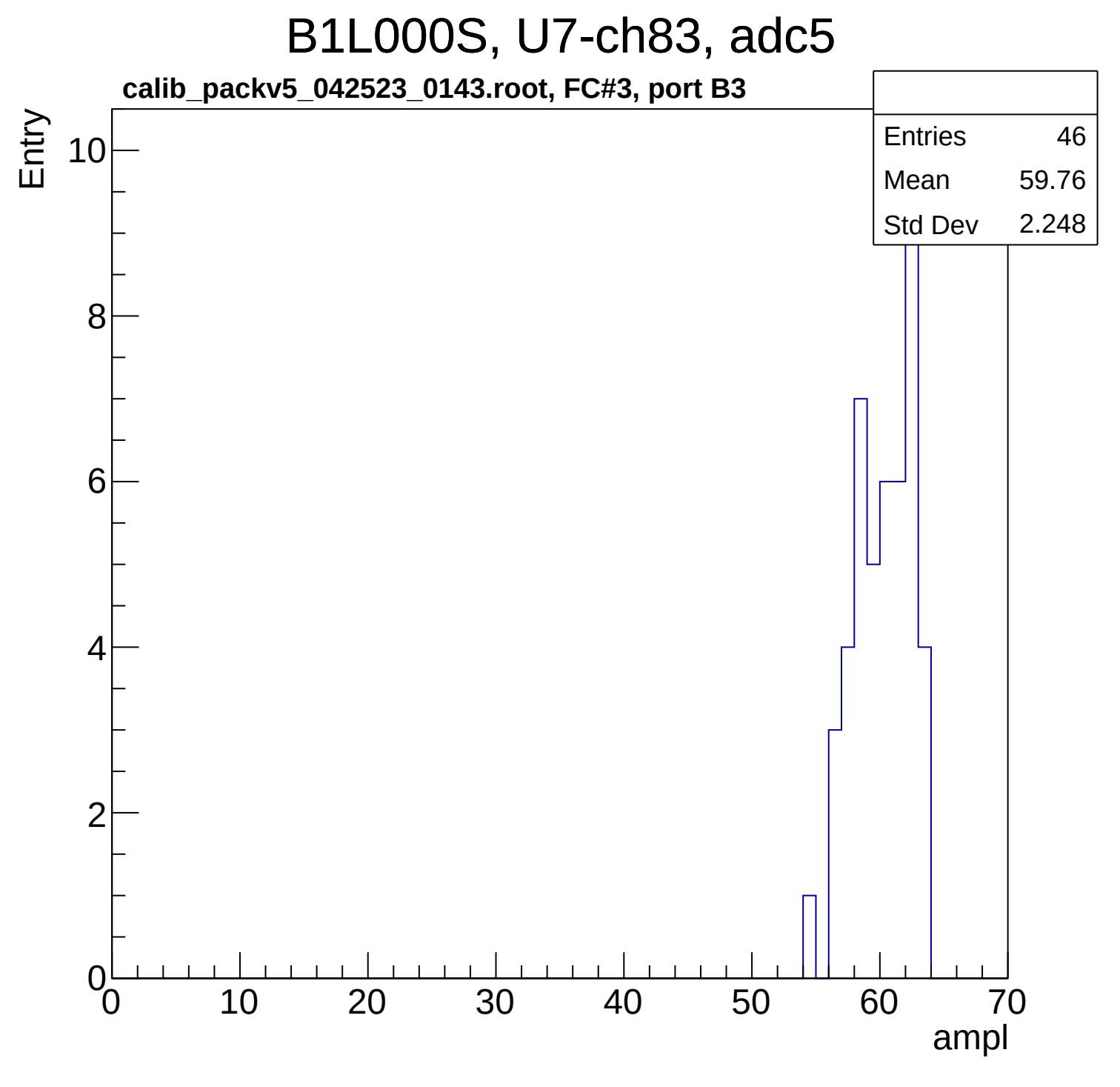
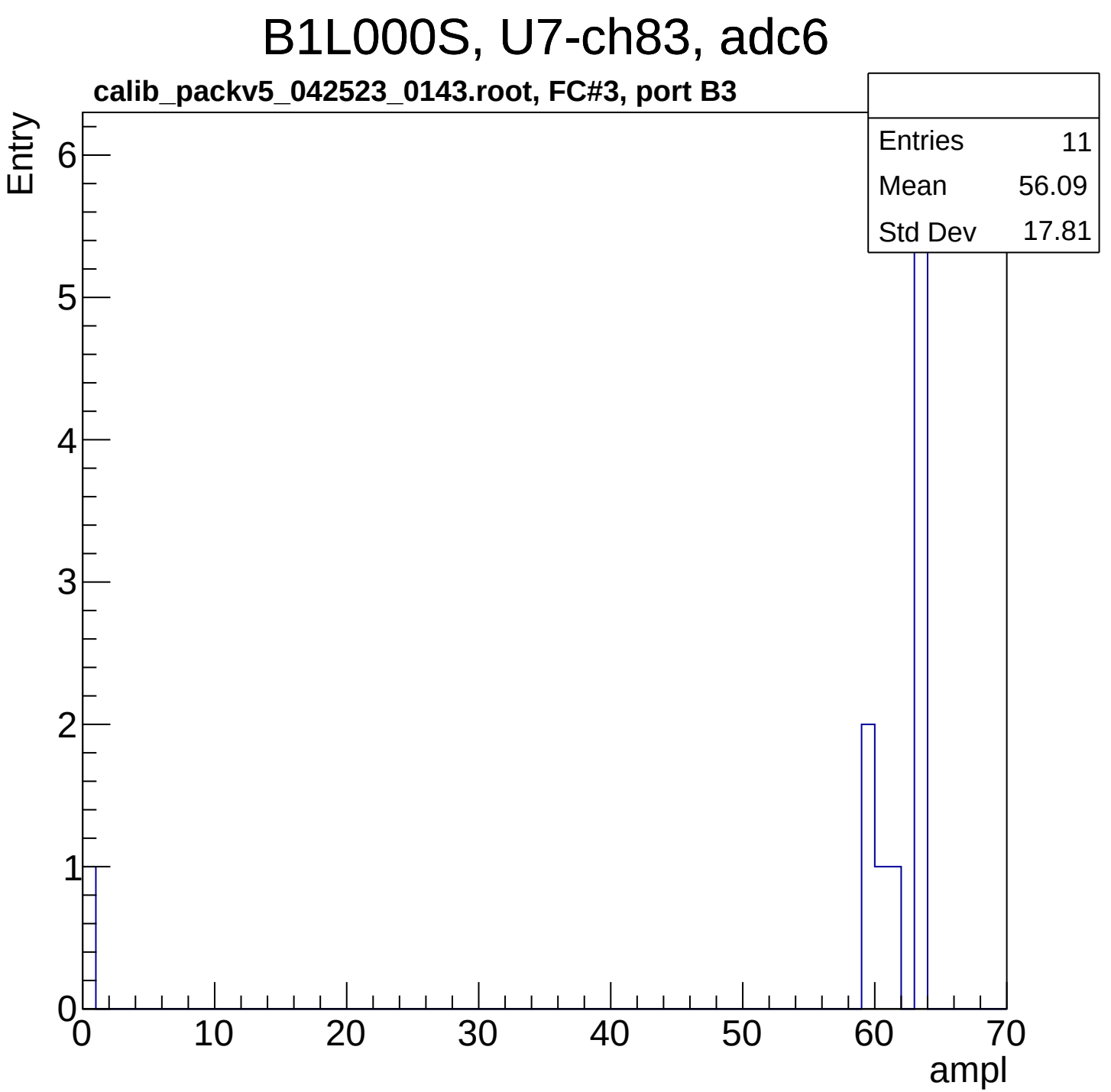
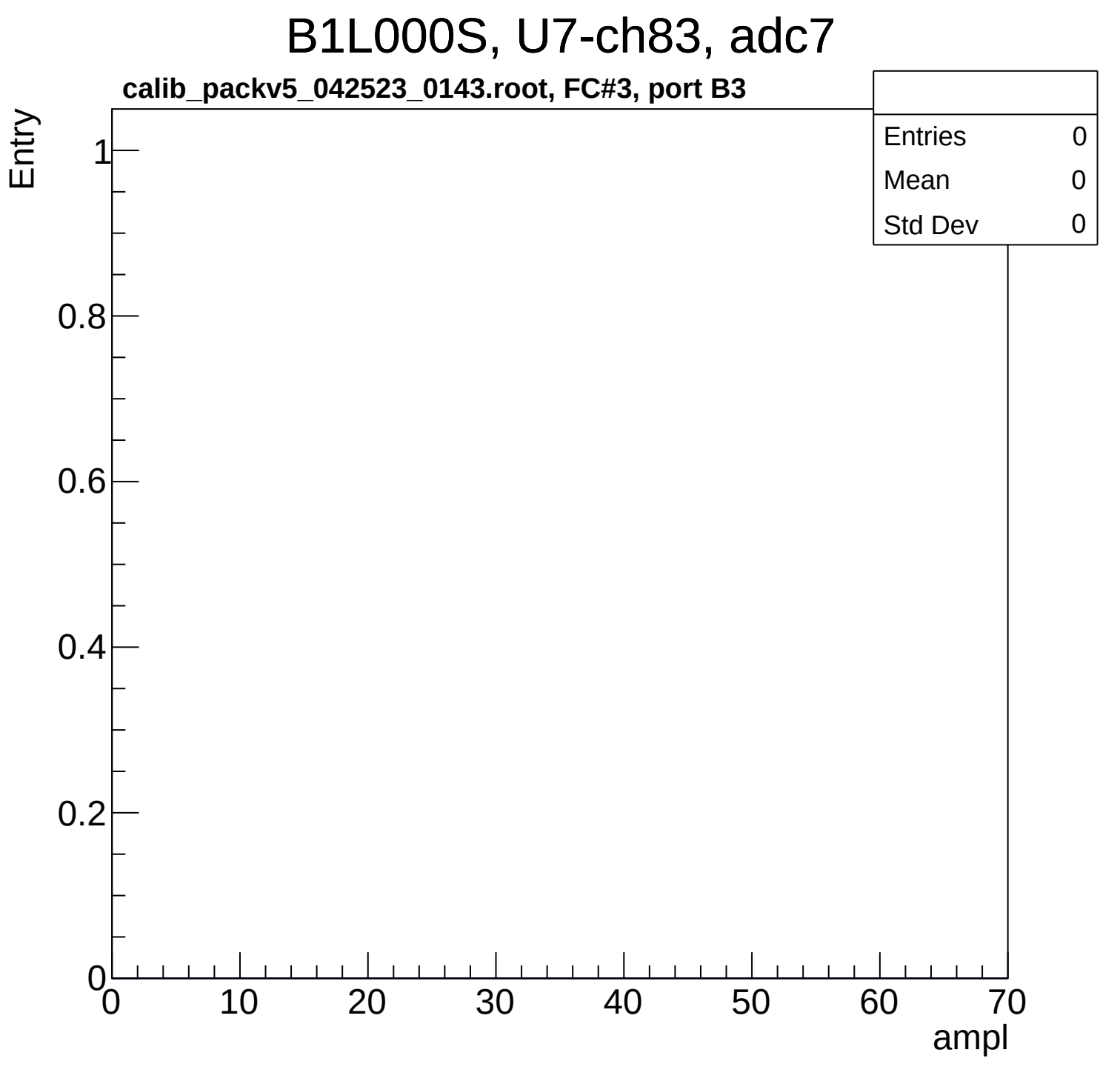
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch83, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 59.76
Std Dev 2.248
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch83, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.09
Std Dev 17.81
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch83, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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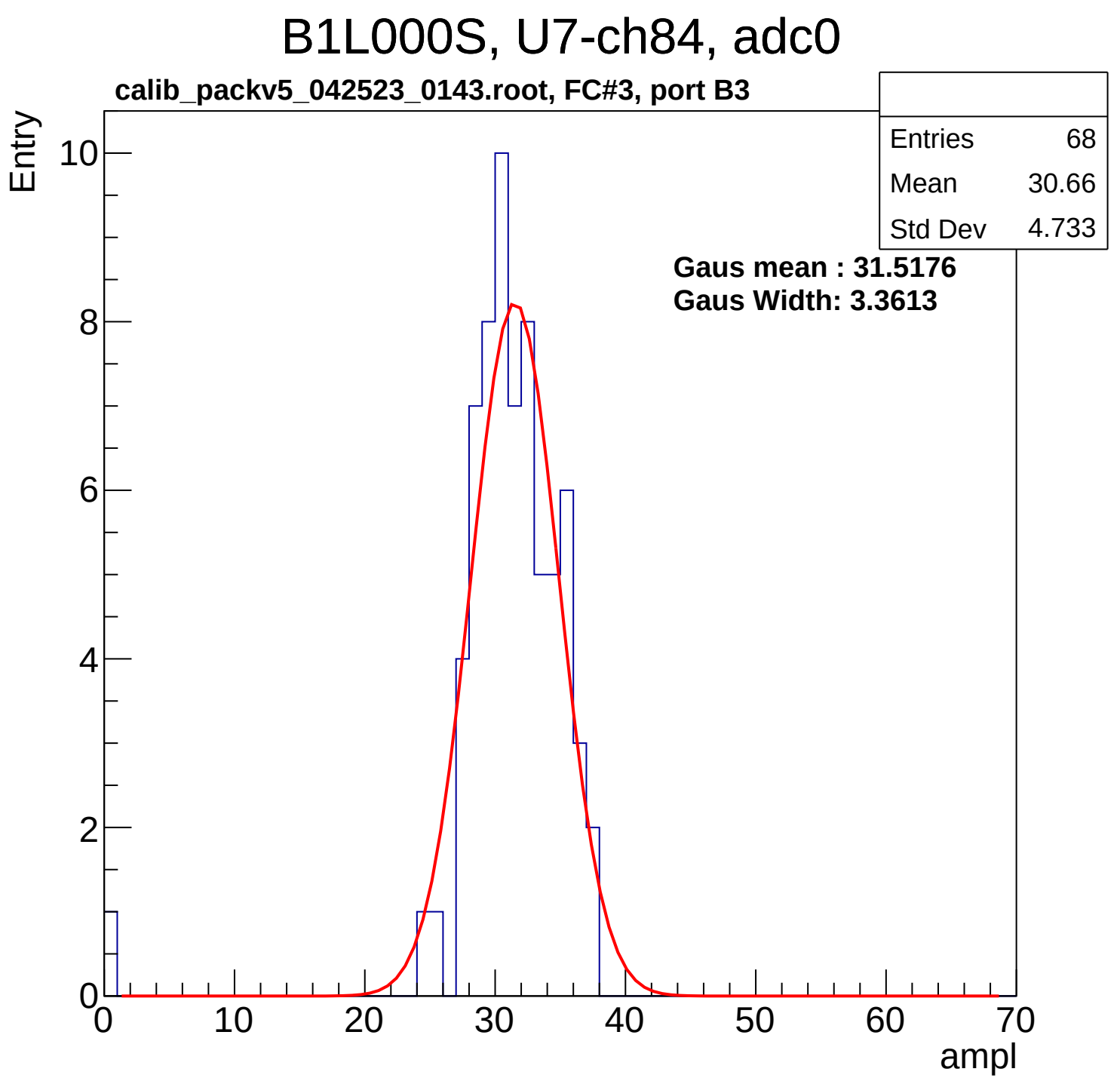
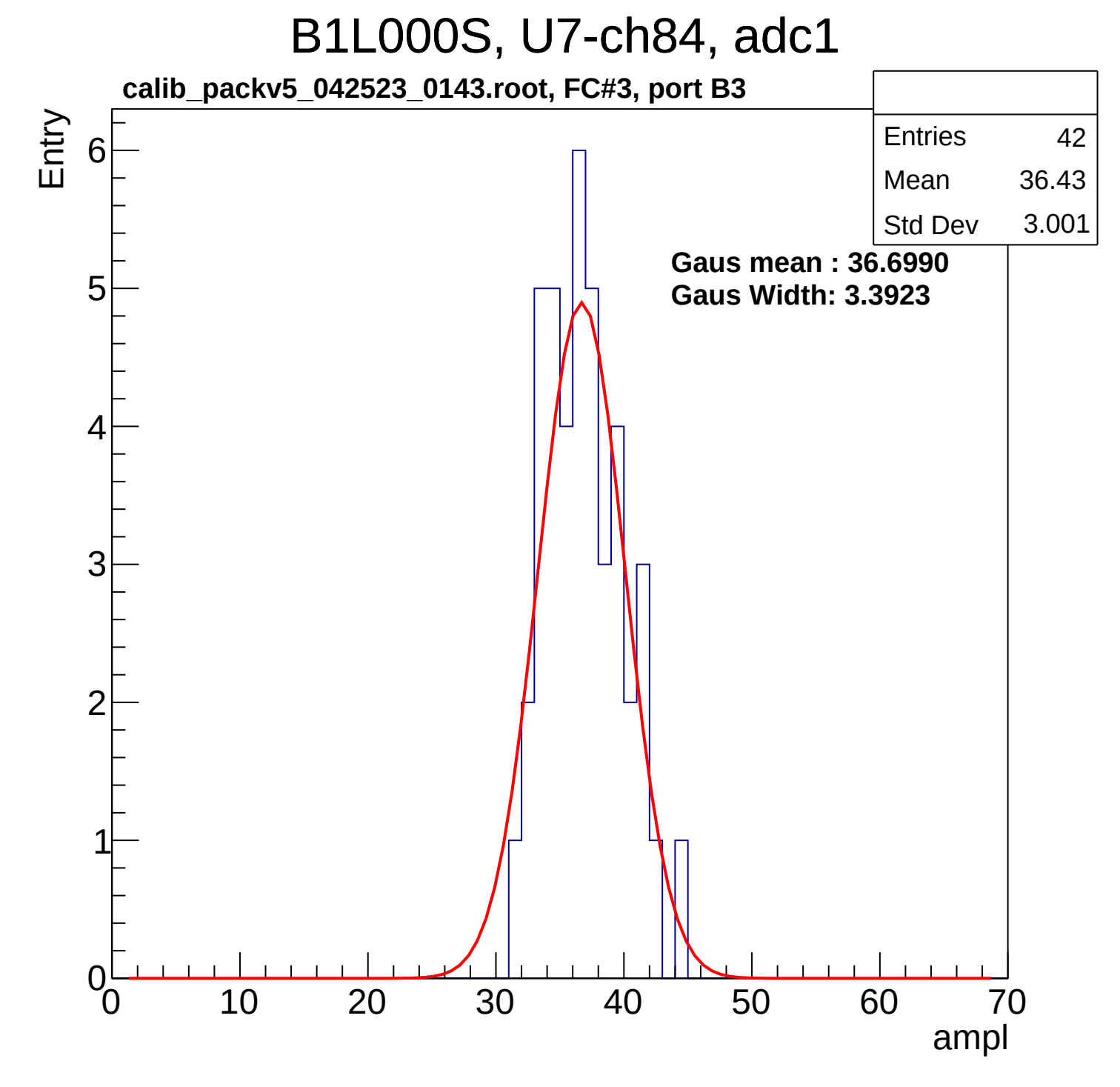
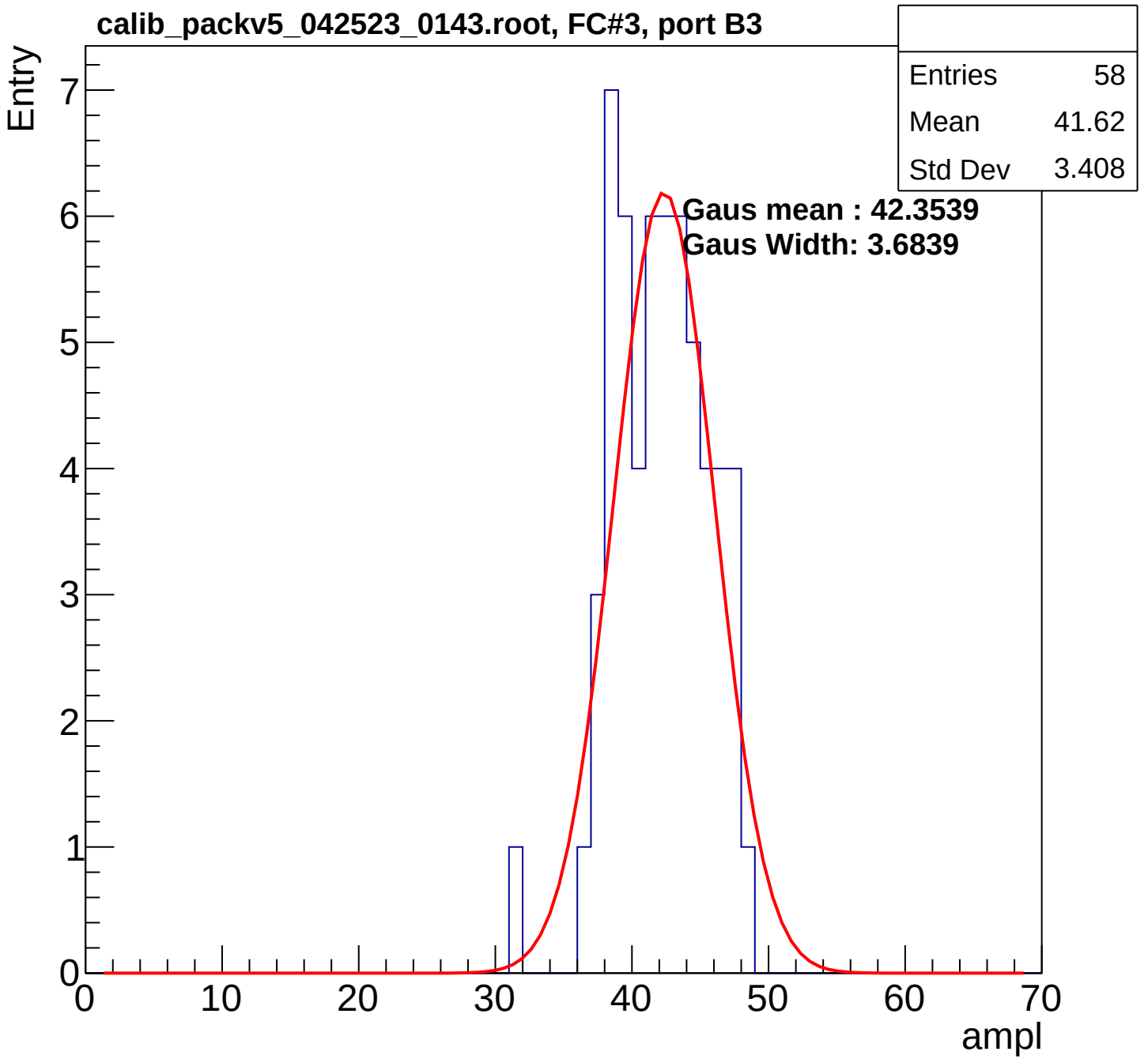
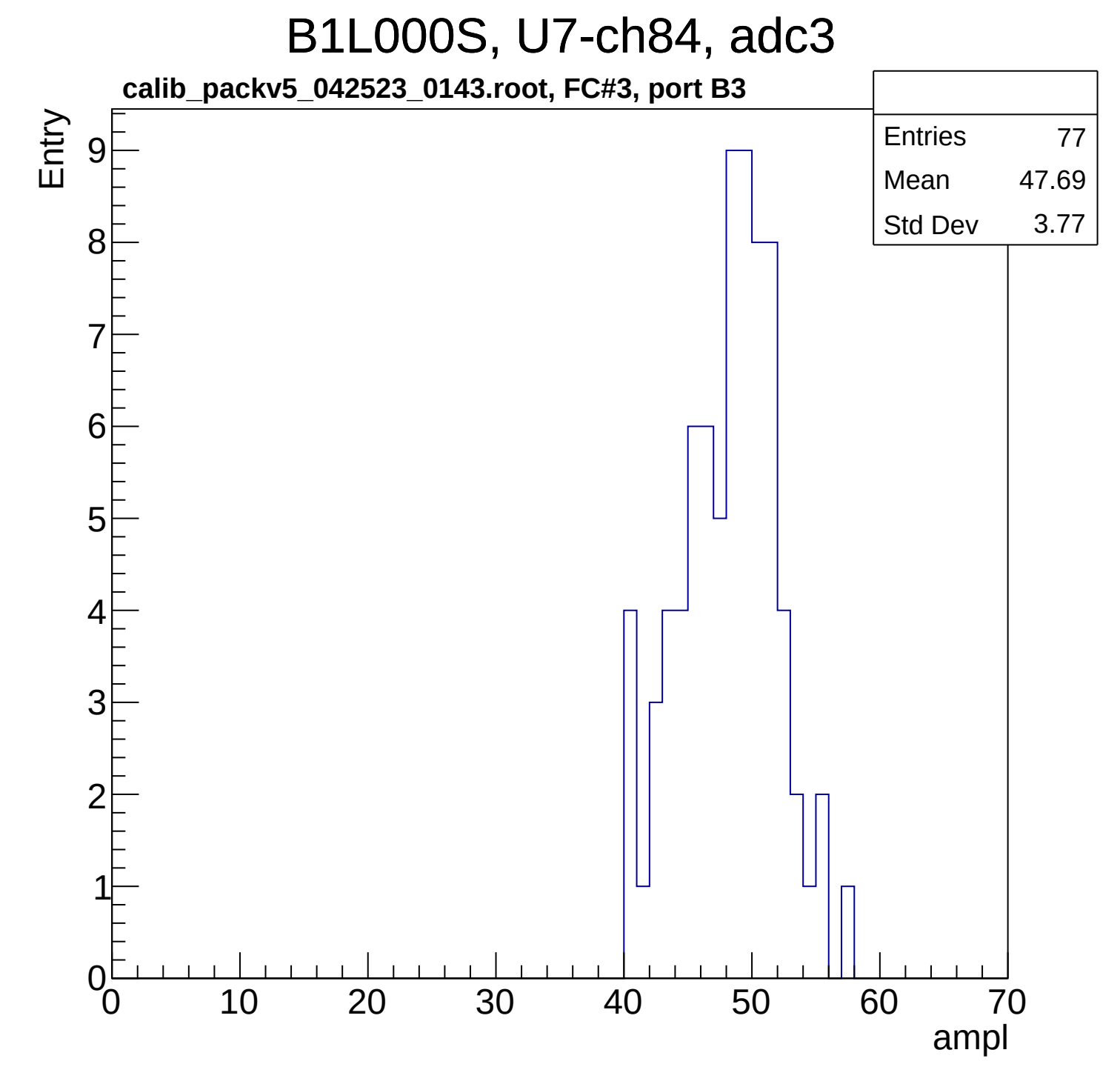
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch84, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 30.66
Std Dev 4.733
Gaus mean : 31.5176
Gaus Width: 3.3613
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch84, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 36.43
Std Dev 3.001
Gaus mean : 36.6990
Gaus Width: 3.3923
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U7-ch84, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 41.62
Std Dev 3.408
Gaus mean : 42.3539
Gaus Width: 3.6839
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch84, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 47.69
Std Dev 3.77
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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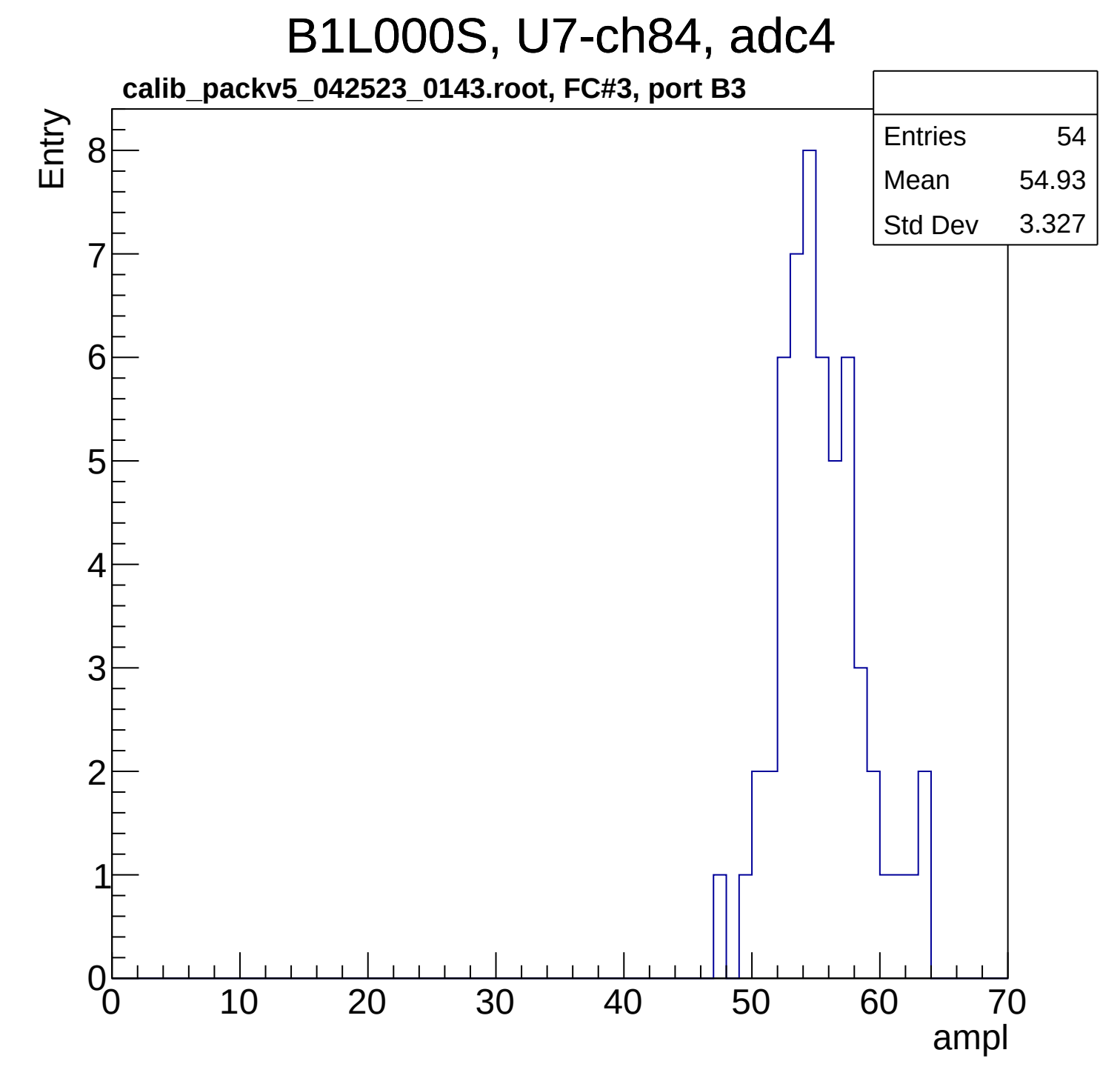
B1L000S, U7-ch84, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 54.93
Std Dev 3.327
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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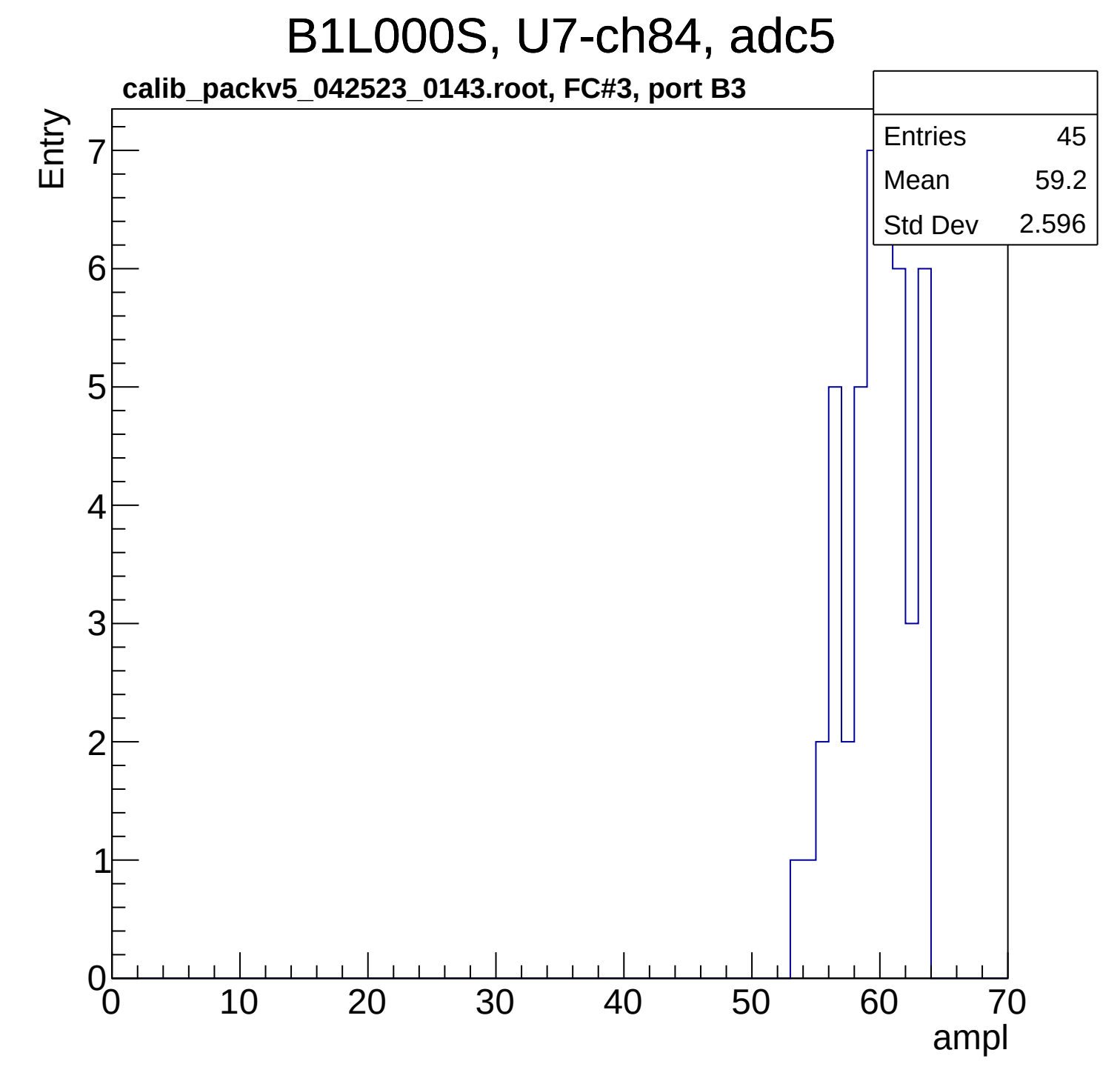
B1L000S, U7-ch84, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 59.2
Std Dev 2.596
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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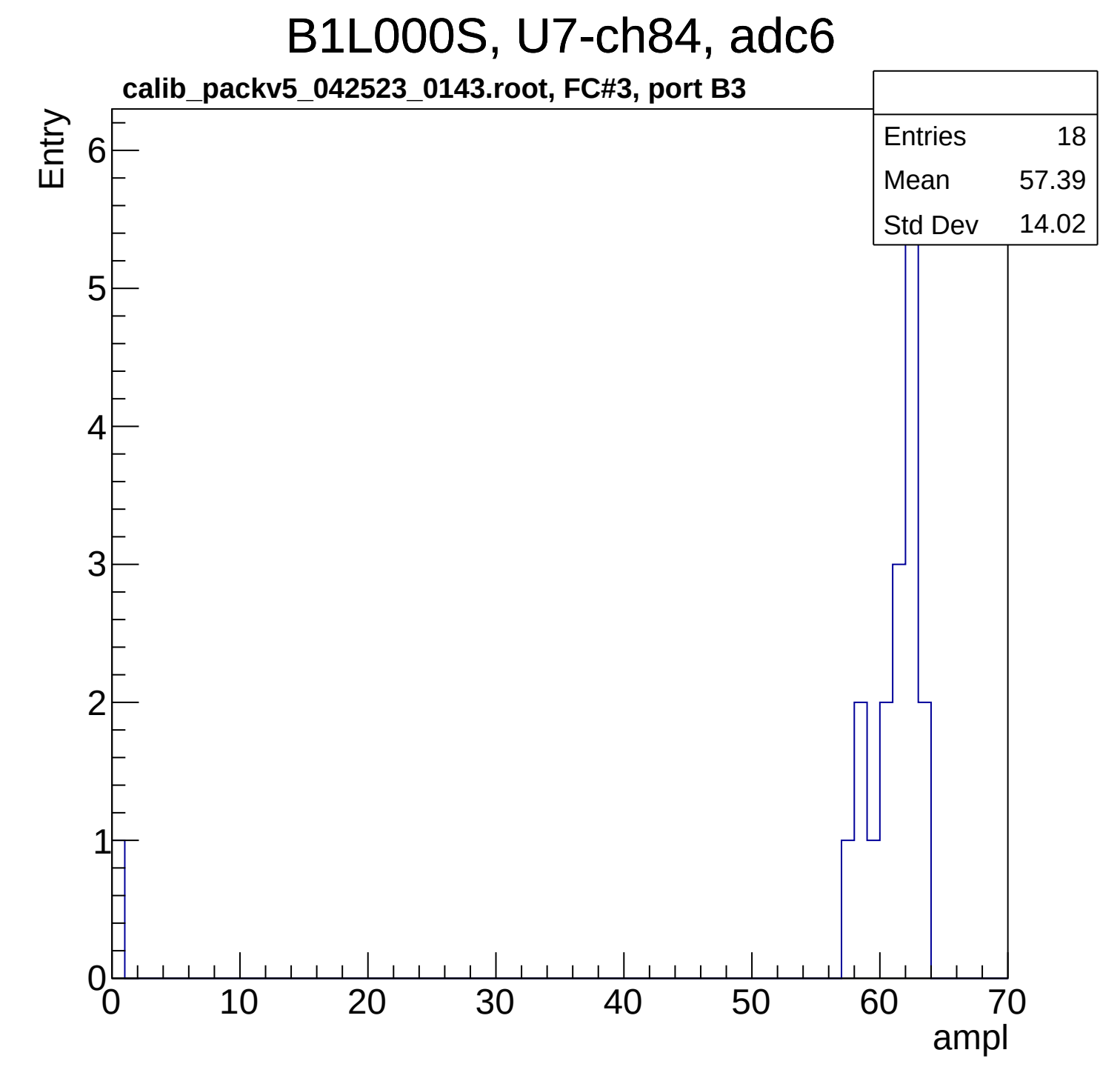
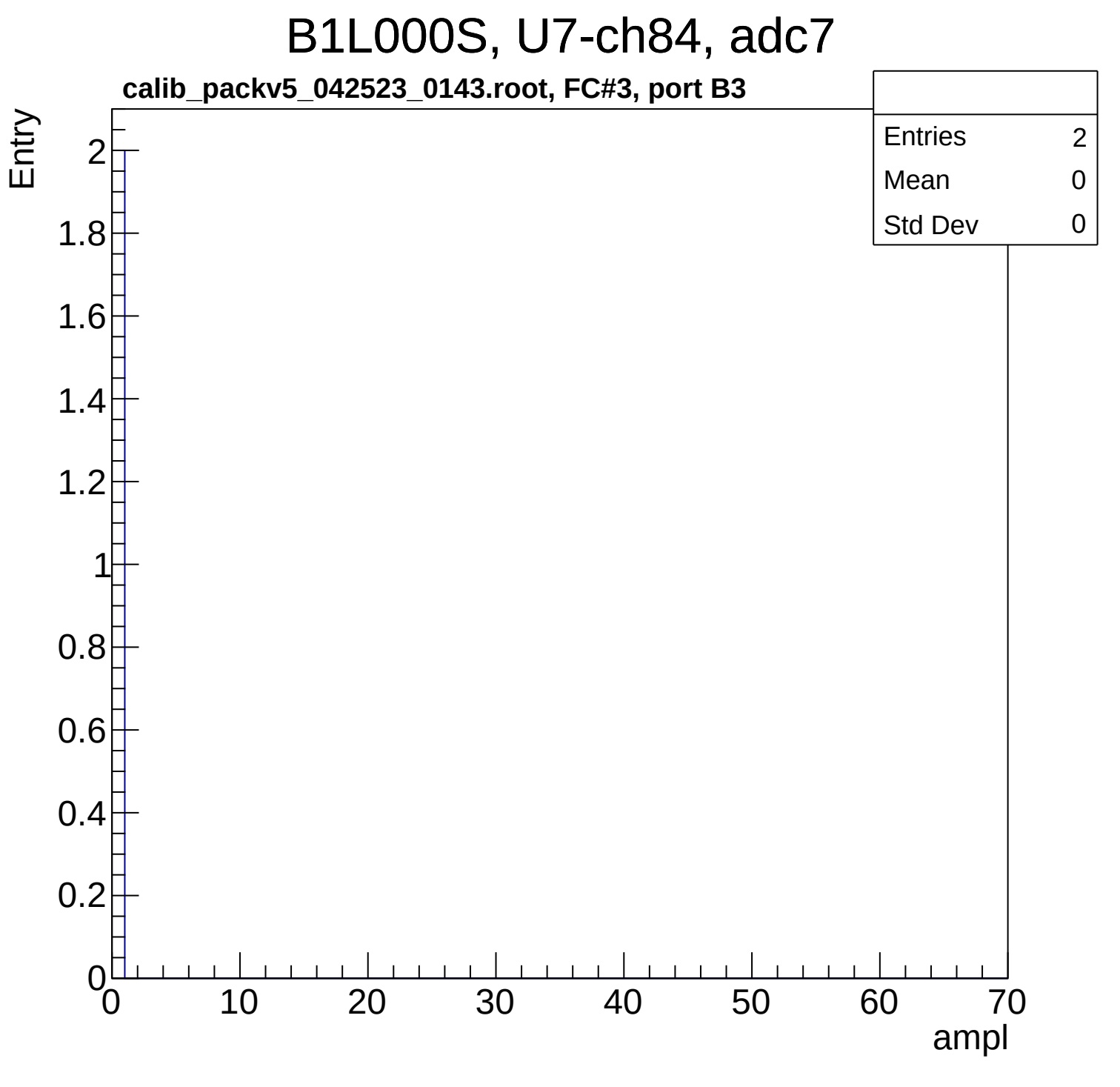

B1L000S, U7-ch84, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 18
Mean 57.39
Std Dev 14.02
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch84, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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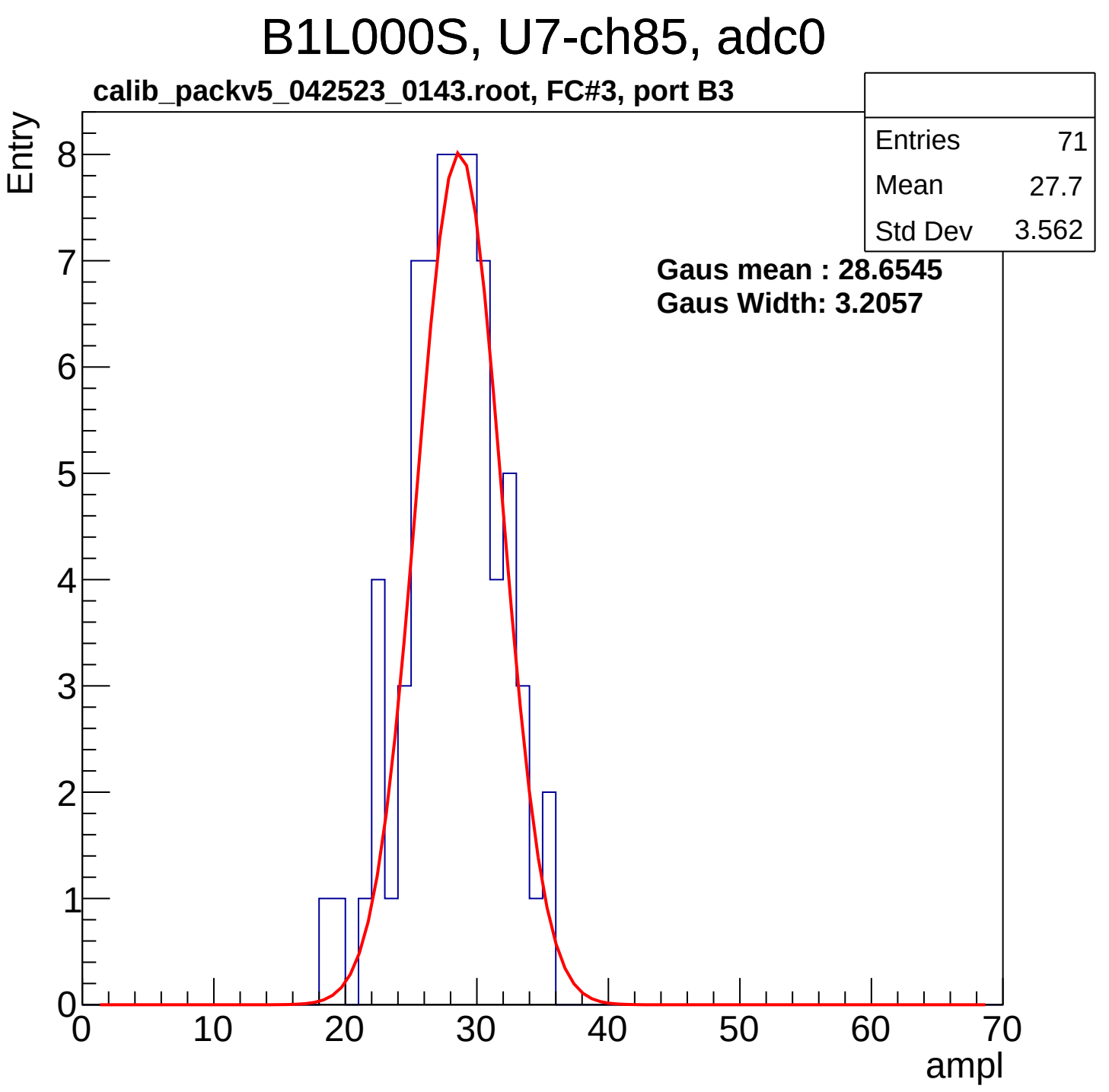
B1L000S, U7-ch85, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 27.7
Std Dev 3.562
Gaus mean : 28.6545
Gaus Width: 3.2057
Entry
ampl
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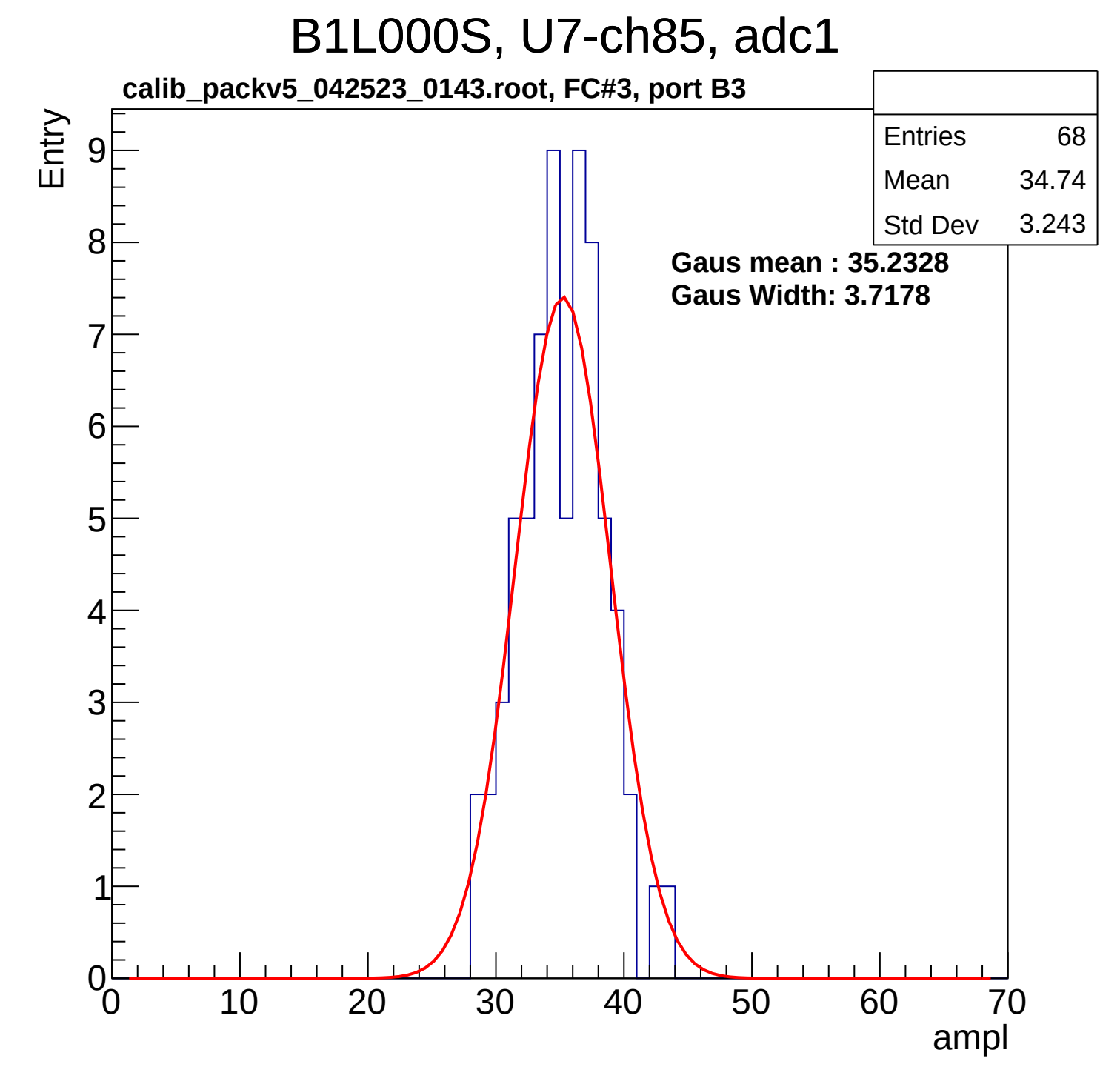
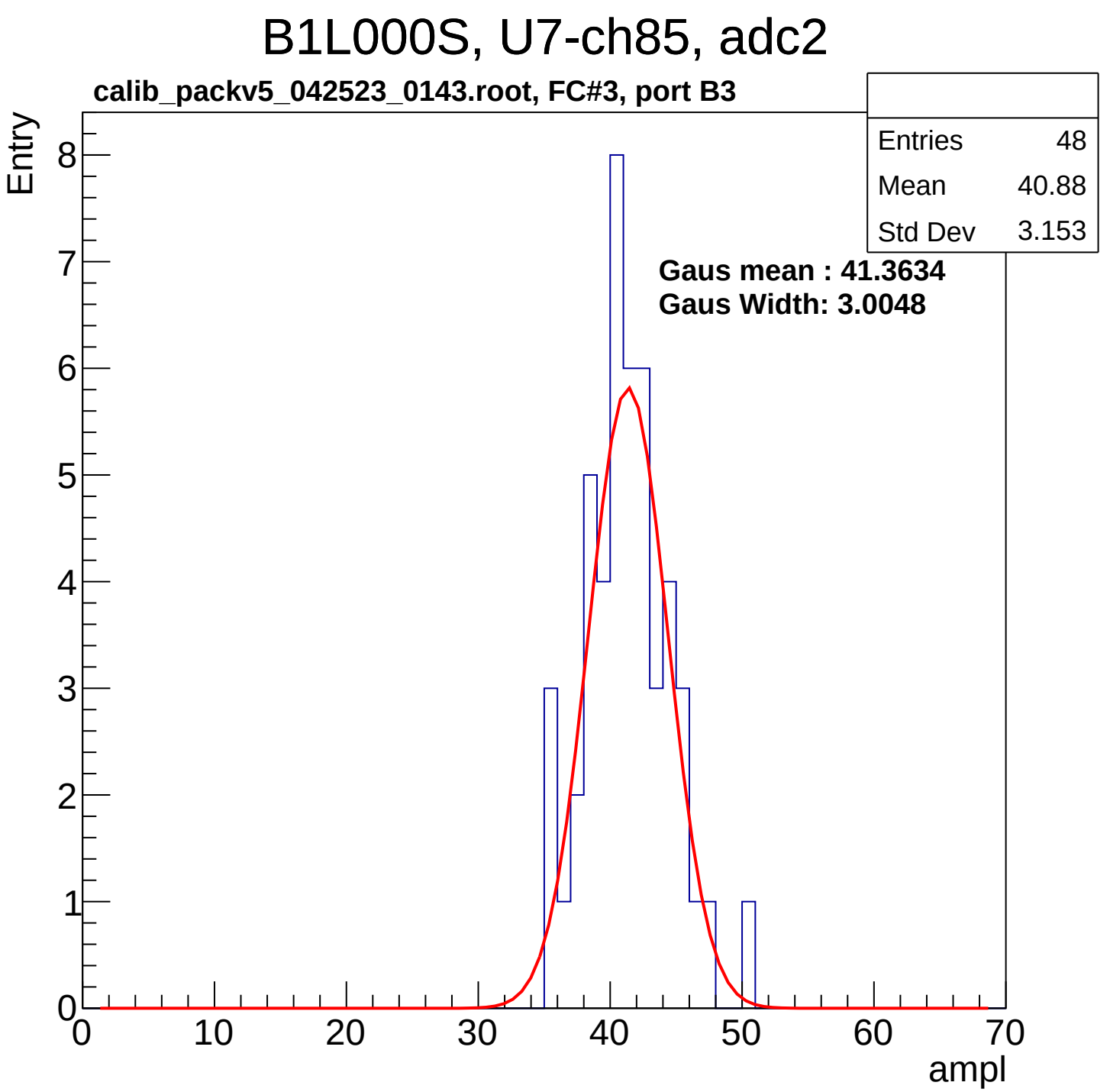
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch85, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 34.74
Std Dev 3.243
Gaus mean : 35.2328
Gaus Width: 3.7178
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch85, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 40.88
Std Dev 3.153
Gaus mean : 41.3634
Gaus Width: 3.0048
Entry
ampl
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2
3
4
5
6
7
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0
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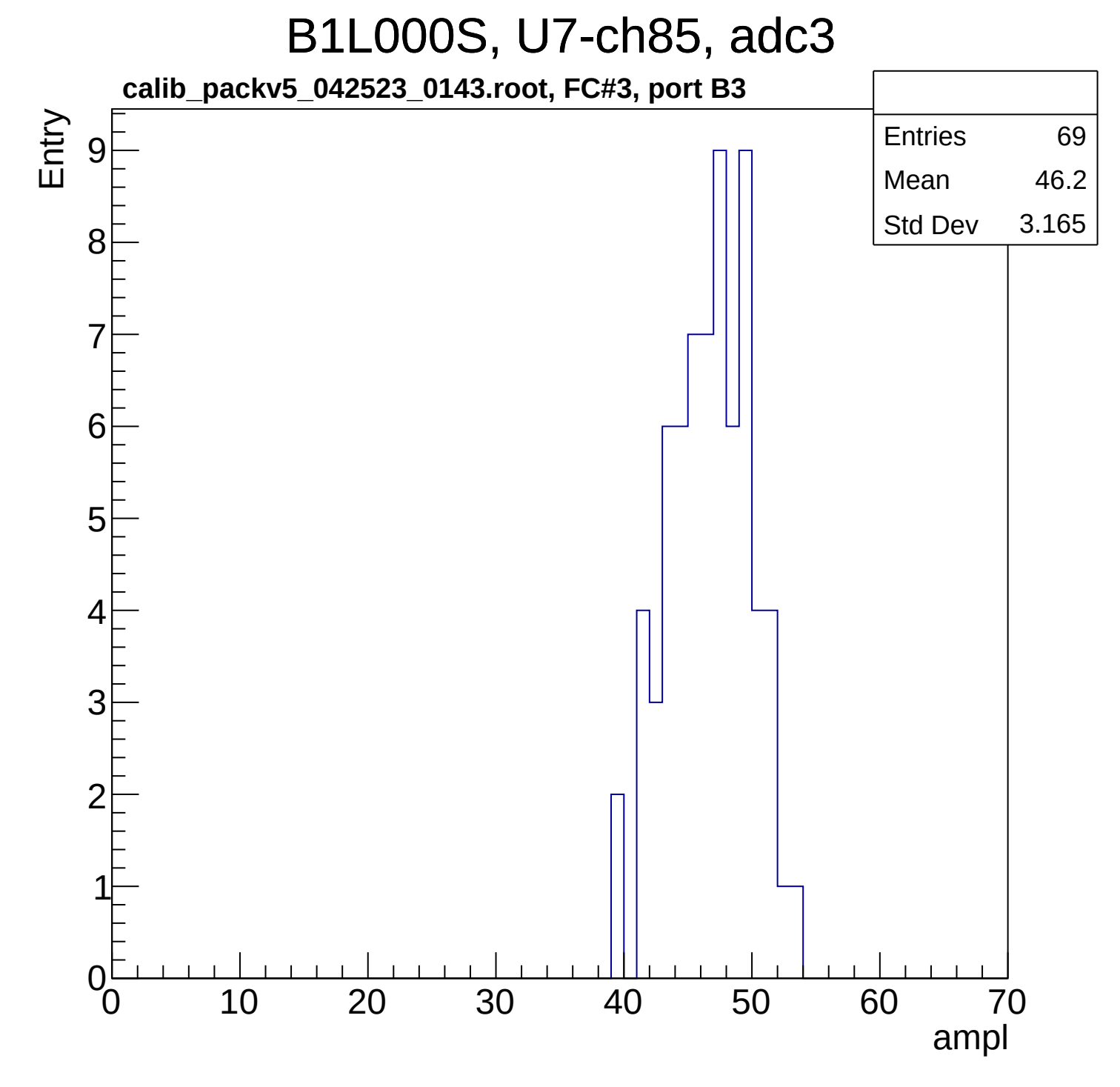
B1L000S, U7-ch85, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 46.2
Std Dev 3.165
Entry
ampl
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3
4
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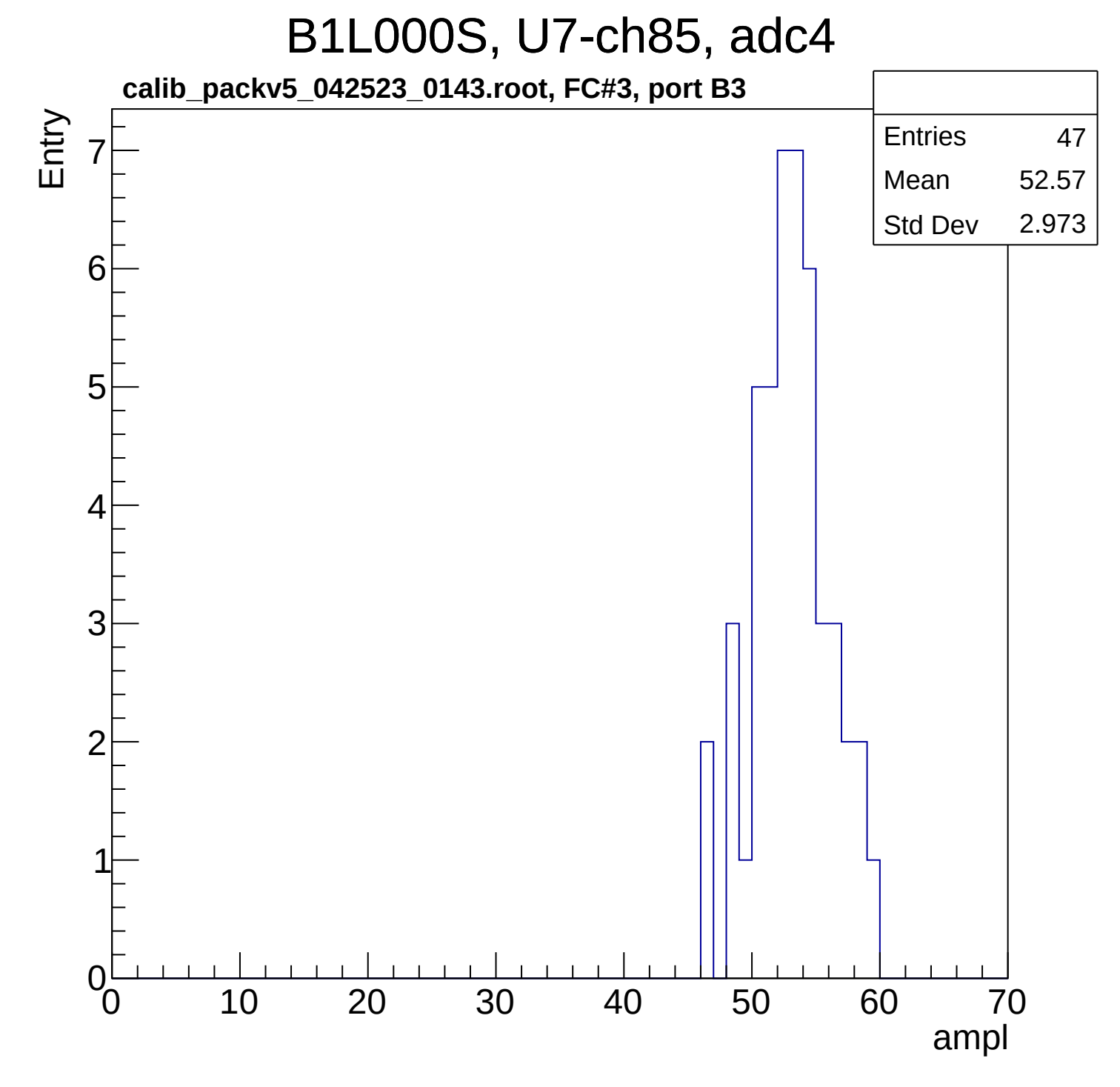
B1L000S, U7-ch85, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 52.57
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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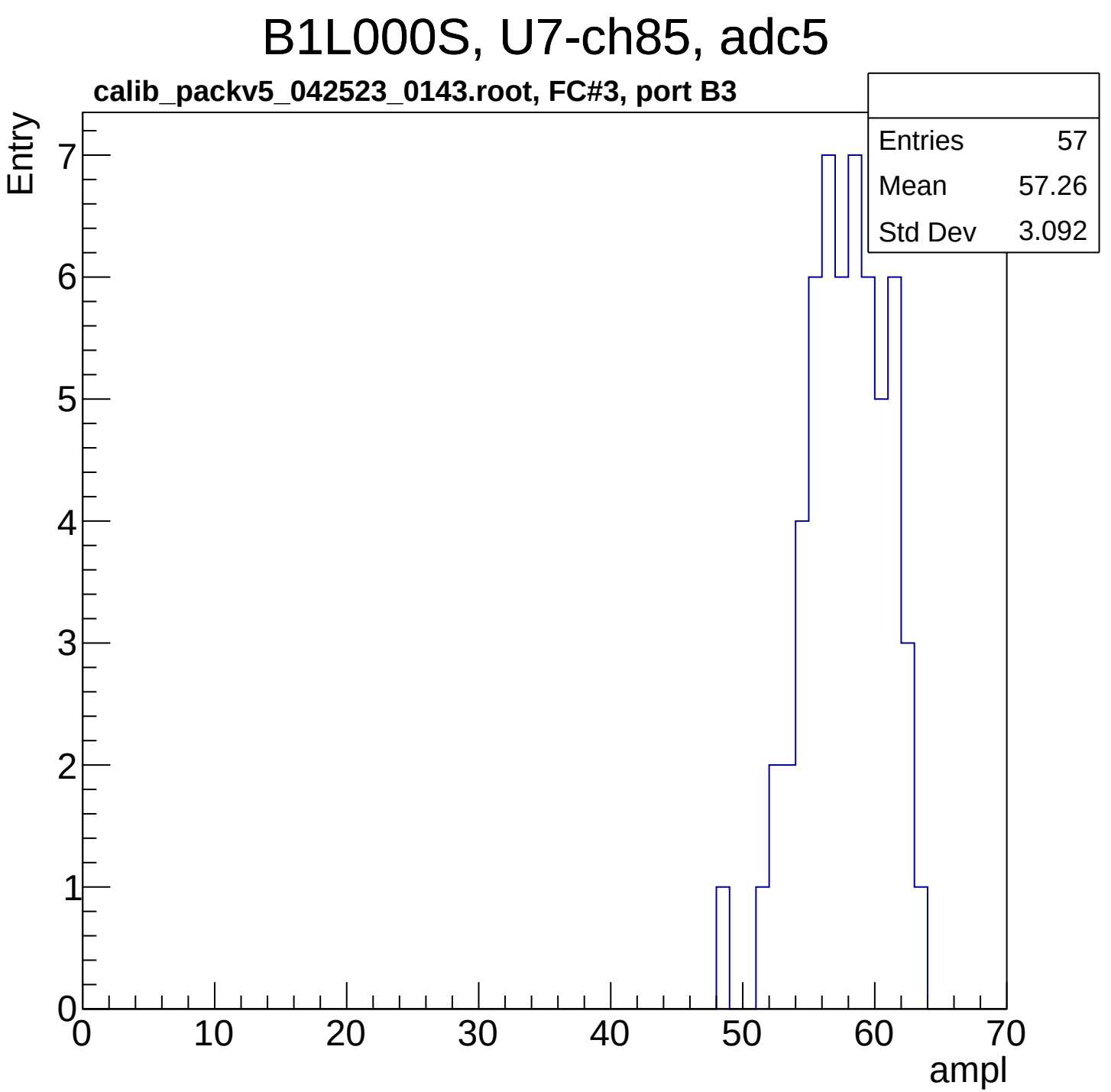
B1L000S, U7-ch85, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 57.26
Std Dev 3.092
Entry
ampl
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1
2
3
4
5
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0
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60
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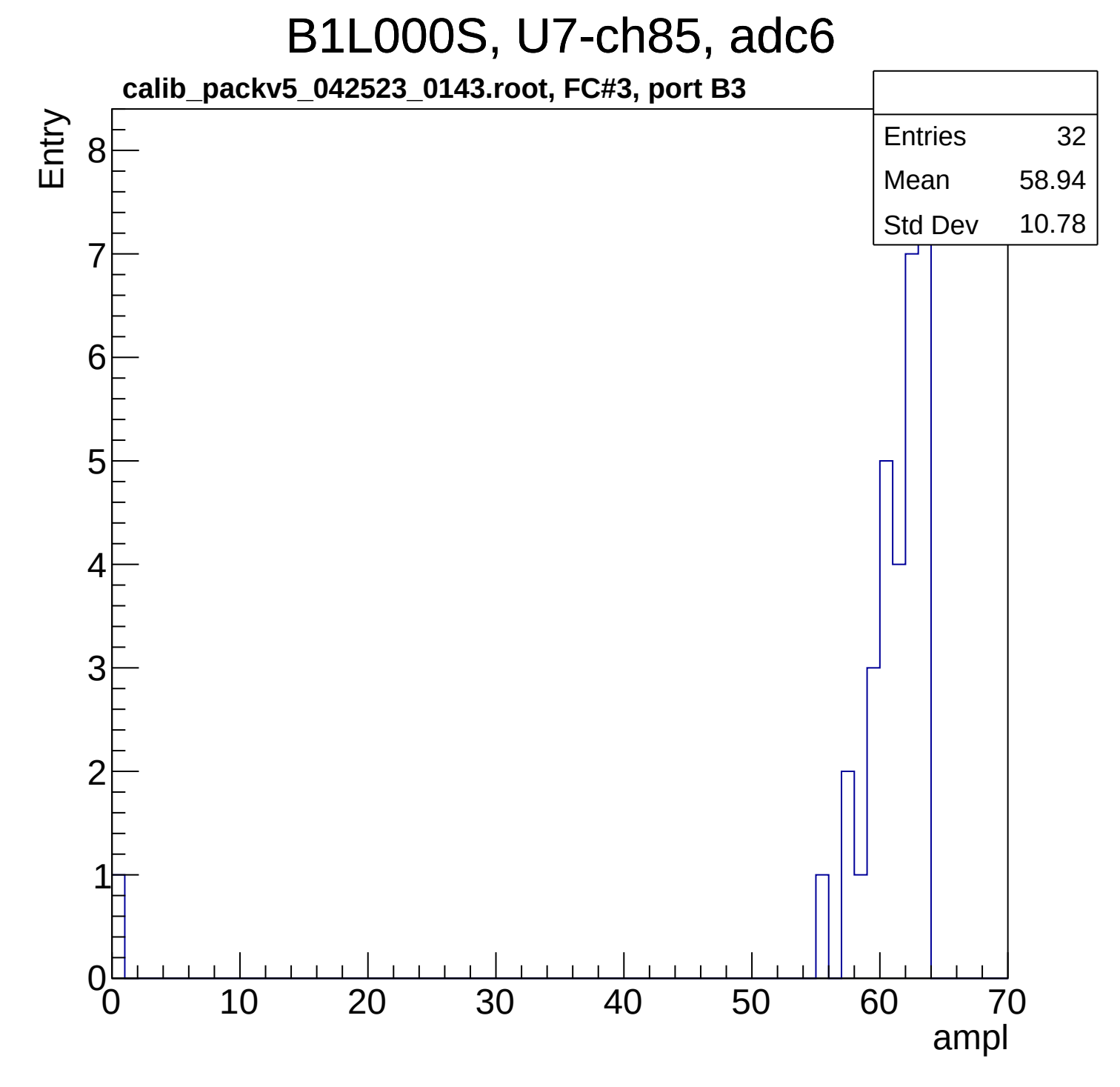
B1L000S, U7-ch85, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 58.94
Std Dev 10.78
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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60
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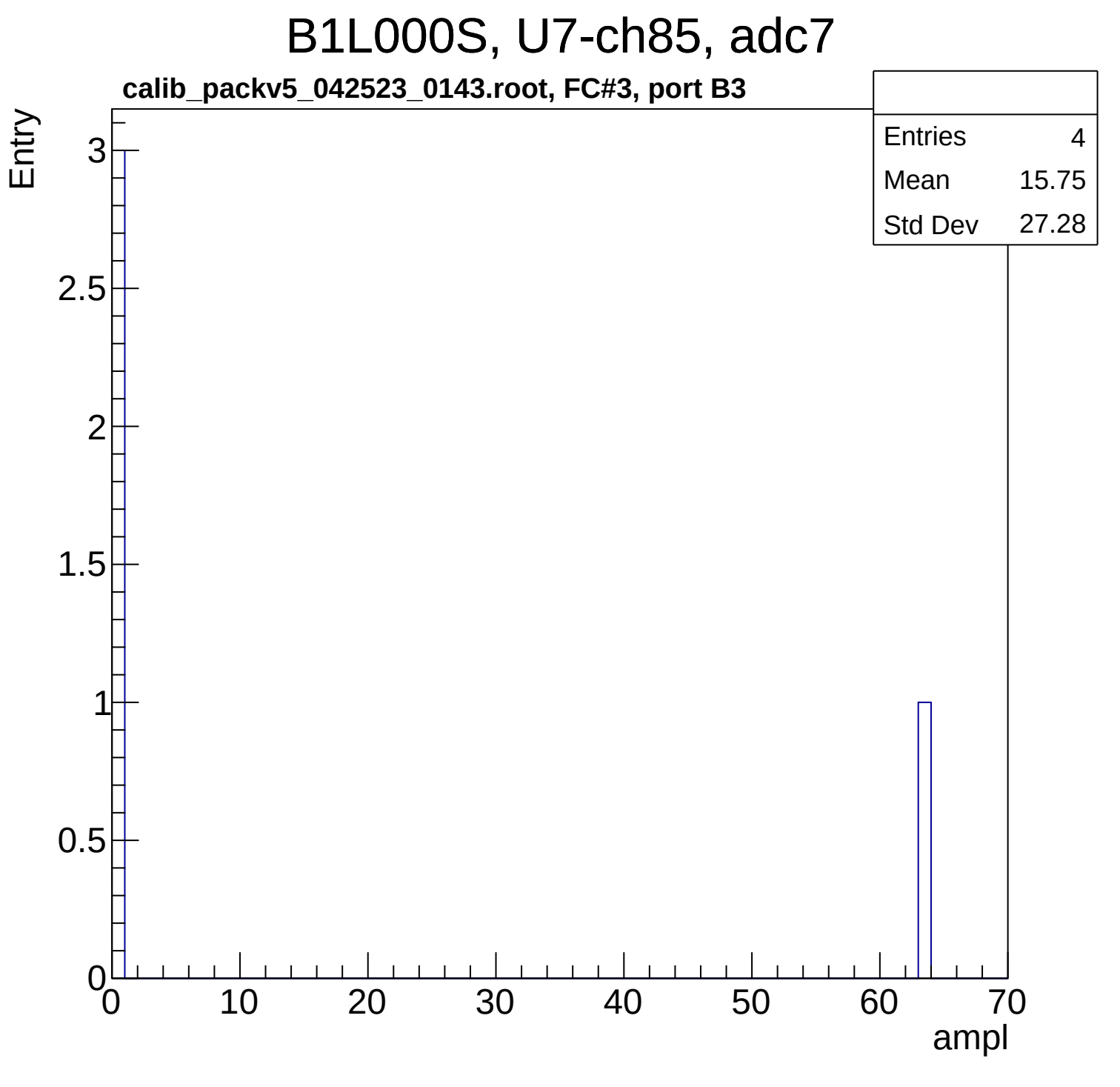
B1L000S, U7-ch85, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 15.75
Std Dev 27.28
Entry
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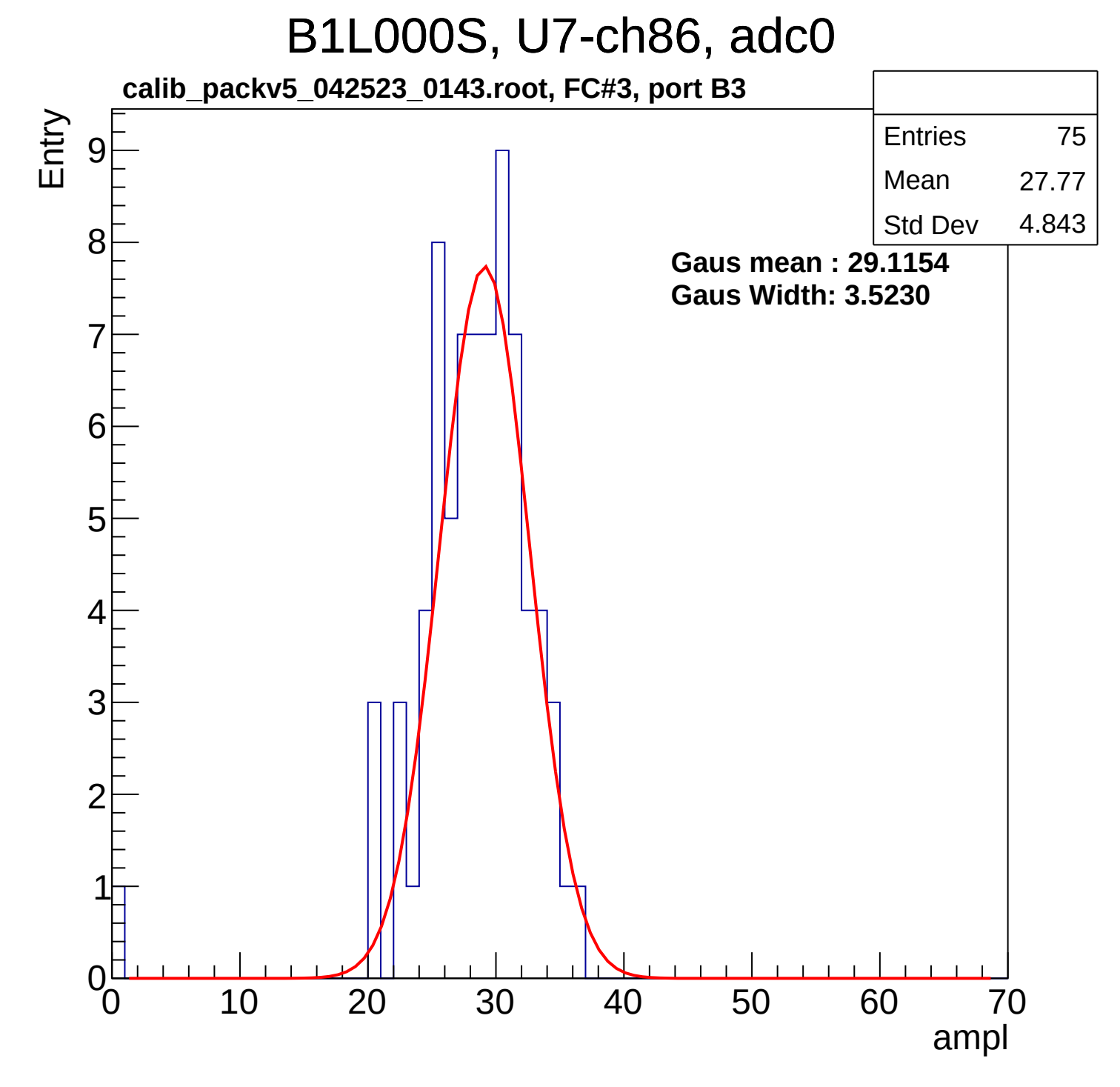
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch86, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 27.77
Std Dev 4.843
Gaus mean : 29.1154
Gaus Width: 3.5230
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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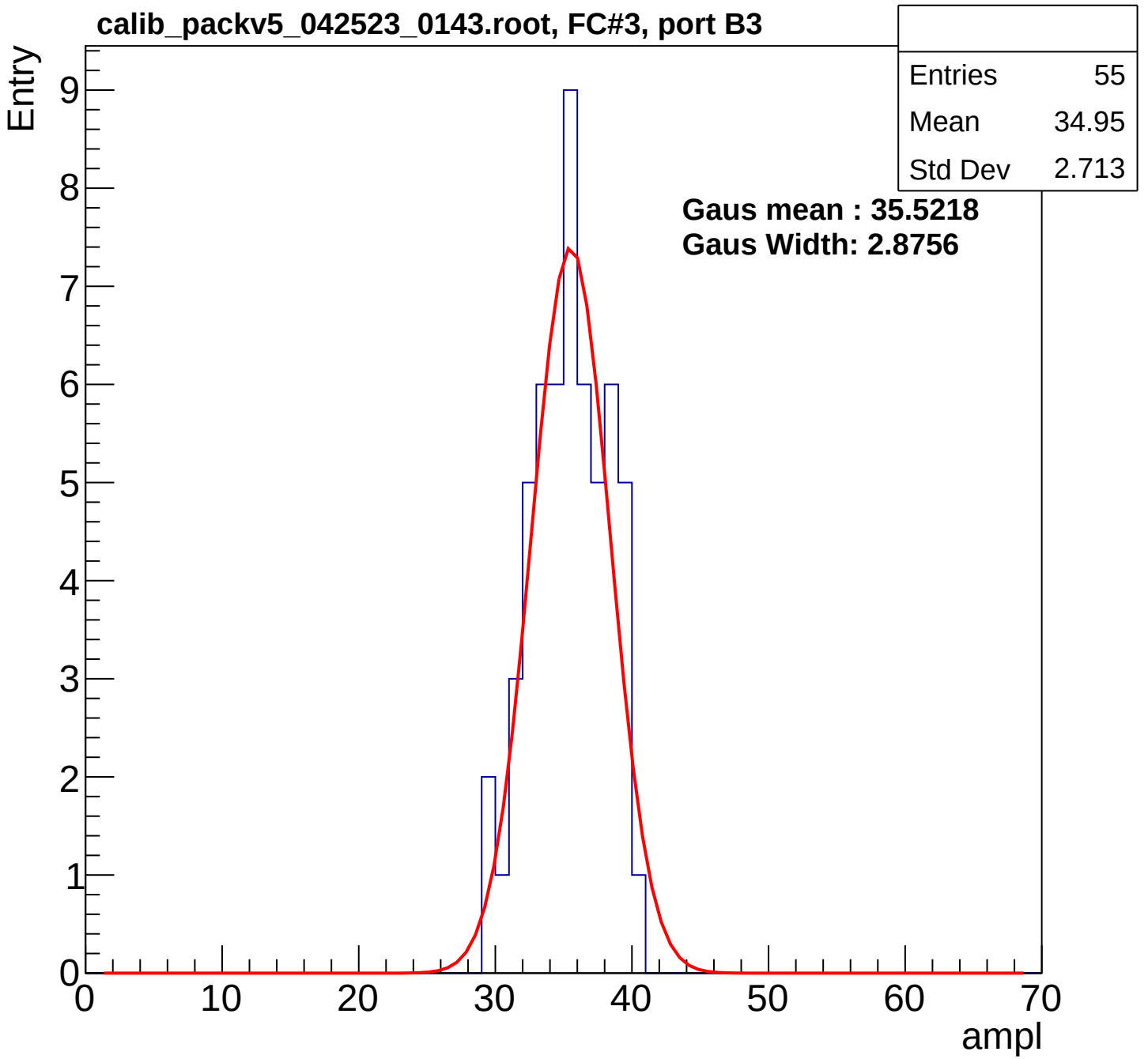
B1L000S, U7-ch86, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 34.95
Std Dev 2.713
Gaus mean : 35.5218
Gaus Width: 2.8756
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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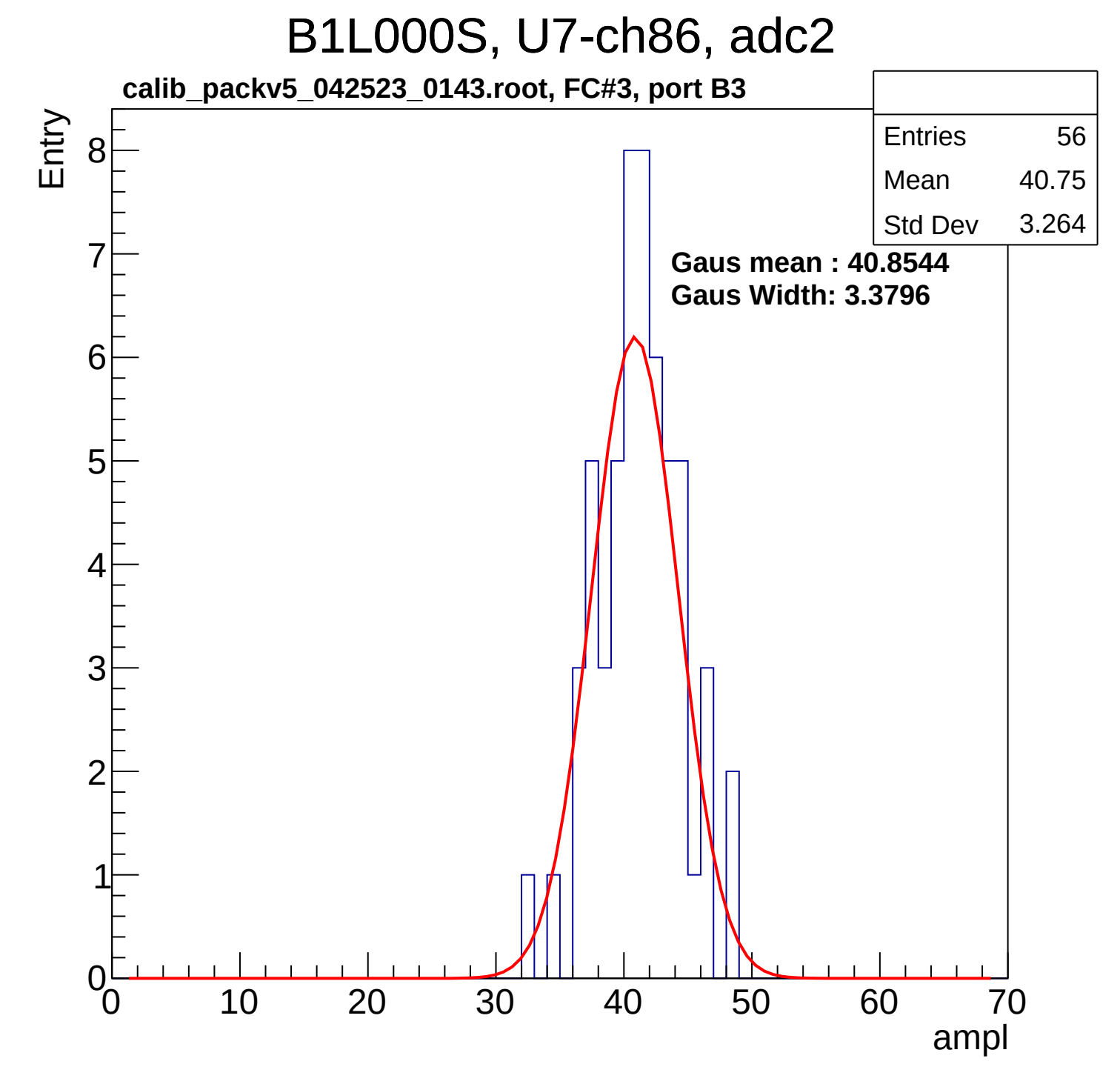
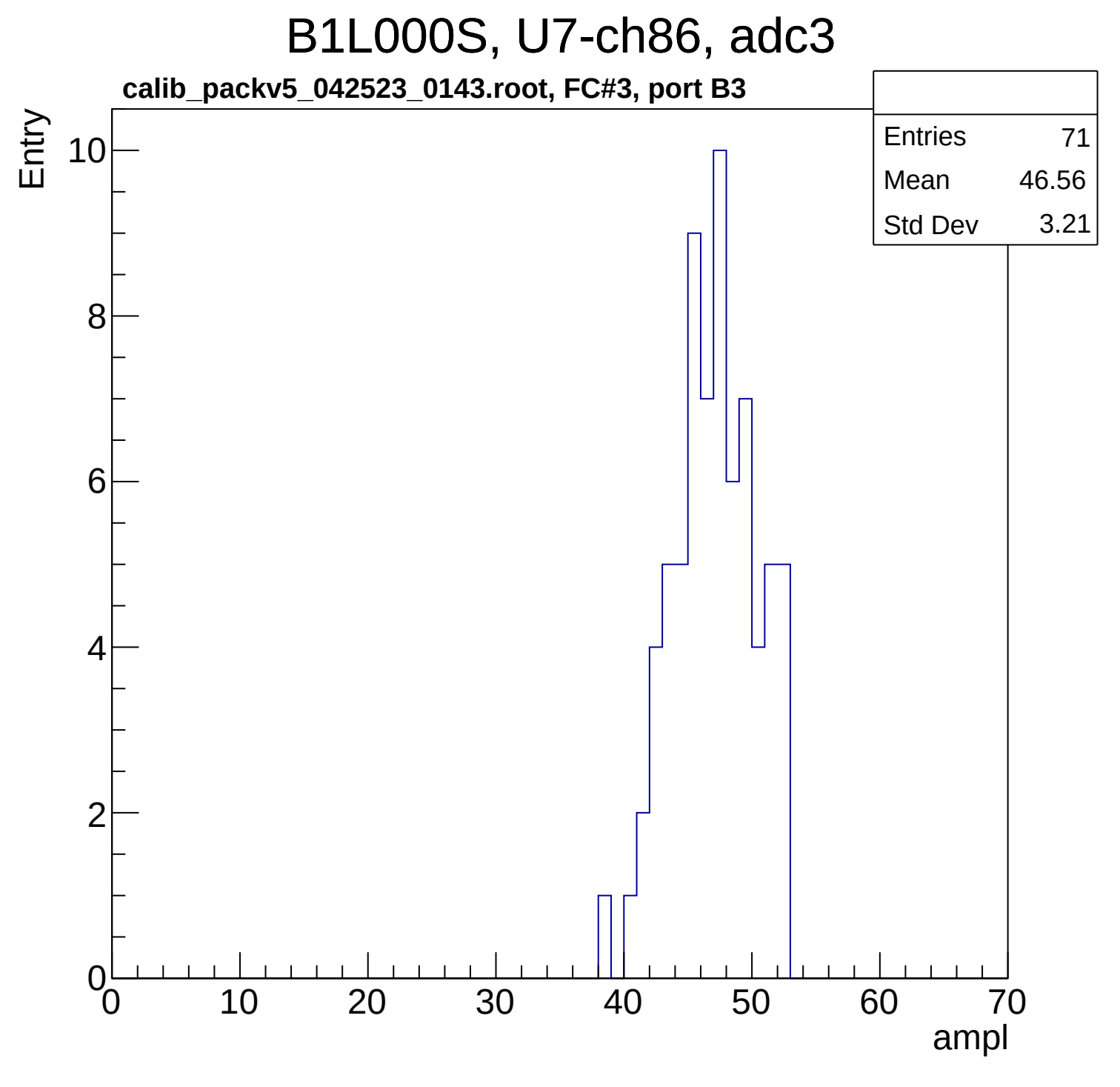
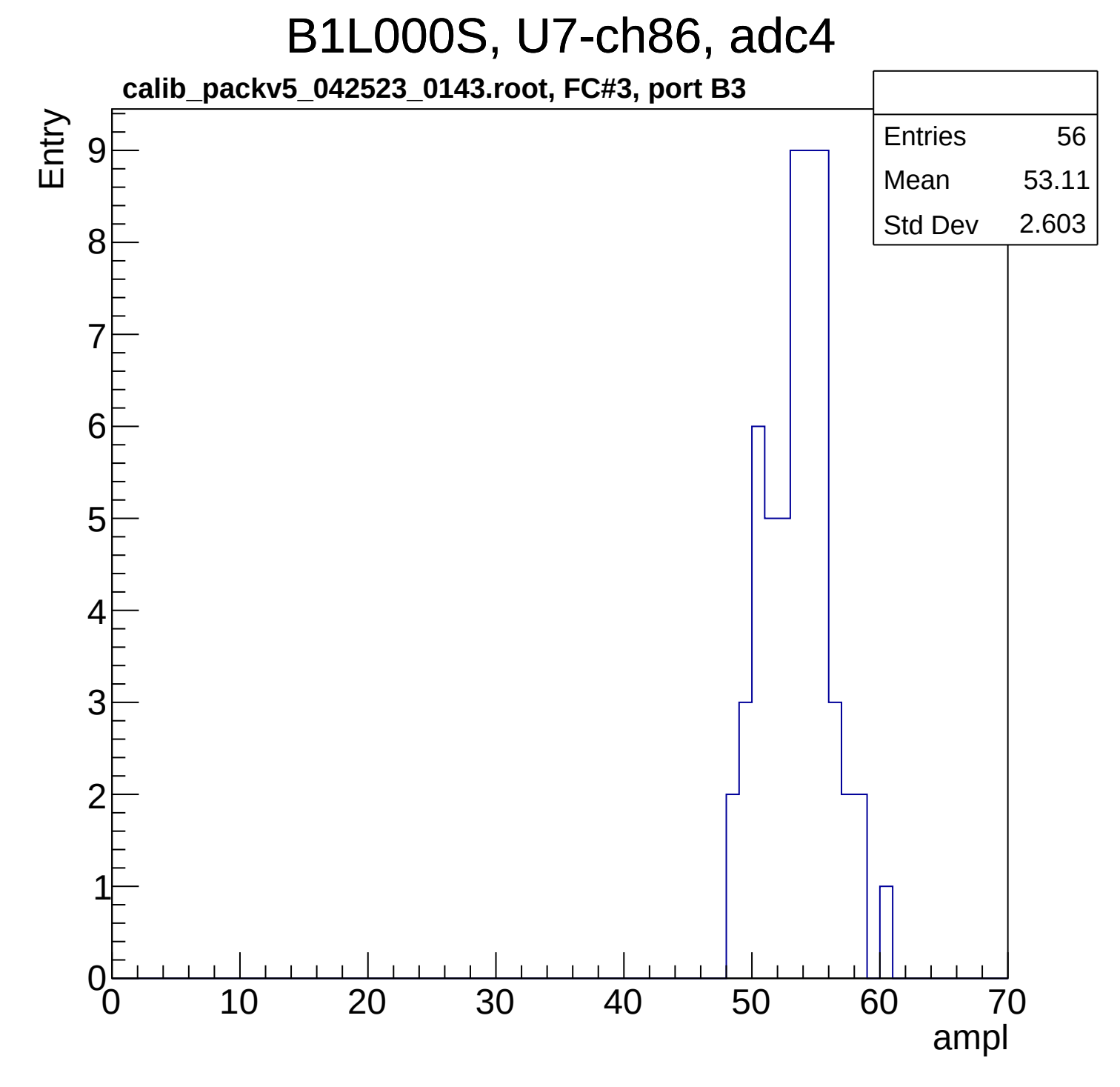

B1L000S, U7-ch86, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 40.75
Std Dev 3.264
Gaus mean : 40.8544
Gaus Width: 3.3796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch86, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 46.56
Std Dev 3.21
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch86, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.11
Std Dev 2.603
Entry
ampl
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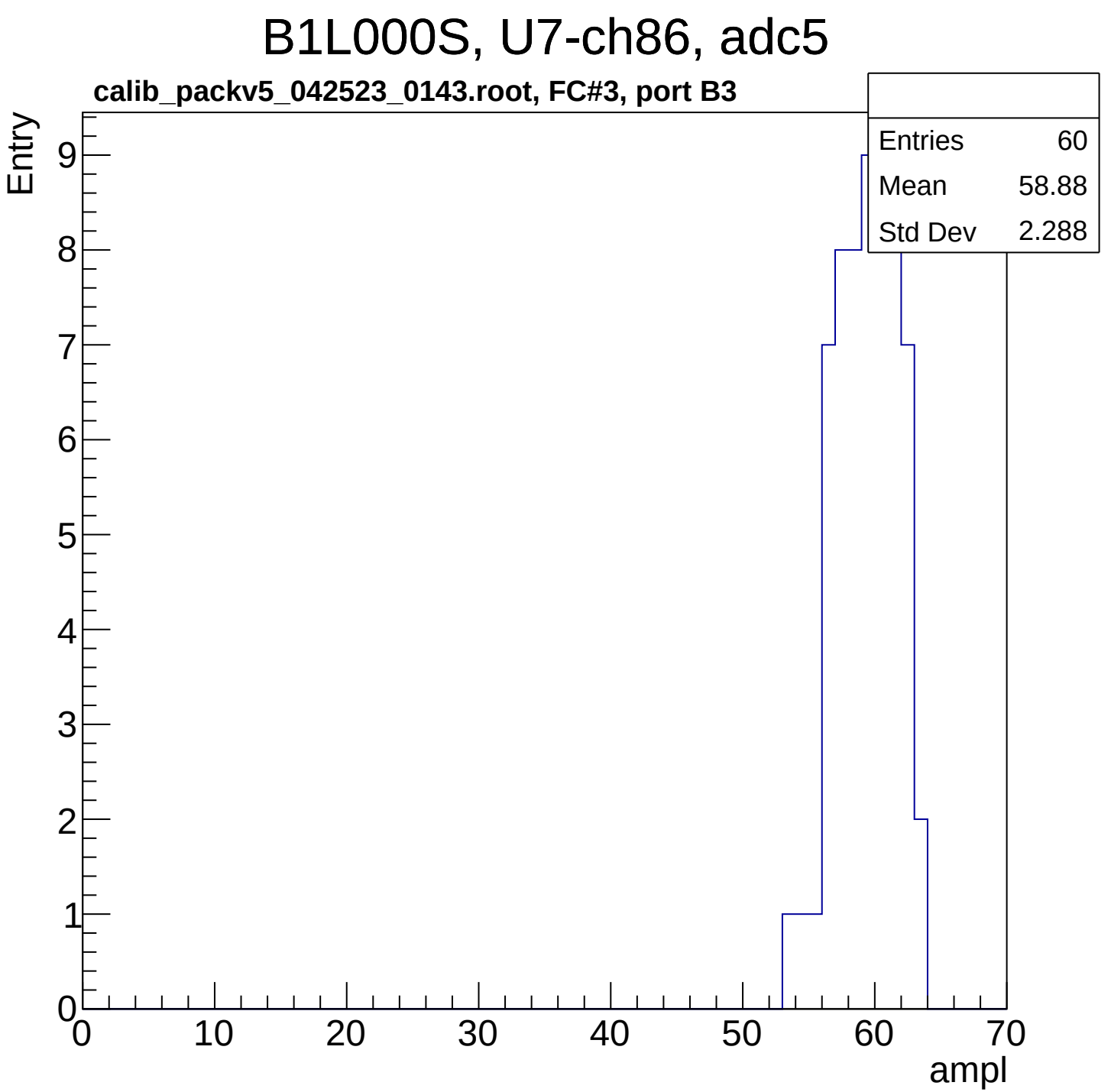
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch86, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.88
Std Dev 2.288
Entry
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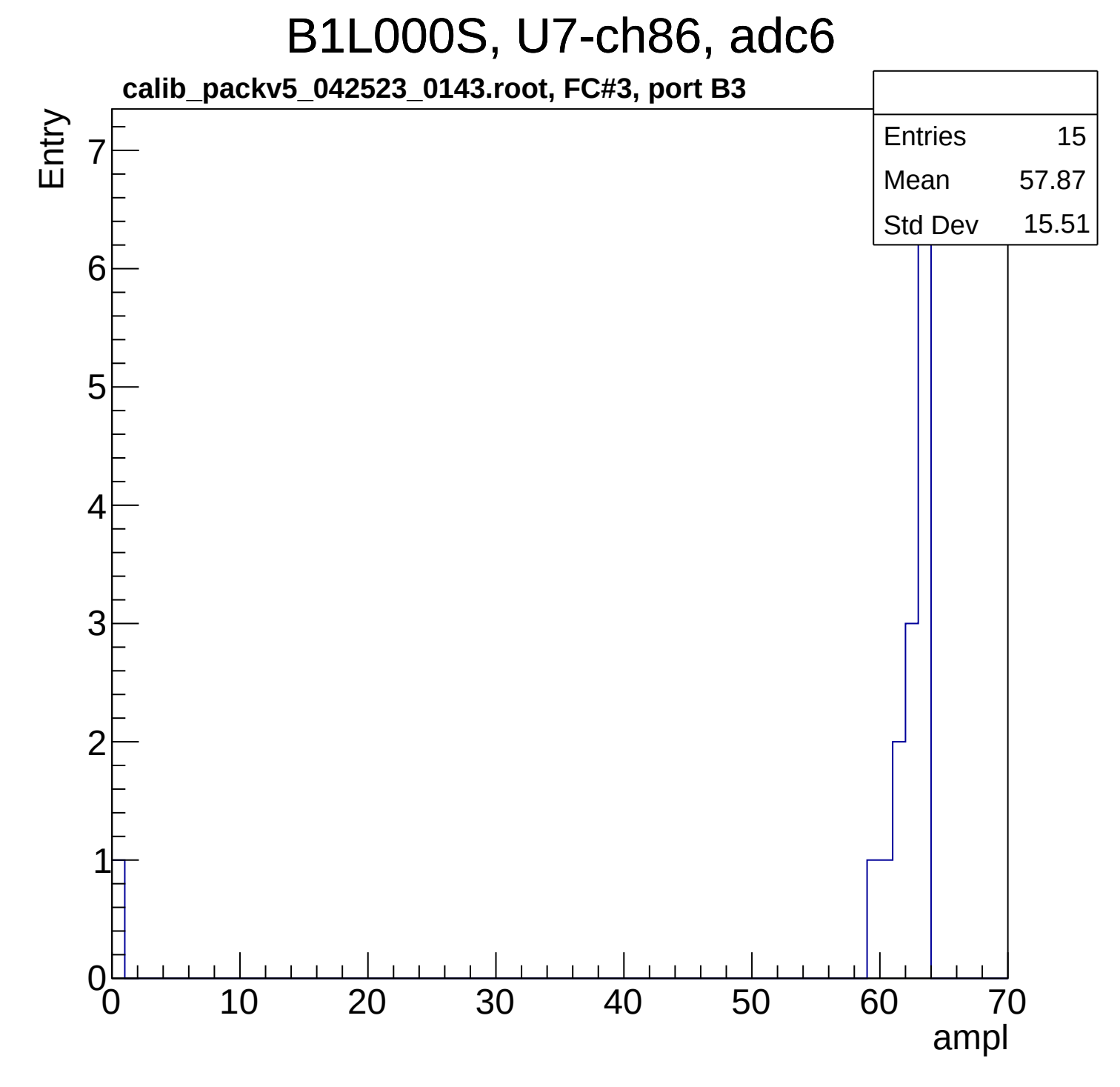
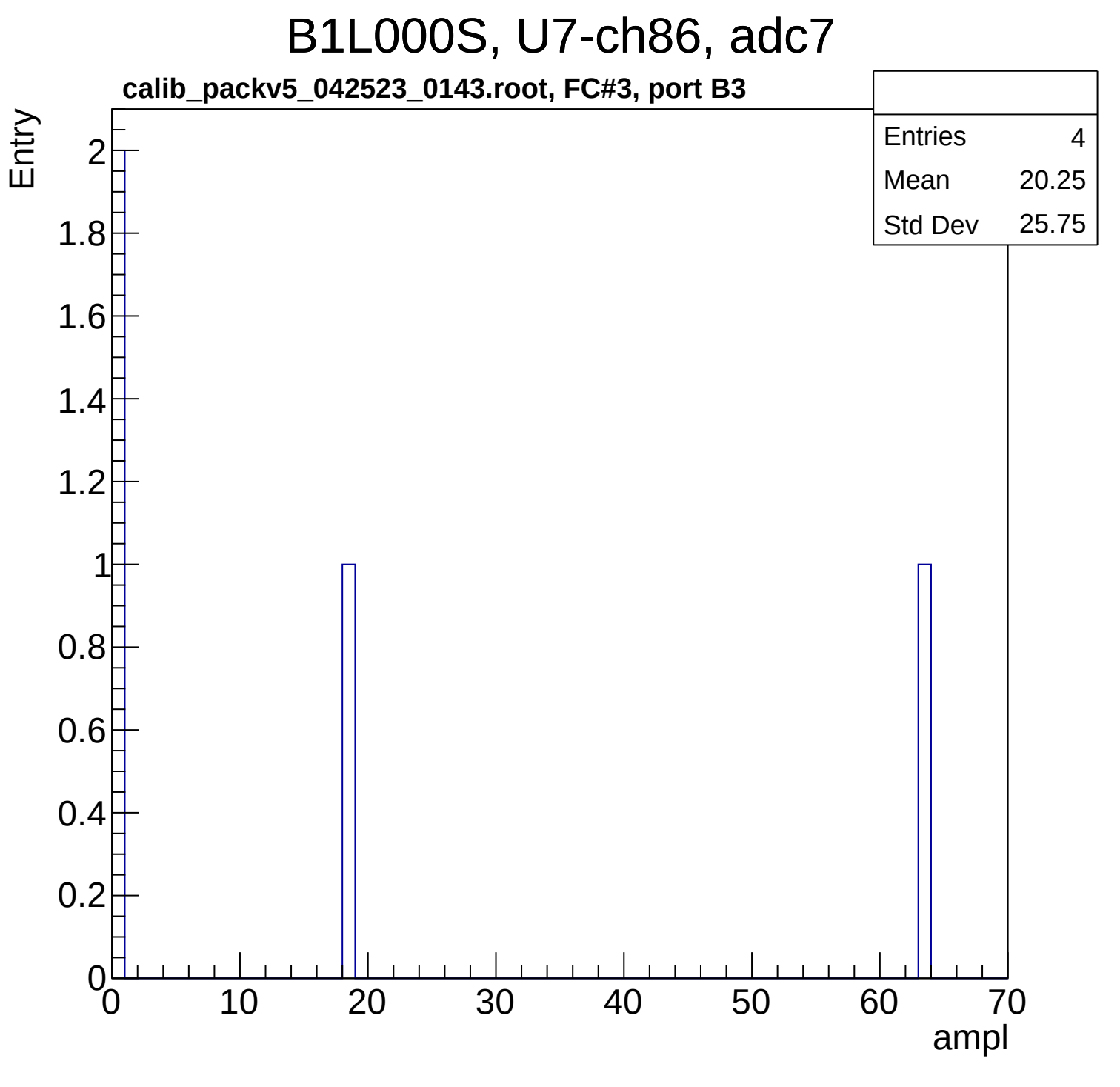
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch86, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.87
Std Dev 15.51
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch86, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 20.25
Std Dev 25.75
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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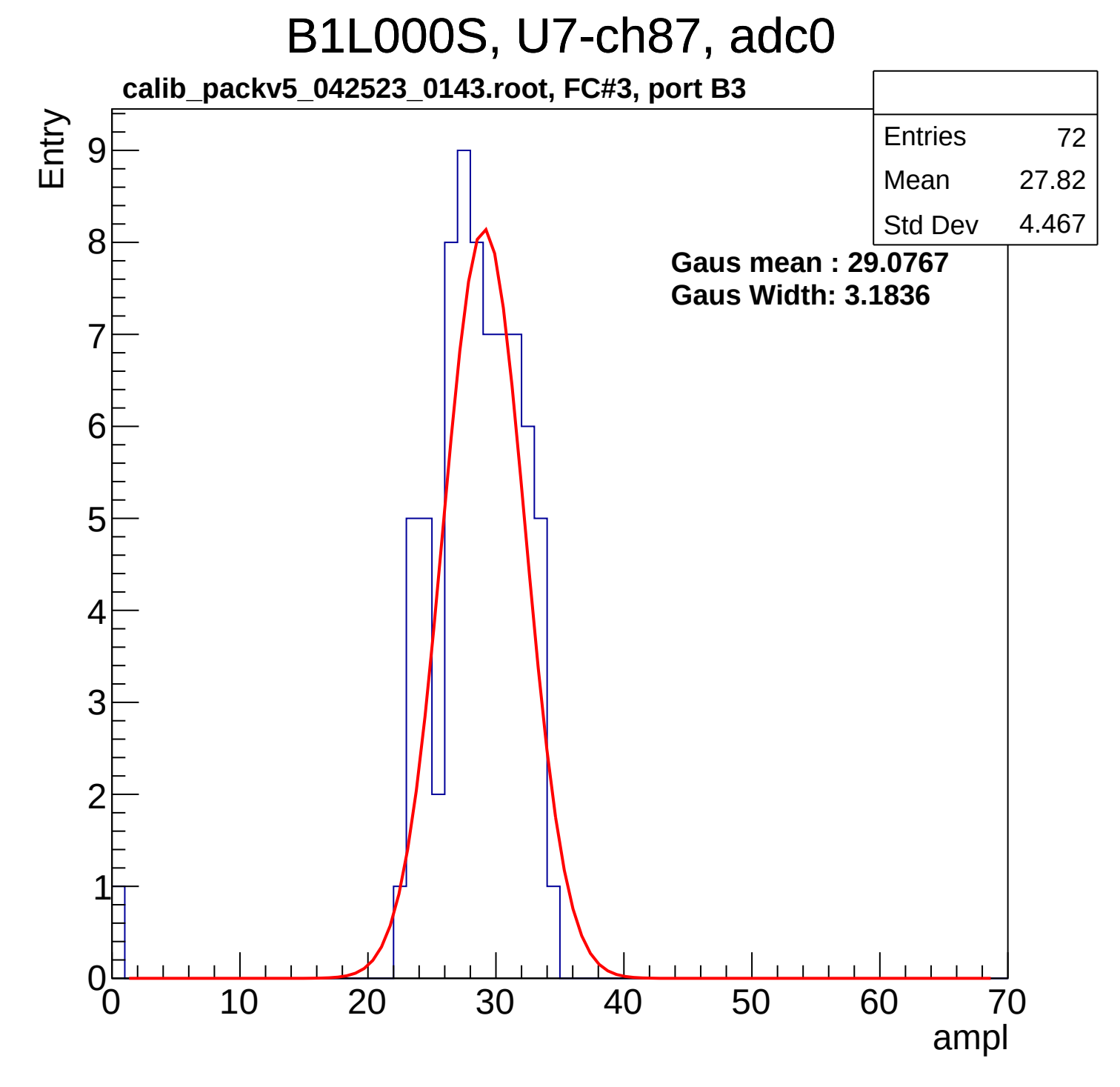
B1L000S, U7-ch87, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 27.82
Std Dev 4.467
Gaus mean : 29.0767
Gaus Width: 3.1836
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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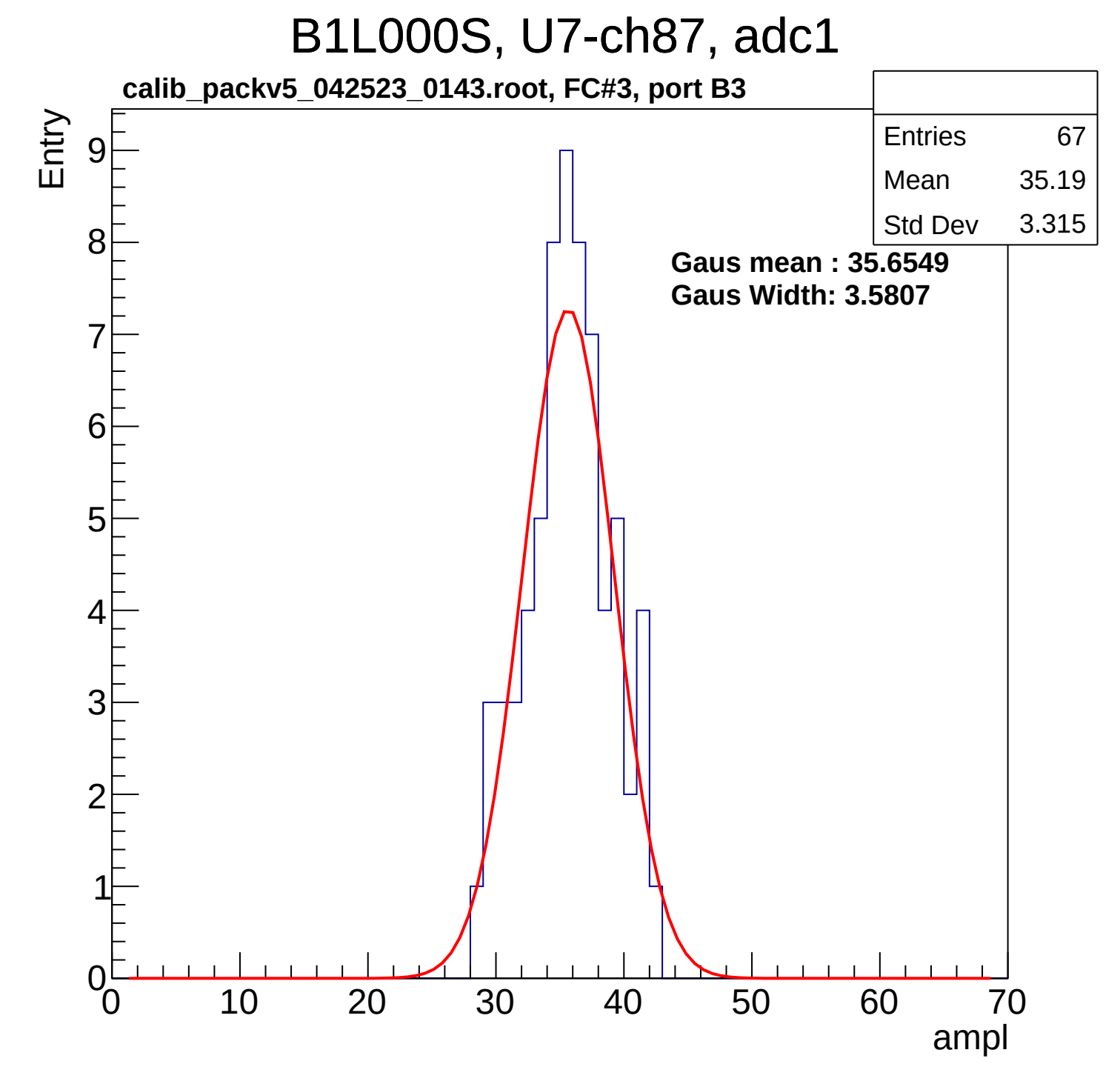
B1L000S, U7-ch87, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 35.19
Std Dev 3.315
Gaus mean : 35.6549
Gaus Width: 3.5807
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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60
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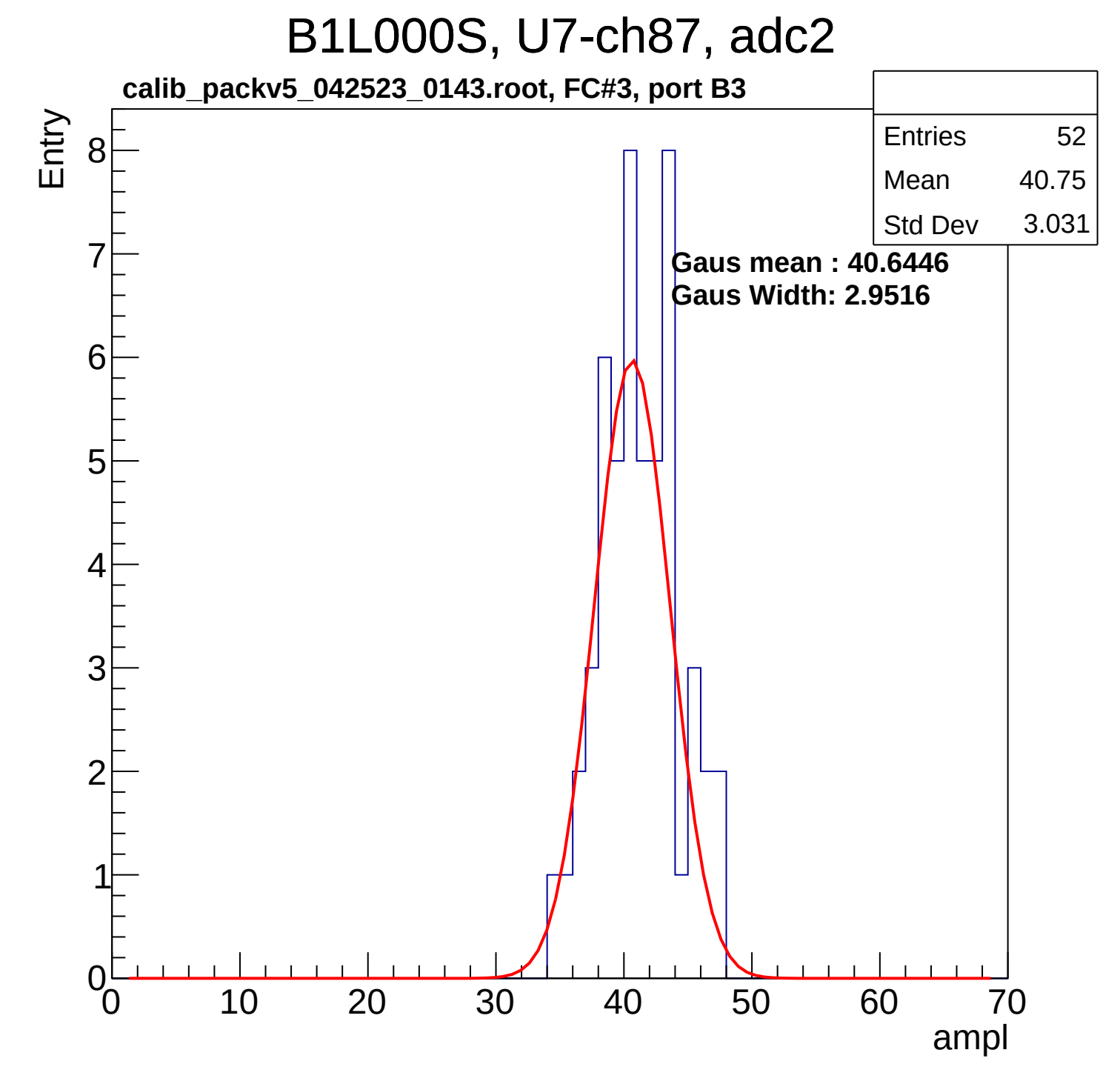
B1L000S, U7-ch87, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 40.75
Std Dev 3.031
Gaus mean : 40.6446
Gaus Width: 2.9516
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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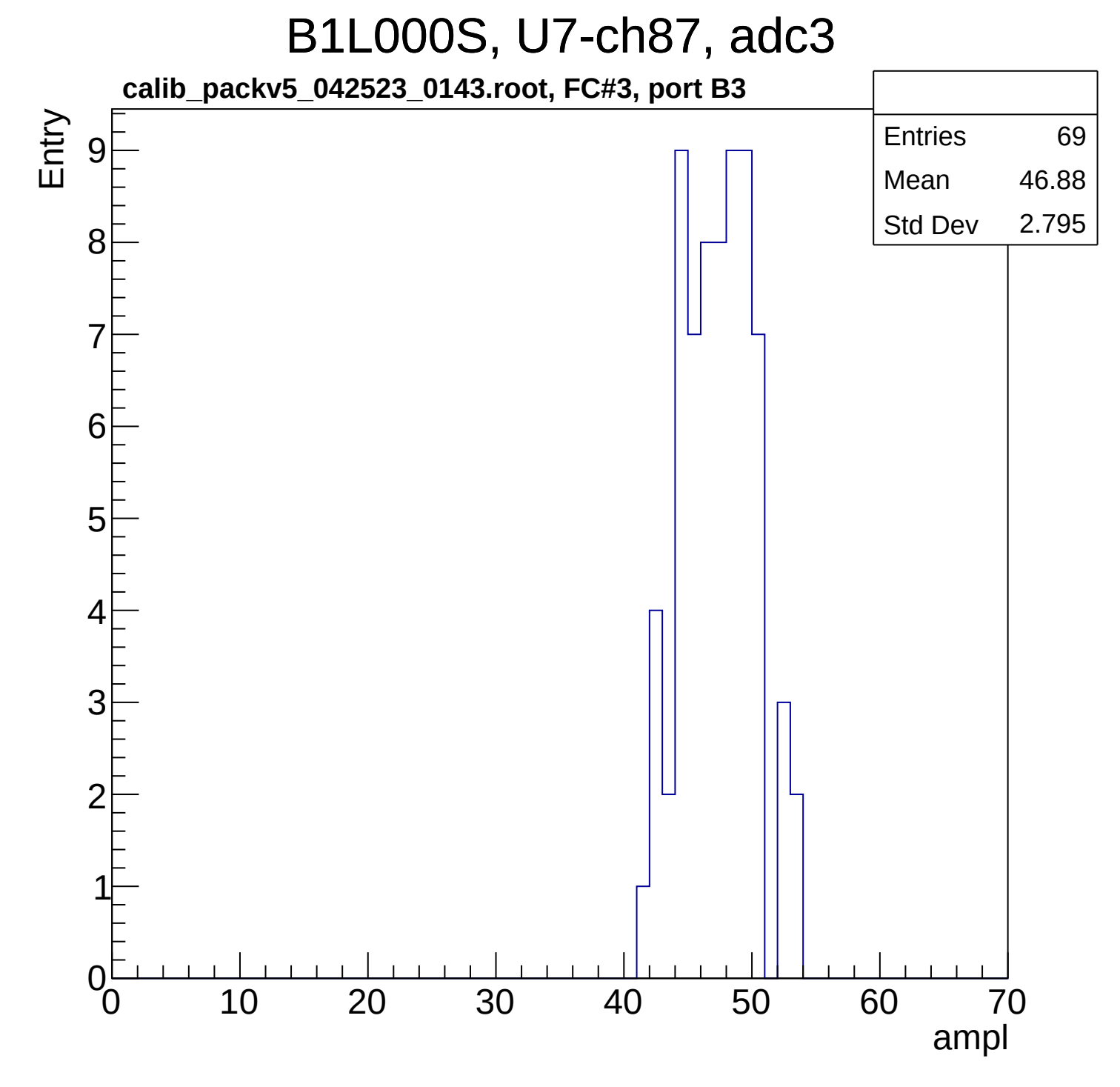
B1L000S, U7-ch87, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 46.88
Std Dev 2.795
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
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60
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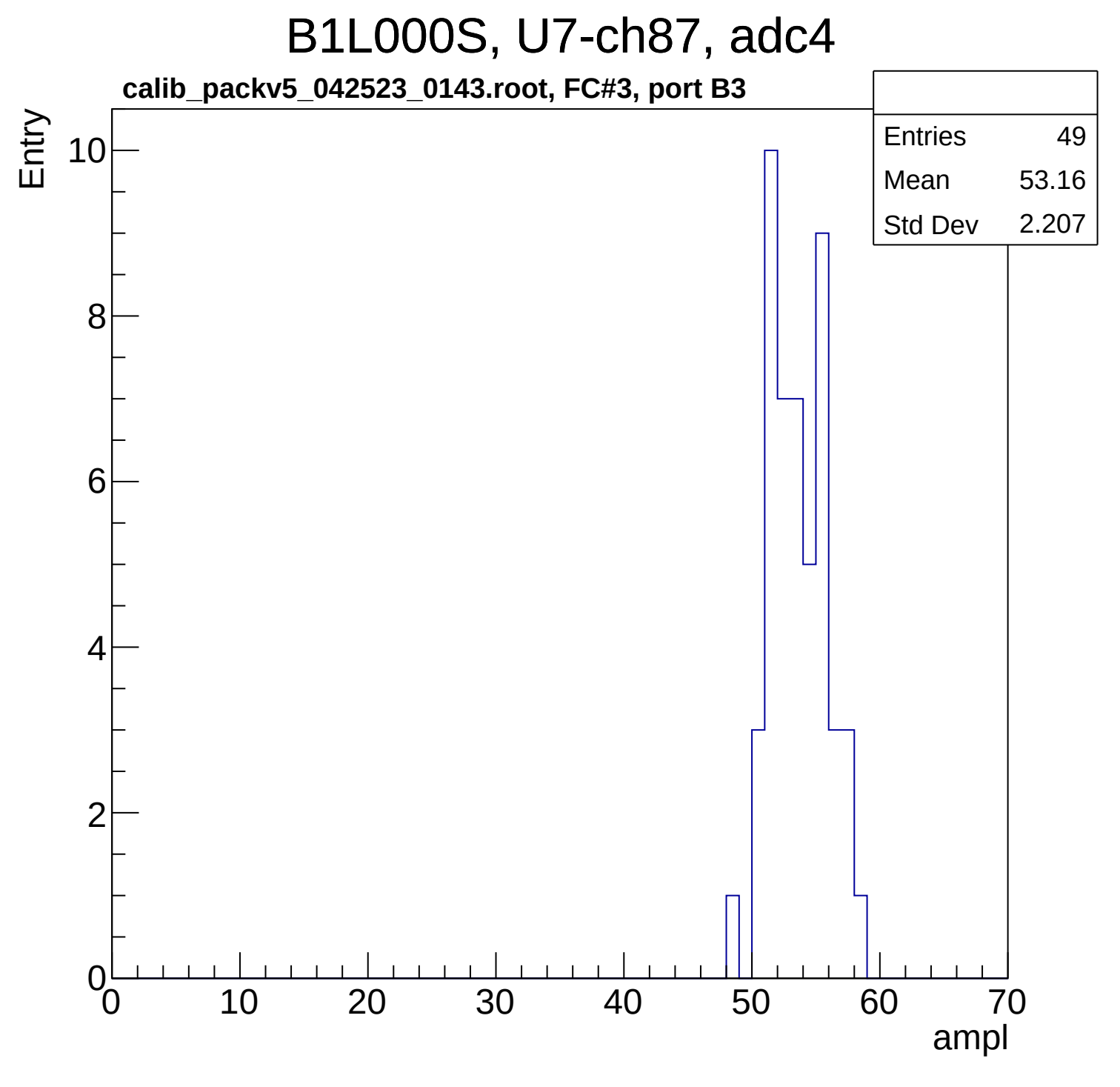
B1L000S, U7-ch87, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 53.16
Std Dev 2.207
Entry
ampl
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0
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20
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40
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60
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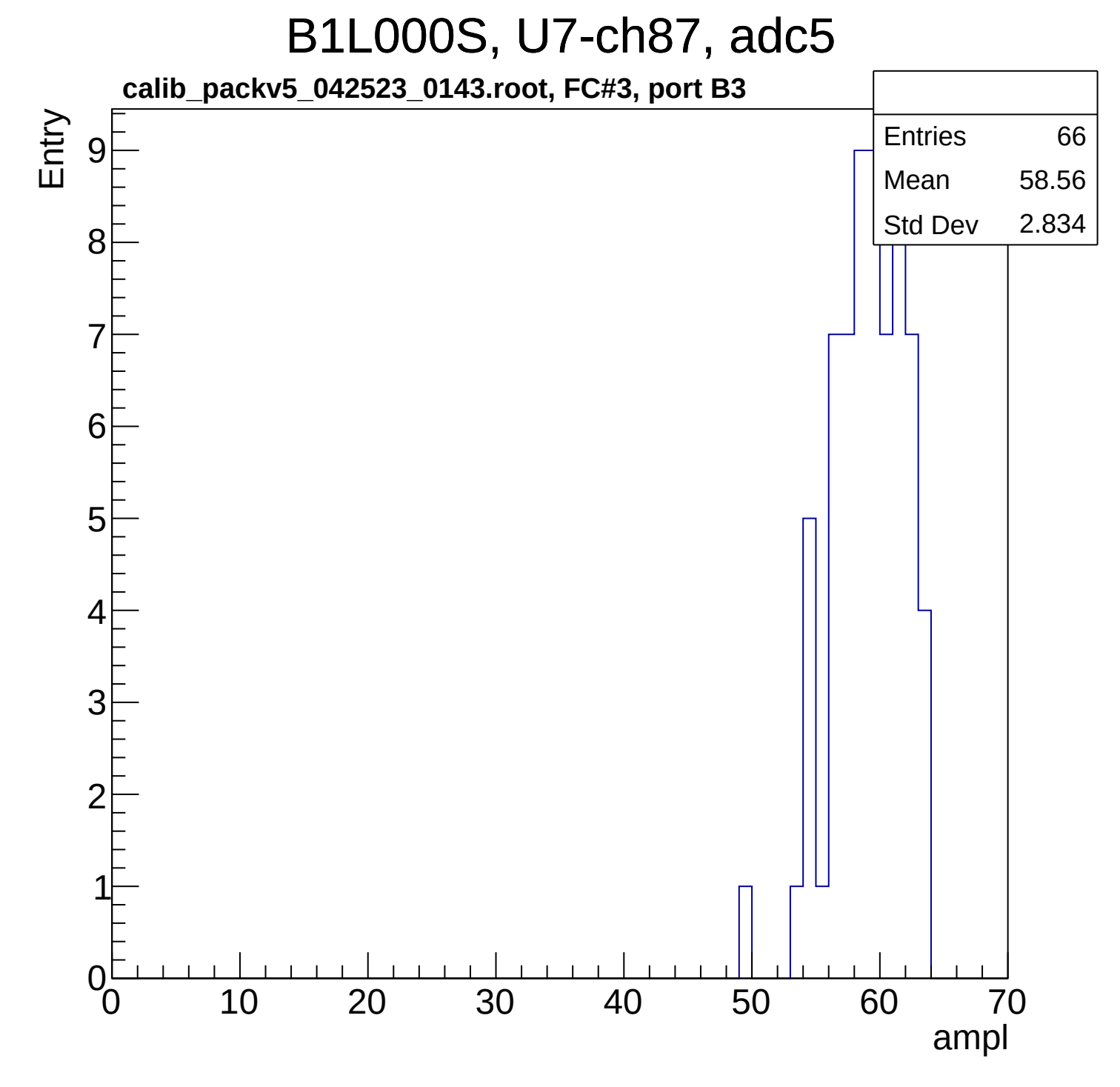
B1L000S, U7-ch87, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 58.56
Std Dev 2.834
Entry
ampl
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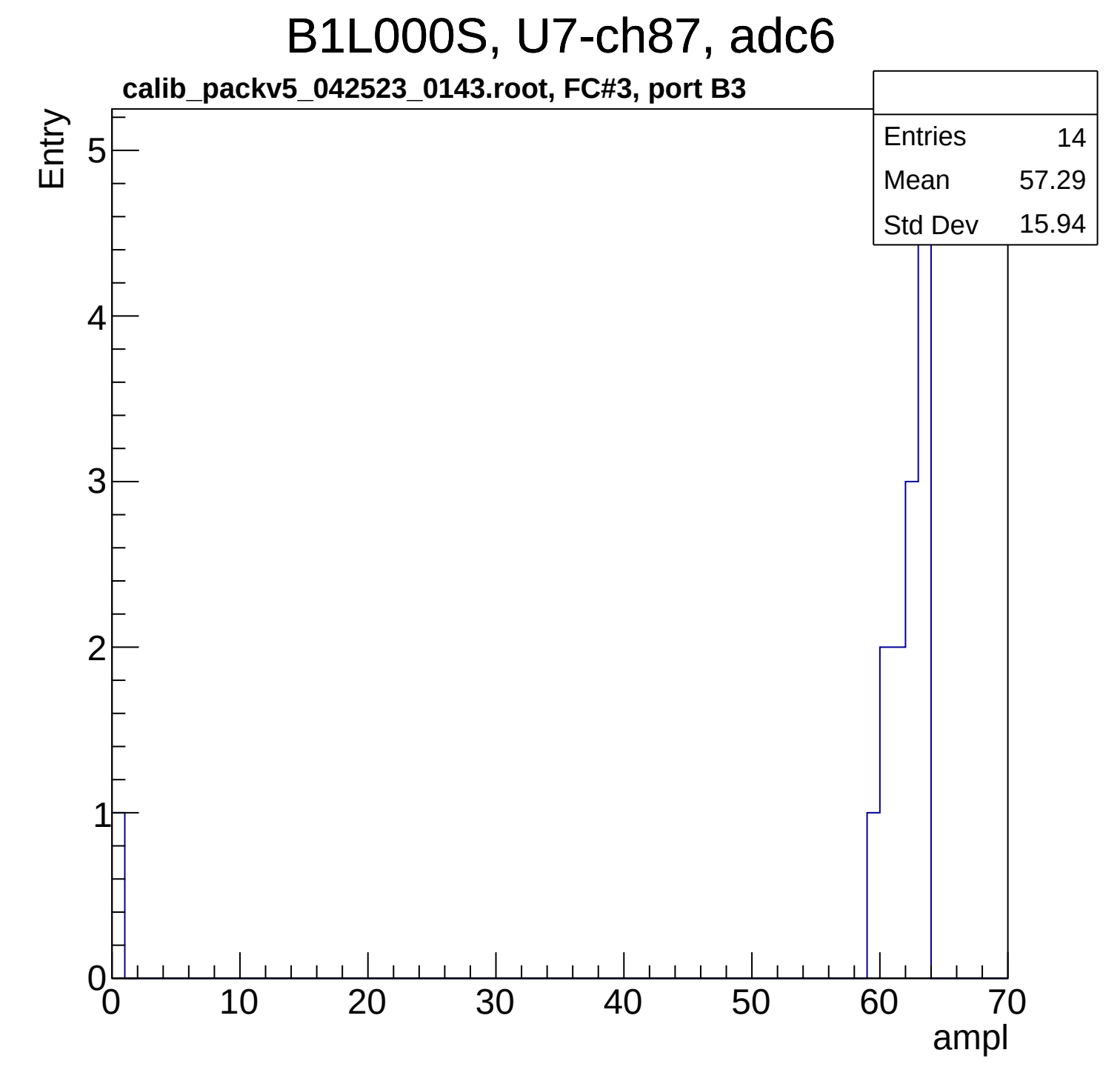
B1L000S, U7-ch87, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 14
Mean 57.29
Std Dev 15.94
Entry
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1
2
3
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10
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30
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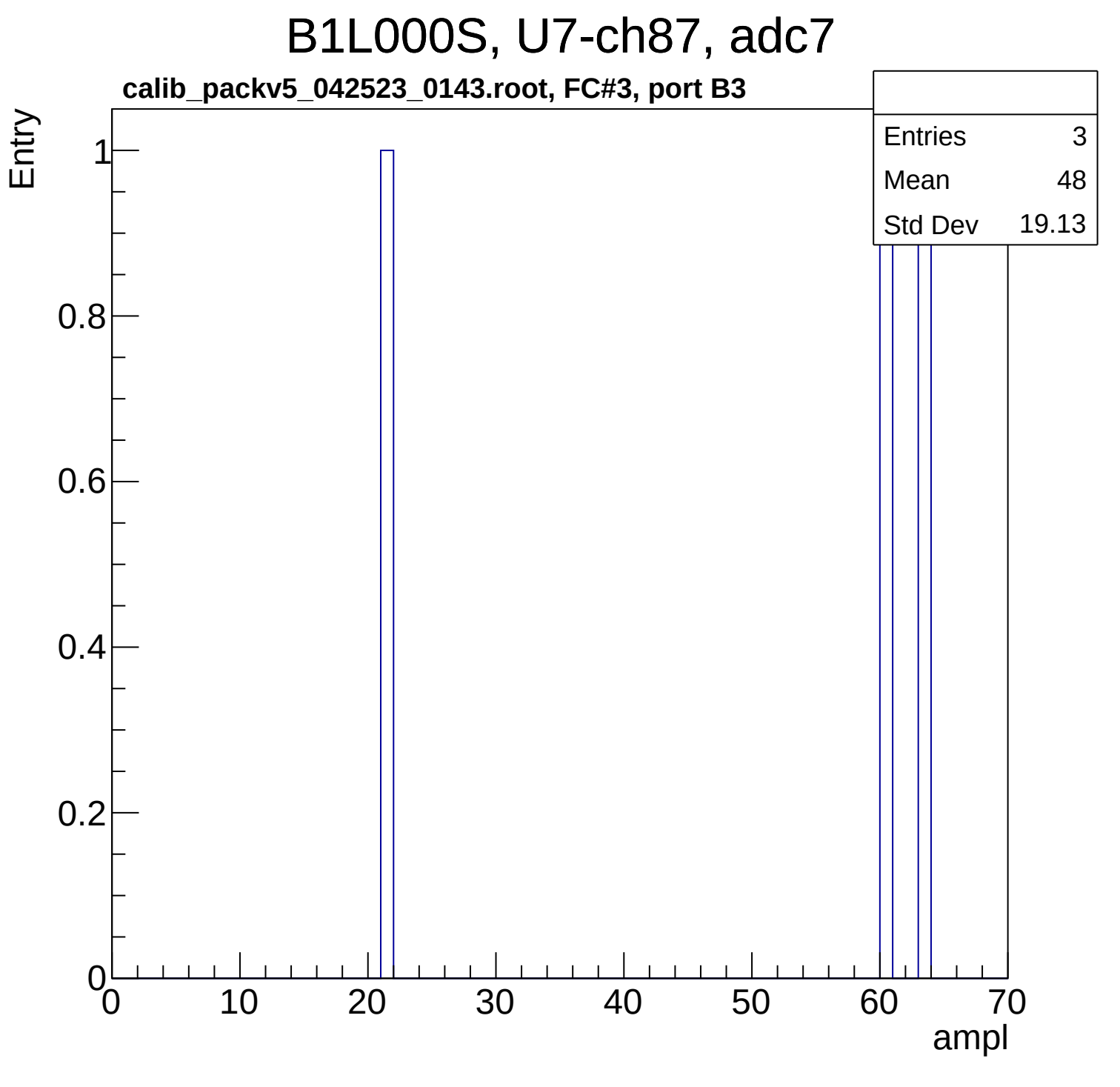
50
60
70
ampl


B1L000S, U7-ch87, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 48
Std Dev 19.13
Entry
ampl
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0.6
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20
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40
50
60
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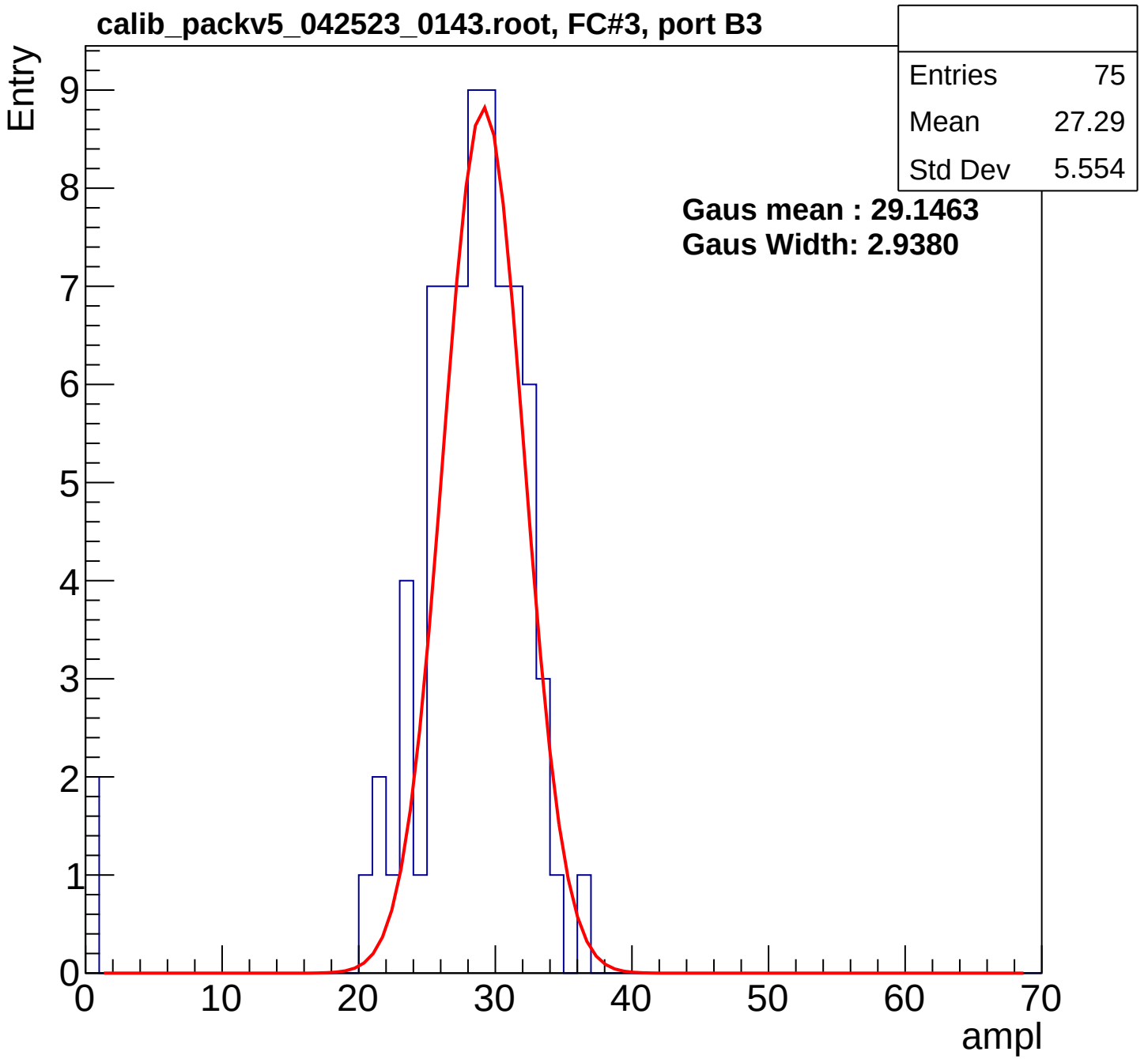
B1L000S, U7-ch88, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 27.29
Std Dev 5.554
Gaus mean : 29.1463
Gaus Width: 2.9380
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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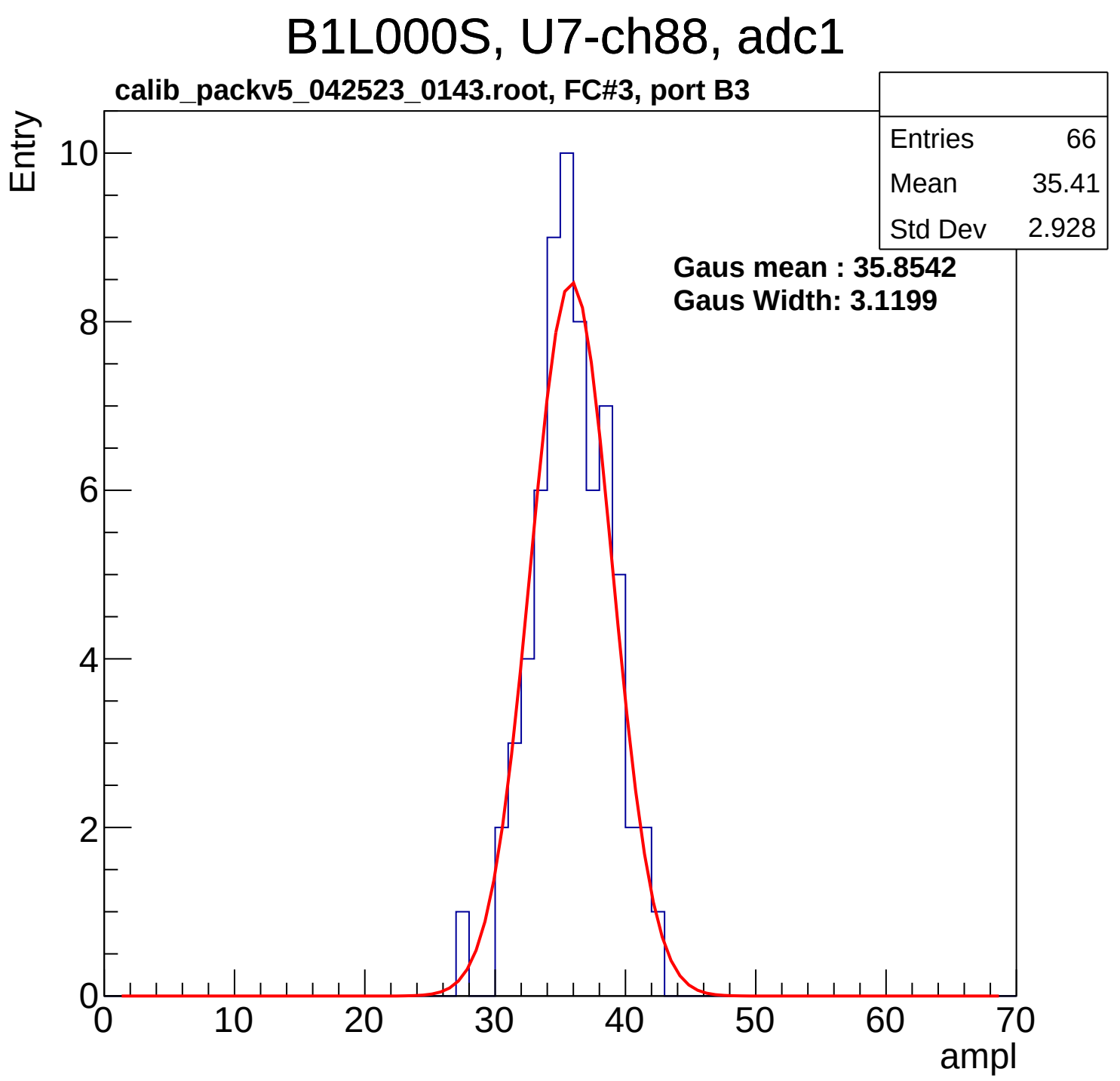
B1L000S, U7-ch88, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 35.41
Std Dev 2.928
Gaus mean : 35.8542
Gaus Width: 3.1199
Entry
ampl
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4
6
8
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0
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20
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60
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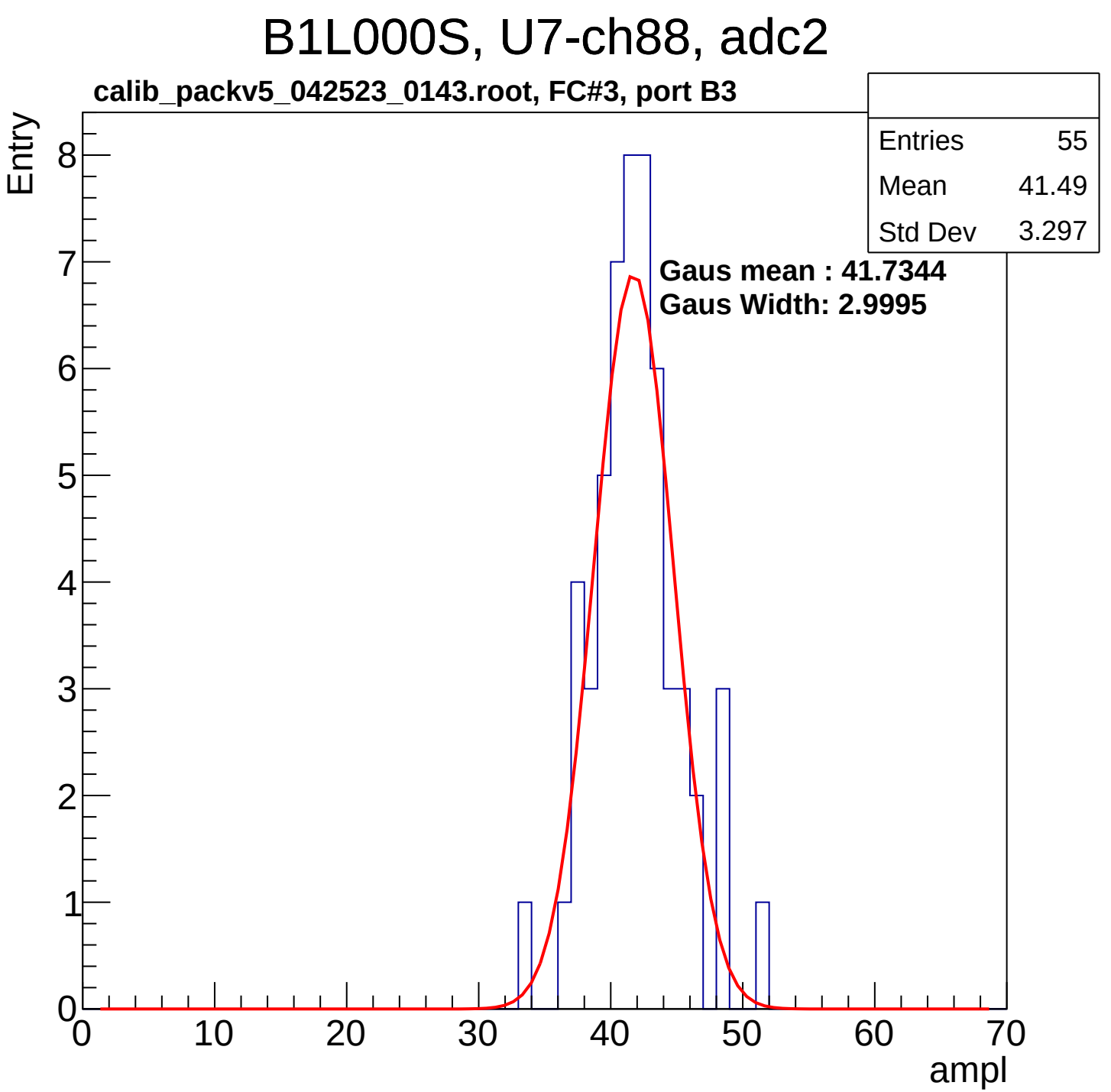
B1L000S, U7-ch88, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 41.49
Std Dev 3.297
Gaus mean : 41.7344
Gaus Width: 2.9995
Entry
ampl
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2
3
4
5
6
7
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0
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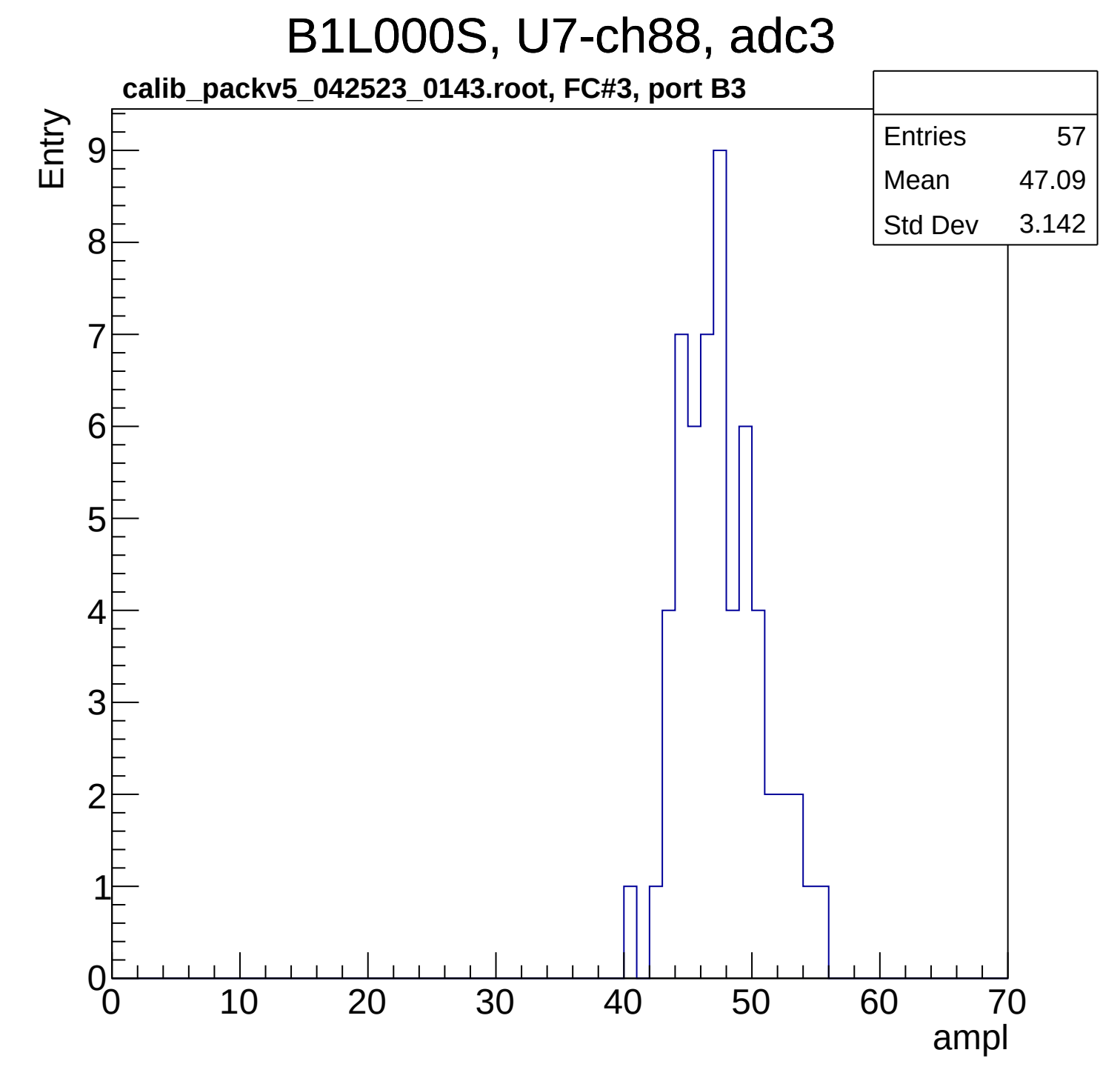
B1L000S, U7-ch88, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.09
Std Dev 3.142
Entry
ampl
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0
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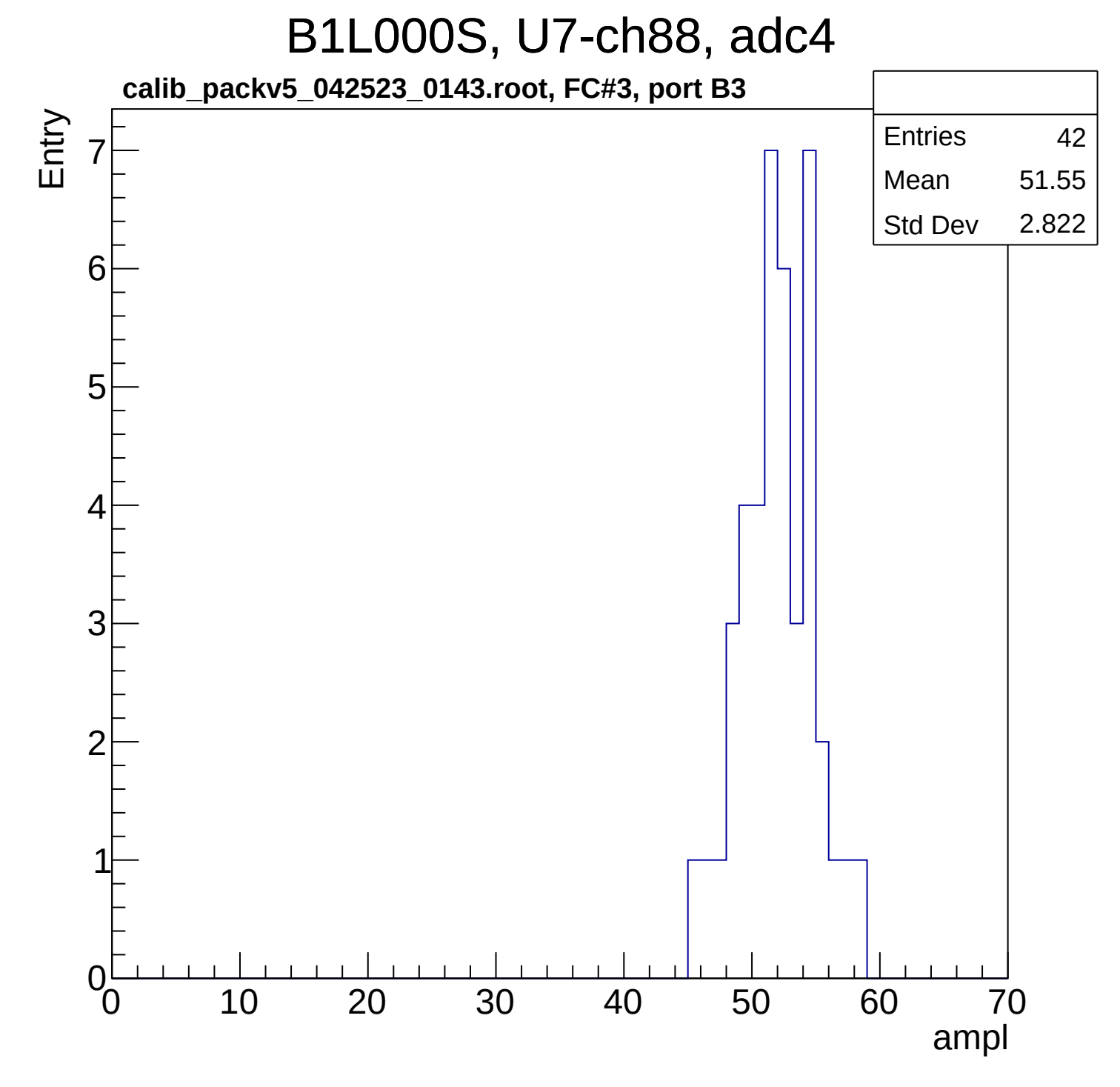
B1L000S, U7-ch88, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 42
Mean 51.55
Std Dev 2.822
Entry
ampl
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3
4
5
6
7
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20
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60
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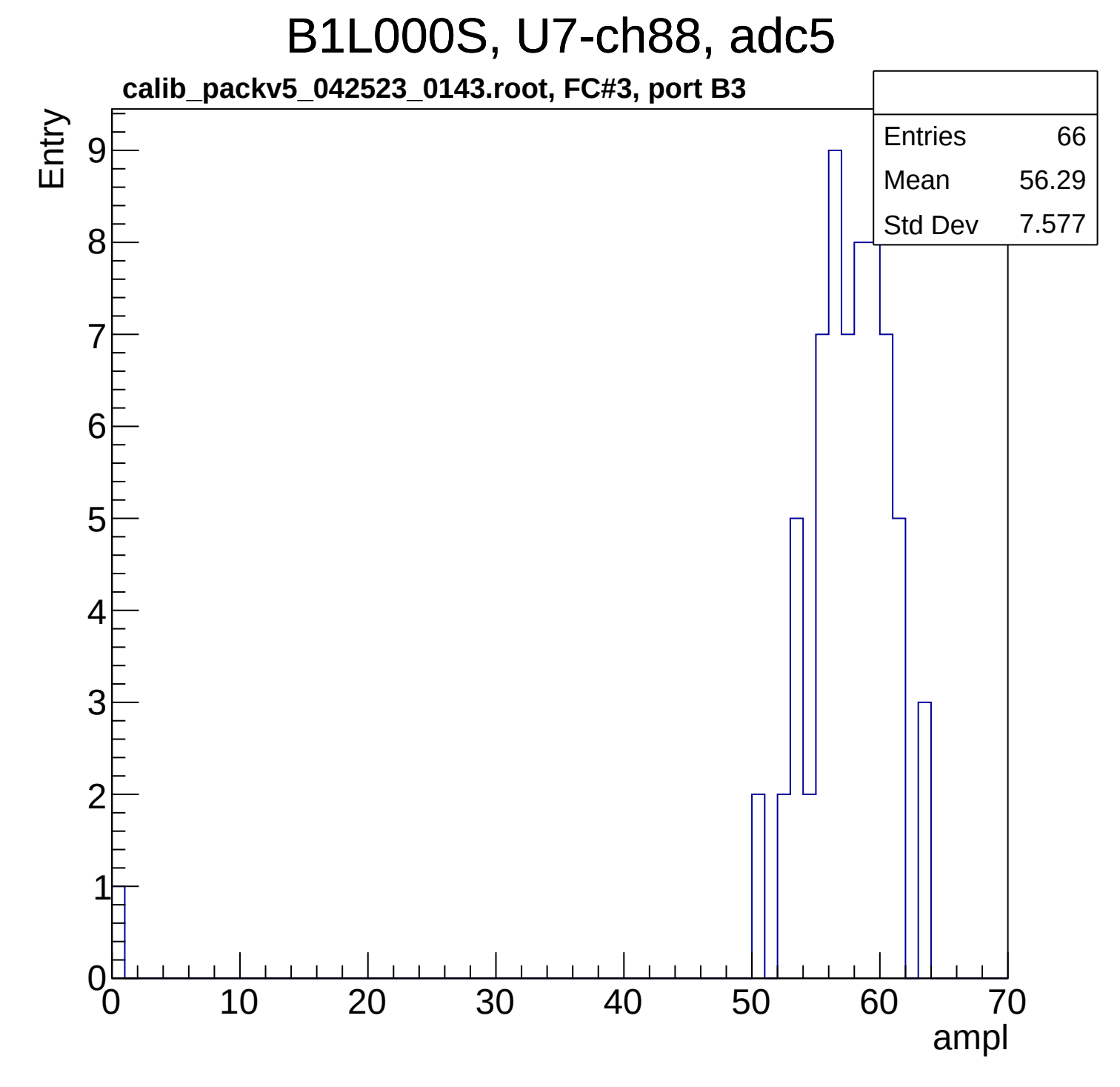
B1L000S, U7-ch88, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 56.29
Std Dev 7.577
Entry
ampl
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3
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7
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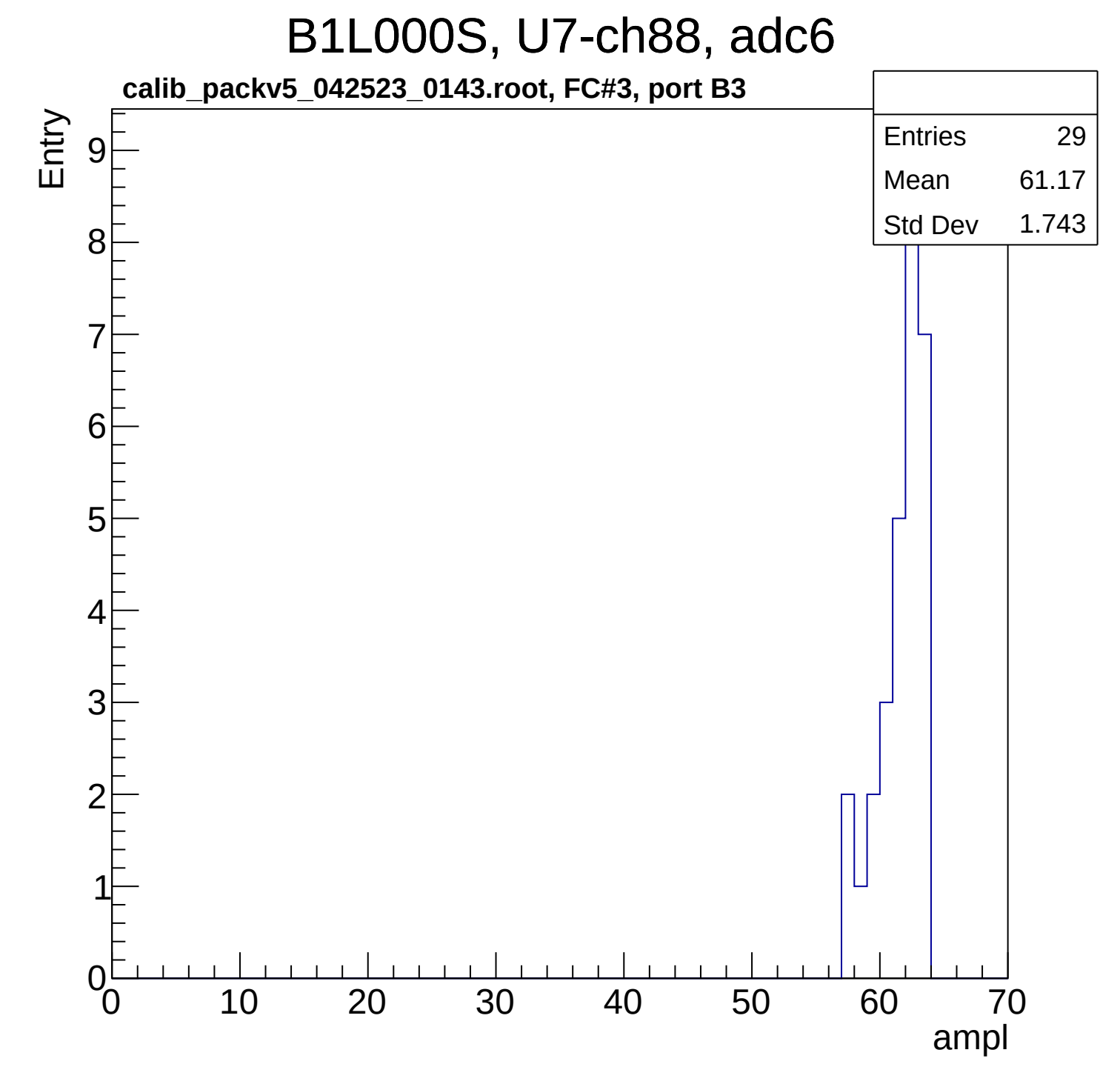
B1L000S, U7-ch88, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 29
Mean 61.17
Std Dev 1.743
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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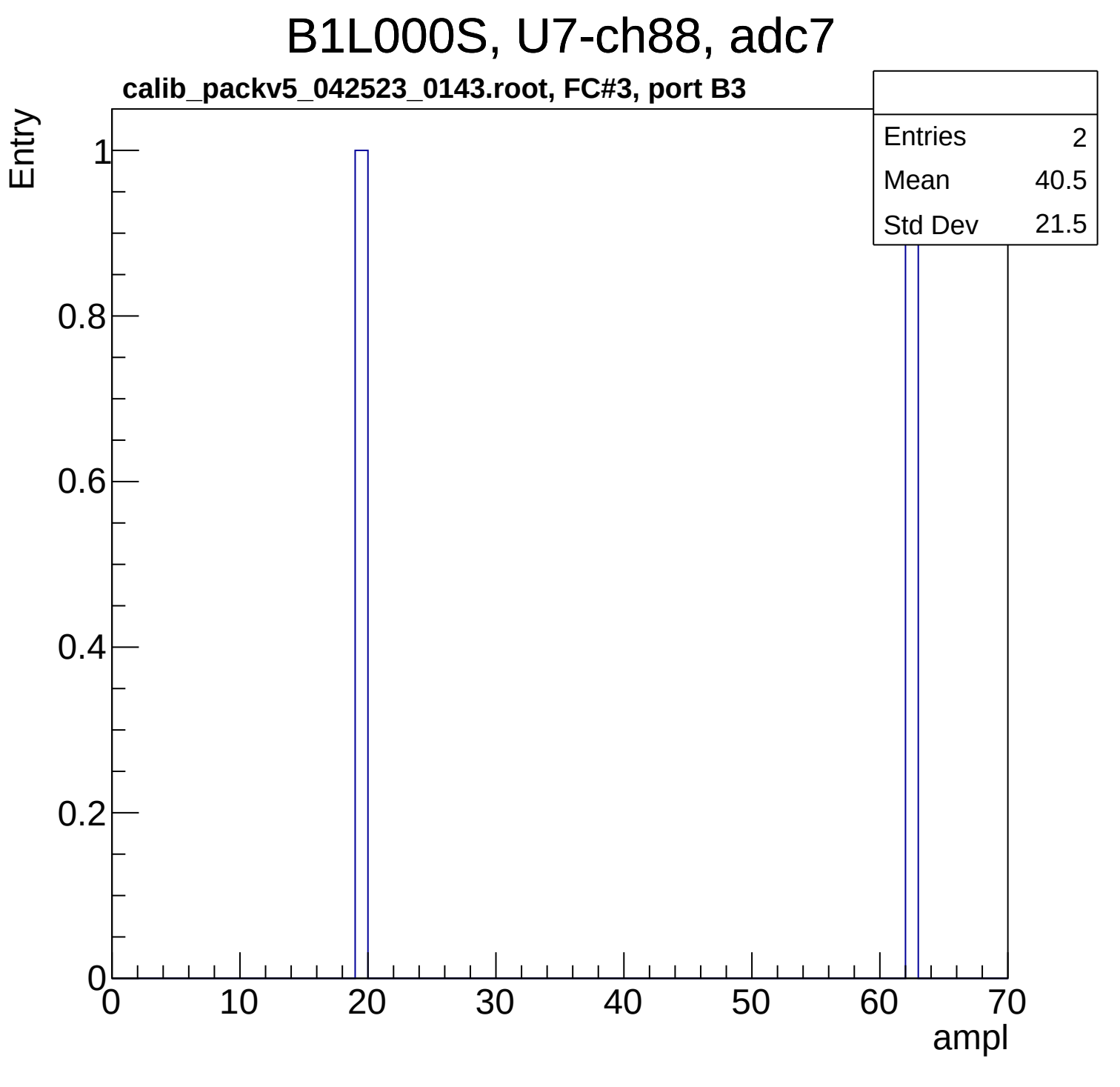
B1L000S, U7-ch88, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 40.5
Std Dev 21.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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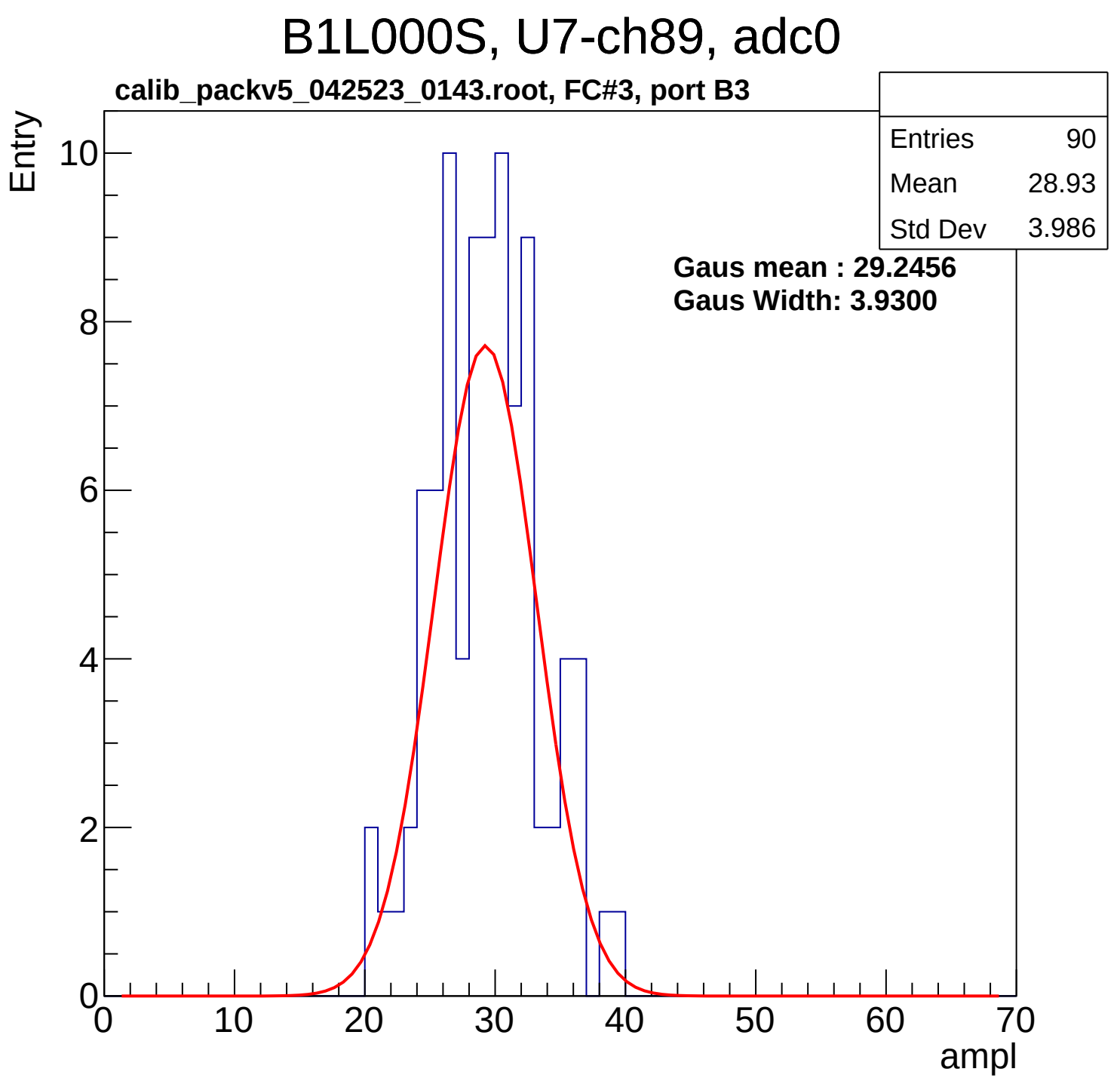
B1L000S, U7-ch89, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 90
Mean 28.93
Std Dev 3.986
Gaus mean : 29.2456
Gaus Width: 3.9300
Entry
ampl
0
2
4
6
8
10
0
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20
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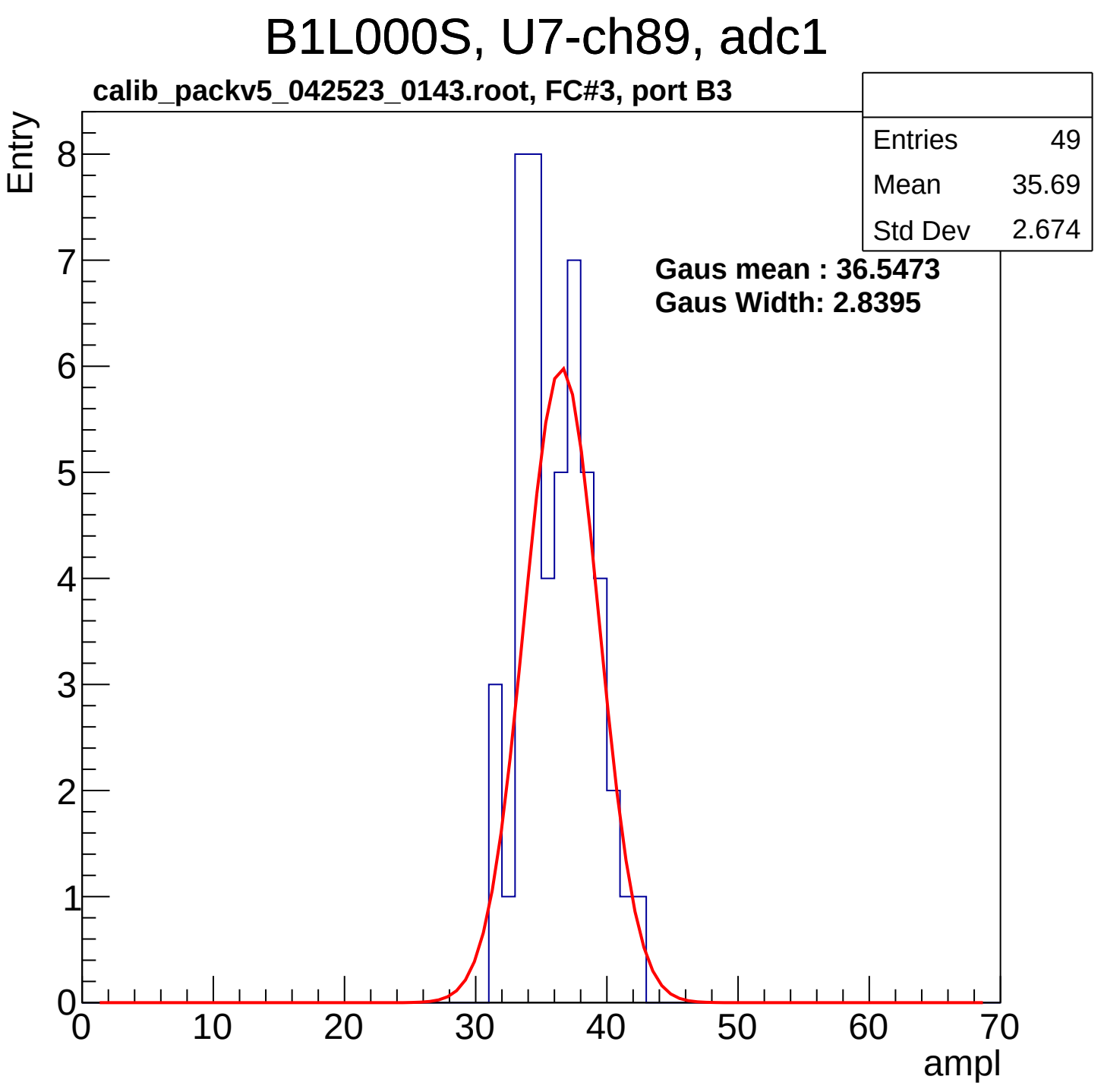
B1L000S, U7-ch89, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 35.69
Std Dev 2.674
Gaus mean : 36.5473
Gaus Width: 2.8395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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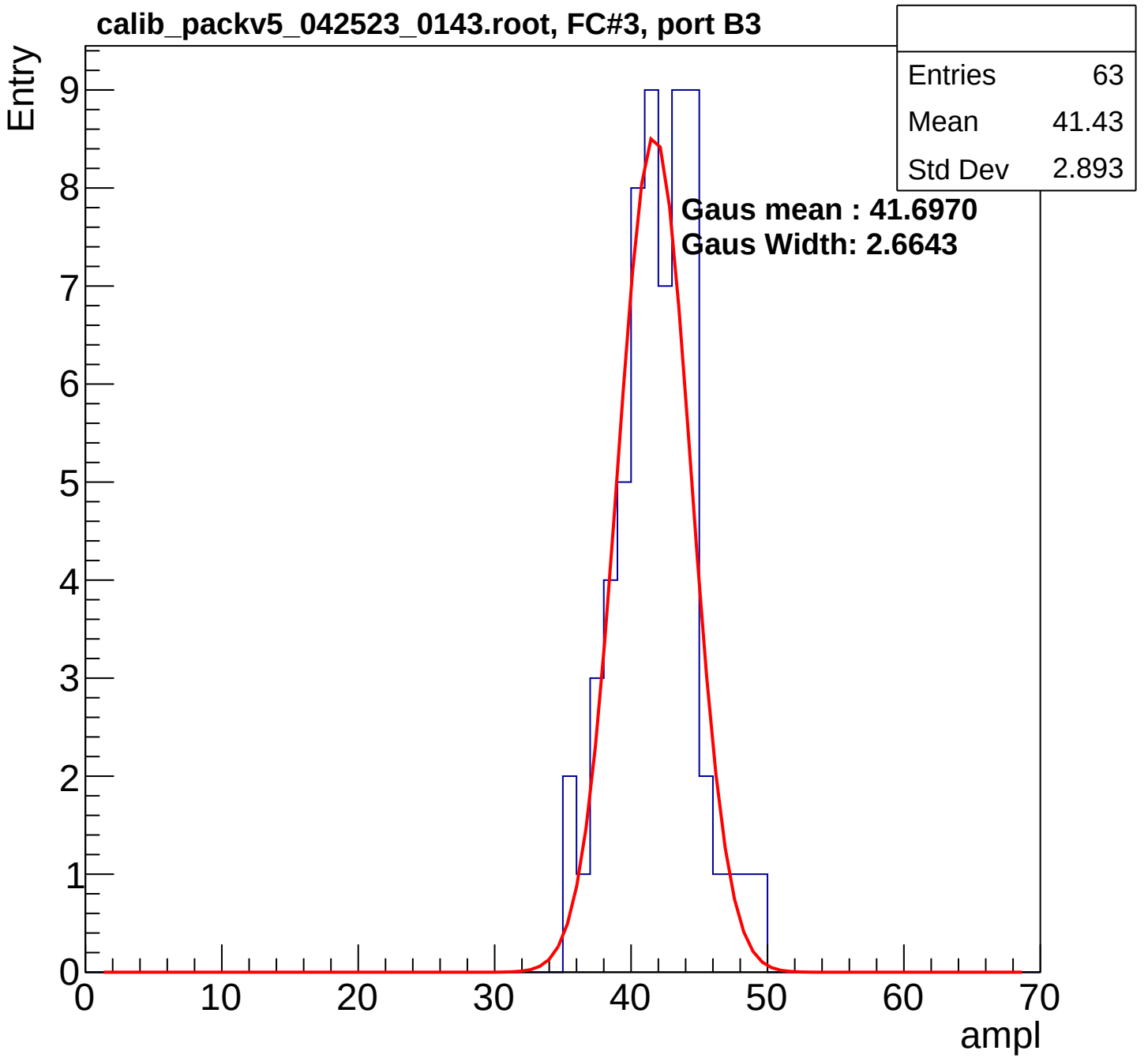
B1L000S, U7-ch89, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 41.43
Std Dev 2.893
Gaus mean : 41.6970
Gaus Width: 2.6643
Entry
ampl
0
10
20
30
40
50
60
70
1
2
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4
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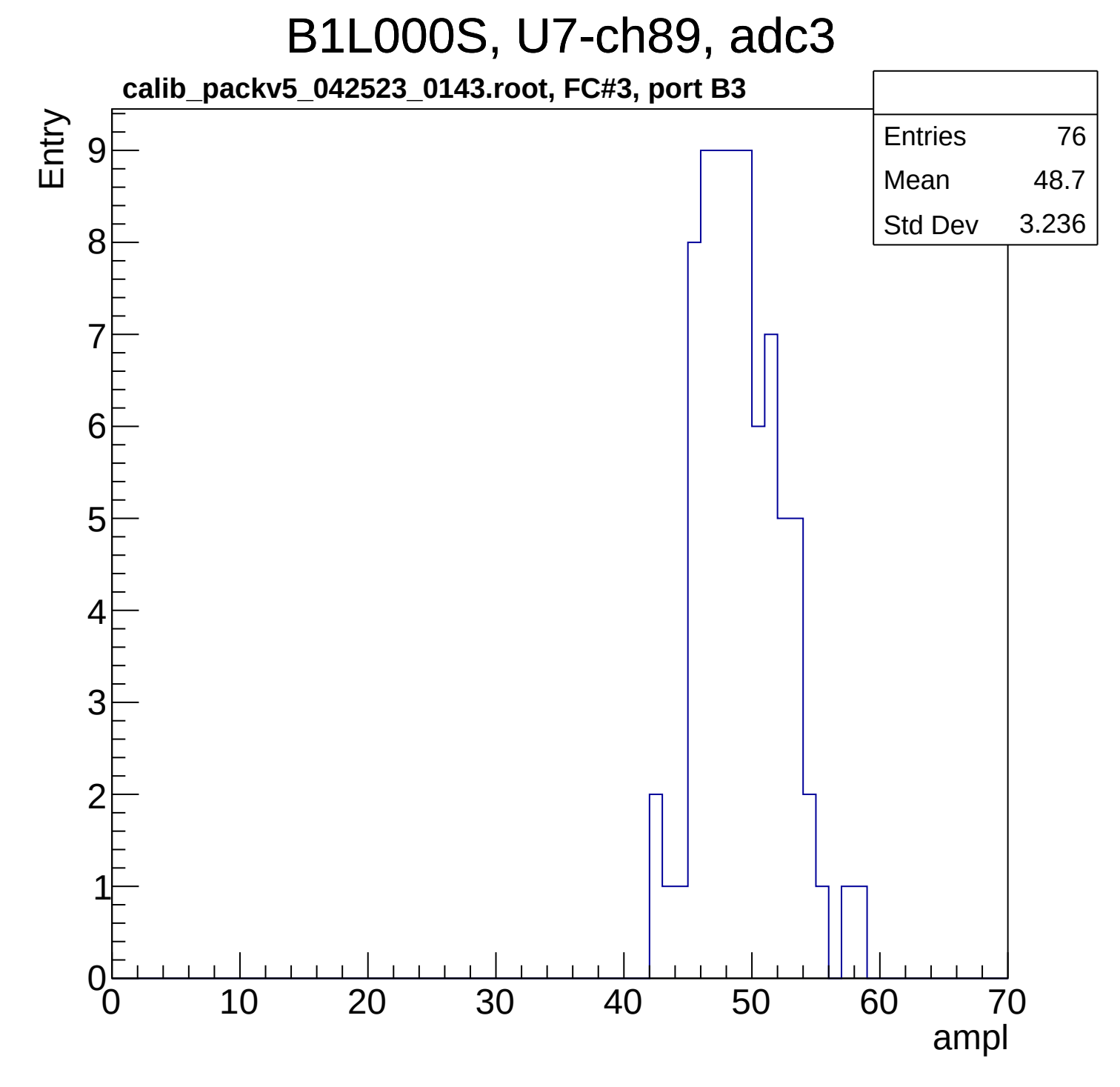
6
7
8
9


B1L000S, U7-ch89, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 48.7
Std Dev 3.236
Entry
ampl
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0
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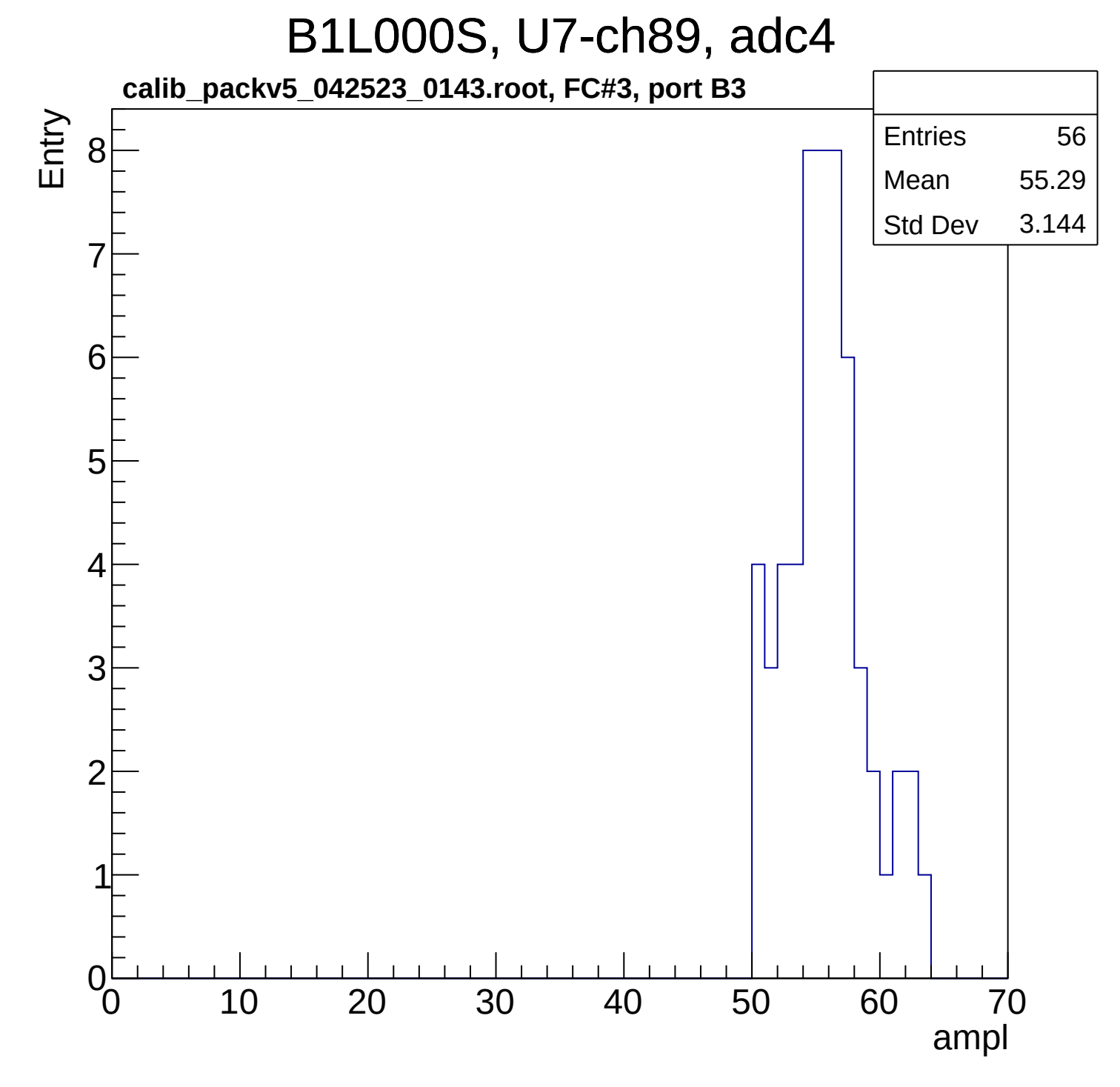
B1L000S, U7-ch89, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 55.29
Std Dev 3.144
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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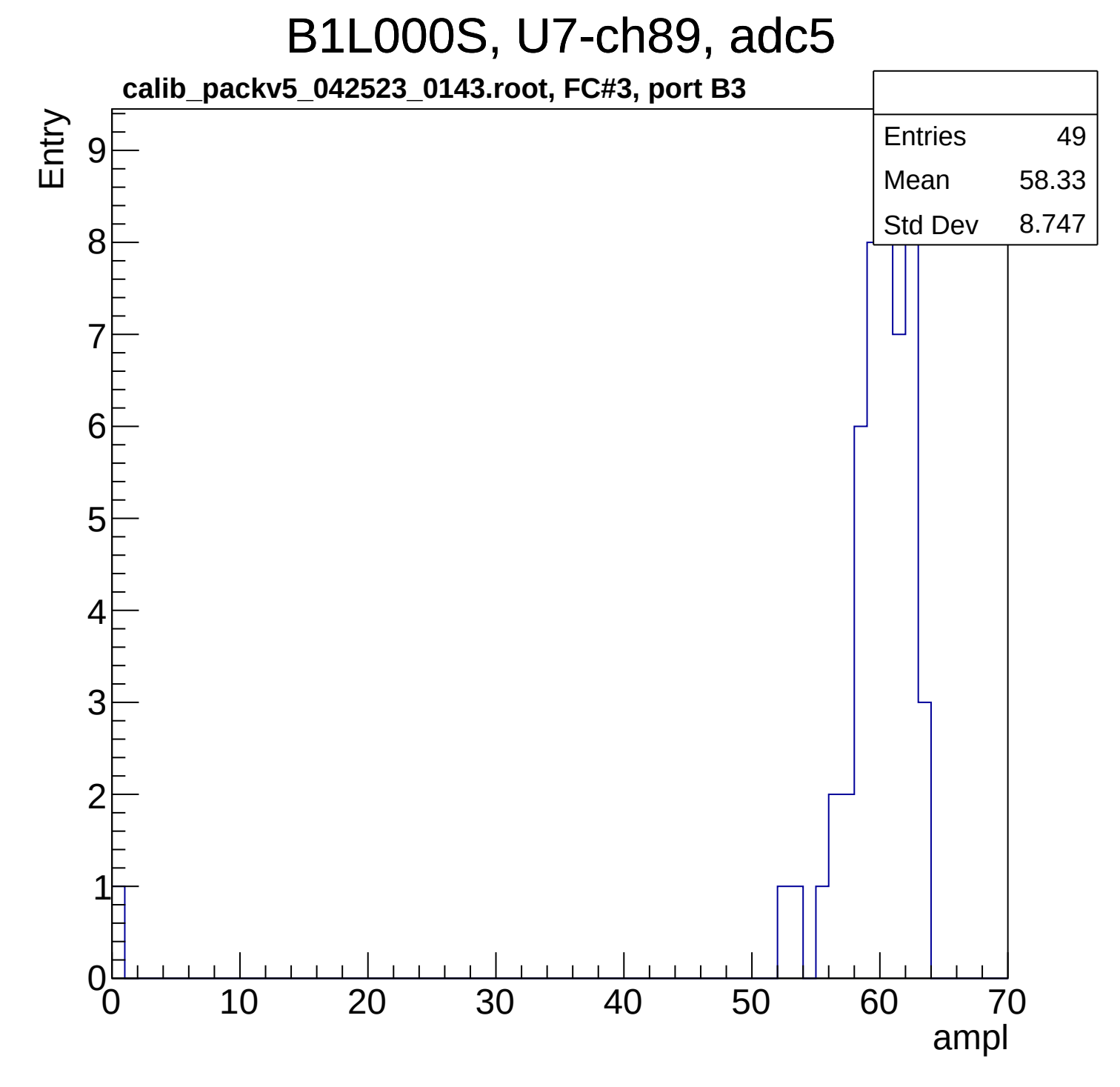
B1L000S, U7-ch89, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 58.33
Std Dev 8.747
Entry
ampl
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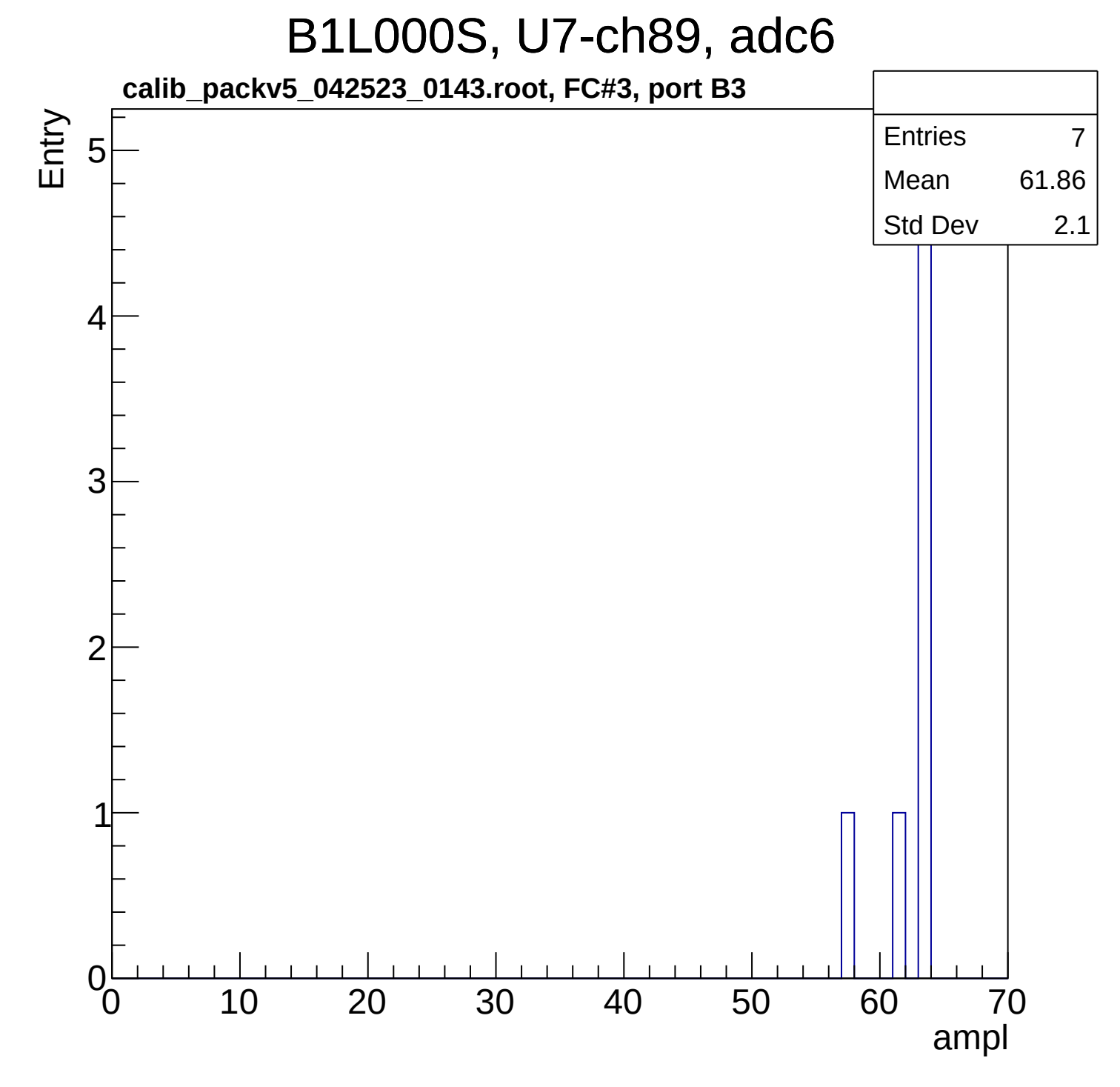
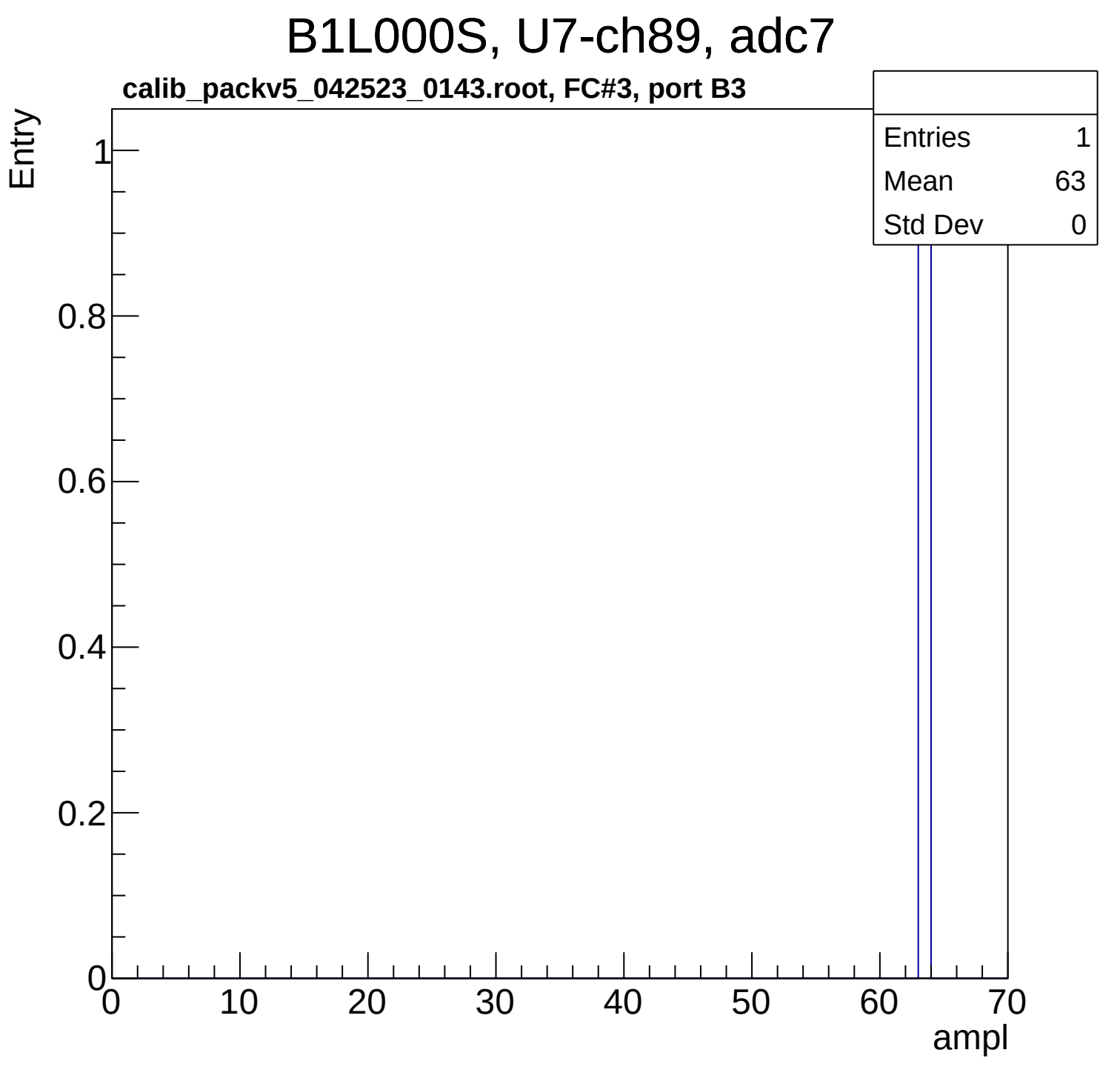
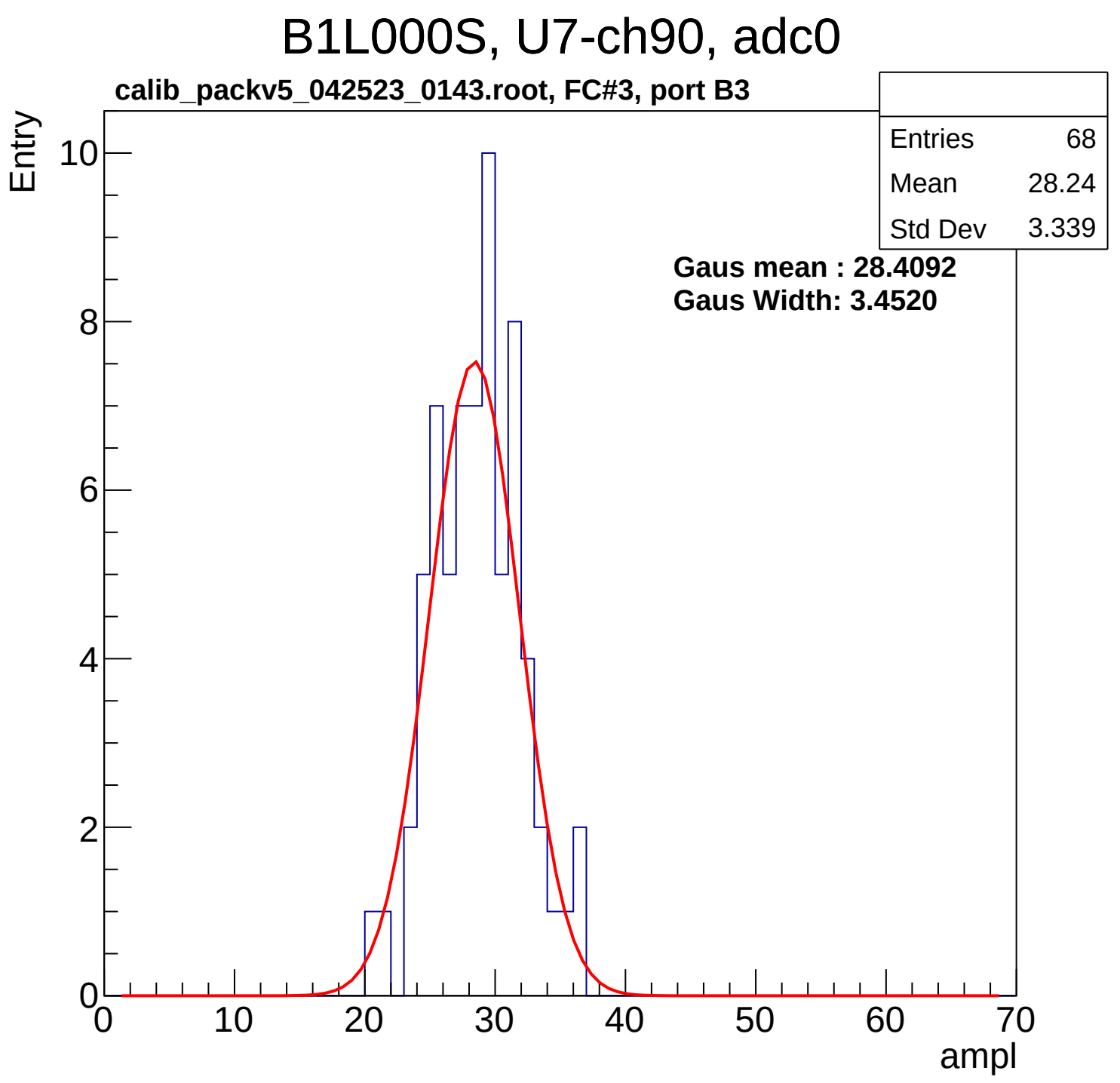

B1L000S, U7-ch89, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 61.86
Std Dev 2.1
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch89, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch90, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 28.24
Std Dev 3.339
Gaus mean : 28.4092
Gaus Width: 3.4520
Entry
ampl
0
2
4
6
8
10
0
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30
40
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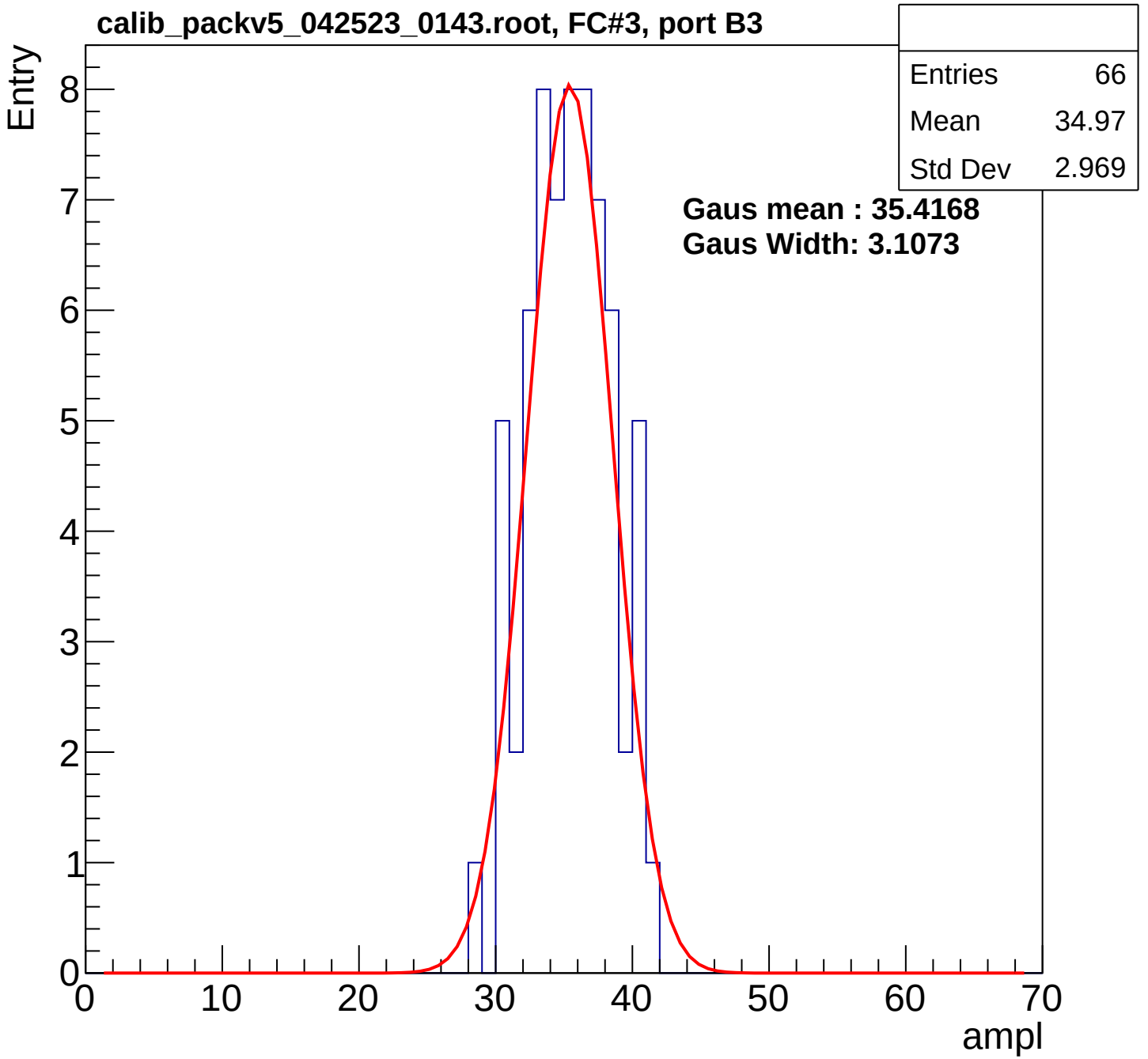
B1L000S, U7-ch90, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 34.97
Std Dev 2.969
Gaus mean : 35.4168
Gaus Width: 3.1073
Entry
ampl
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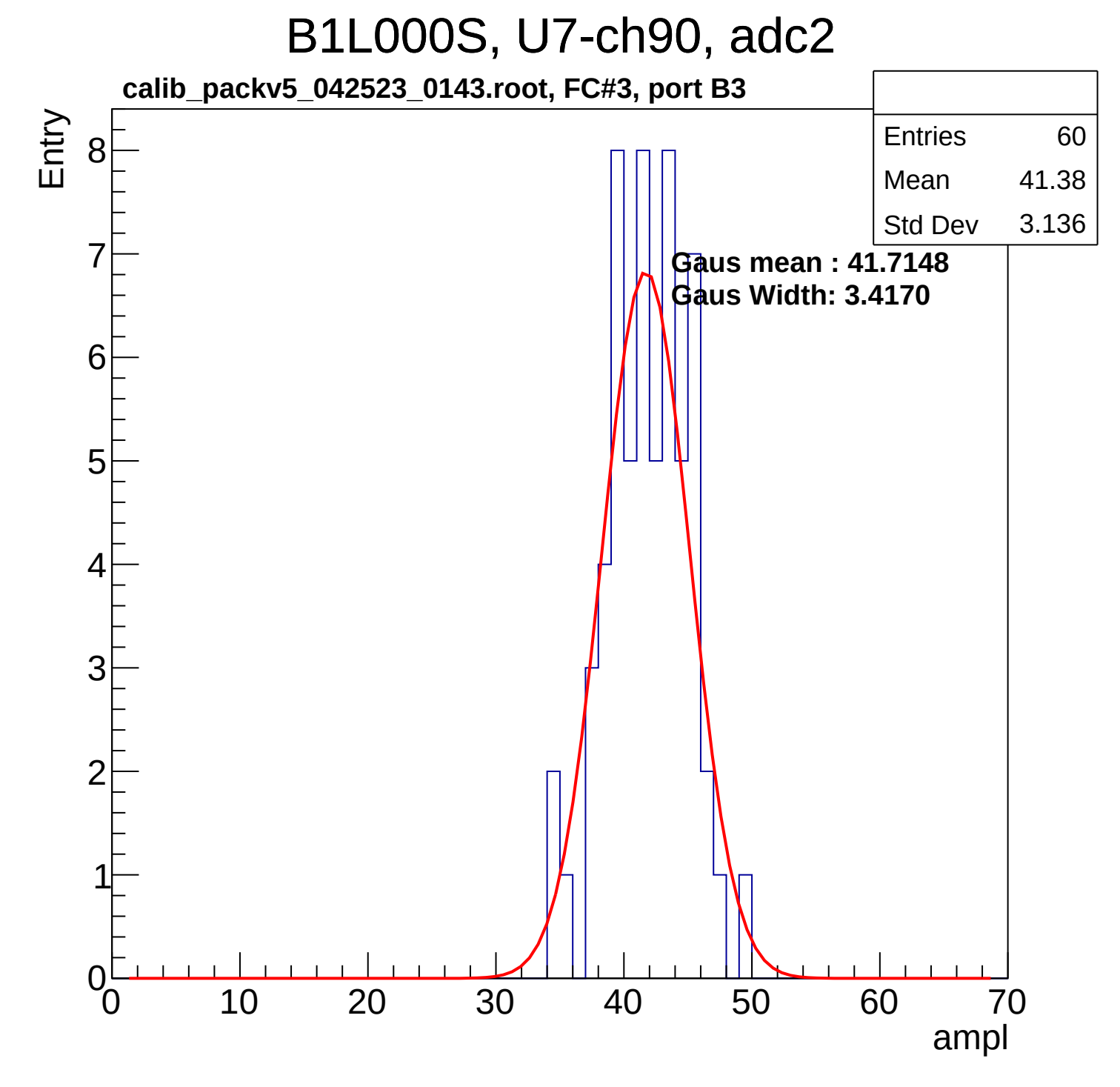
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch90, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 41.38
Std Dev 3.136
Gaus mean : 41.7148
Gaus Width: 3.4170
Entry
ampl
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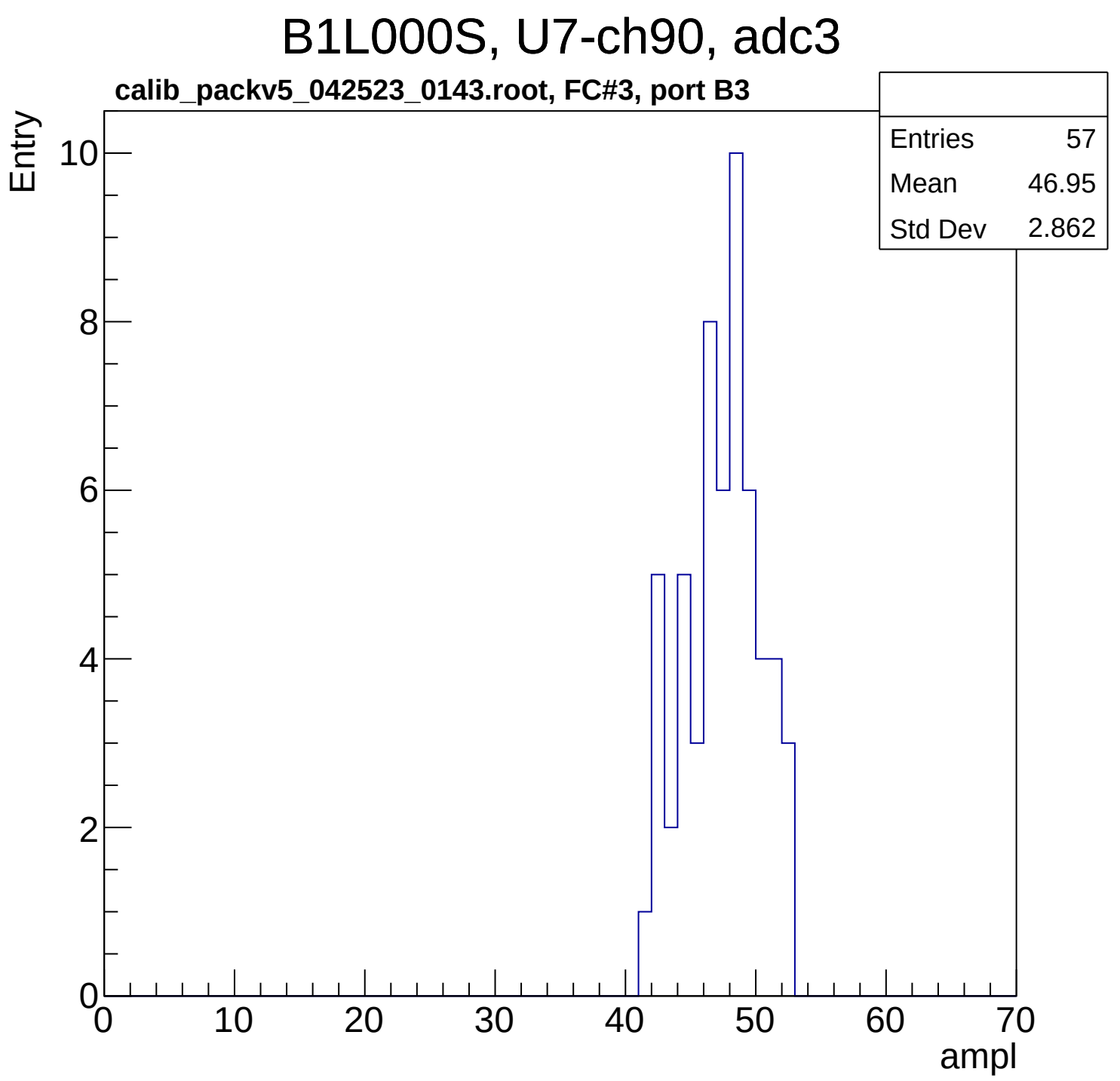
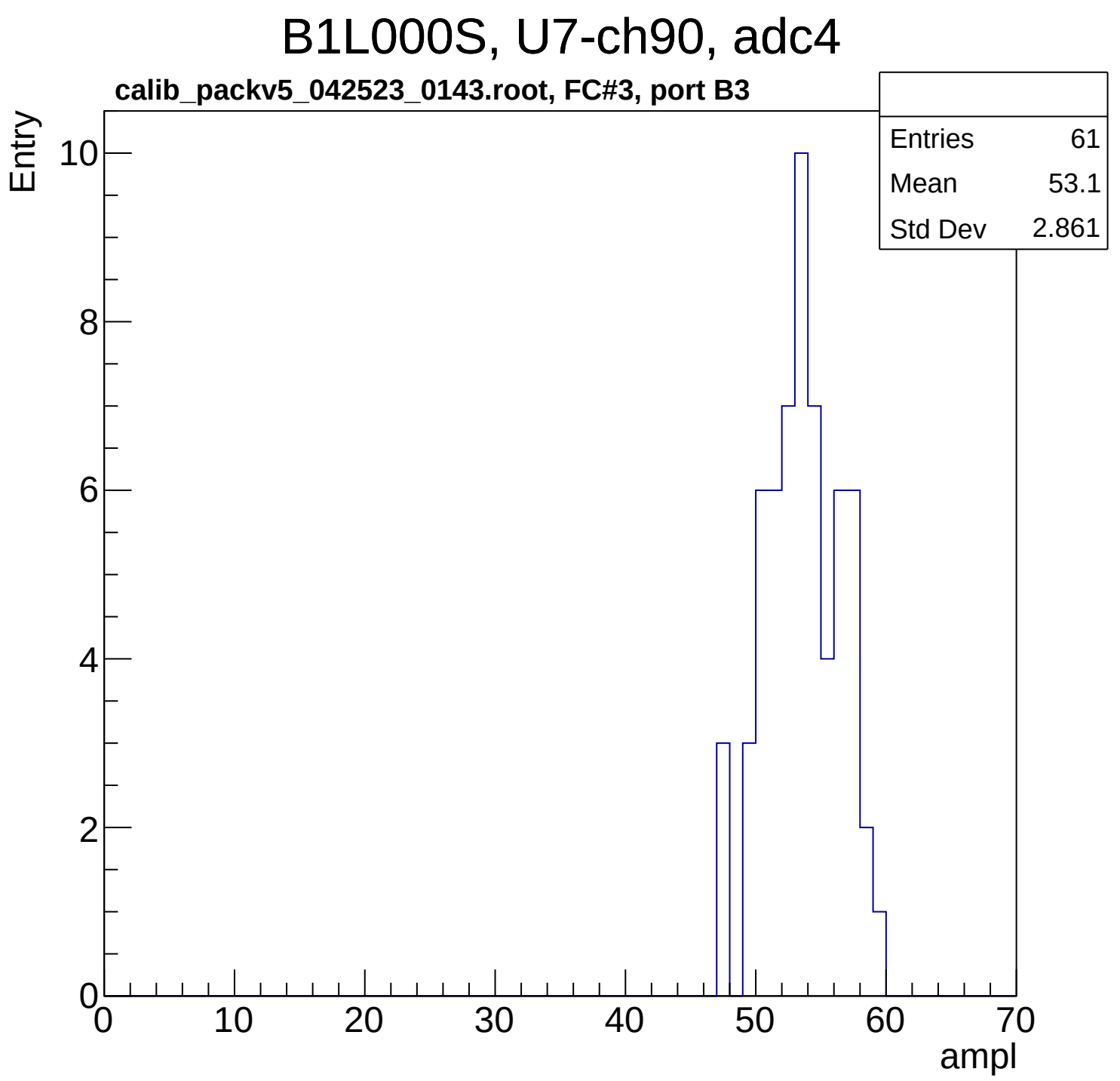
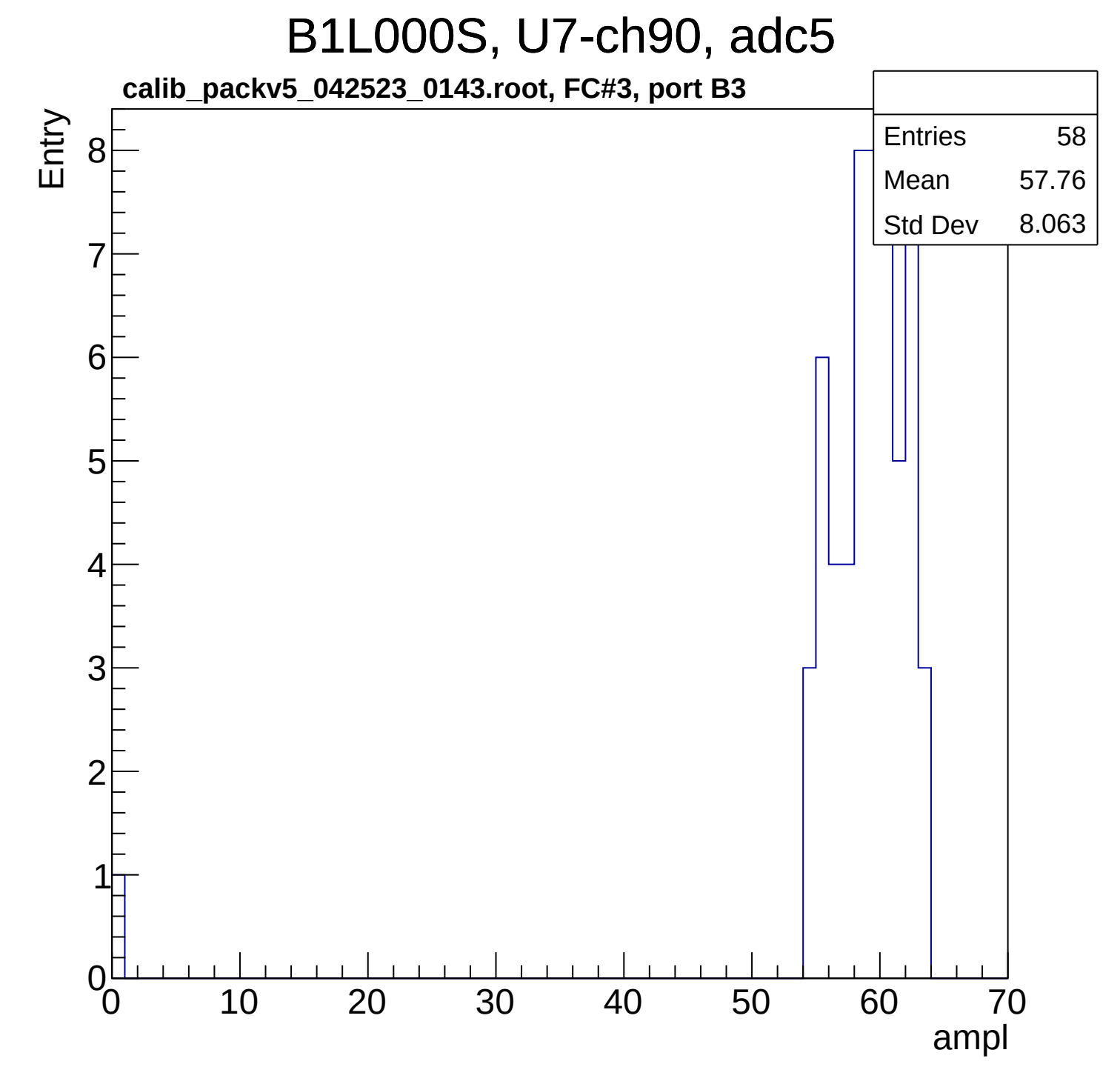
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch90, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 46.95
Std Dev 2.862
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch90, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 53.1
Std Dev 2.861
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch90, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 57.76
Std Dev 8.063
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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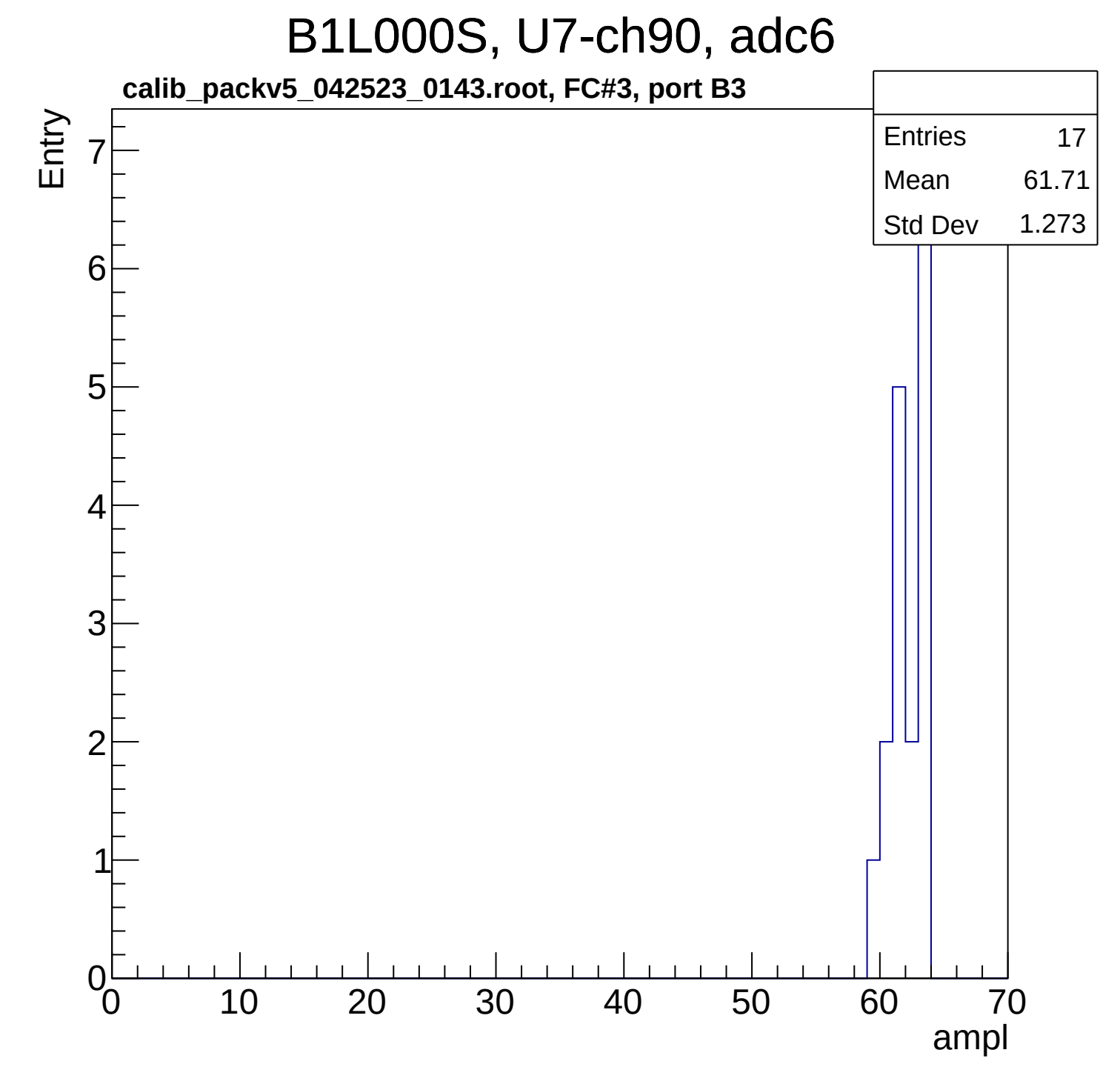
B1L000S, U7-ch90, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.71
Std Dev 1.273
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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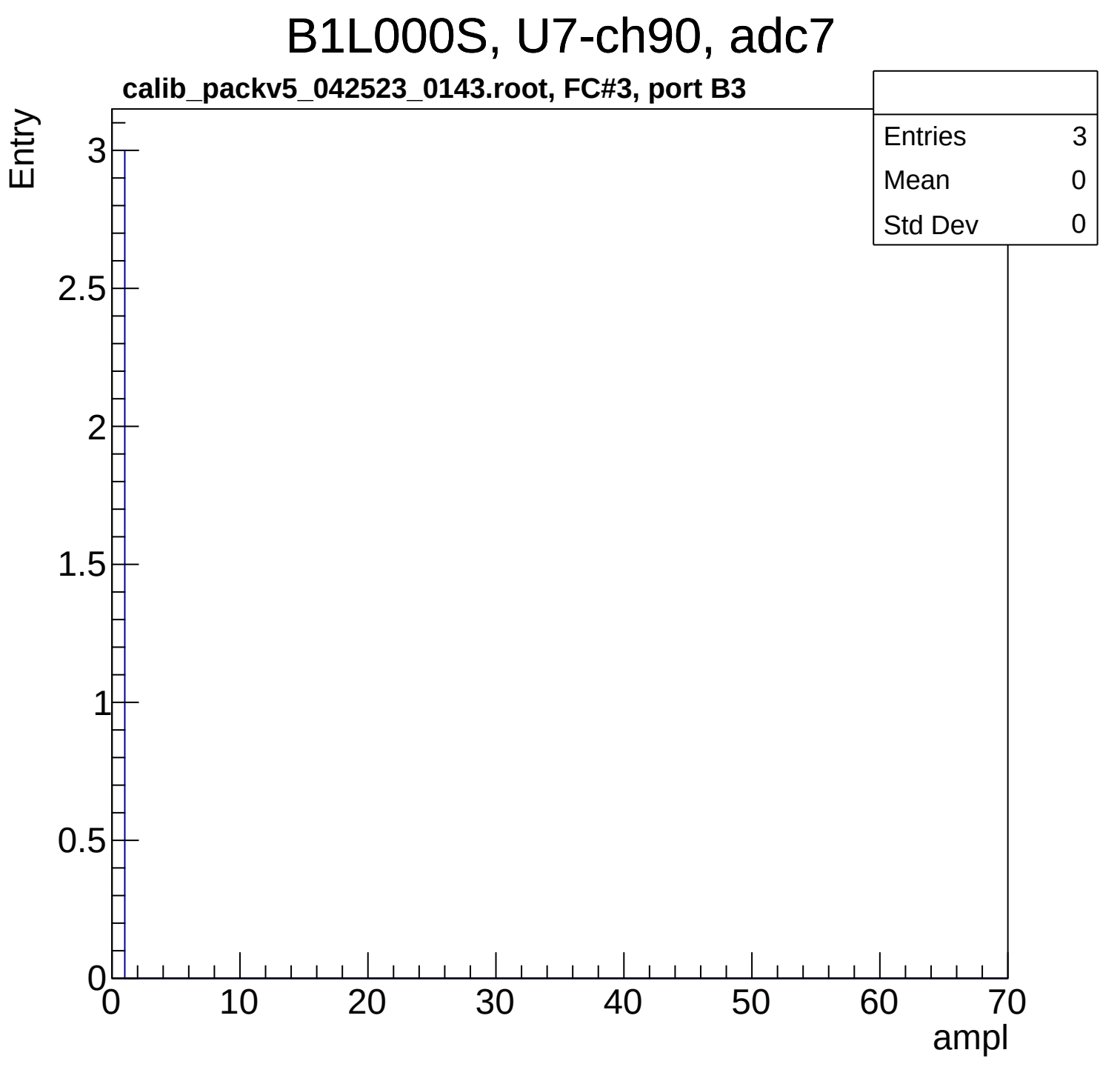
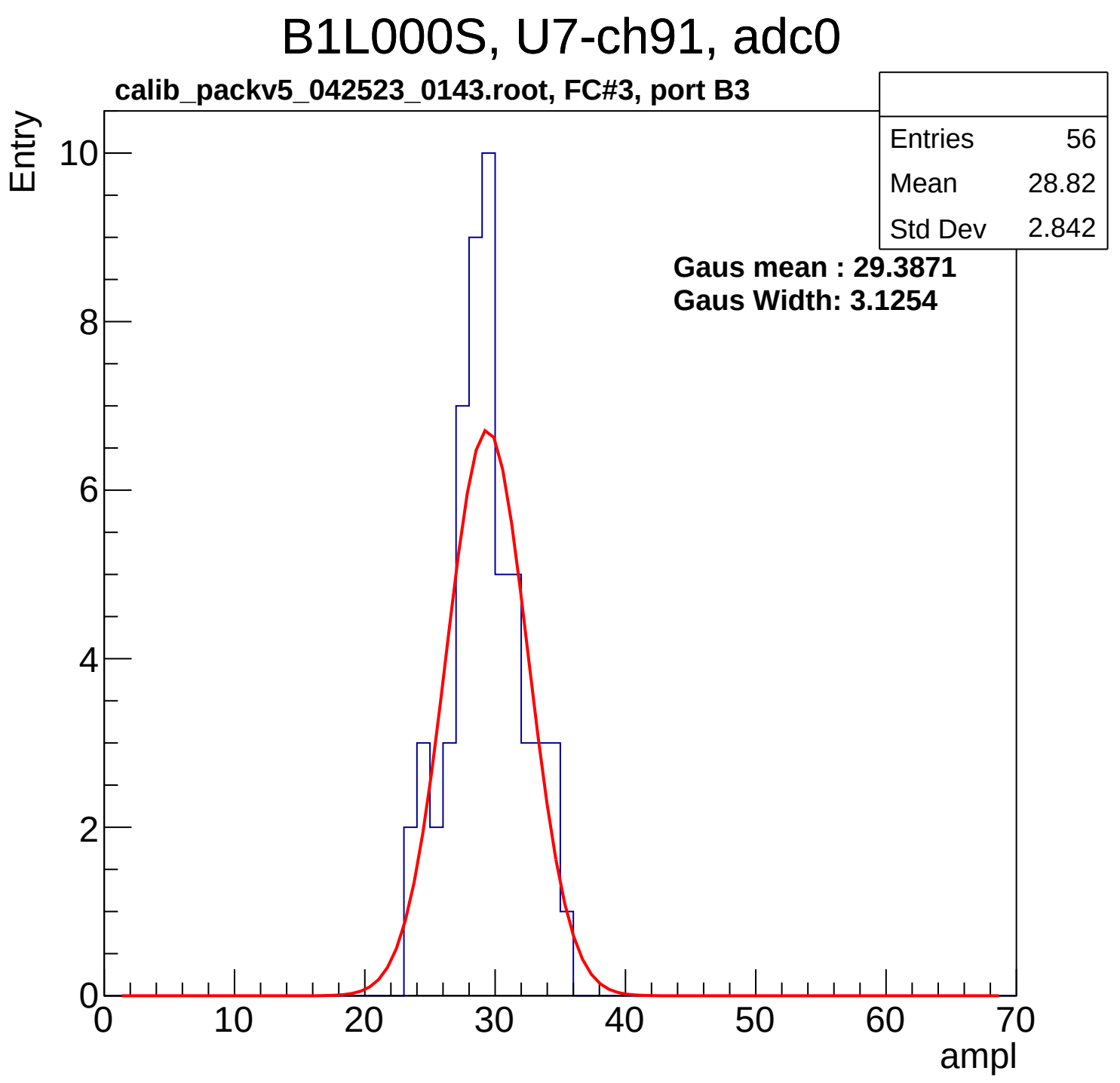

B1L000S, U7-ch90, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch91, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 28.82
Std Dev 2.842
Gaus mean : 29.3871
Gaus Width: 3.1254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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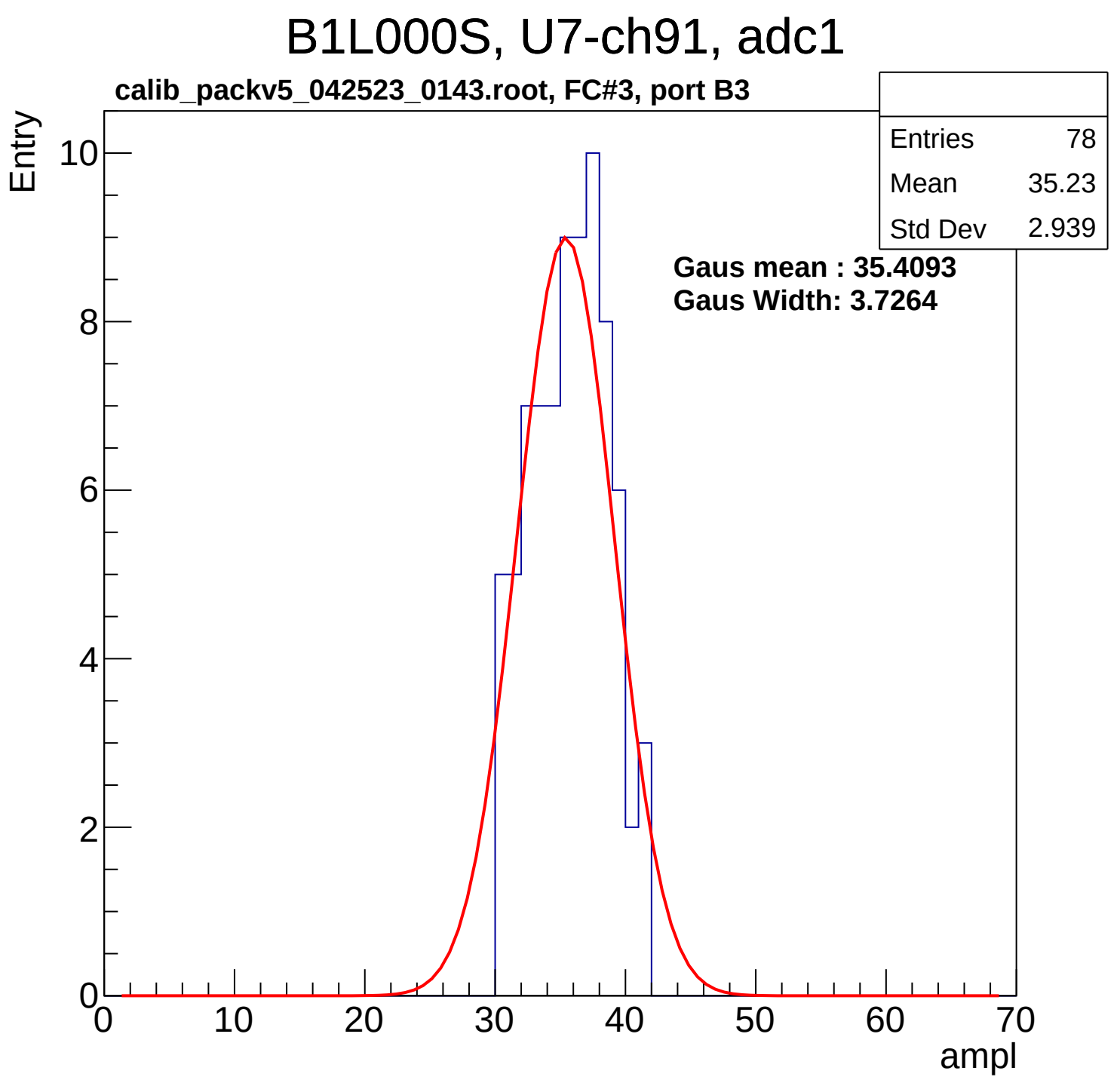
B1L000S, U7-ch91, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 35.23
Std Dev 2.939
Gaus mean : 35.4093
Gaus Width: 3.7264
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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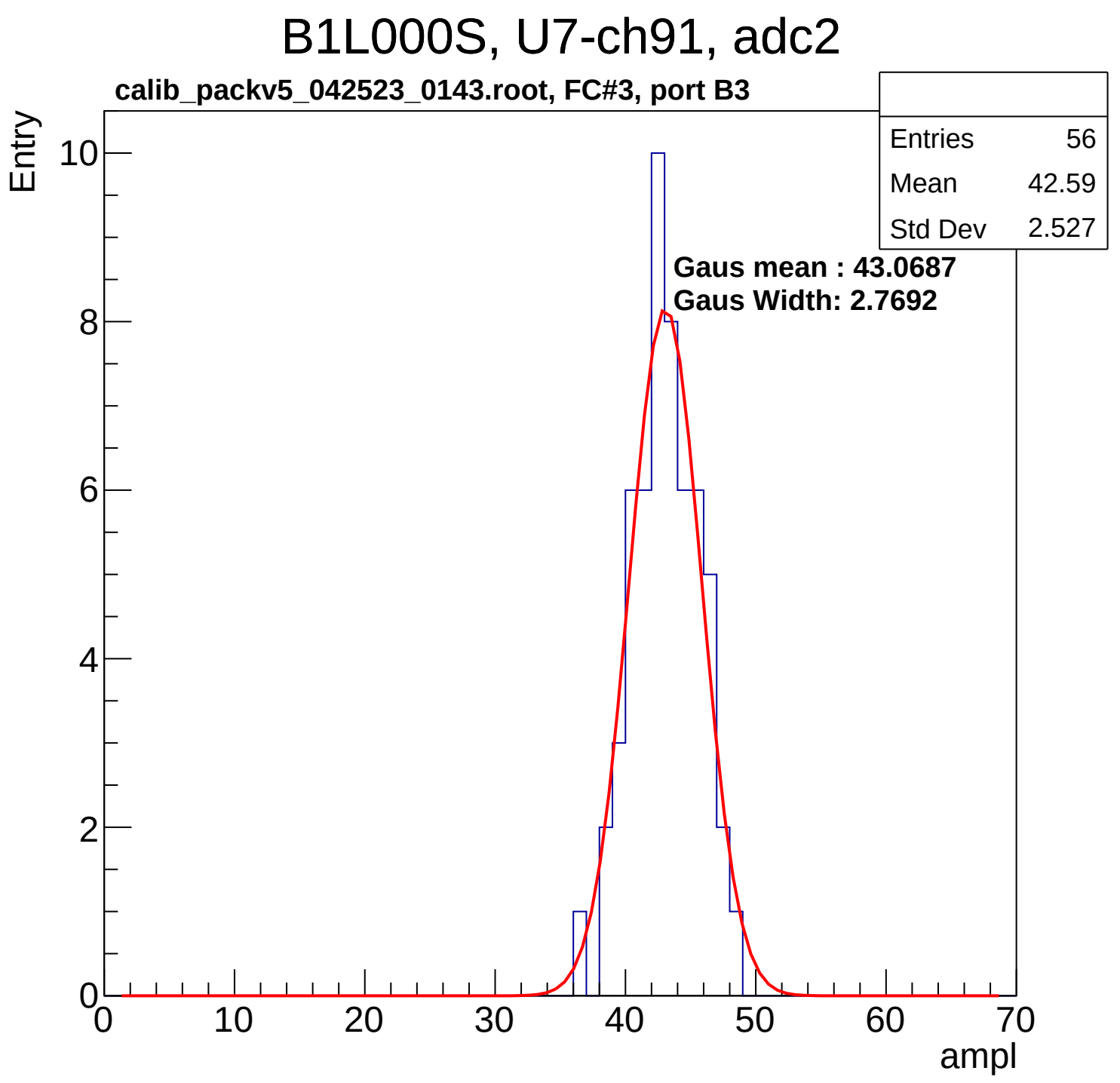
B1L000S, U7-ch91, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.59
Std Dev 2.527
Gaus mean : 43.0687
Gaus Width: 2.7692
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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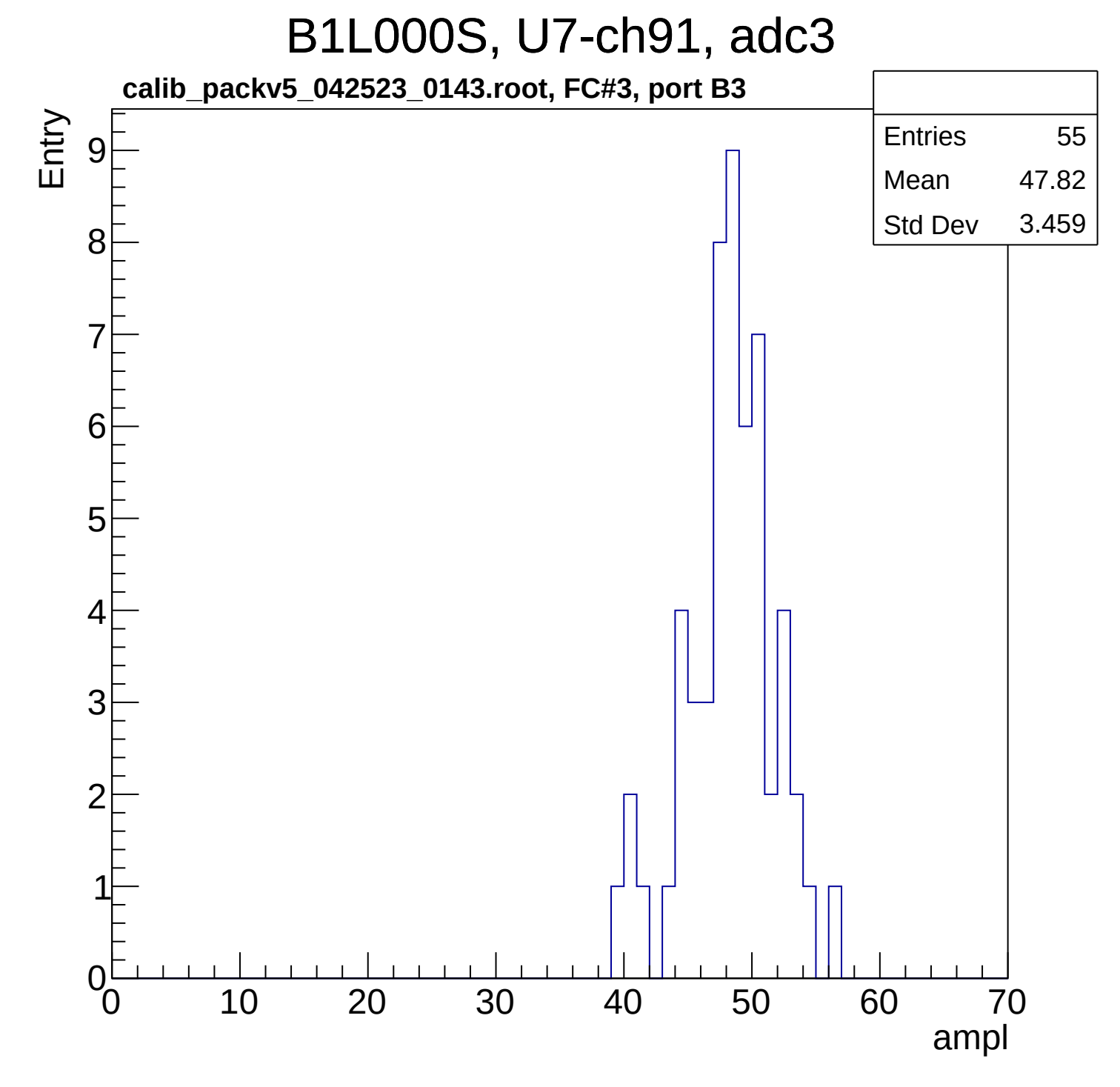
60
70


B1L000S, U7-ch91, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 47.82
Std Dev 3.459
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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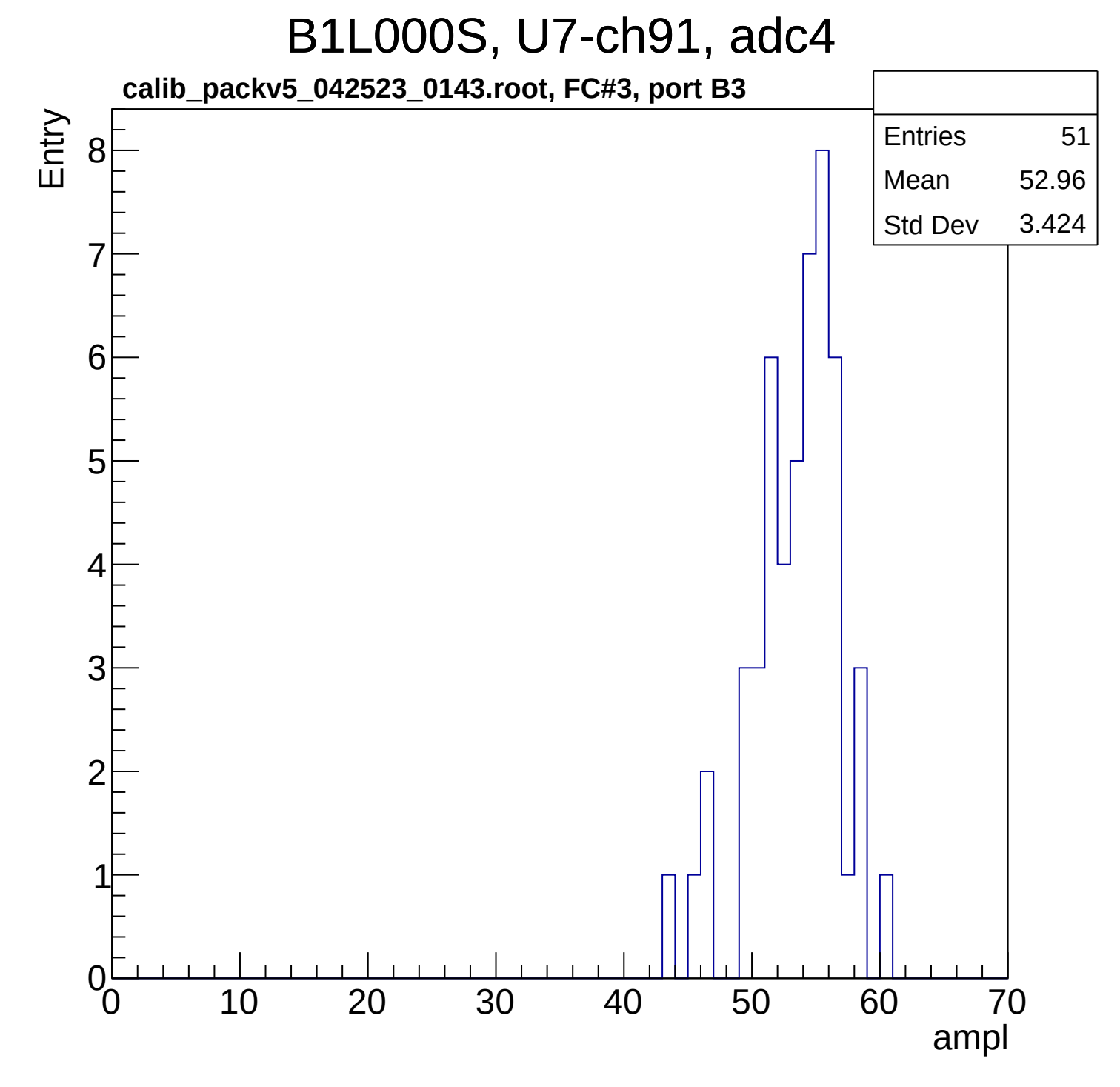
B1L000S, U7-ch91, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 52.96
Std Dev 3.424
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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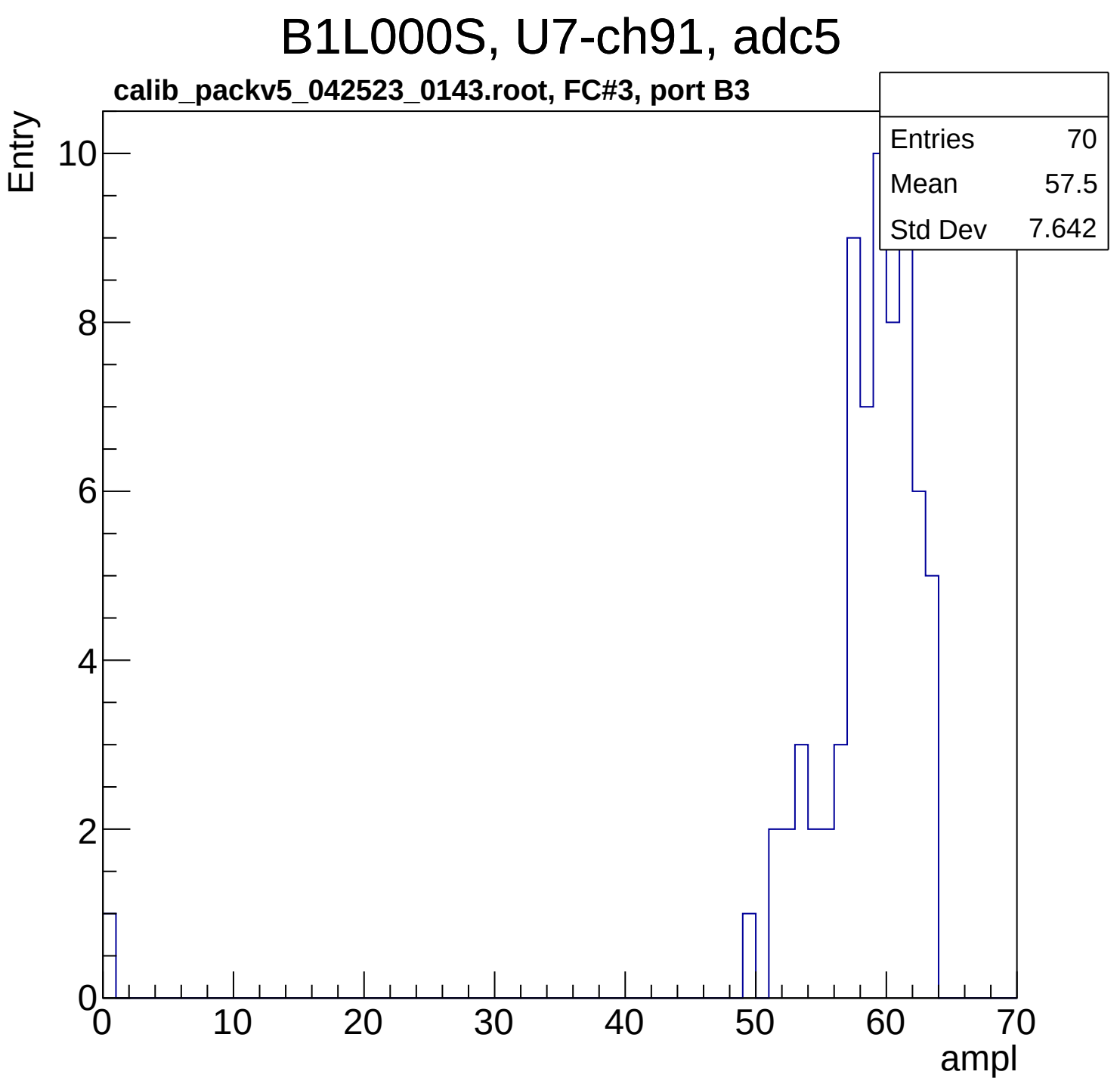
B1L000S, U7-ch91, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 57.5
Std Dev 7.642
Entry
ampl
0
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4
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8
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0
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60
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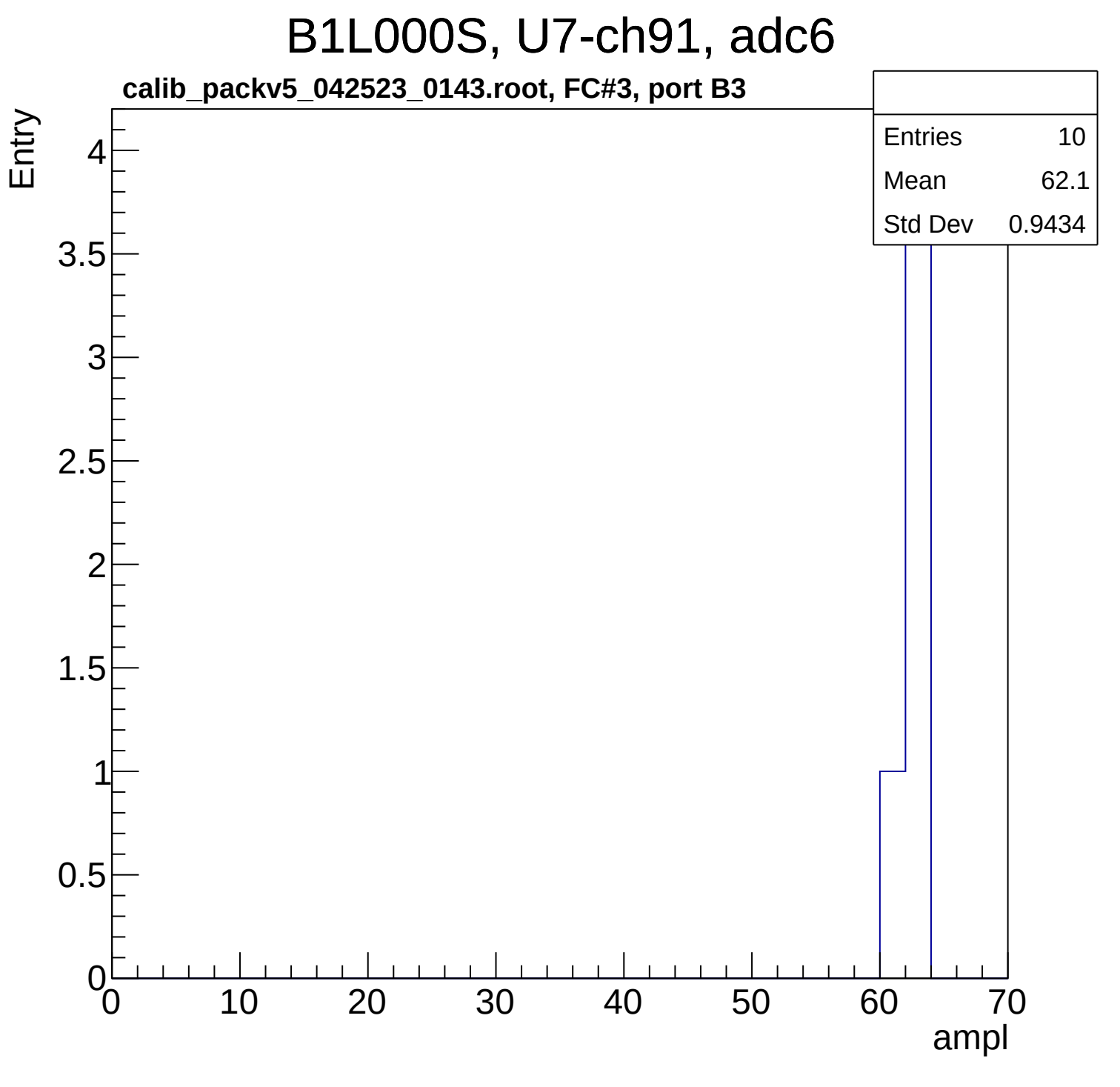
B1L000S, U7-ch91, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 62.1
Std Dev 0.9434
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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60
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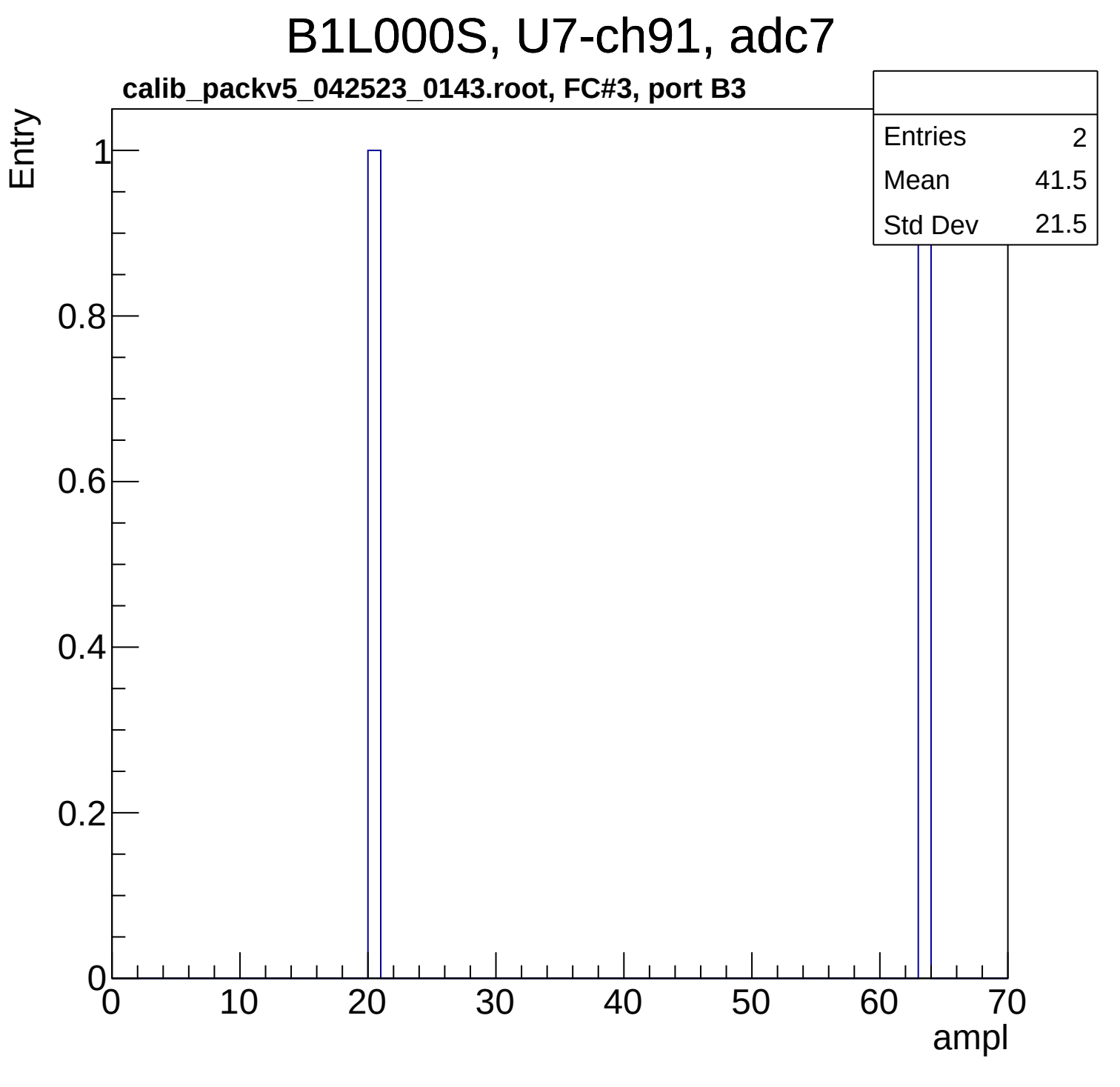
B1L000S, U7-ch91, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
ampl
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0.4
0.6
0.8
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0
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60
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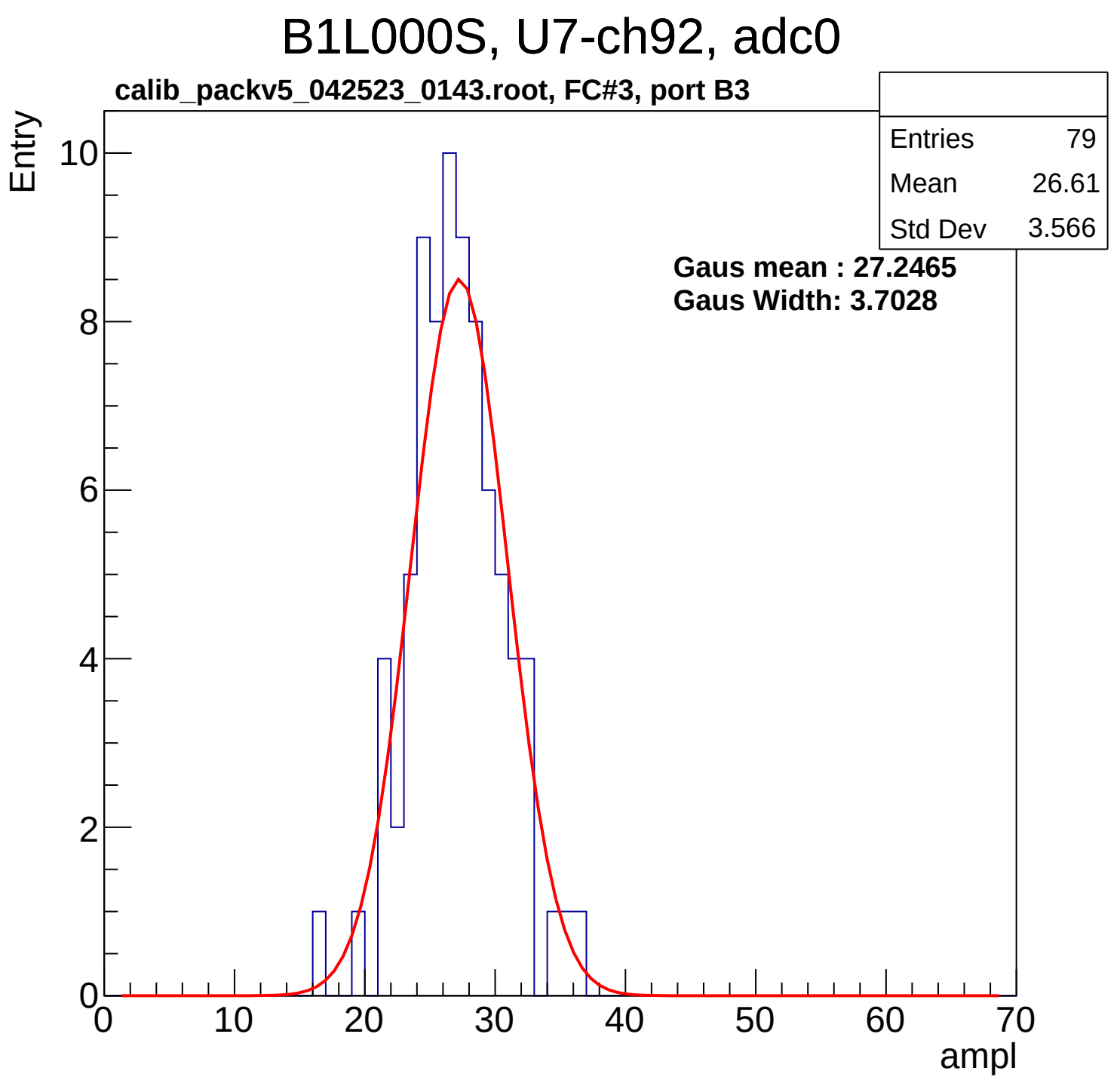
B1L000S, U7-ch92, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 26.61
Std Dev 3.566
Gaus mean : 27.2465
Gaus Width: 3.7028
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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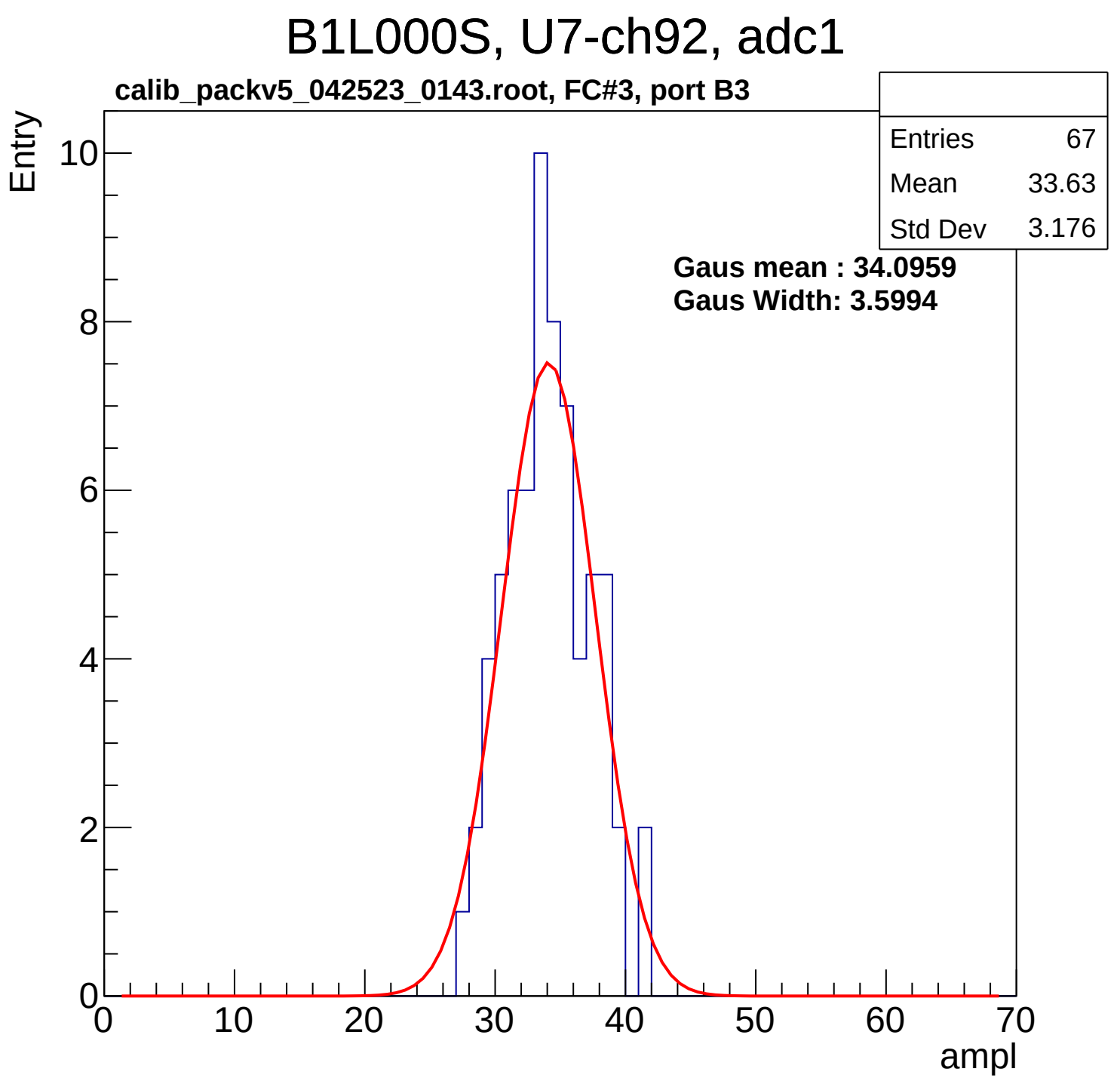
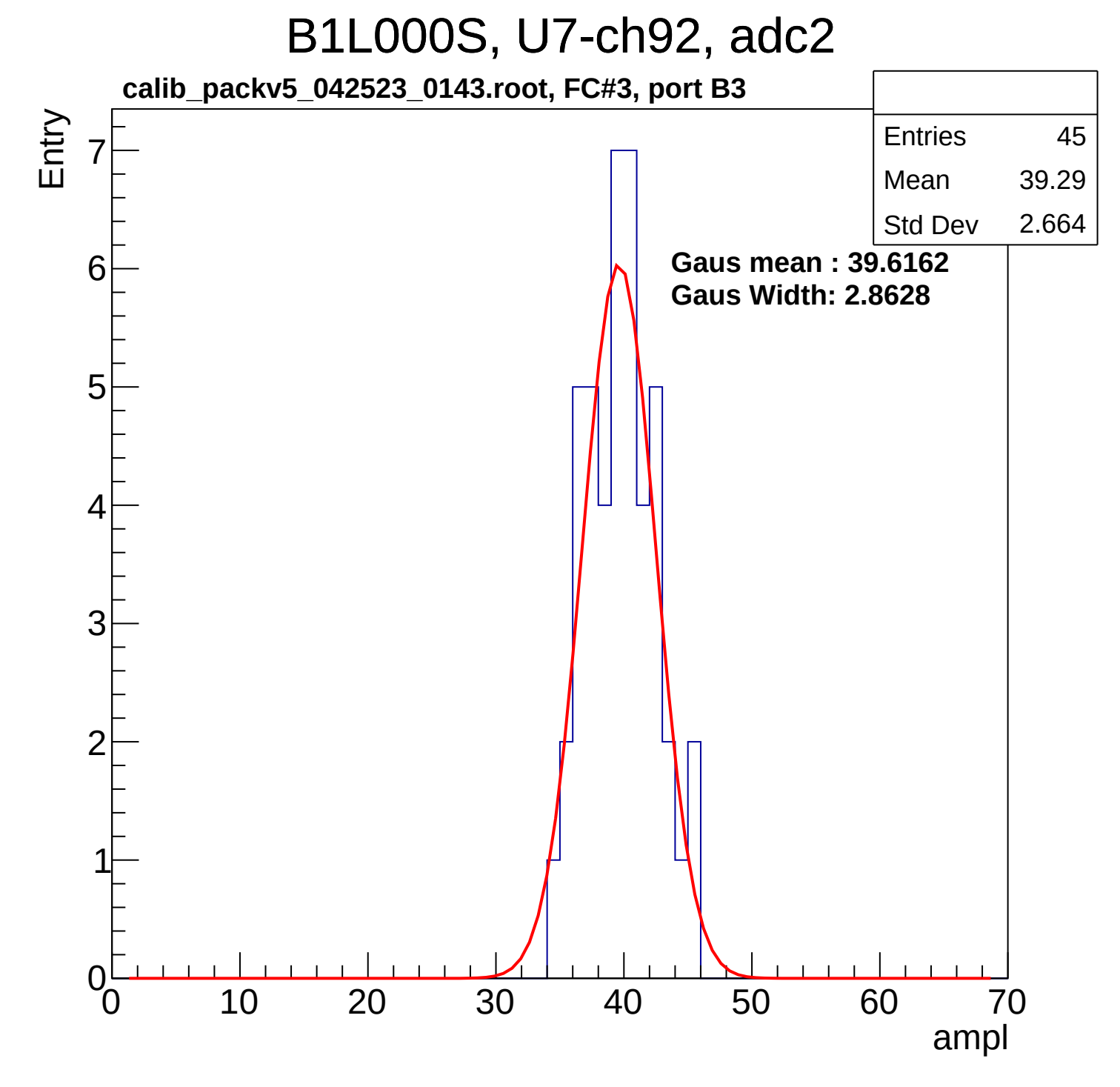

B1L000S, U7-ch92, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 33.63
Std Dev 3.176
Gaus mean : 34.0959
Gaus Width: 3.5994
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch92, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 39.29
Std Dev 2.664
Gaus mean : 39.6162
Gaus Width: 2.8628
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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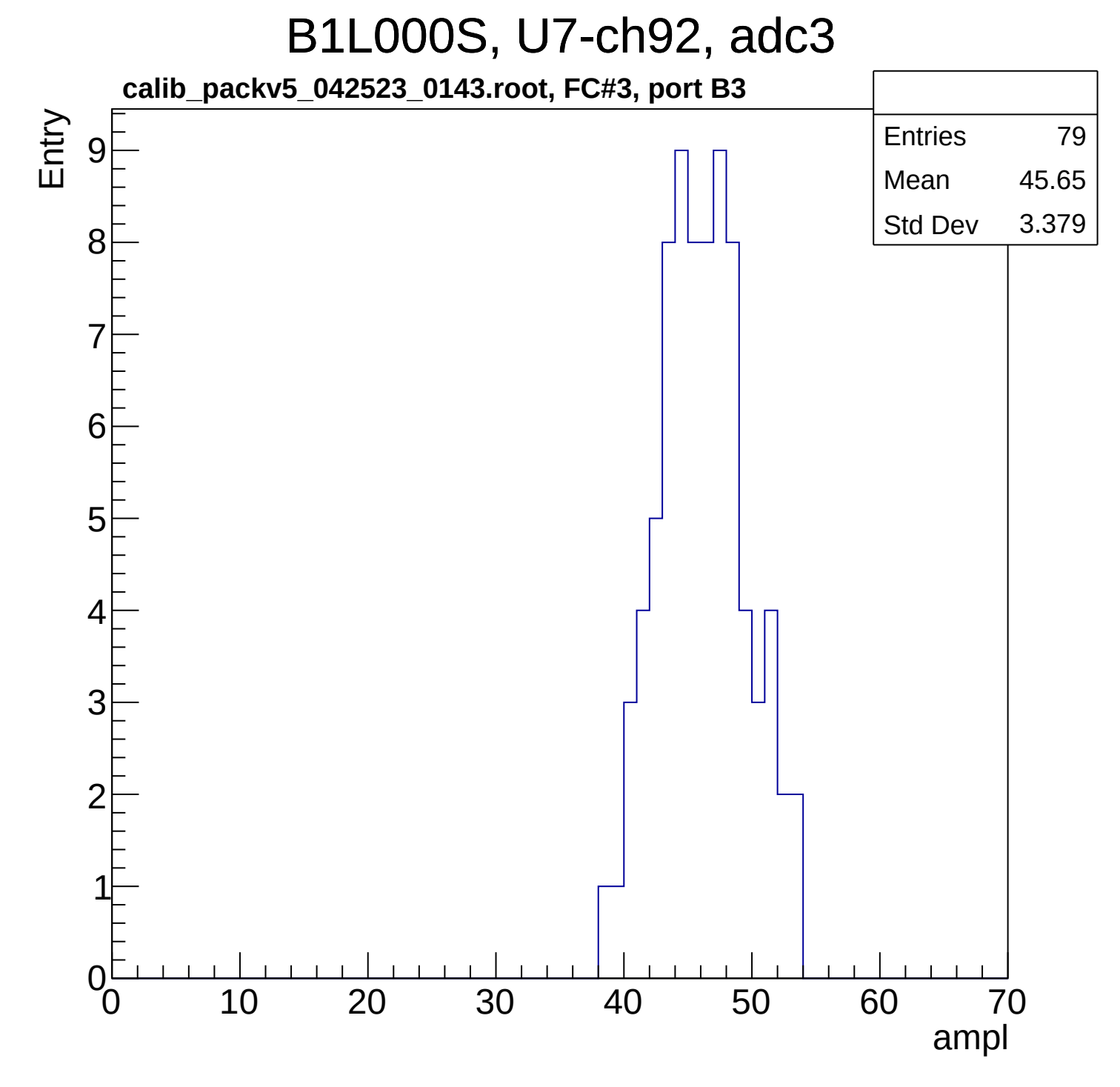
B1L000S, U7-ch92, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 45.65
Std Dev 3.379
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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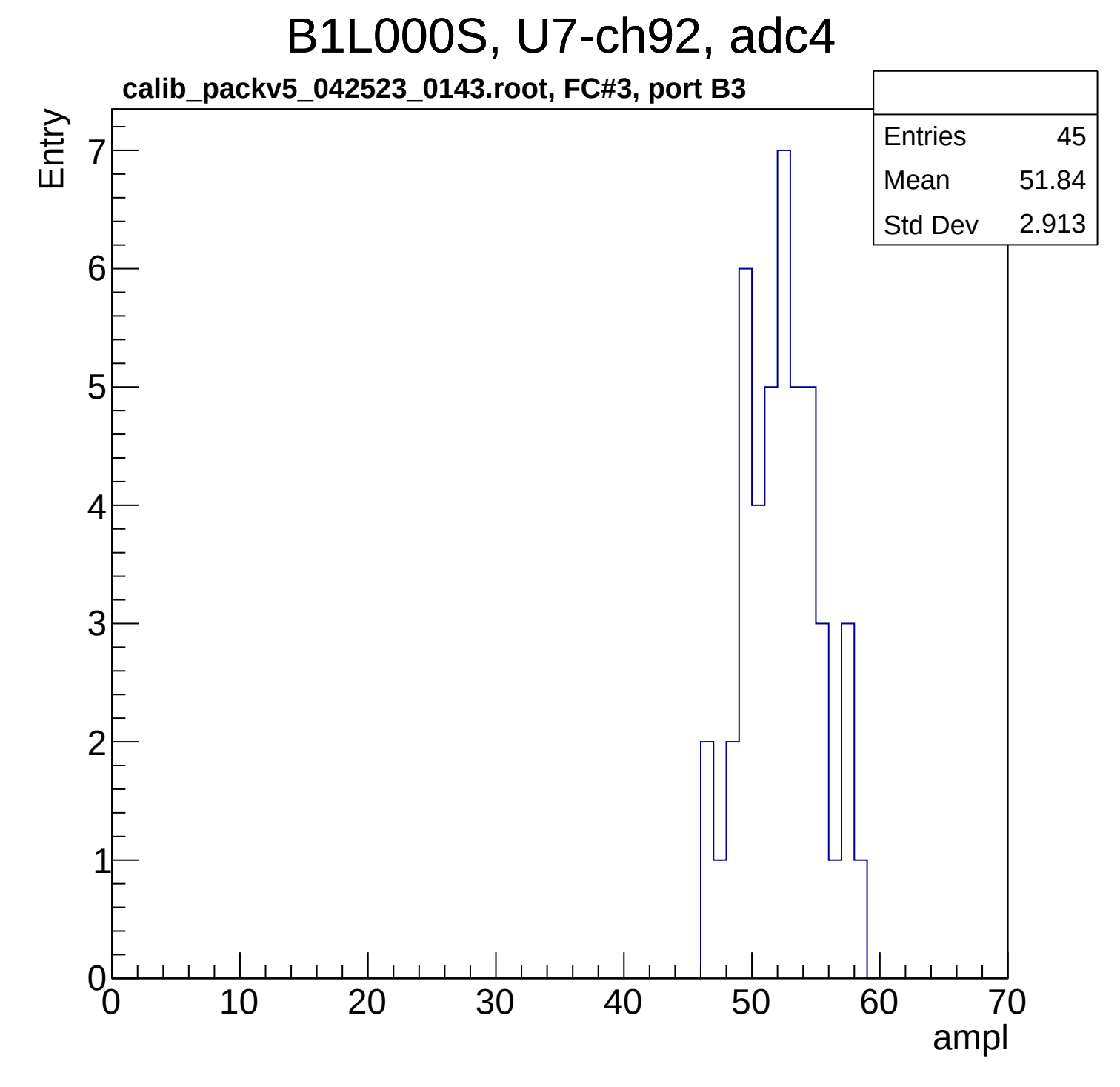
B1L000S, U7-ch92, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 51.84
Std Dev 2.913
Entry
ampl
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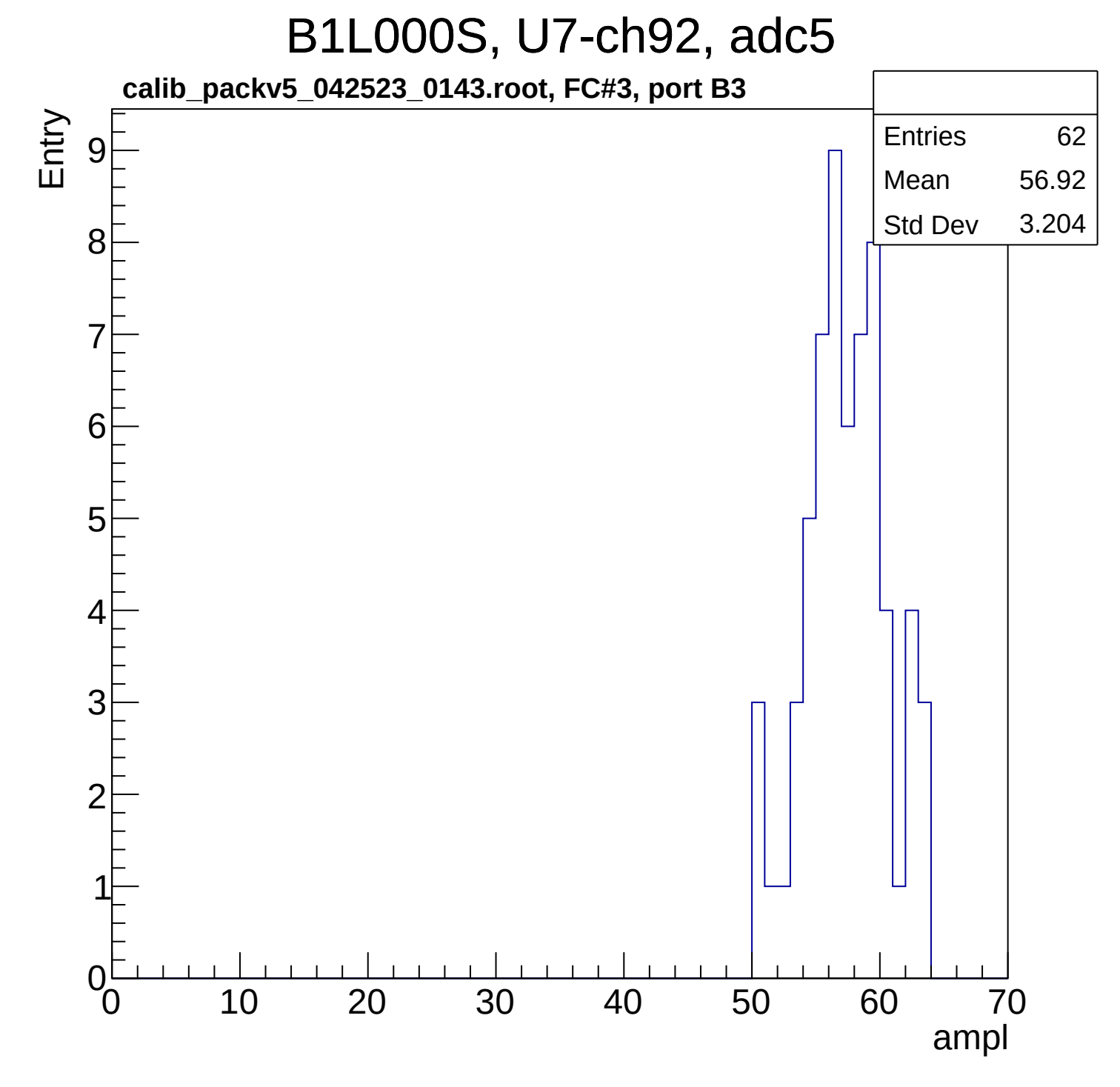
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch92, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 56.92
Std Dev 3.204
Entry
ampl
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0
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40
50
60
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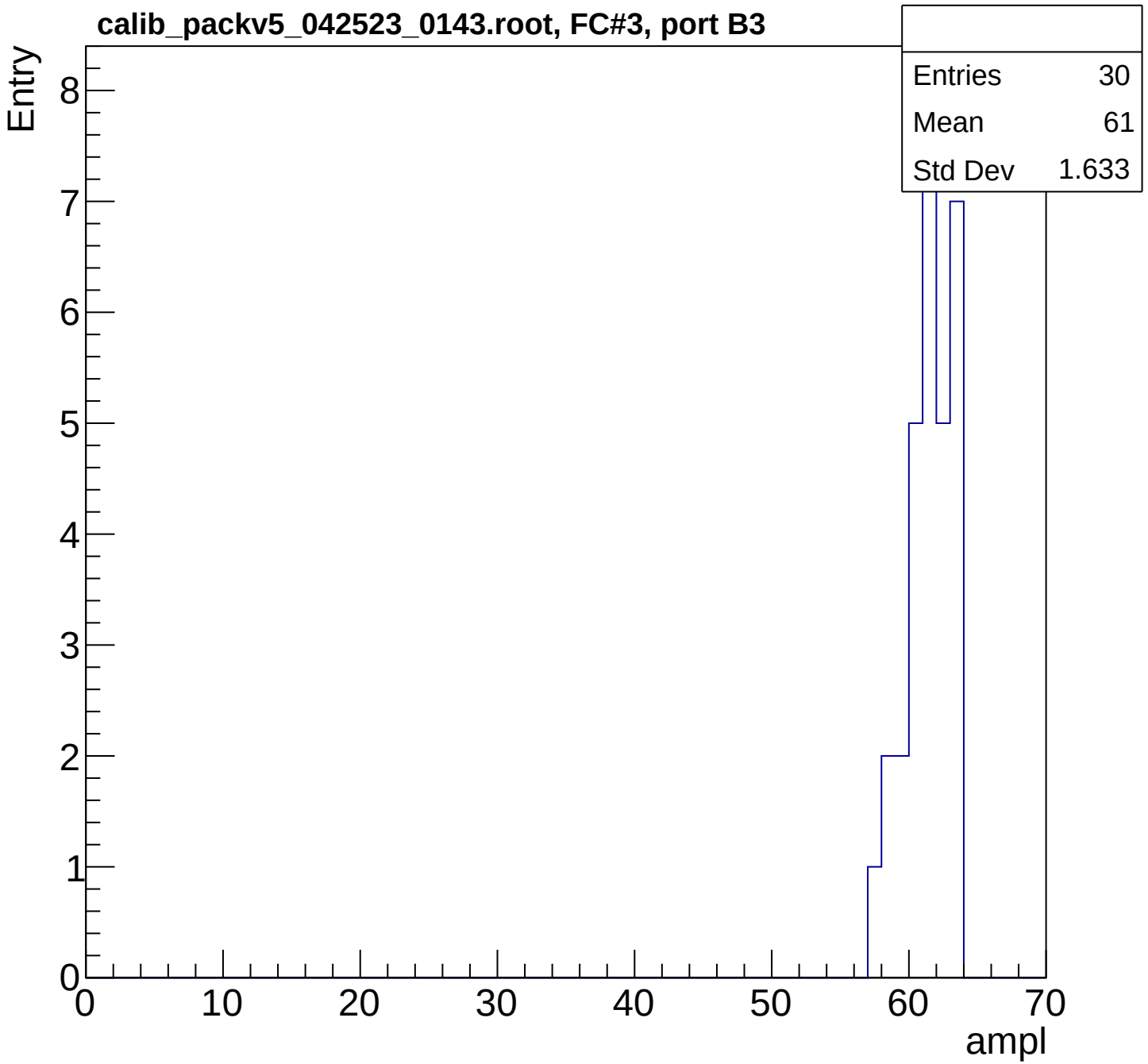
B1L000S, U7-ch92, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 30
Mean 61
Std Dev 1.633
Entry
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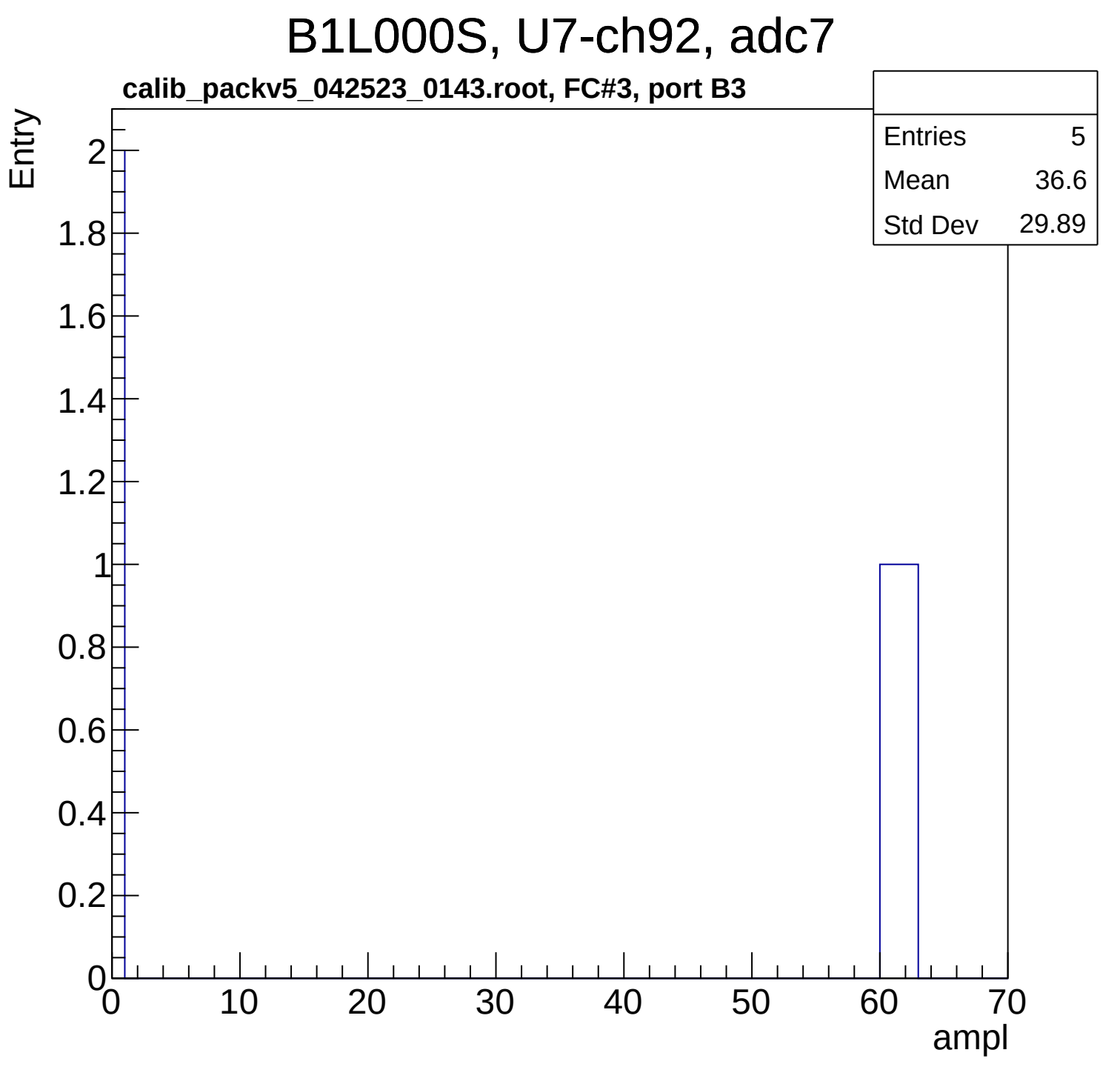
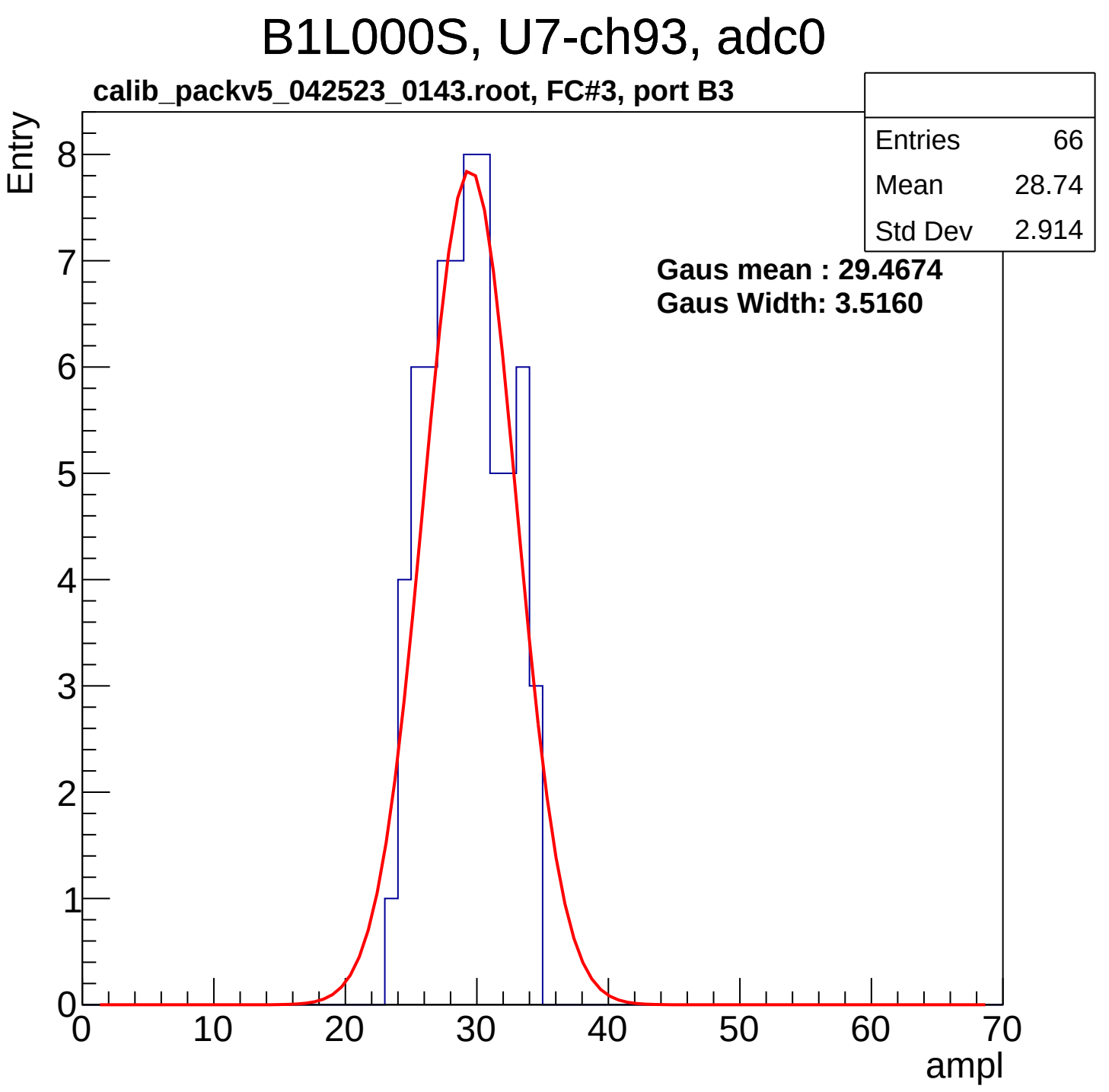
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch92, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 36.6
Std Dev 29.89
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch93, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.74
Std Dev 2.914
Gaus mean : 29.4674
Gaus Width: 3.5160
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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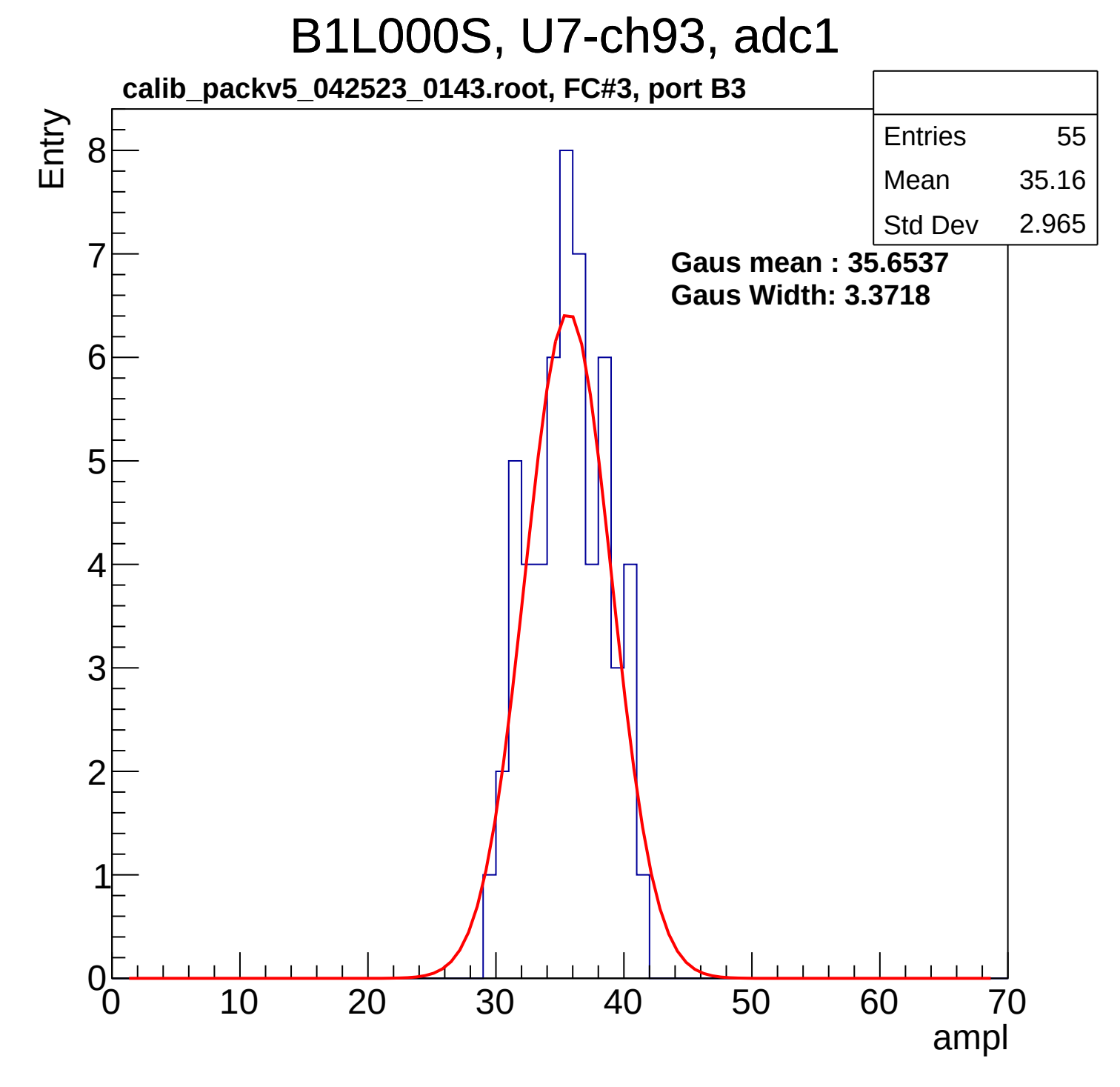
B1L000S, U7-ch93, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 35.16
Std Dev 2.965
Gaus mean : 35.6537
Gaus Width: 3.3718
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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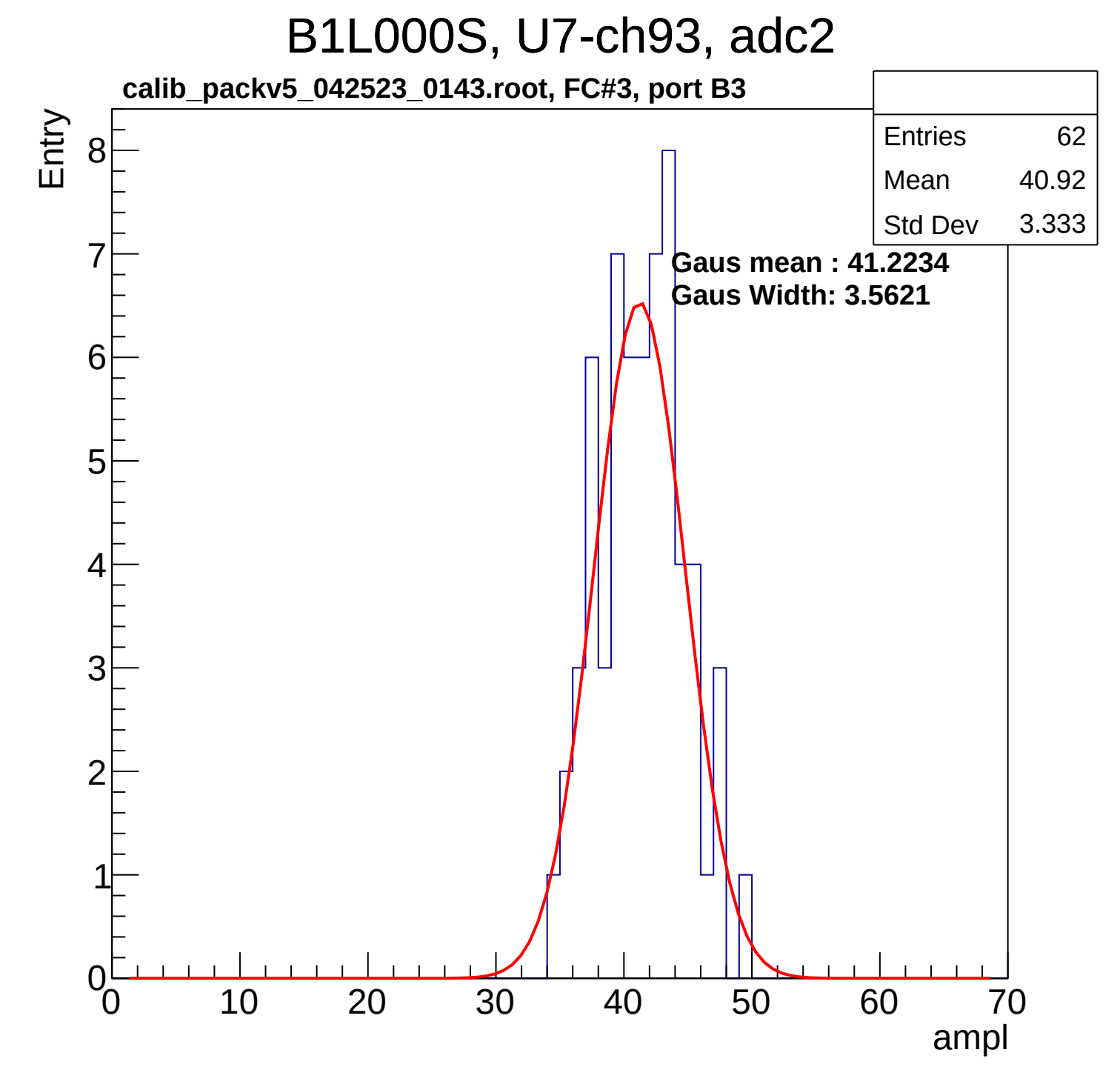
B1L000S, U7-ch93, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 40.92
Std Dev 3.333
Gaus mean : 41.2234
Gaus Width: 3.5621
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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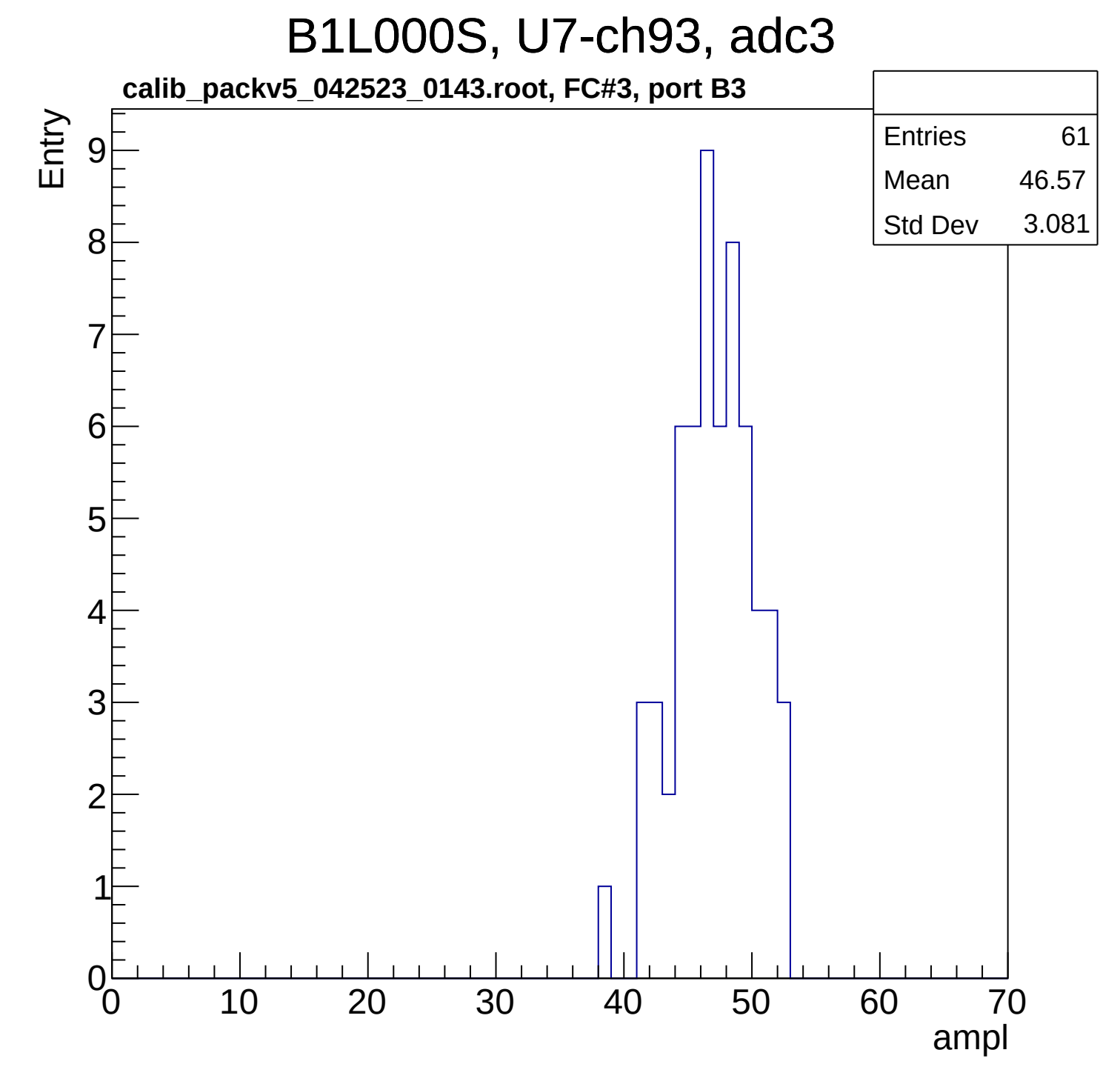
B1L000S, U7-ch93, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 46.57
Std Dev 3.081
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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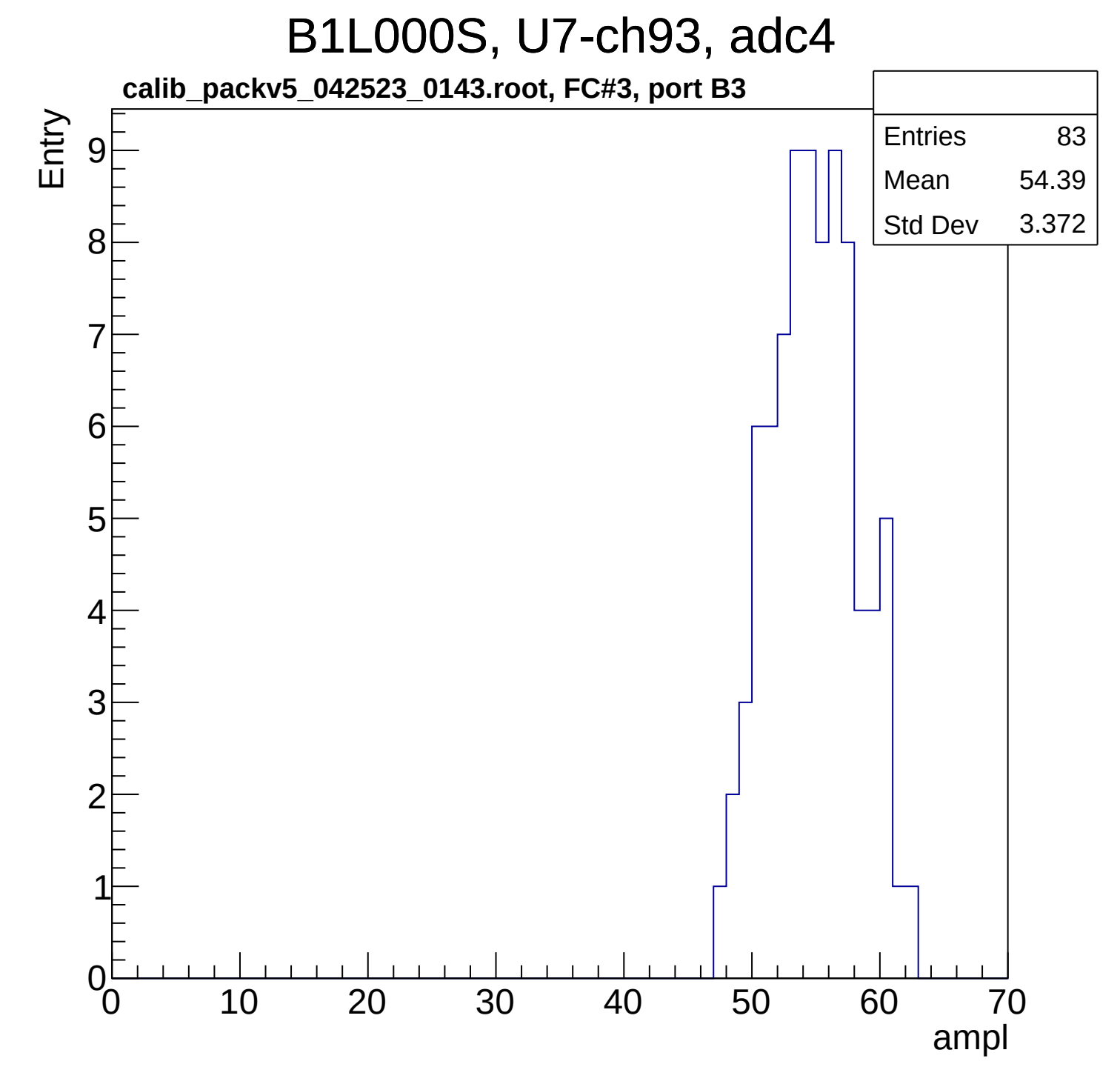
B1L000S, U7-ch93, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 54.39
Std Dev 3.372
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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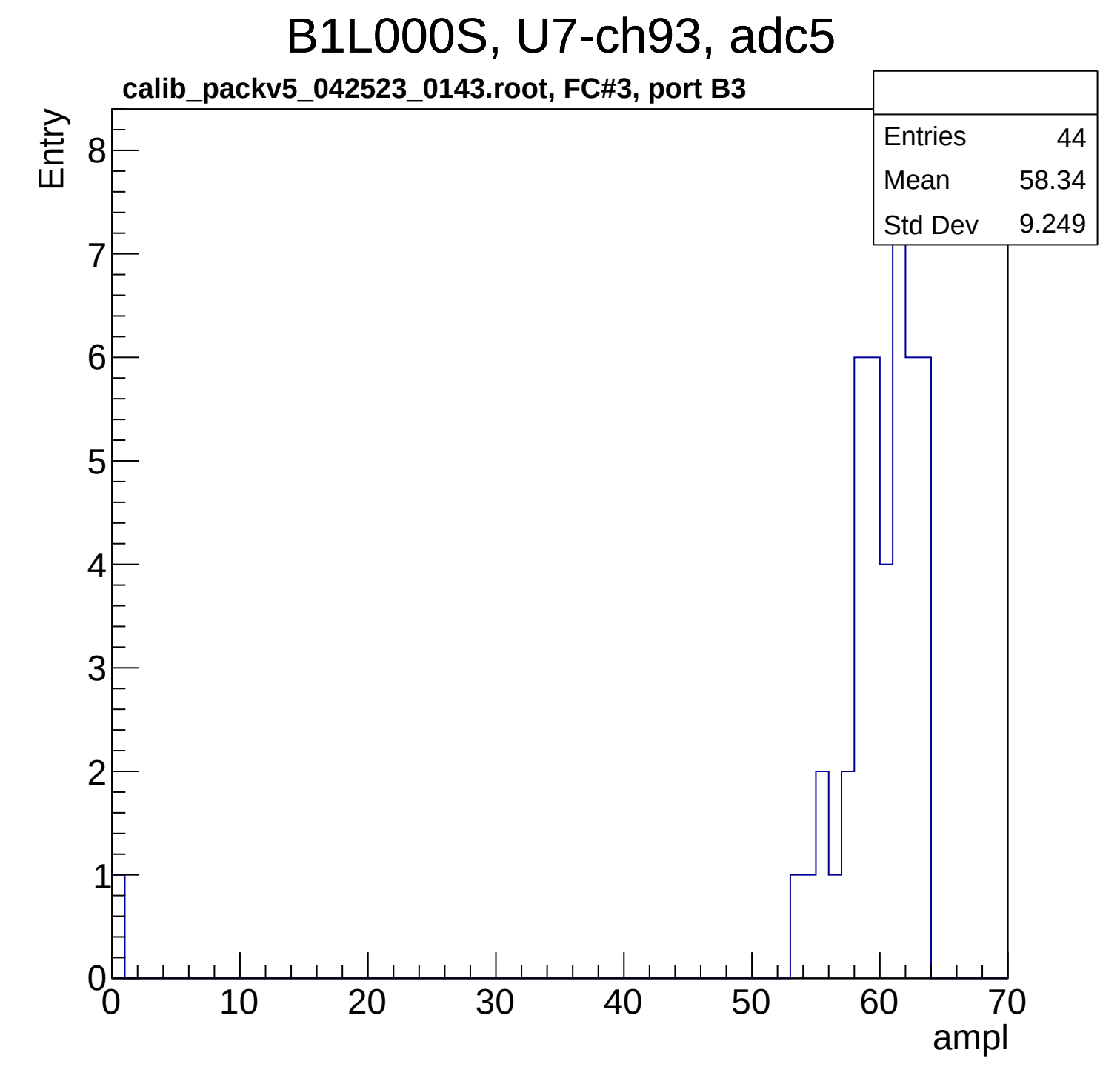
B1L000S, U7-ch93, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 58.34
Std Dev 9.249
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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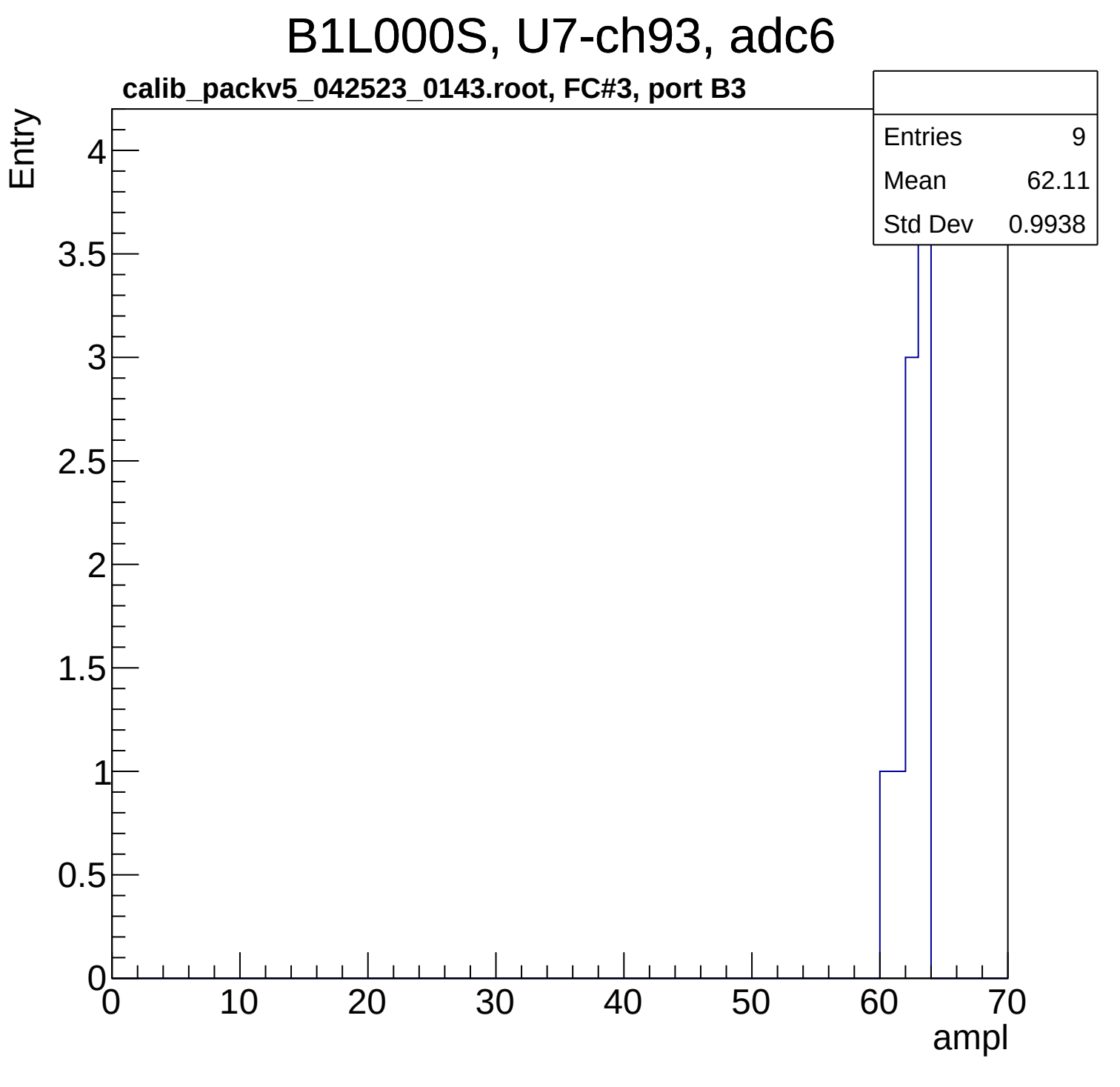
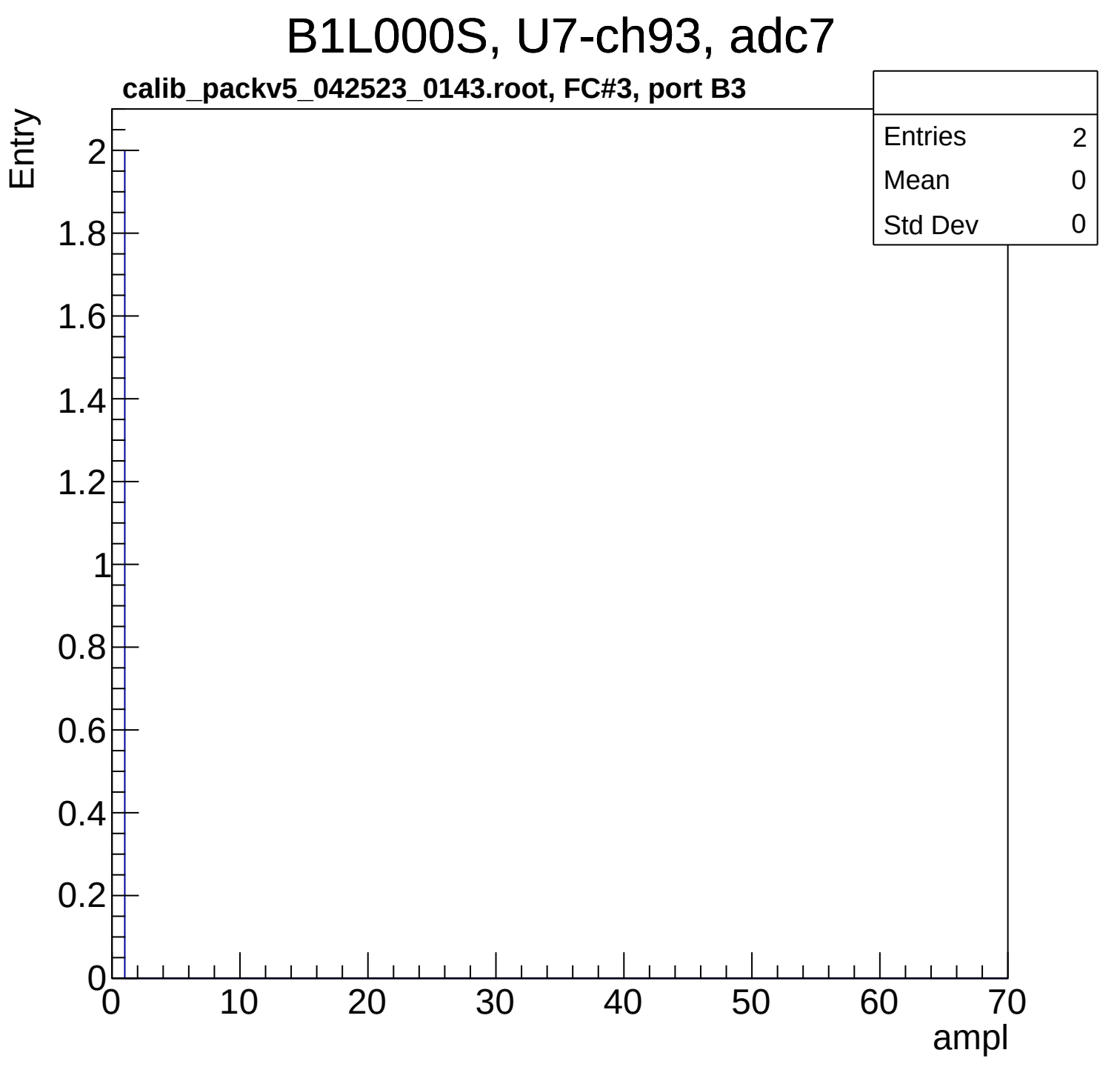
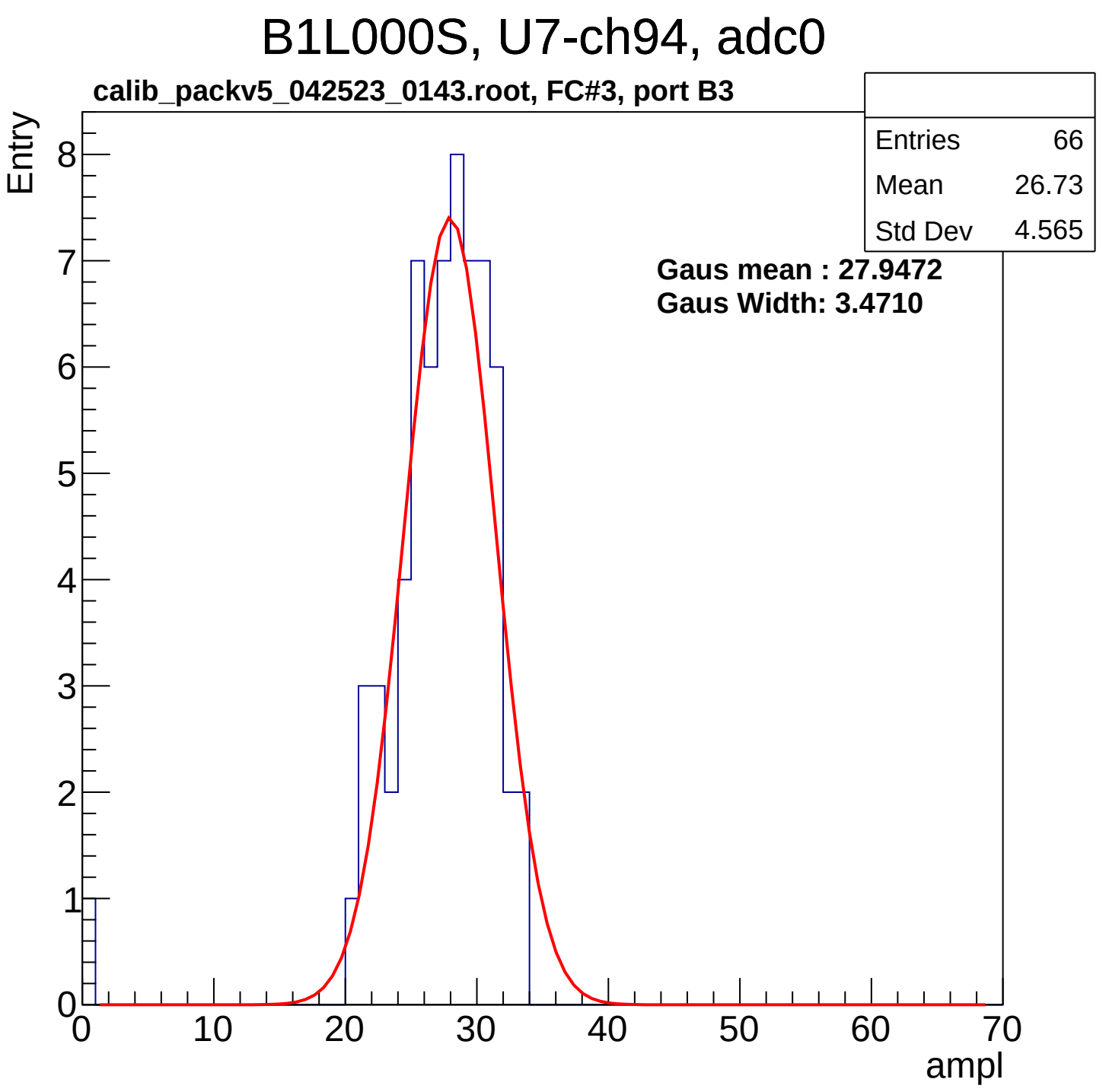

B1L000S, U7-ch93, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch93, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch94, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 26.73
Std Dev 4.565
Gaus mean : 27.9472
Gaus Width: 3.4710
Entry
ampl
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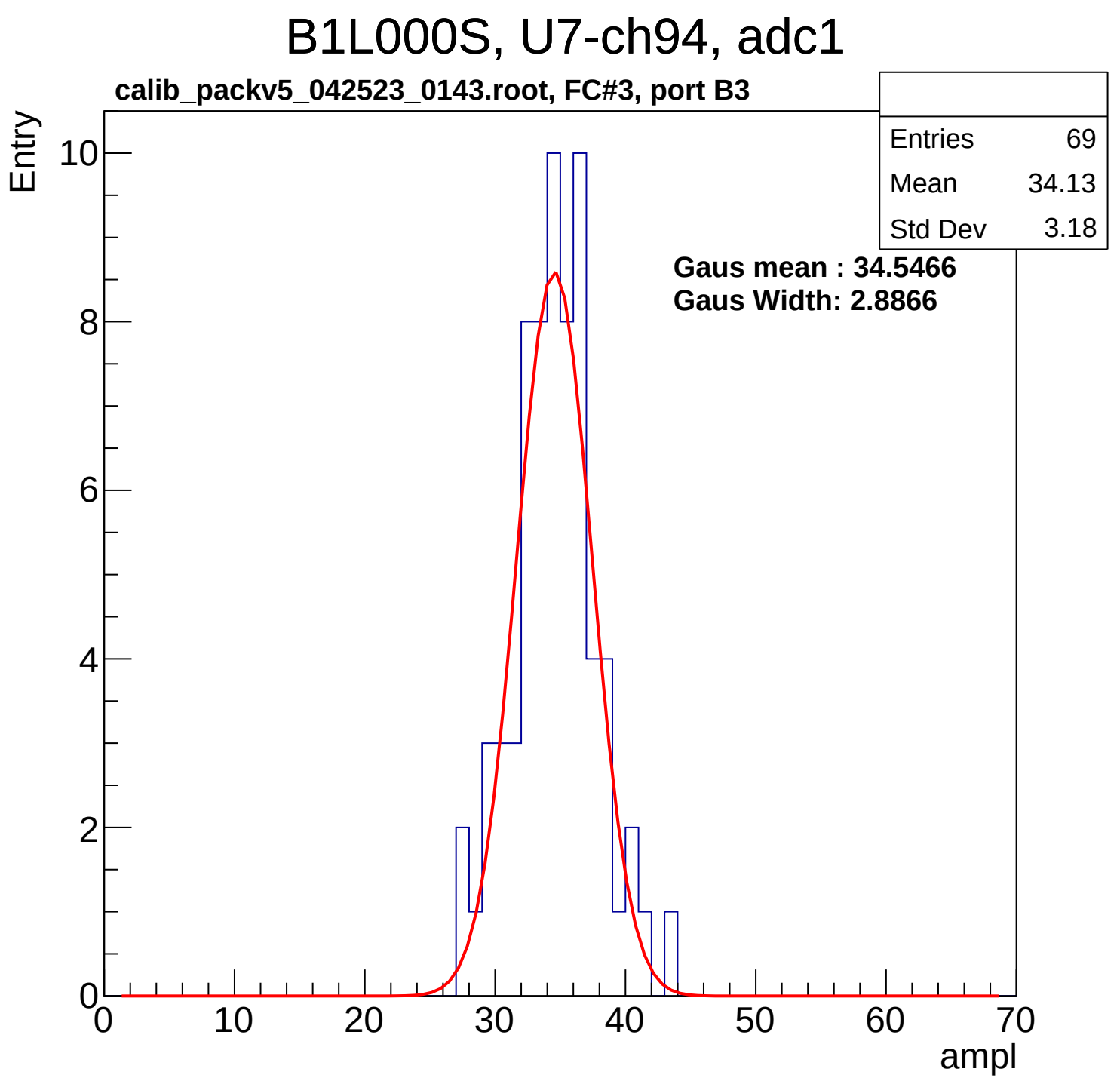
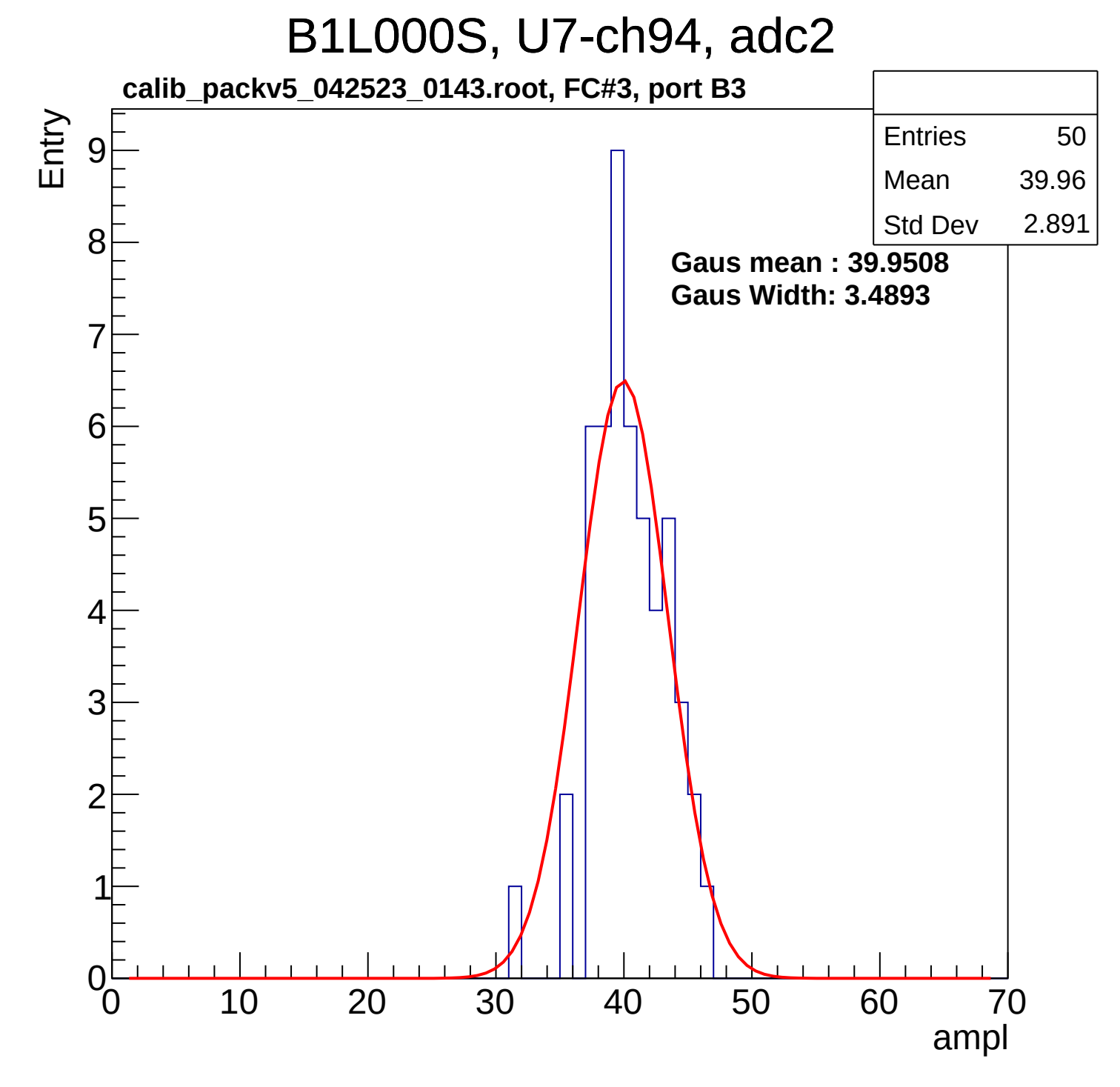
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch94, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.13
Std Dev 3.18
Gaus mean : 34.5466
Gaus Width: 2.8866
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch94, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 39.96
Std Dev 2.891
Gaus mean : 39.9508
Gaus Width: 3.4893
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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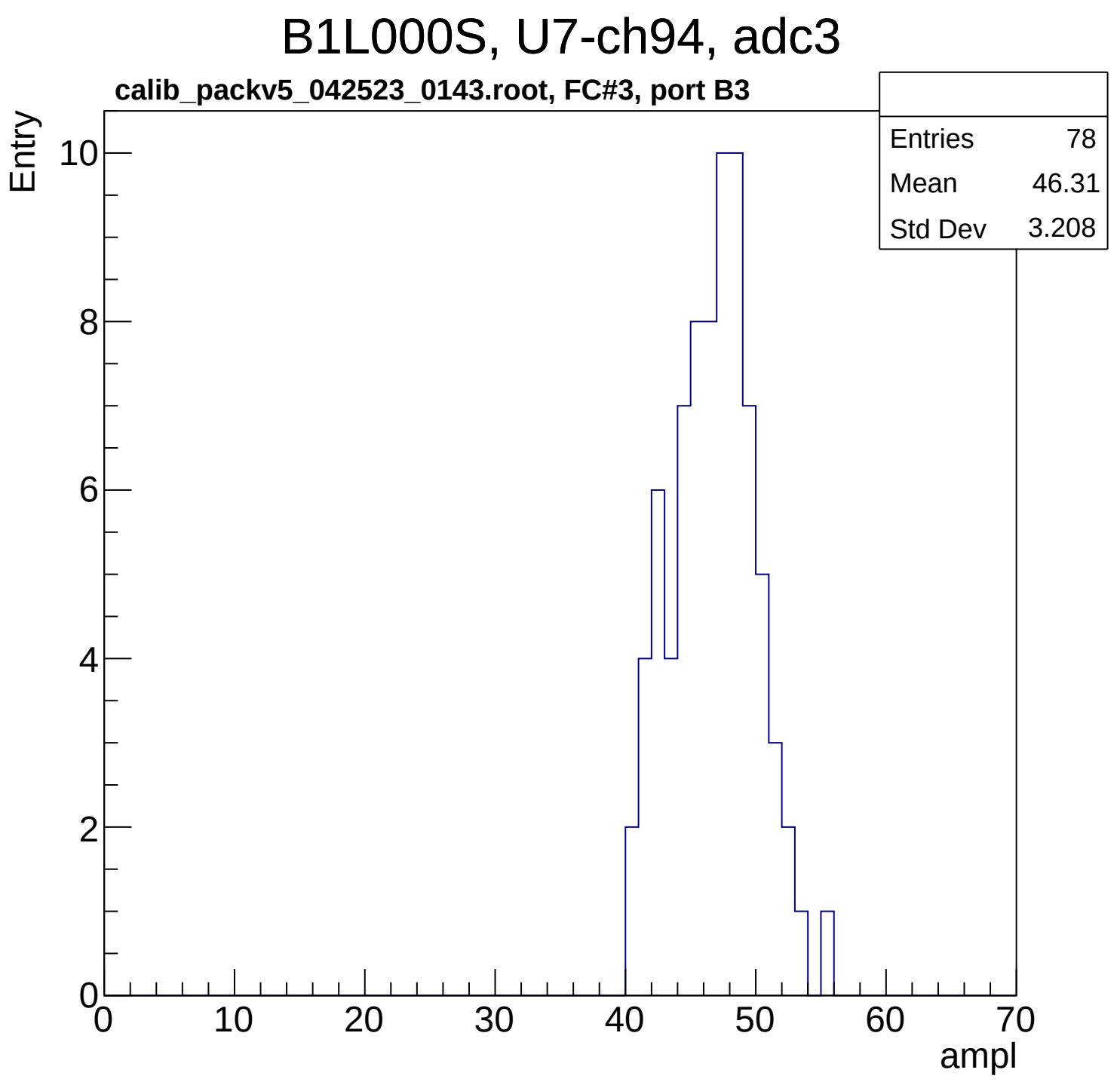
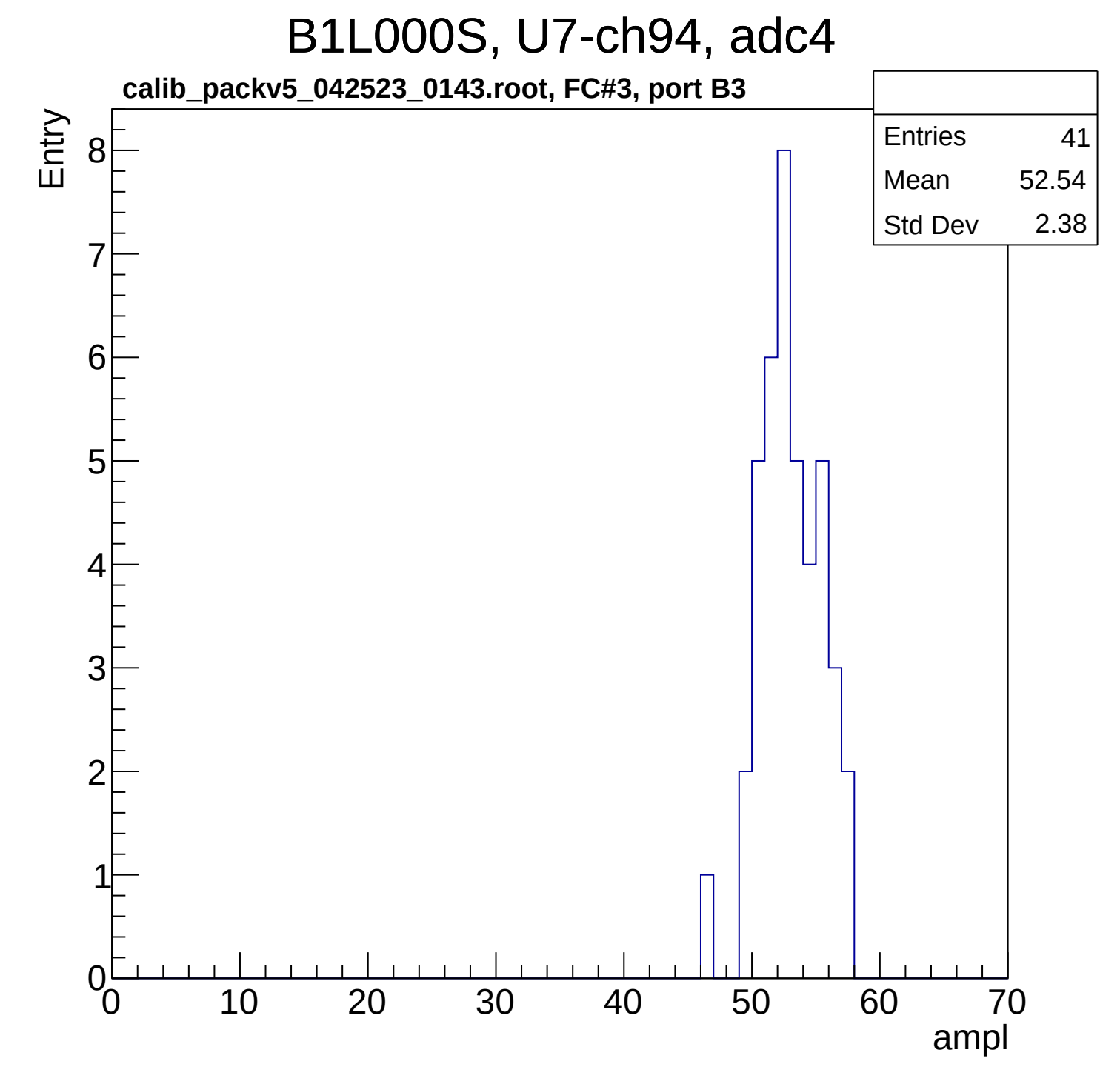

B1L000S, U7-ch94, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 46.31
Std Dev 3.208
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch94, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 52.54
Std Dev 2.38
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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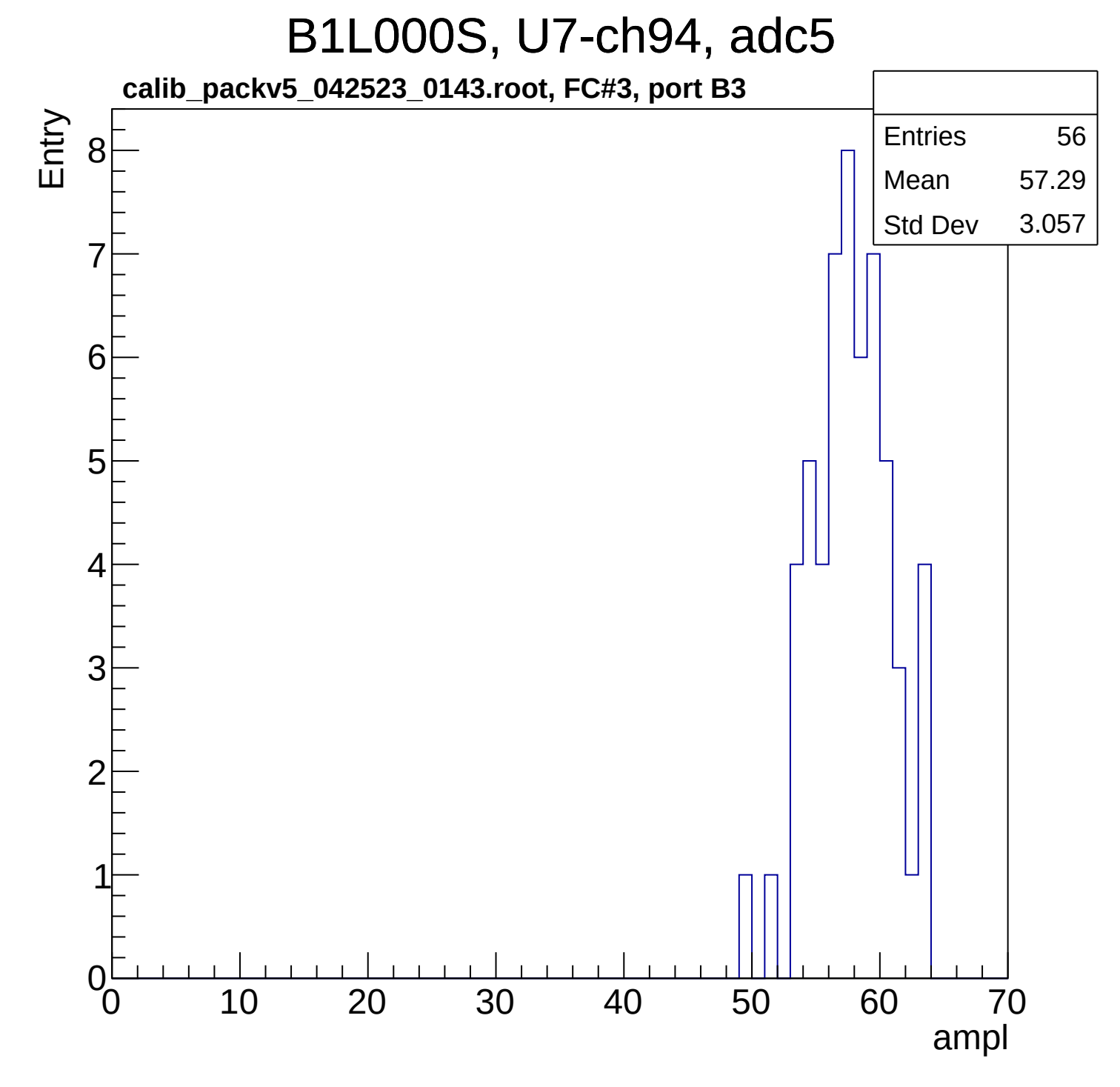
B1L000S, U7-ch94, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 57.29
Std Dev 3.057
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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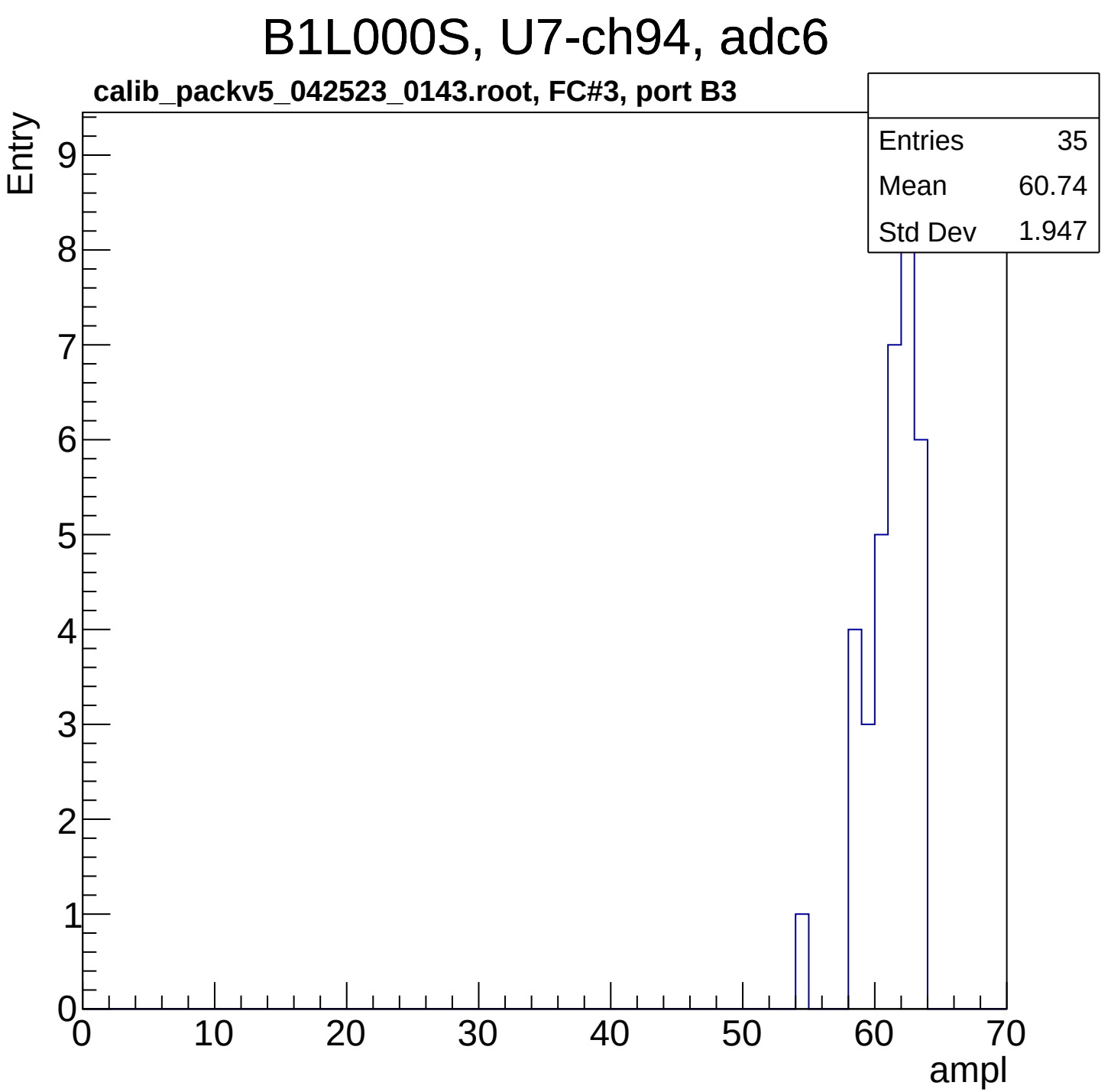
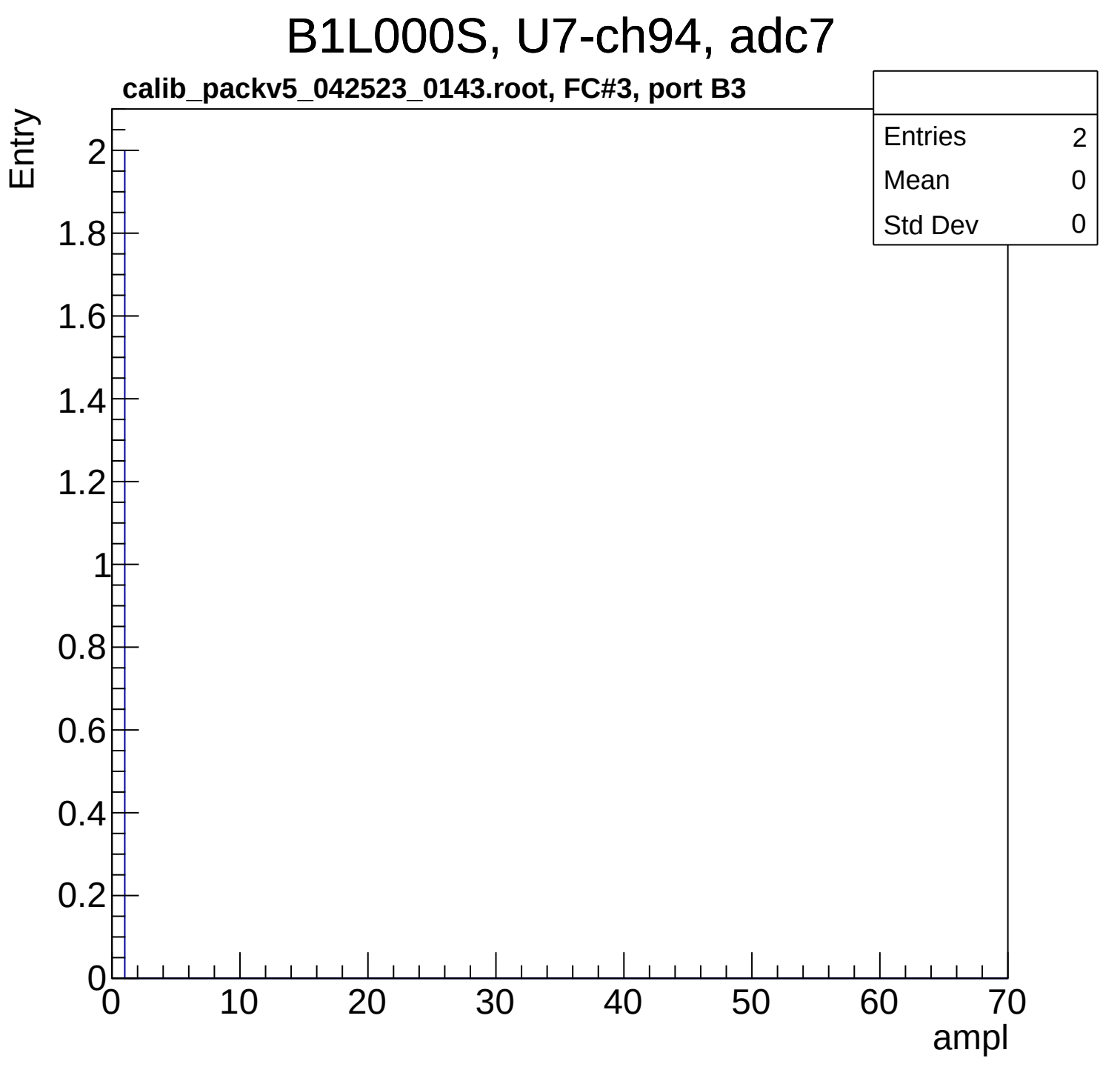
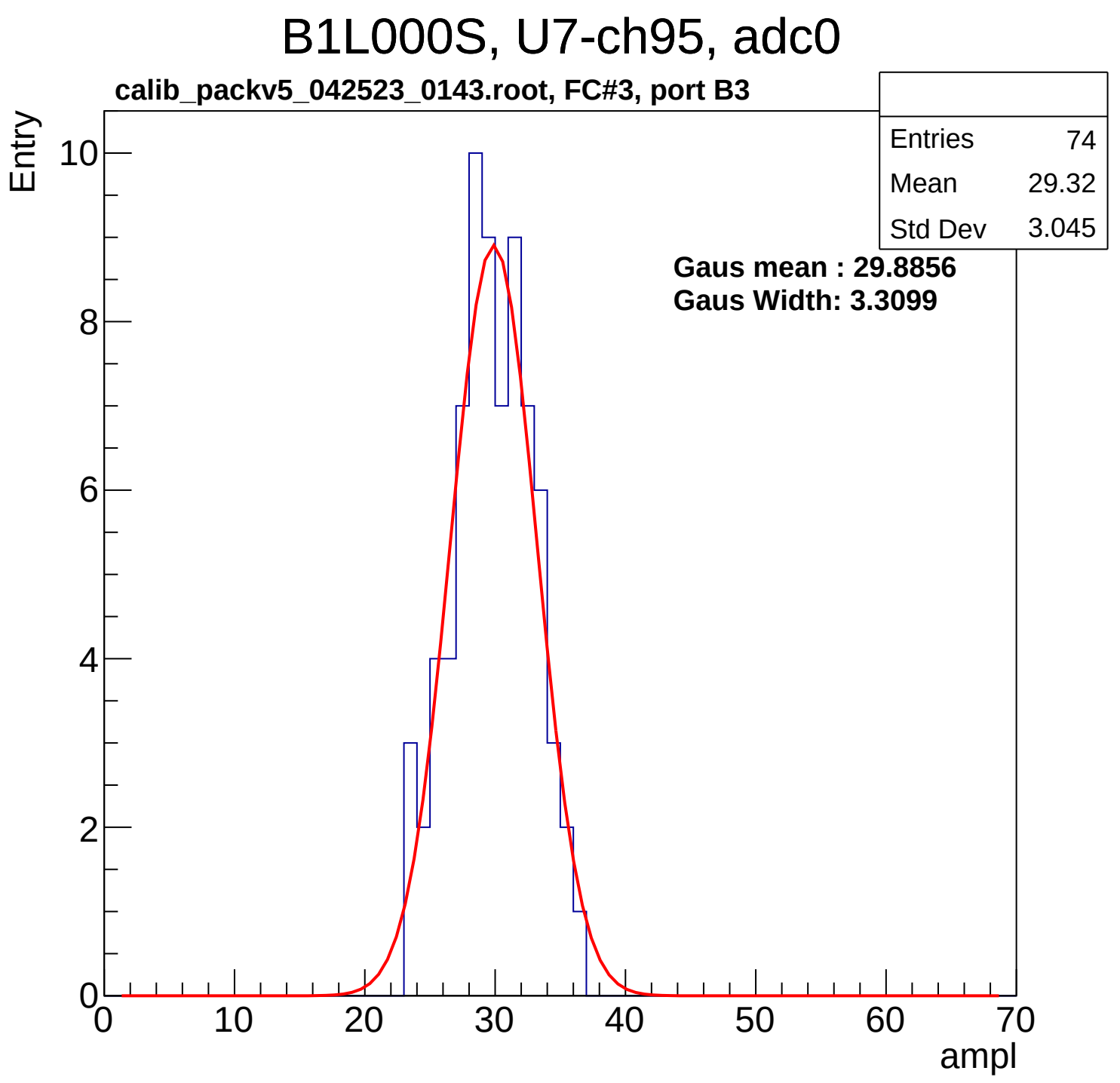
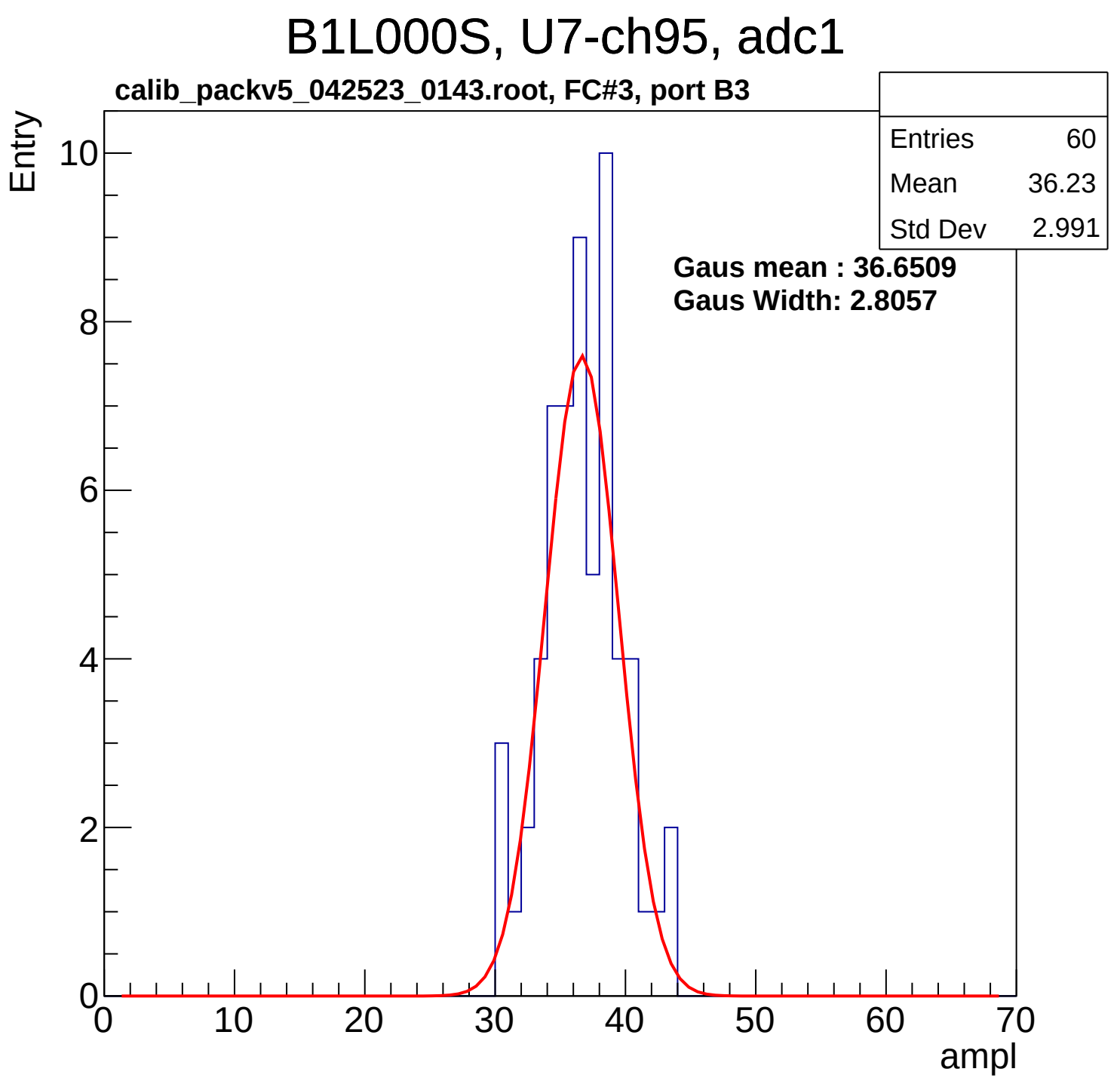
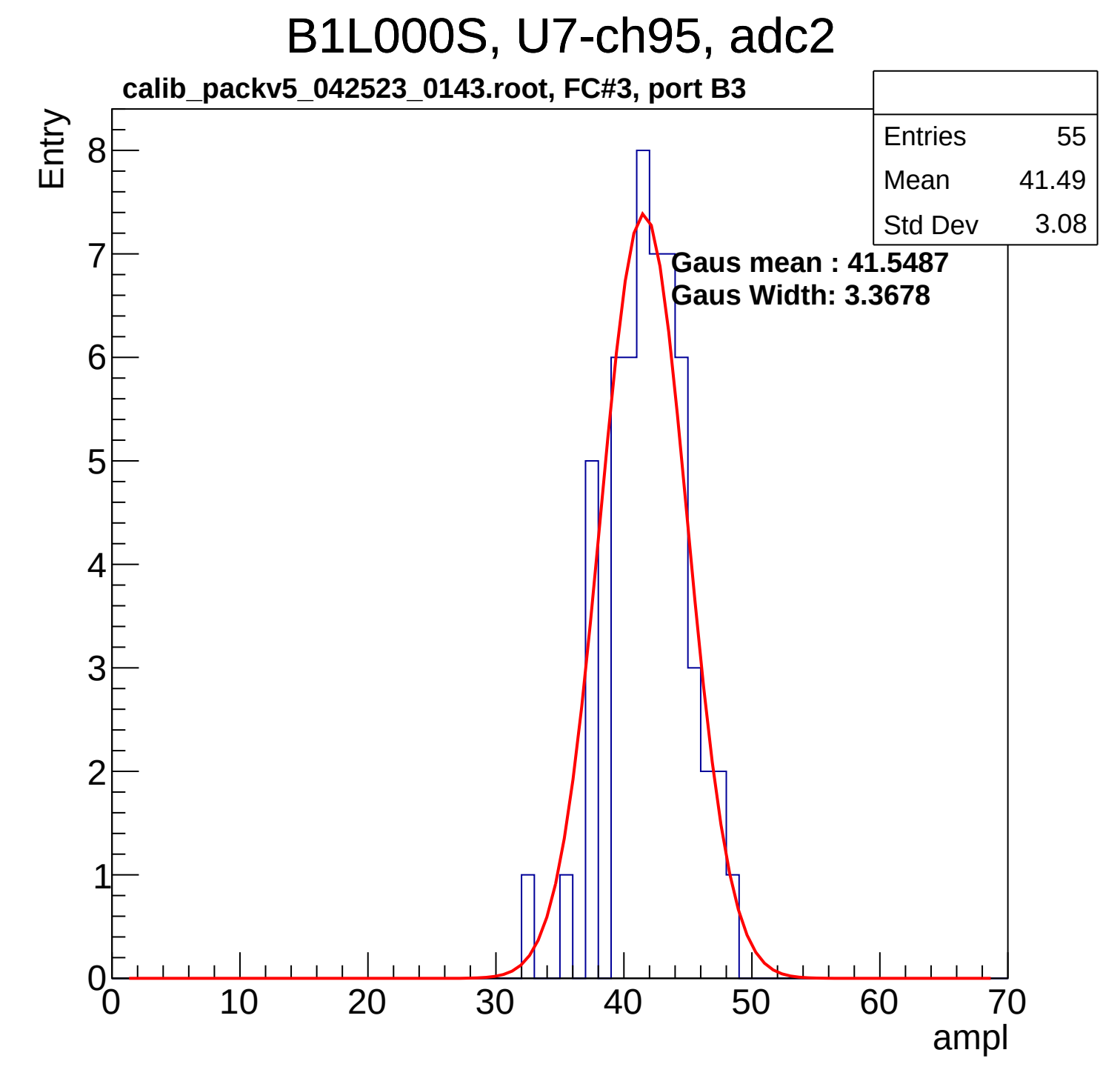

B1L000S, U7-ch94, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 35
Mean 60.74
Std Dev 1.947
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch94, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch95, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.32
Std Dev 3.045
Gaus mean : 29.8856
Gaus Width: 3.3099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch95, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 36.23
Std Dev 2.991
Gaus mean : 36.6509
Gaus Width: 2.8057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch95, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 41.49
Std Dev 3.08
Gaus mean : 41.5487
Gaus Width: 3.3678
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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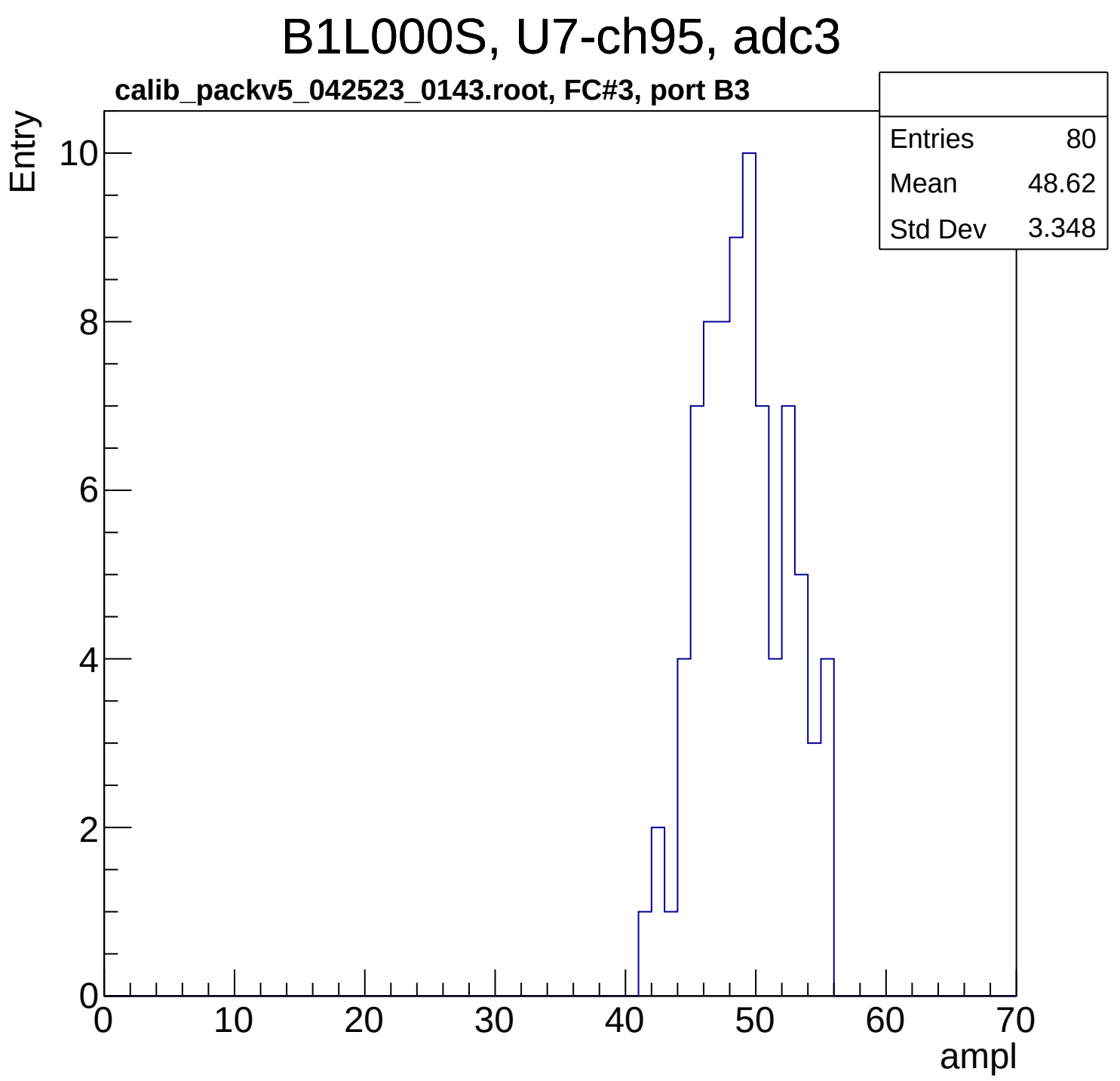
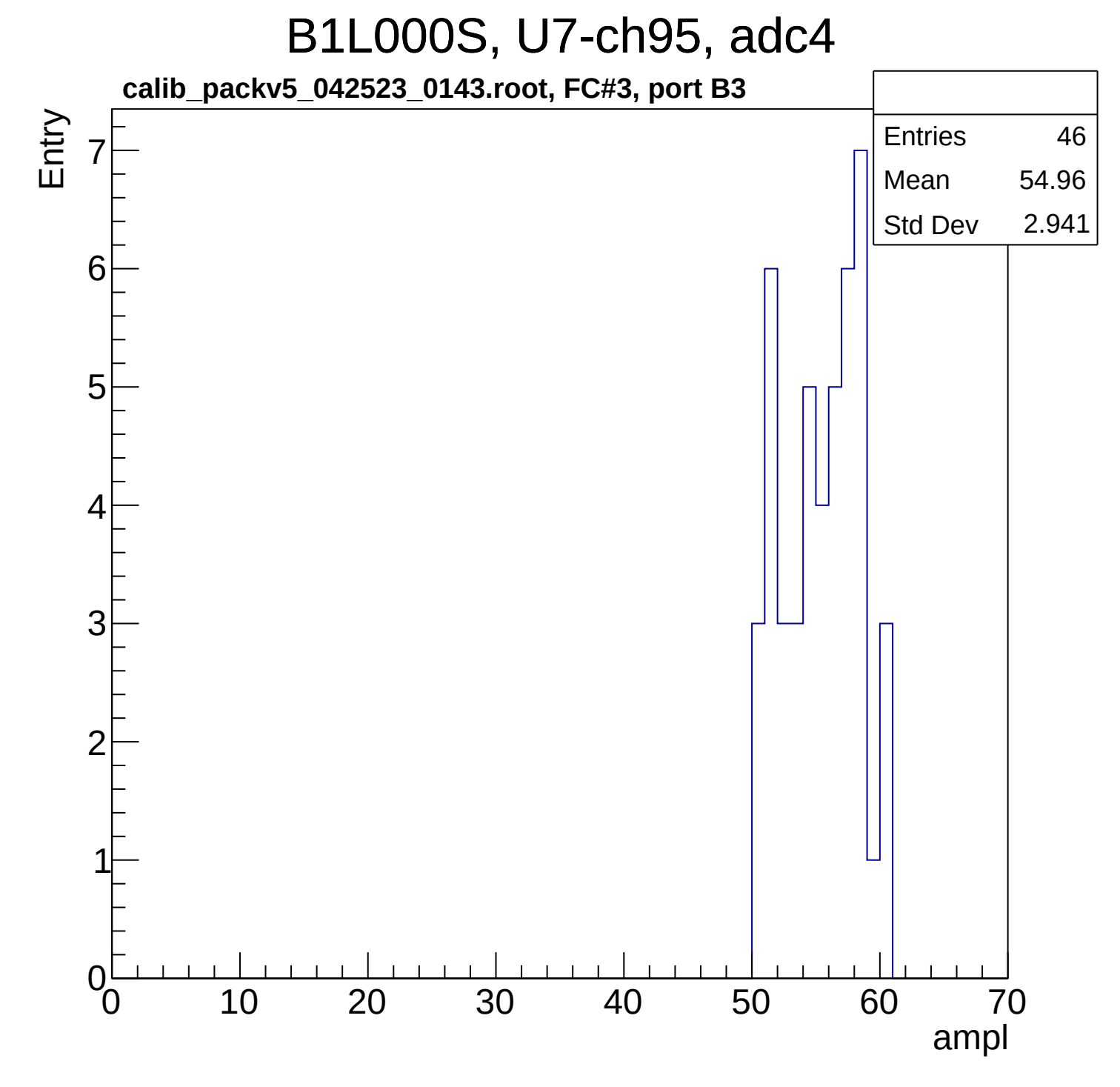

B1L000S, U7-ch95, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 48.62
Std Dev 3.348
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch95, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 54.96
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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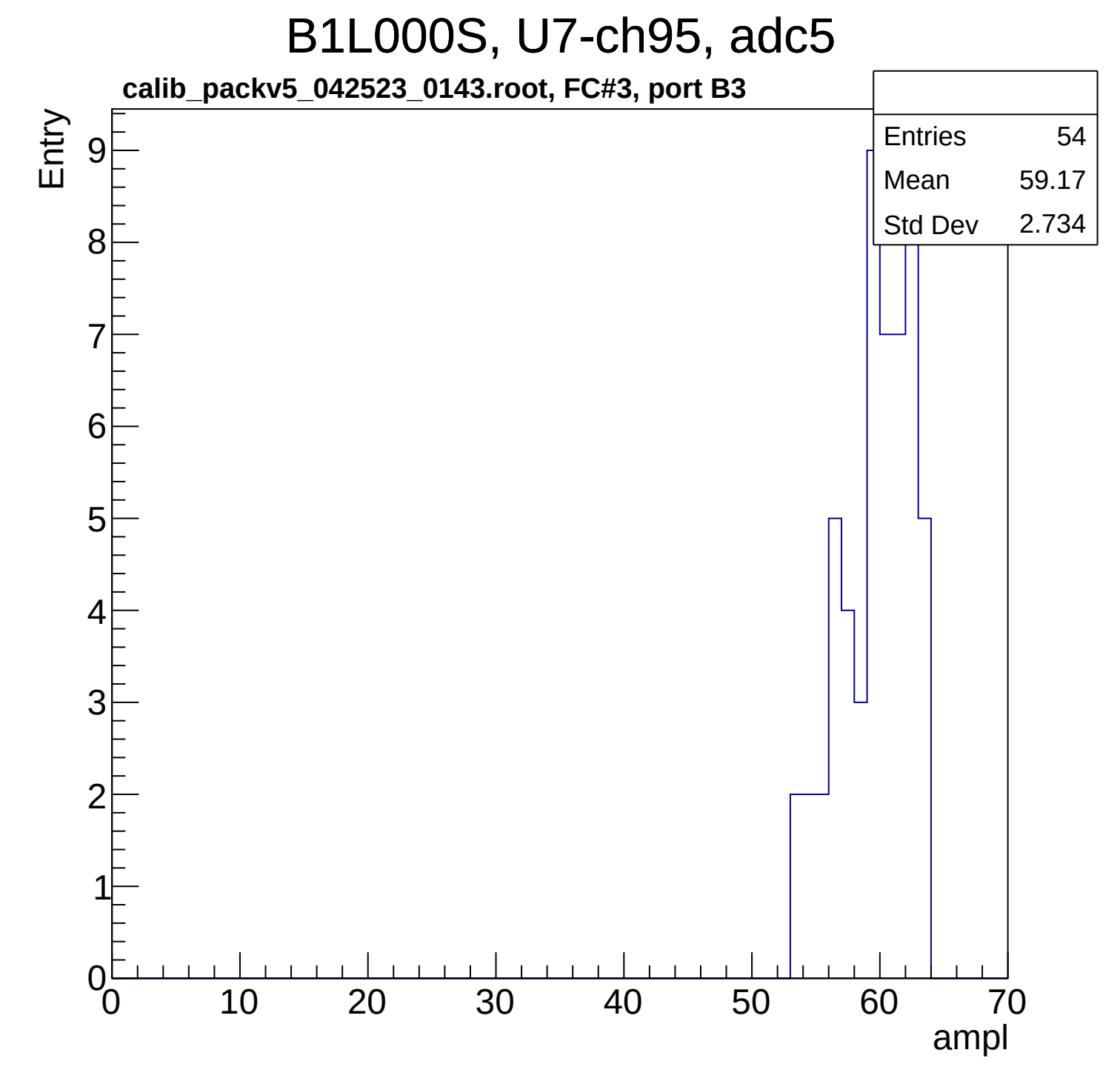
B1L000S, U7-ch95, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 59.17
Std Dev 2.734
Entry
ampl
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2
3
4
5
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7
8
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0
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60
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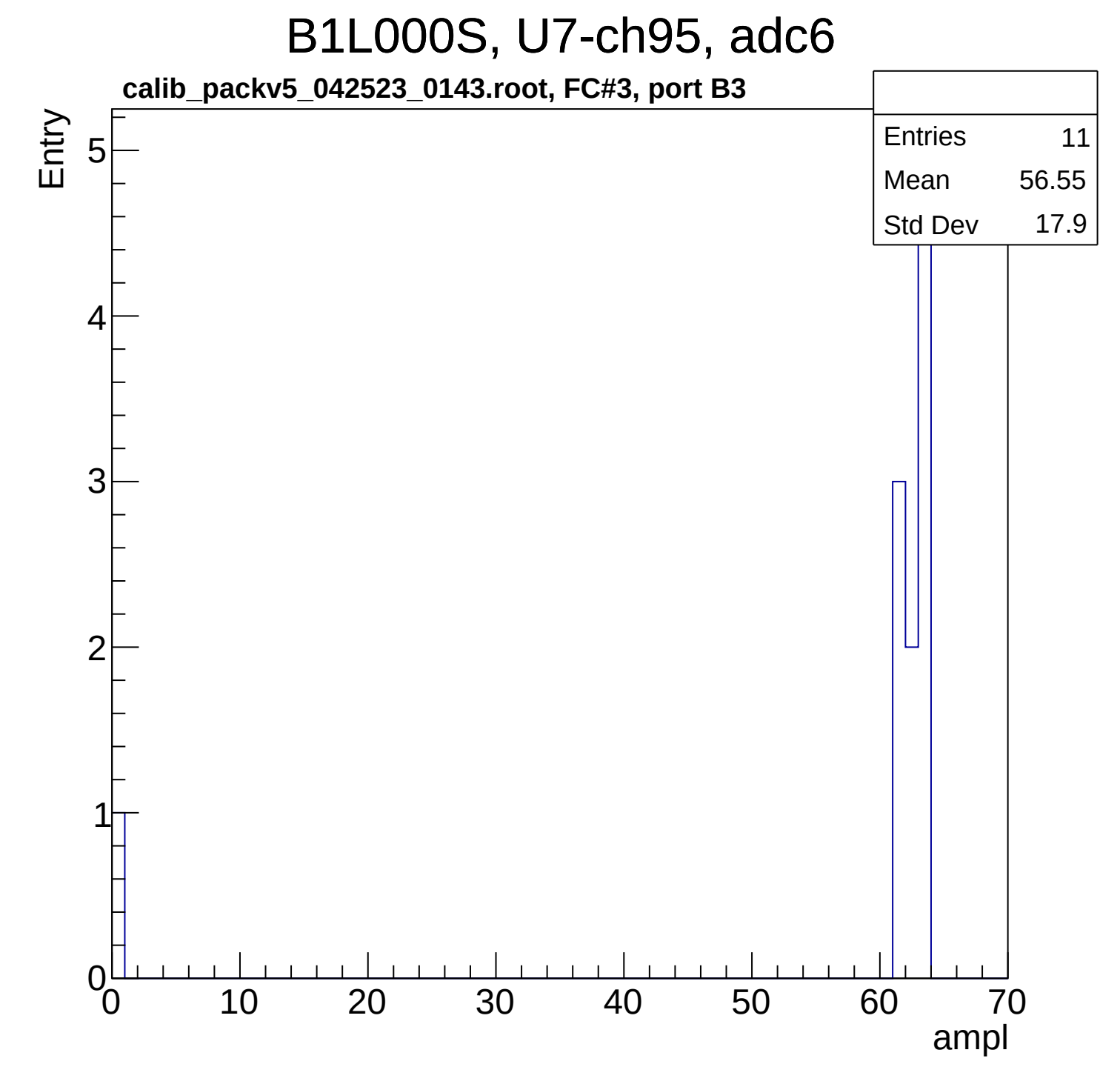
B1L000S, U7-ch95, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 56.55
Std Dev 17.9
Entry
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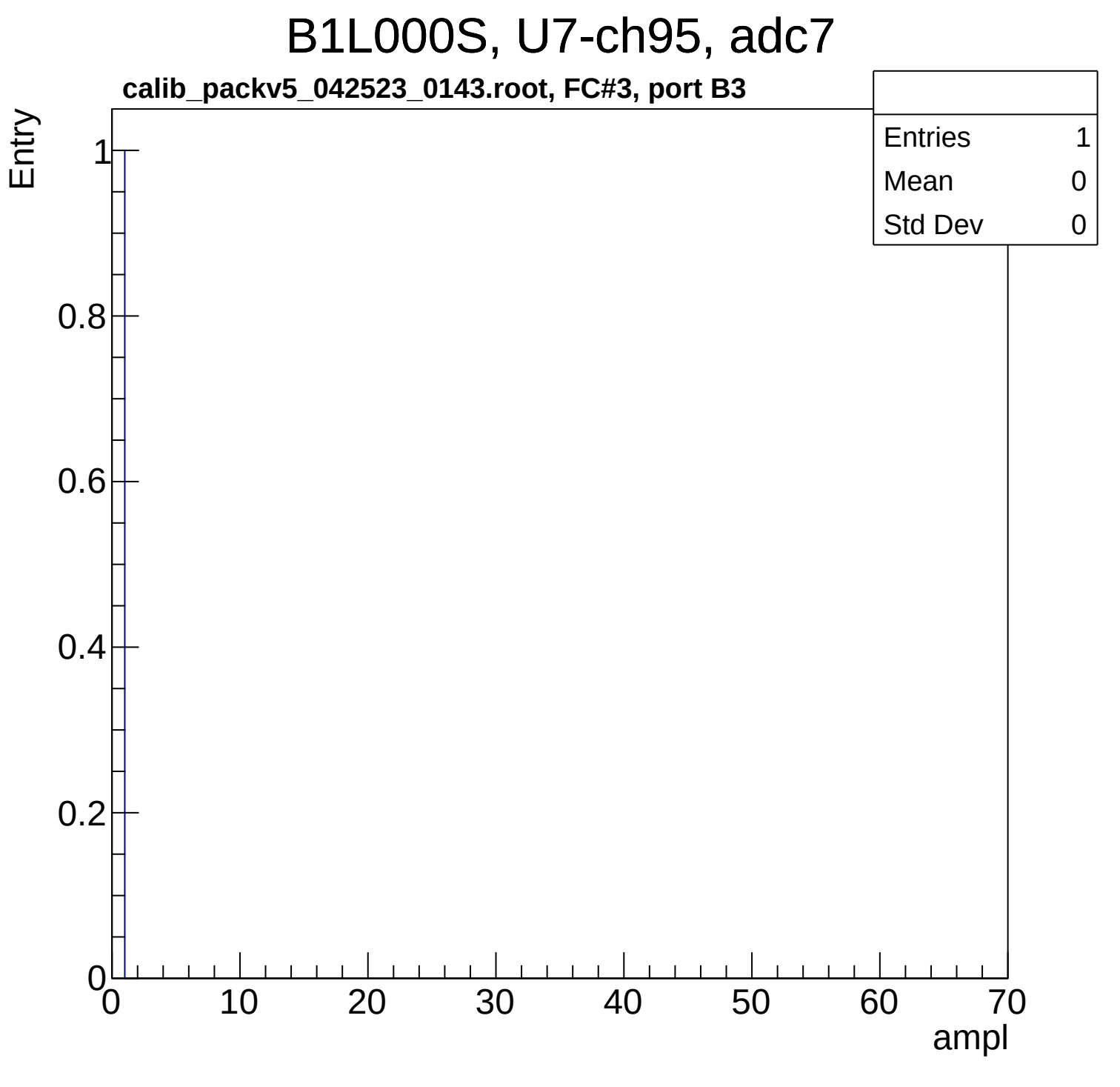
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch95, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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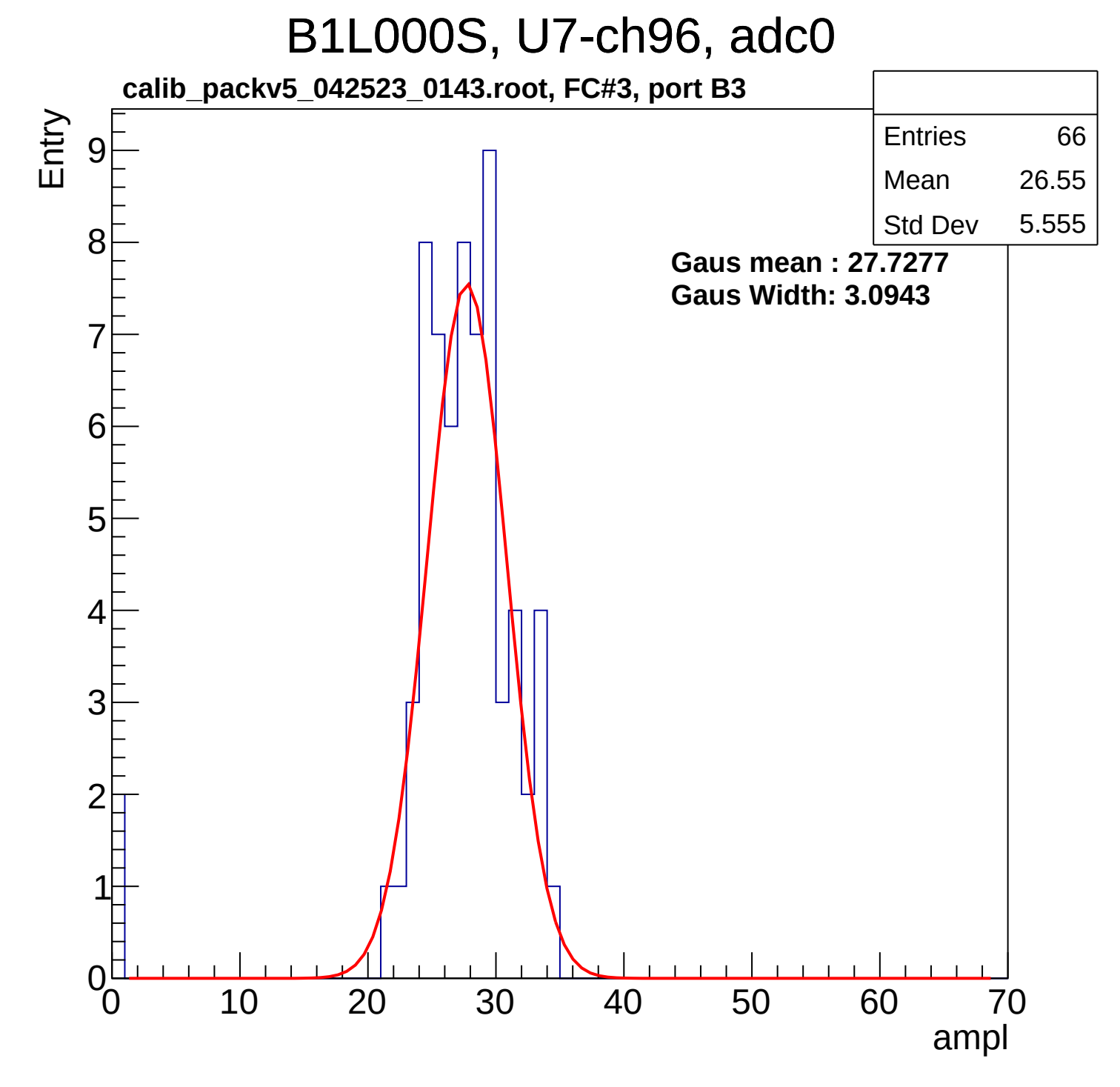
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch96, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 26.55
Std Dev 5.555
Gaus mean : 27.7277
Gaus Width: 3.0943
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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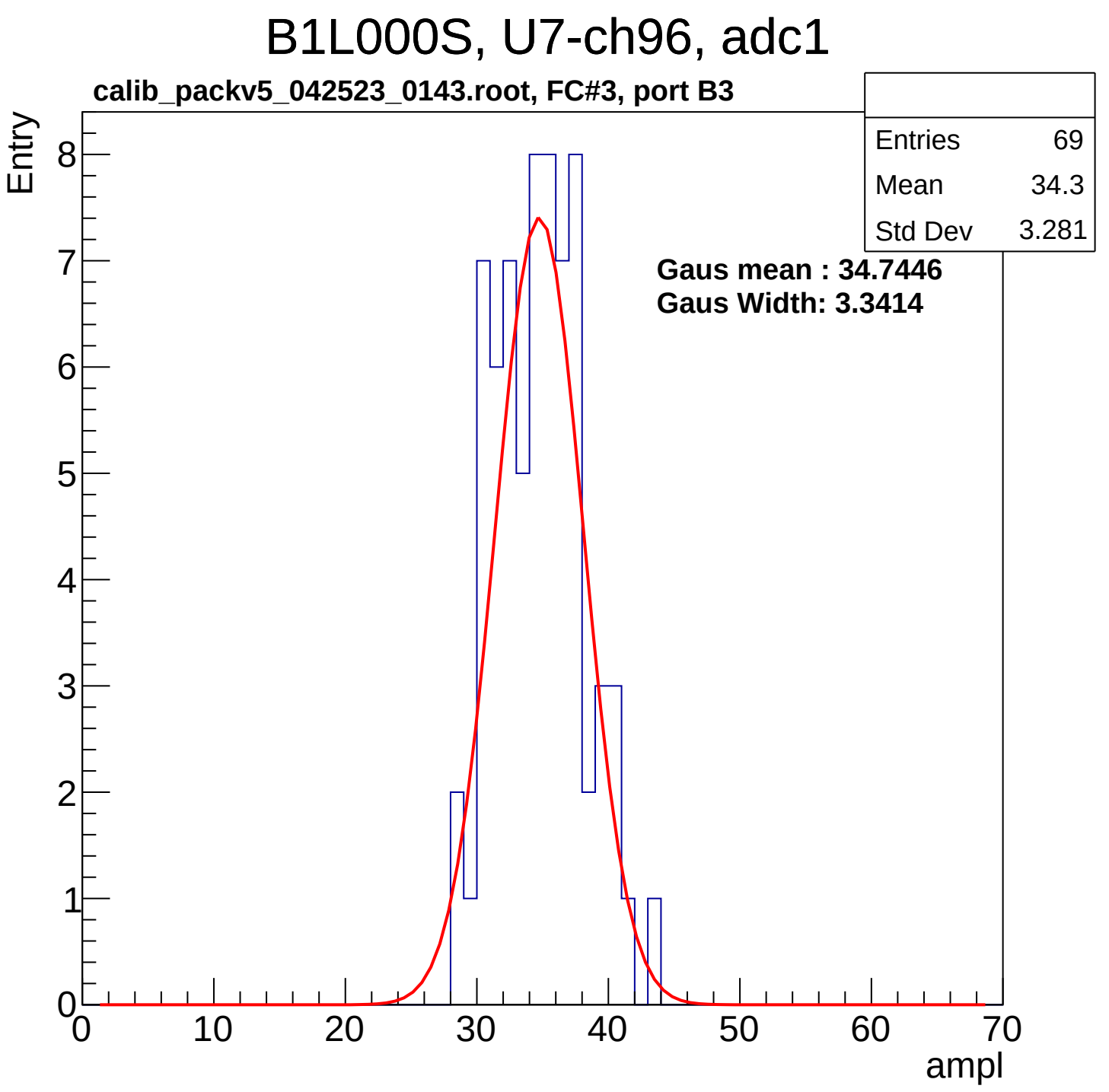
B1L000S, U7-ch96, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 34.3
Std Dev 3.281
Gaus mean : 34.7446
Gaus Width: 3.3414
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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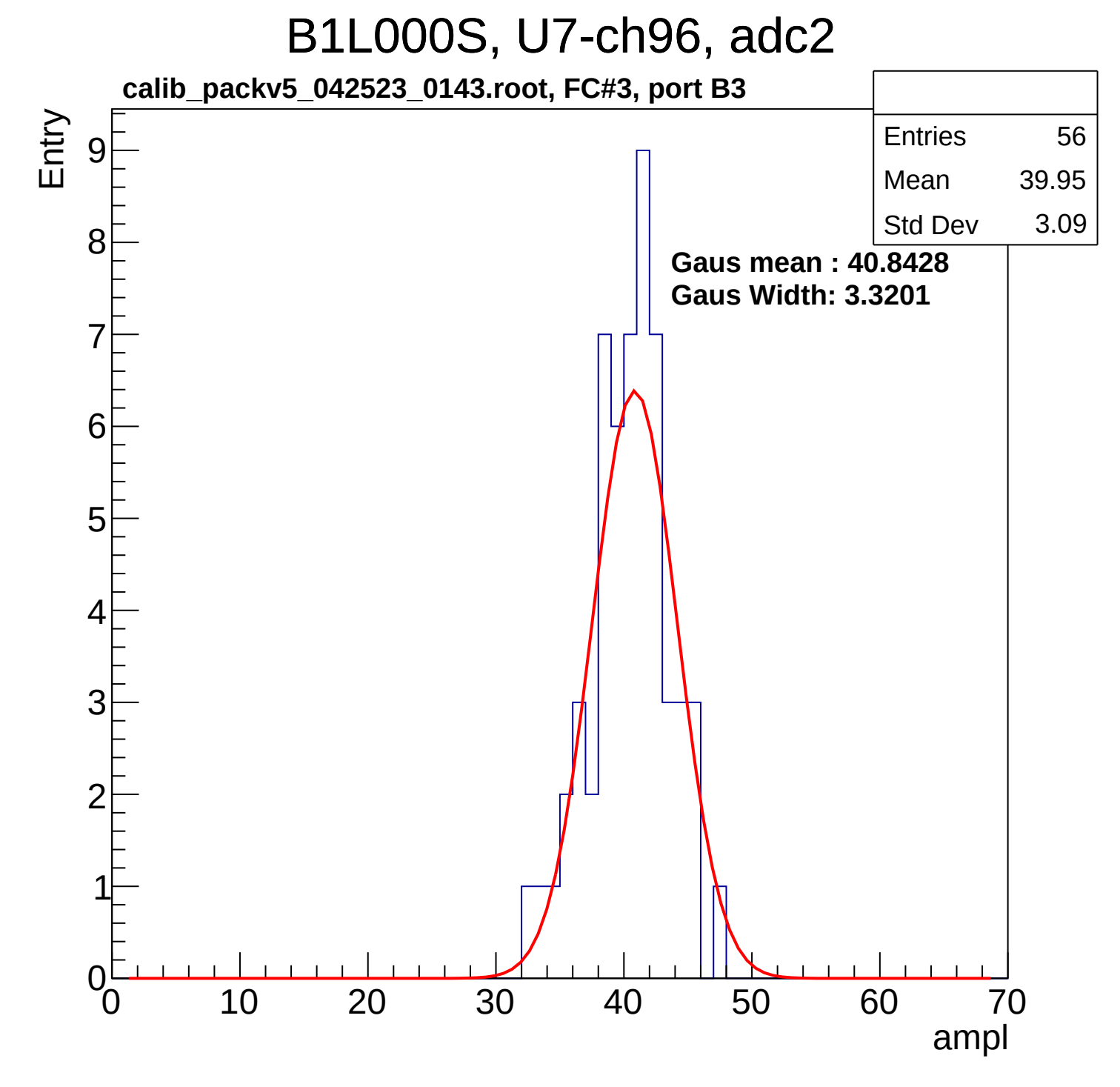
B1L000S, U7-ch96, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 39.95
Std Dev 3.09
Gaus mean : 40.8428
Gaus Width: 3.3201
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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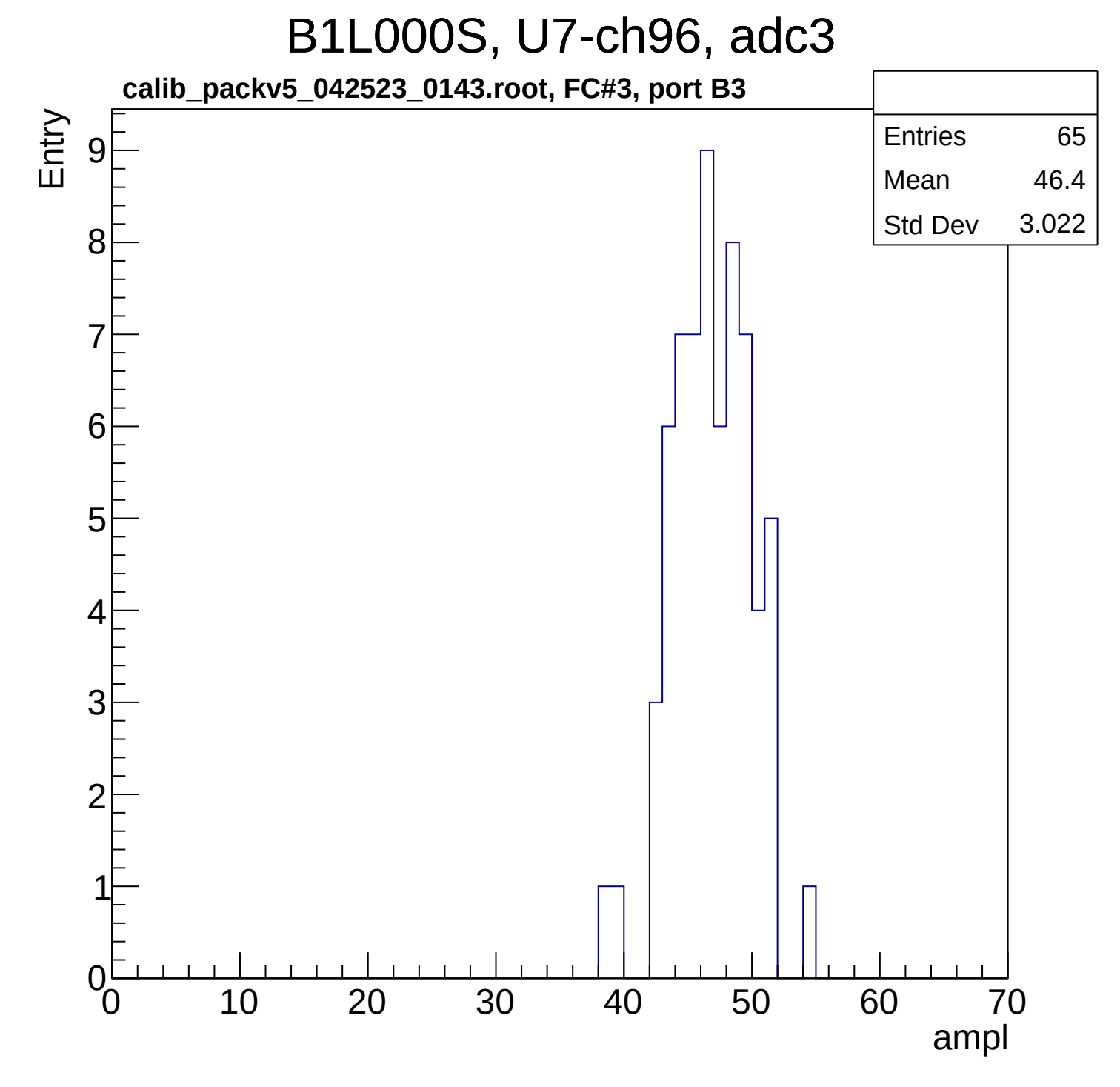
B1L000S, U7-ch96, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 46.4
Std Dev 3.022
Entry
ampl
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2
3
4
5
6
7
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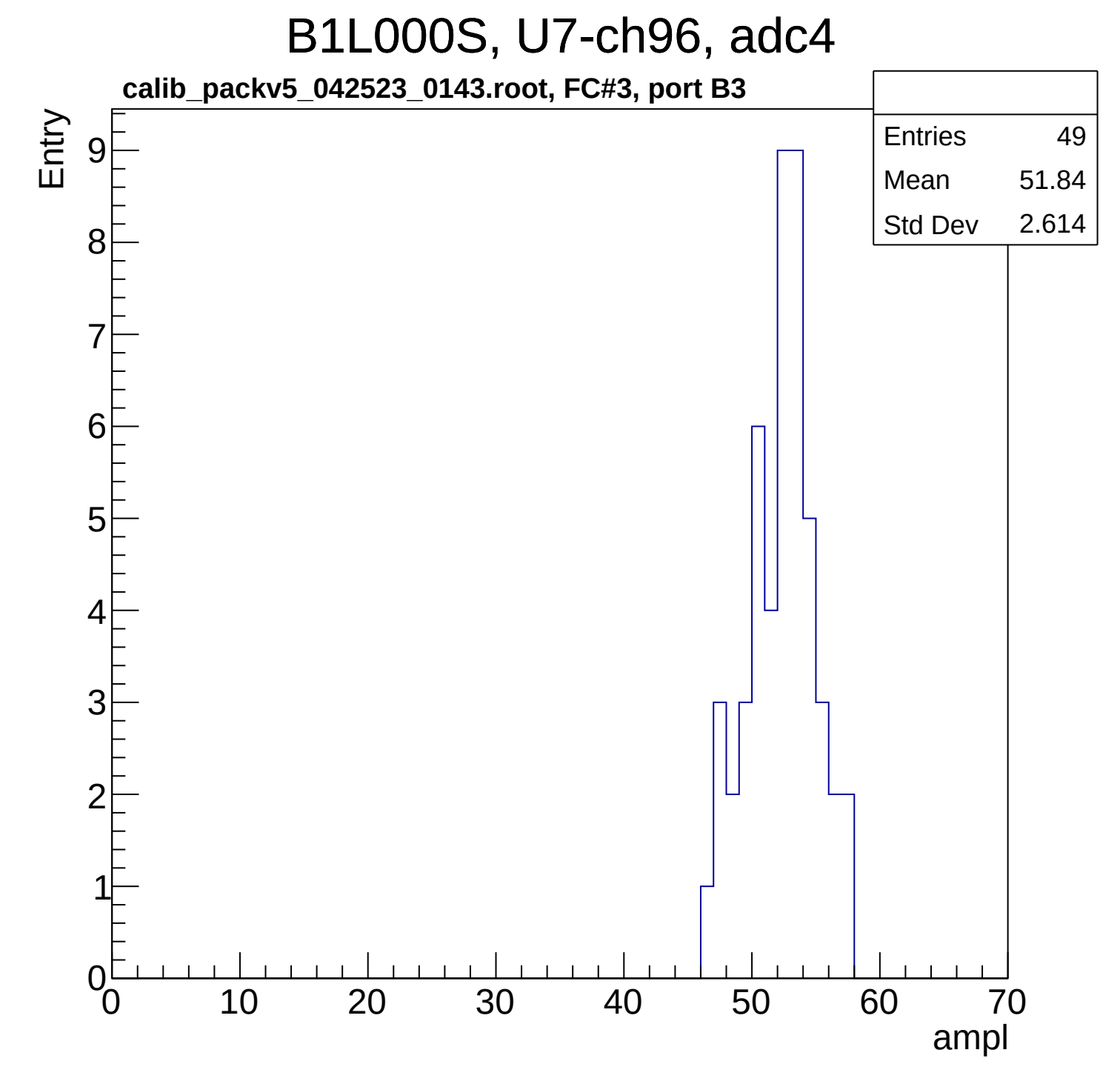
B1L000S, U7-ch96, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 51.84
Std Dev 2.614
Entry
ampl
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3
4
5
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7
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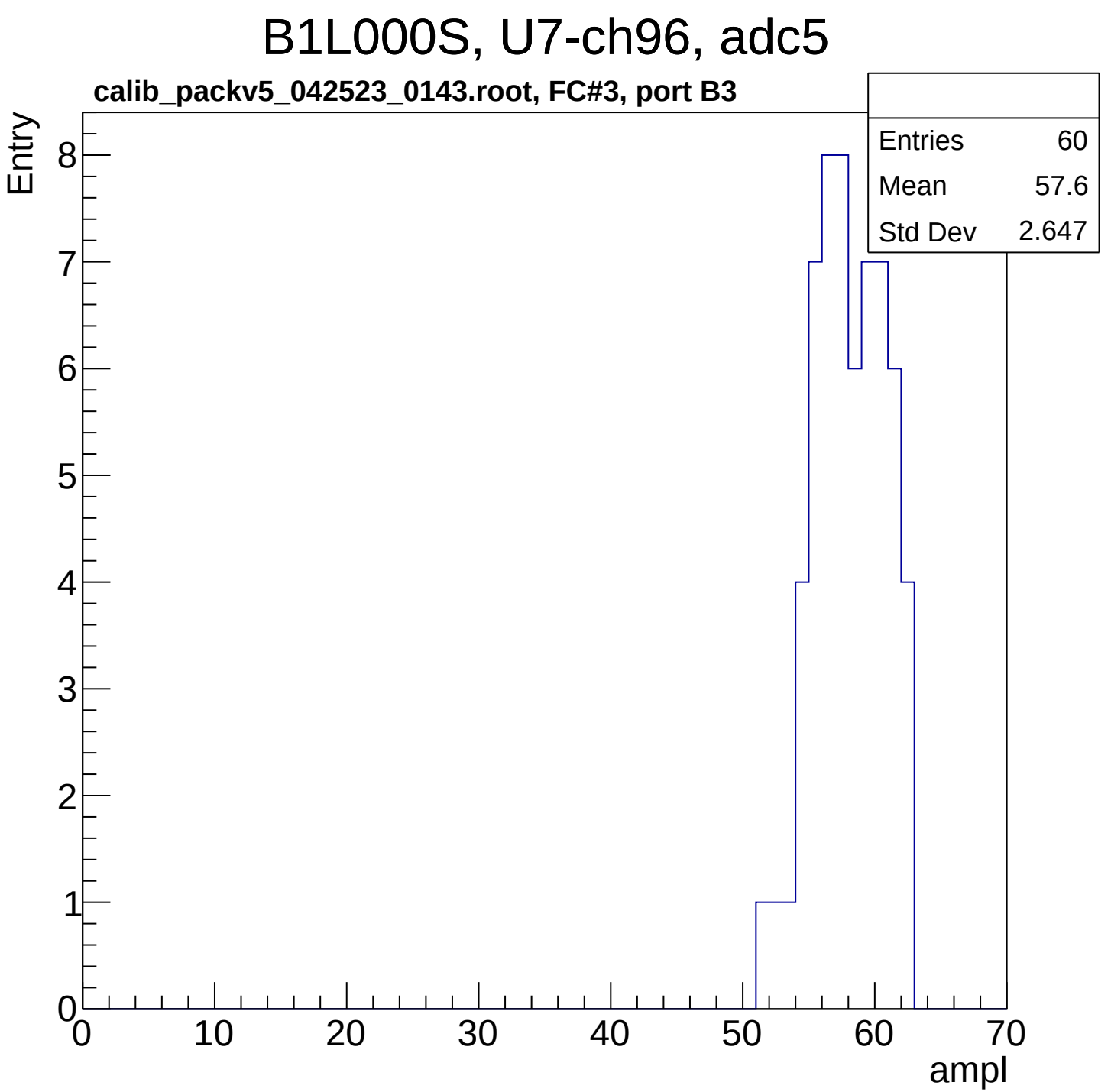
B1L000S, U7-ch96, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 57.6
Std Dev 2.647
Entry
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7
8
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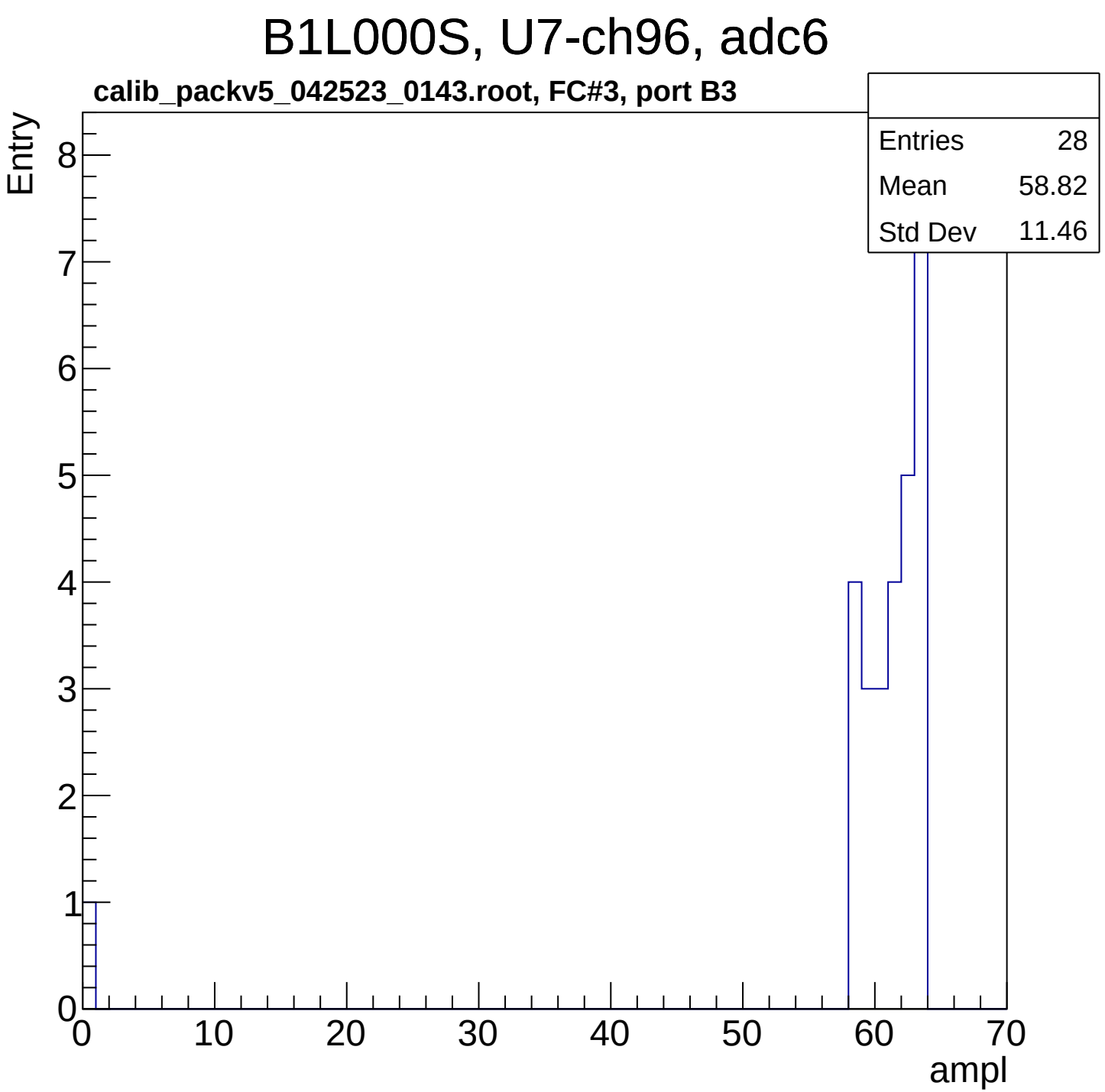
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch96, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 28
Mean 58.82
Std Dev 11.46
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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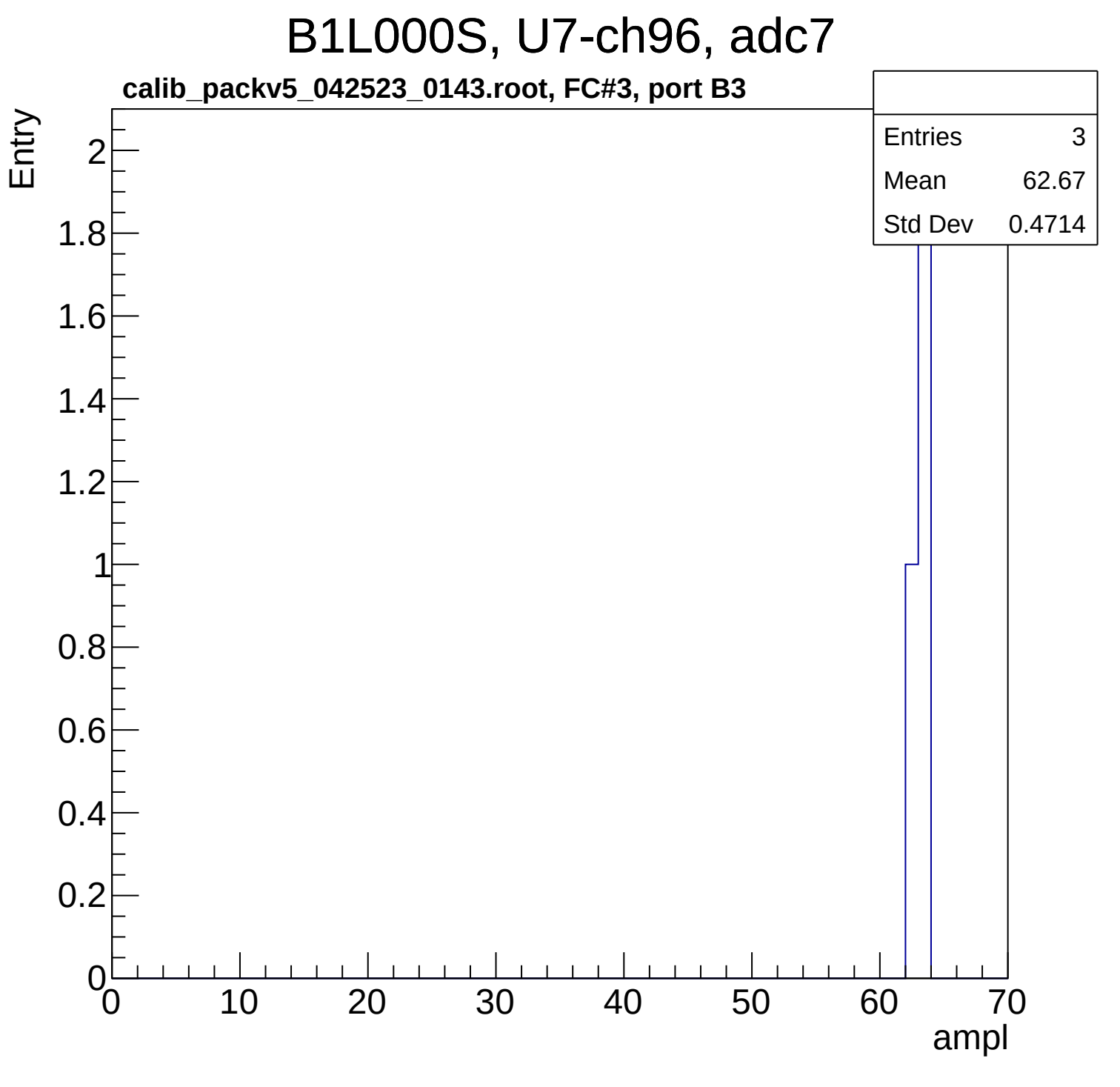
B1L000S, U7-ch96, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
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0
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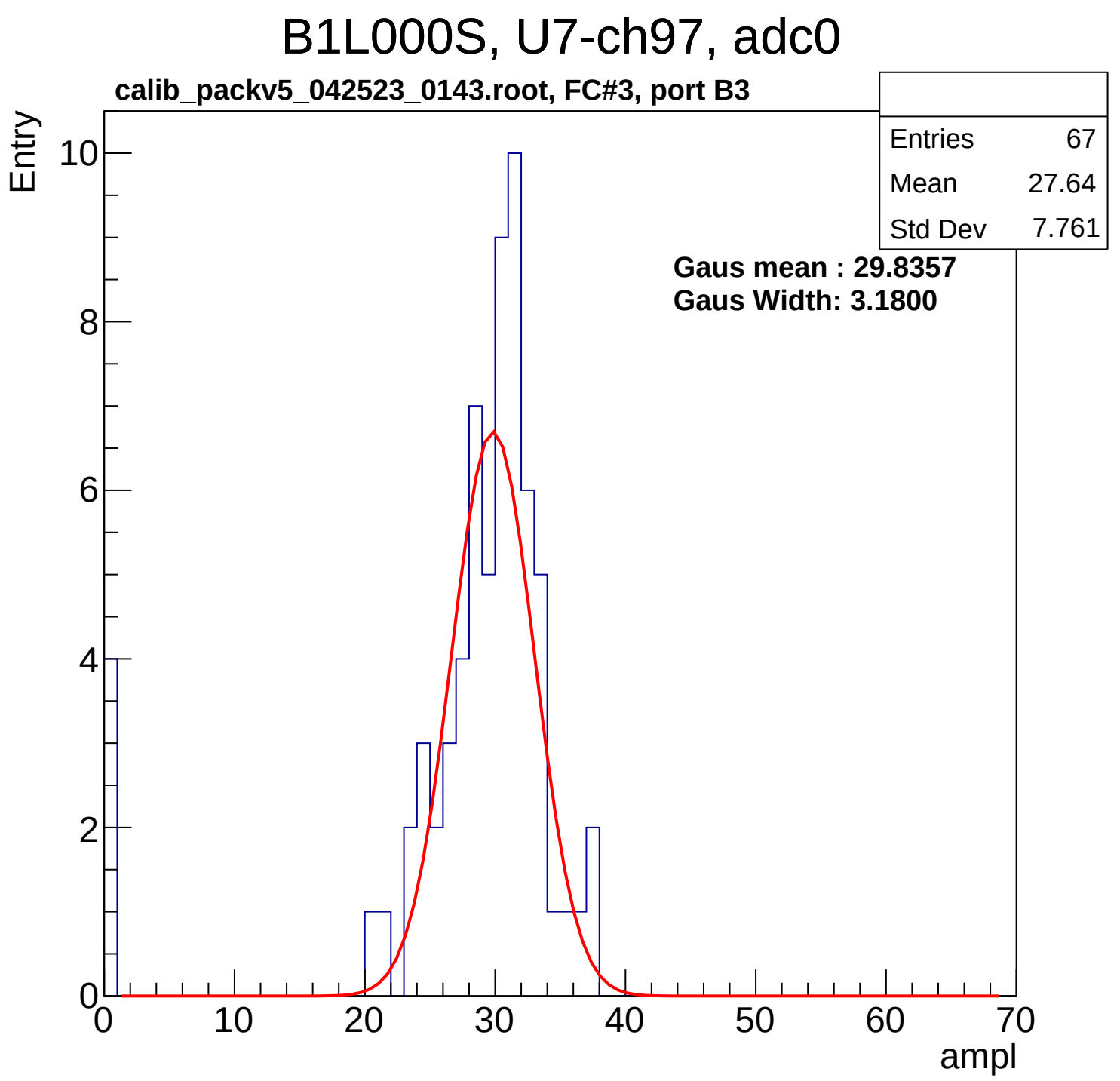
B1L000S, U7-ch97, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.64
Std Dev 7.761
Gaus mean : 29.8357
Gaus Width: 3.1800
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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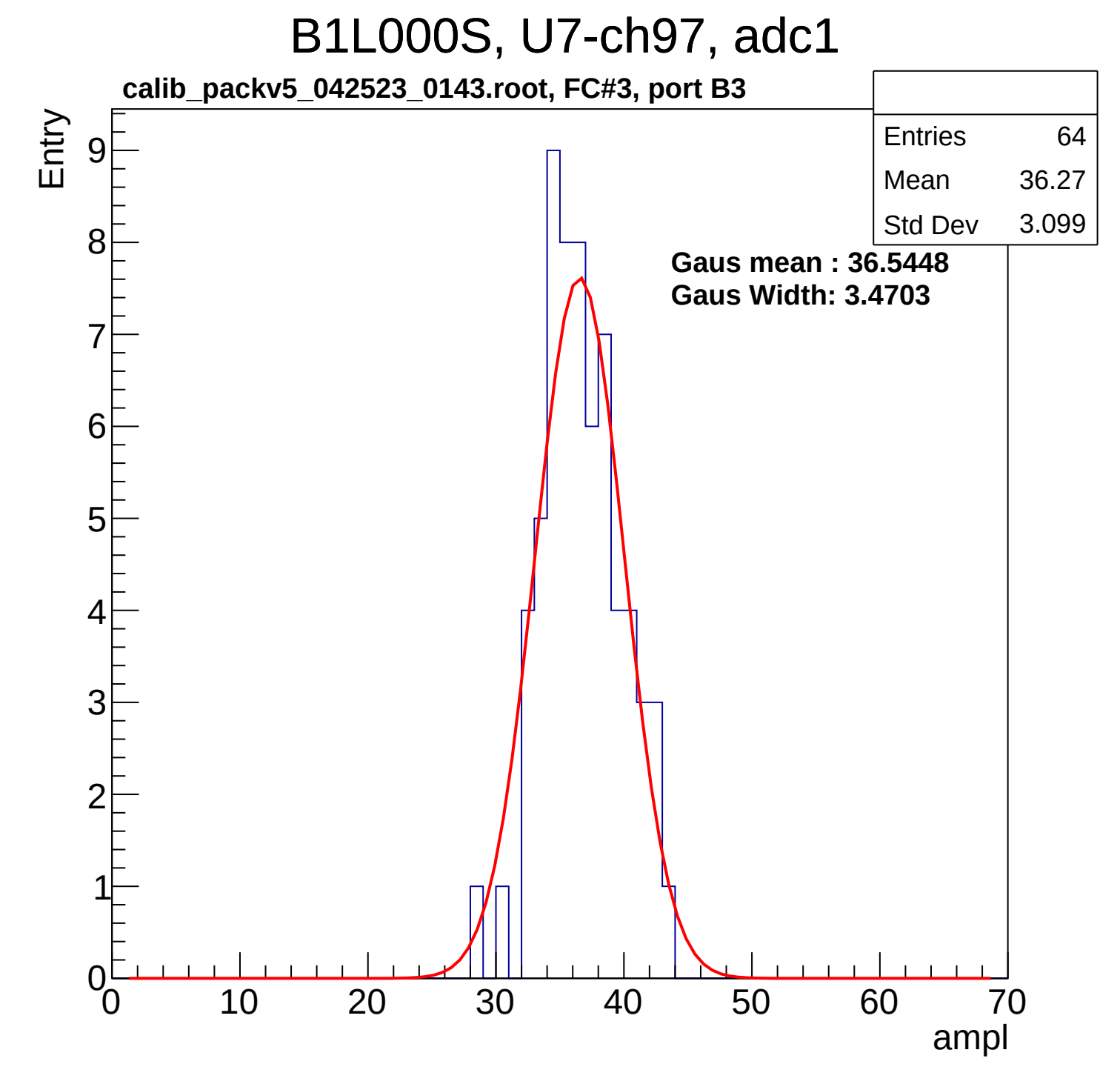
B1L000S, U7-ch97, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 36.27
Std Dev 3.099
Gaus mean : 36.5448
Gaus Width: 3.4703
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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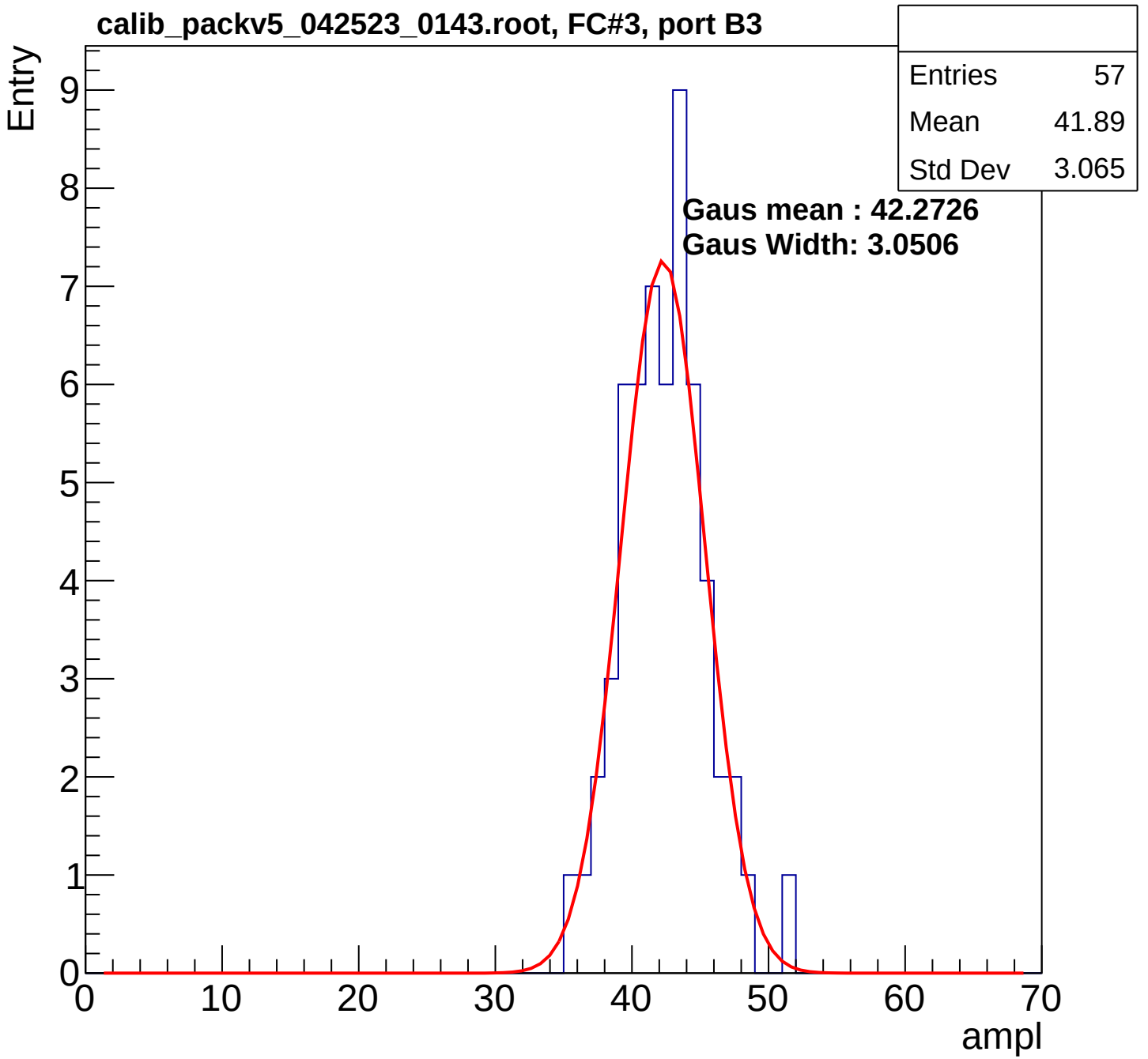
B1L000S, U7-ch97, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 41.89
Std Dev 3.065
Gaus mean : 42.2726
Gaus Width: 3.0506
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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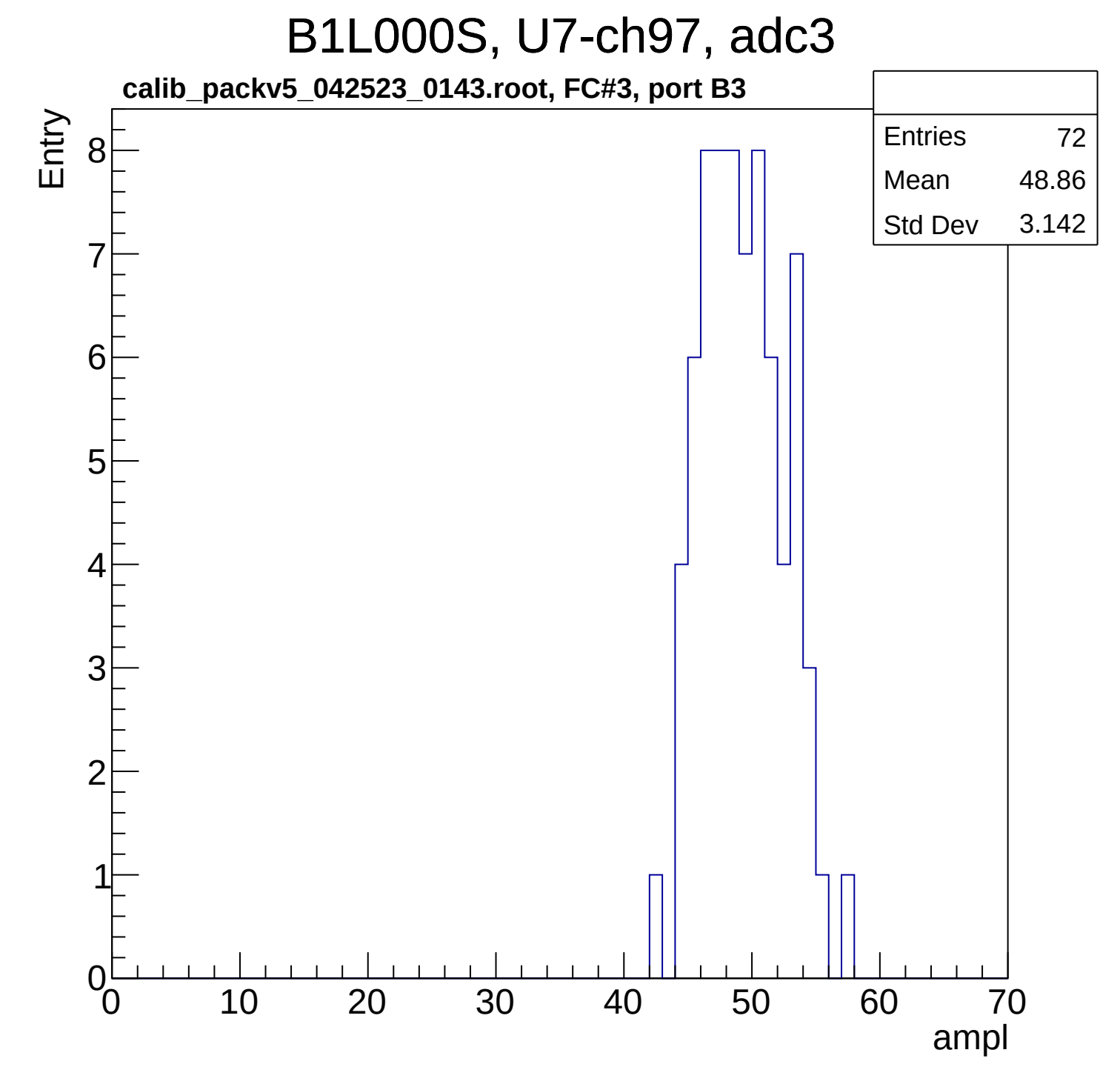
B1L000S, U7-ch97, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 48.86
Std Dev 3.142
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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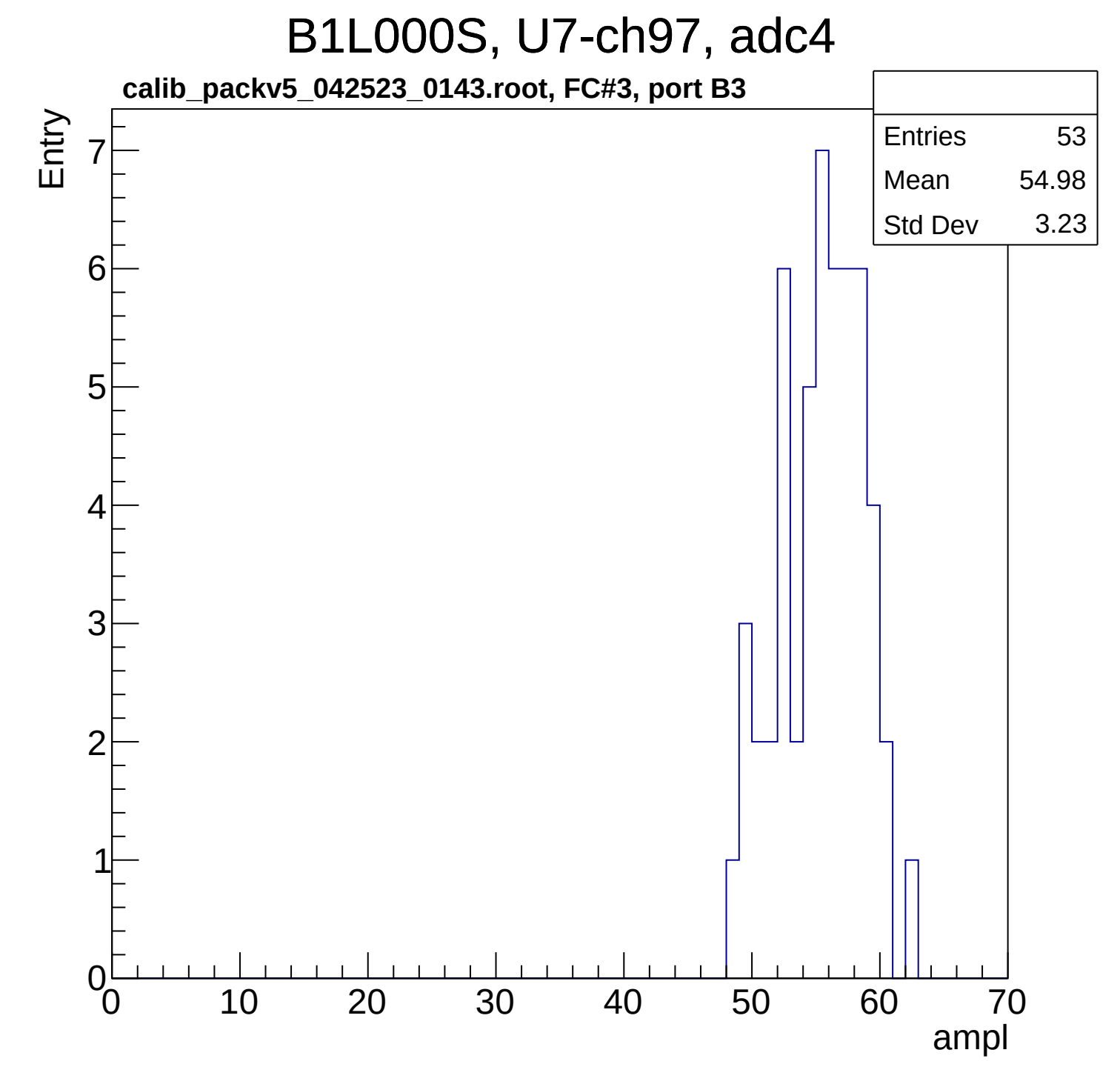
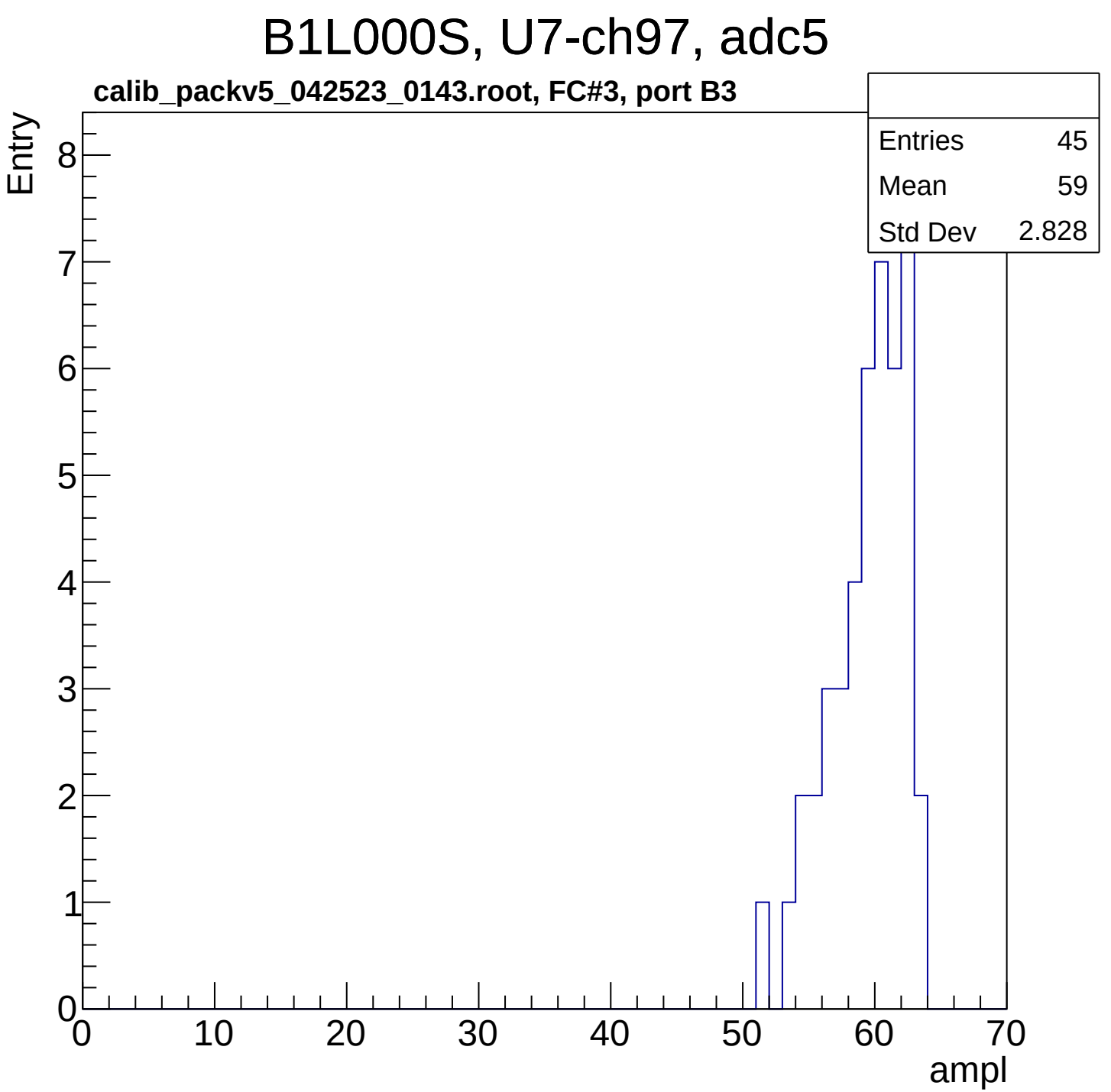

B1L000S, U7-ch97, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 54.98
Std Dev 3.23
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch97, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 59
Std Dev 2.828
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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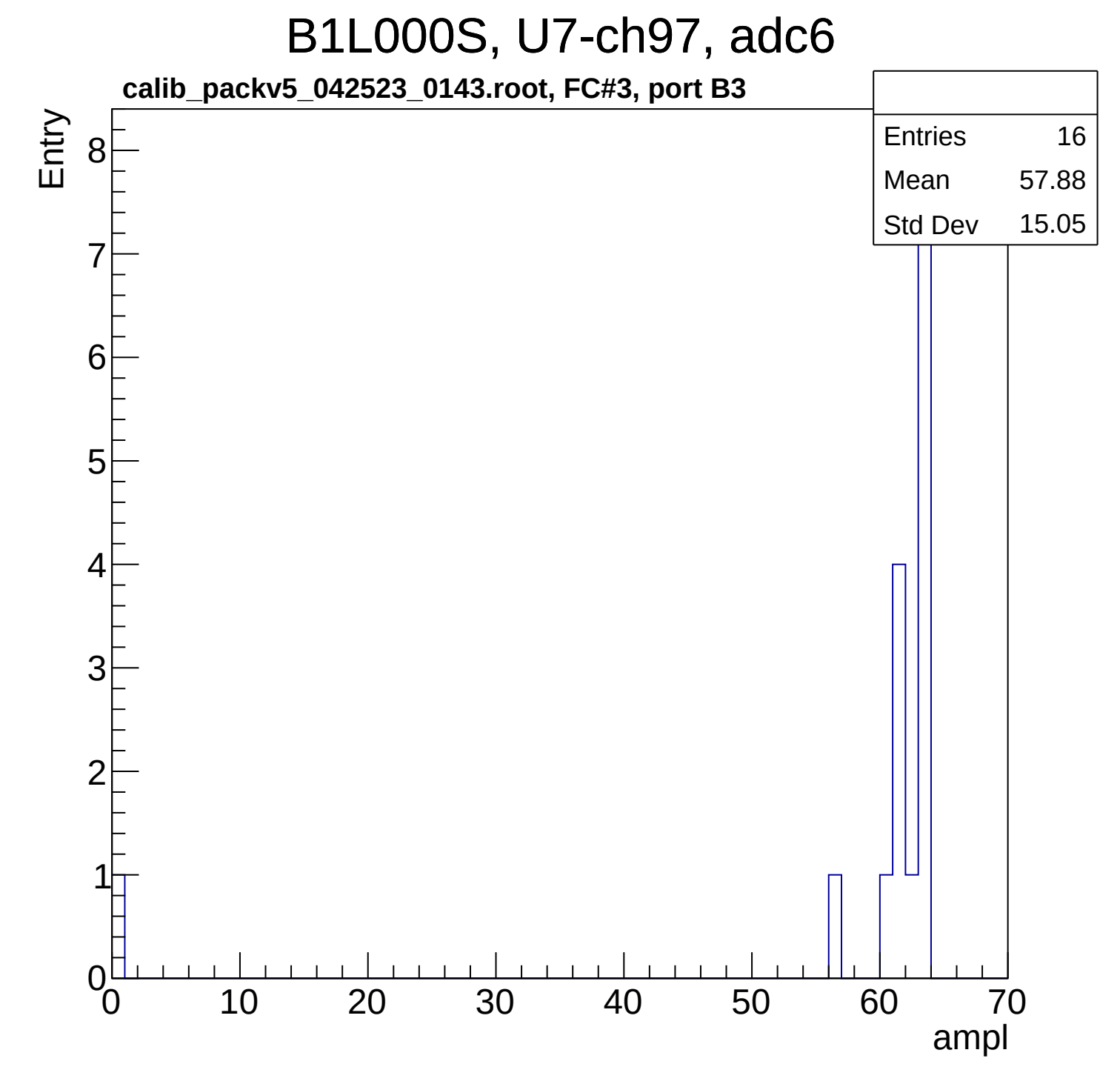
B1L000S, U7-ch97, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.88
Std Dev 15.05
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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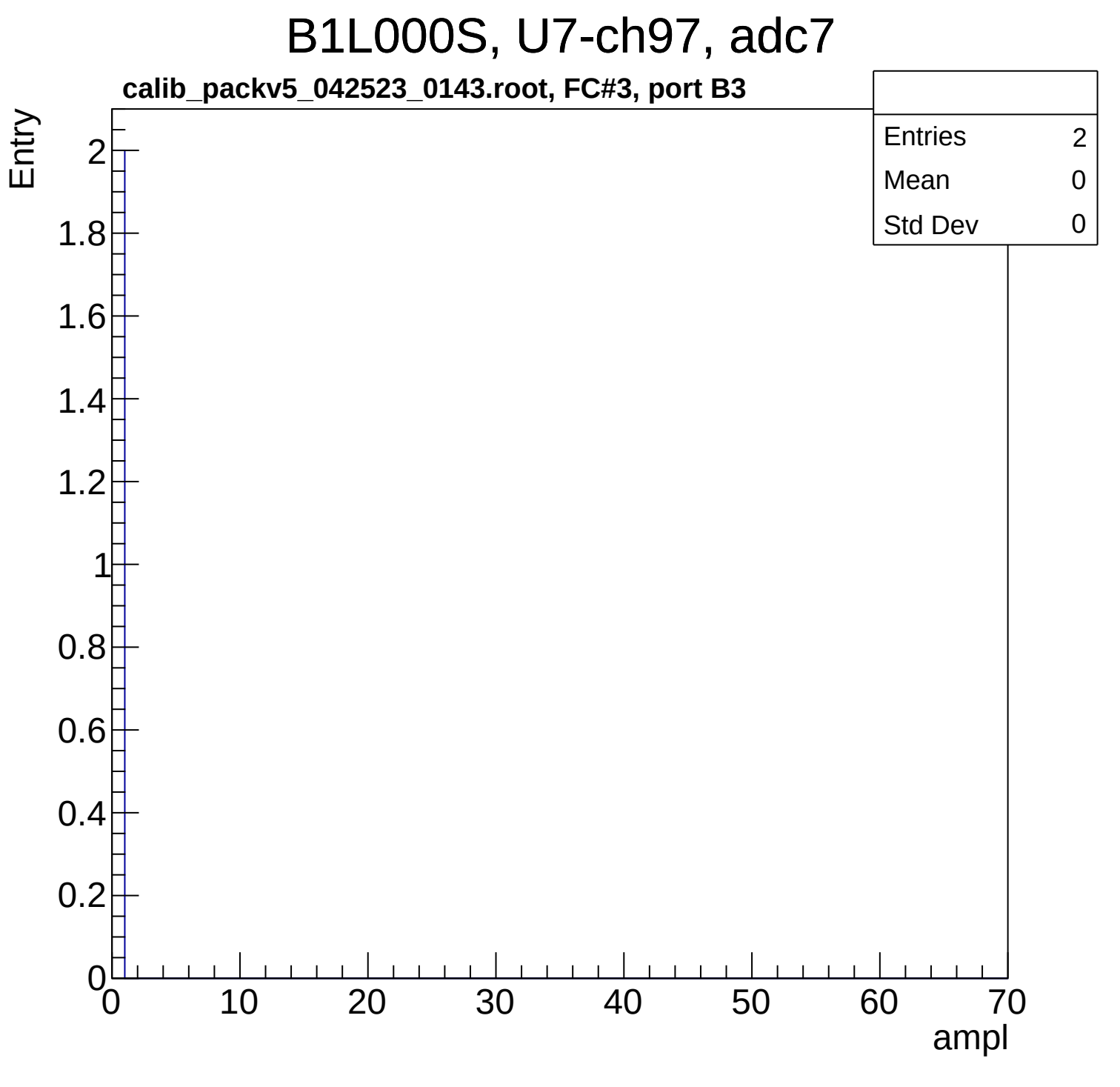
B1L000S, U7-ch97, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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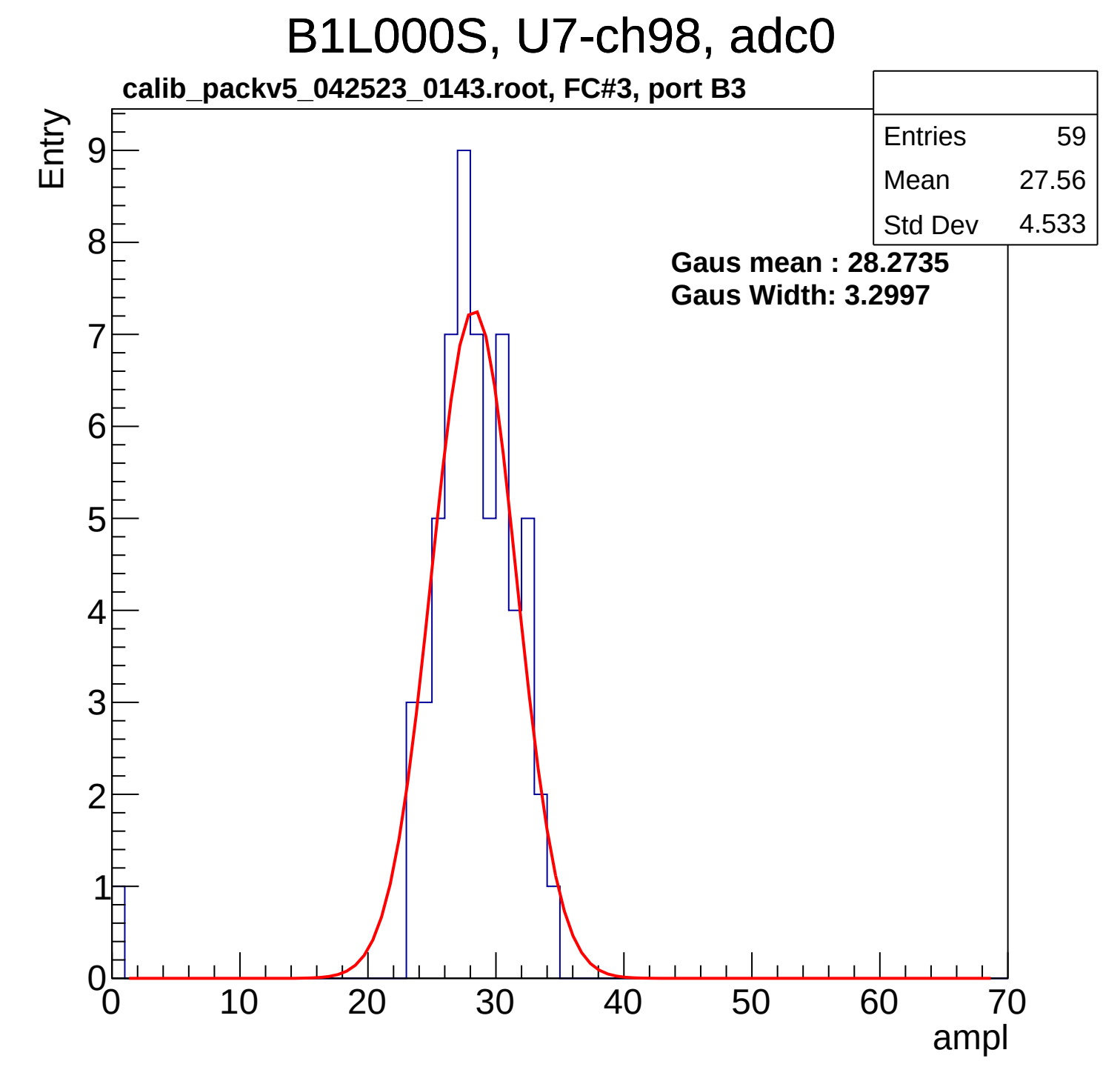
B1L000S, U7-ch98, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 27.56
Std Dev 4.533
Gaus mean : 28.2735
Gaus Width: 3.2997
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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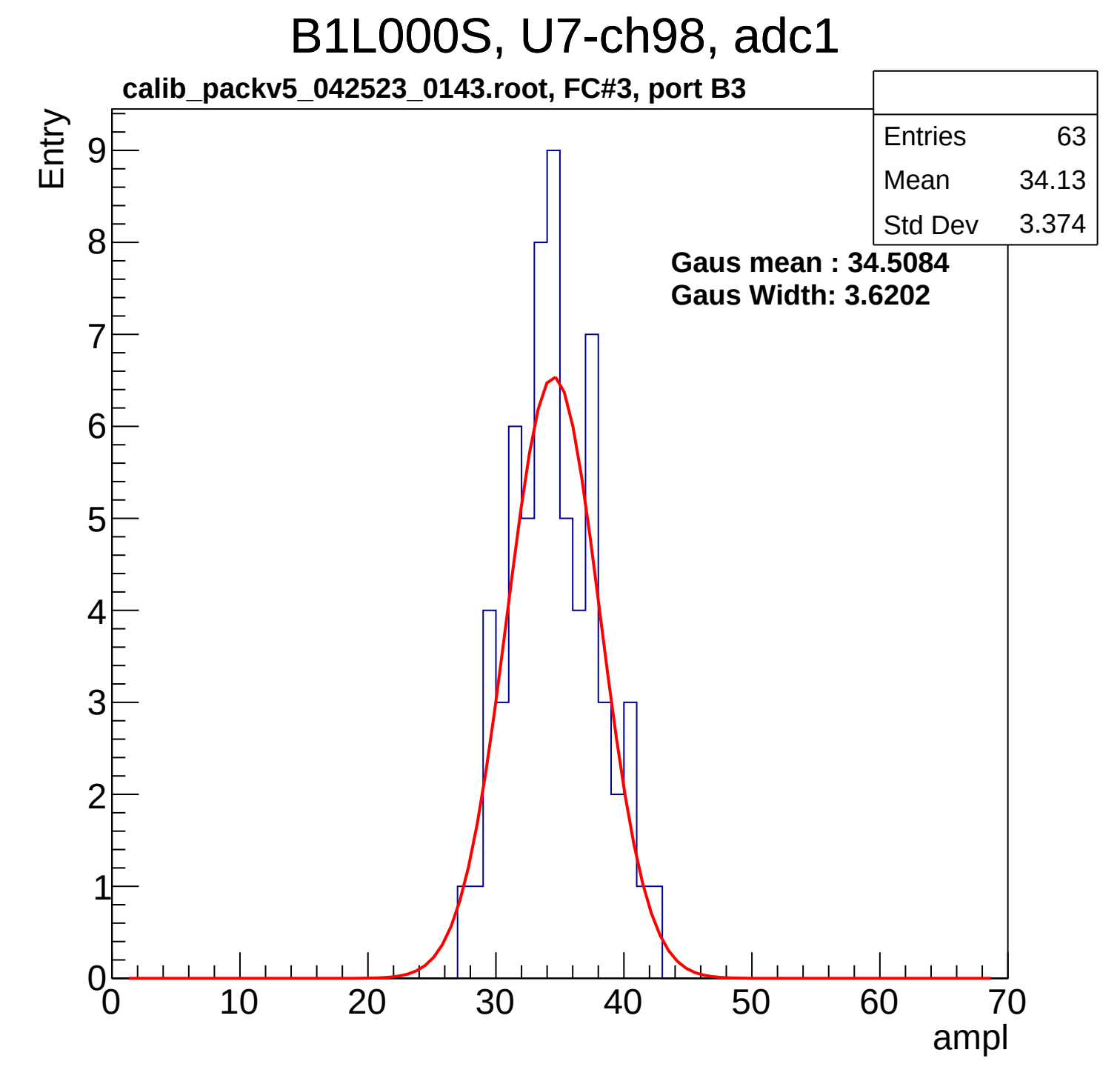
B1L000S, U7-ch98, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.13
Std Dev 3.374
Gaus mean : 34.5084
Gaus Width: 3.6202
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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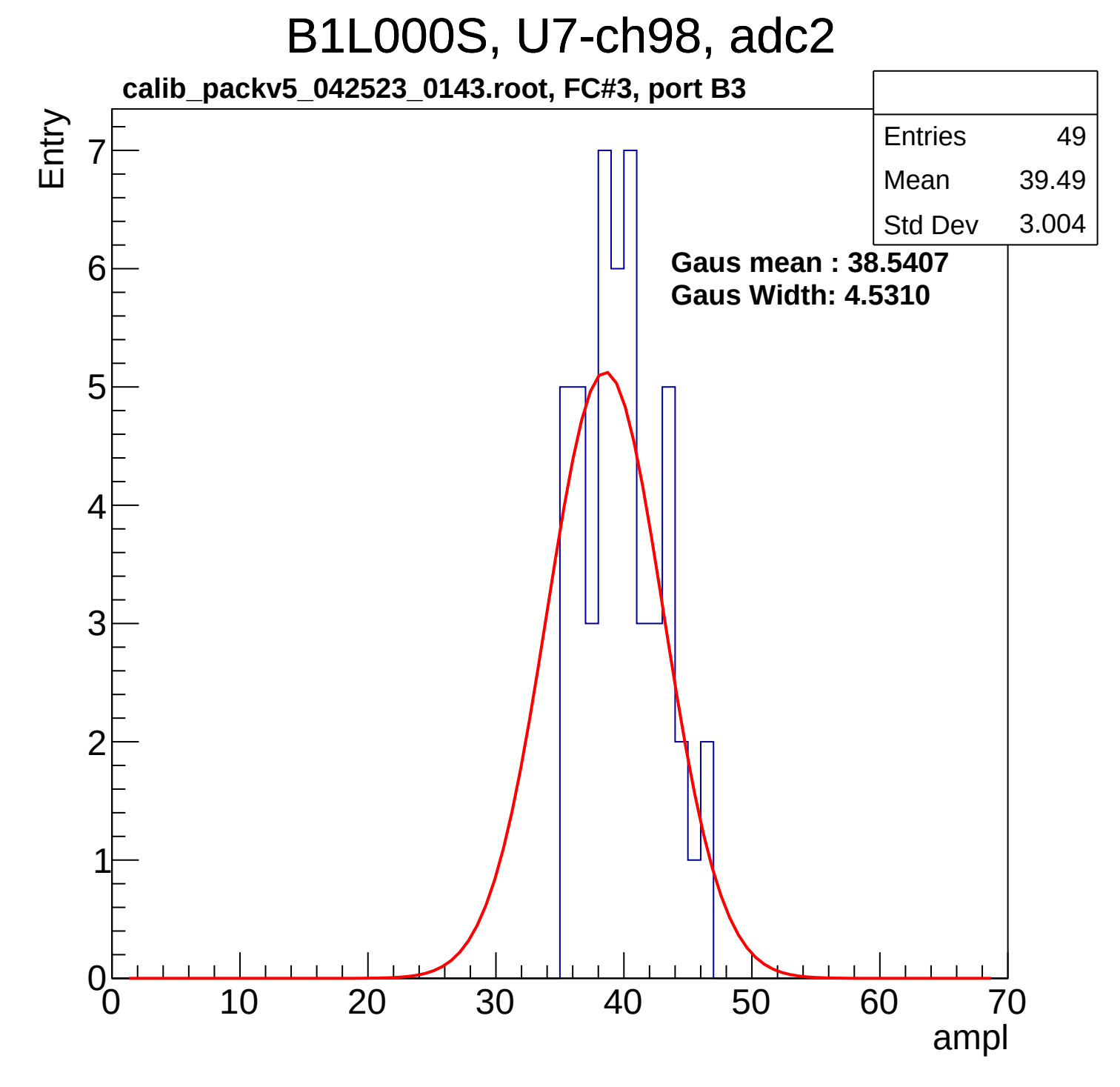
B1L000S, U7-ch98, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 39.49
Std Dev 3.004
Gaus mean : 38.5407
Gaus Width: 4.5310
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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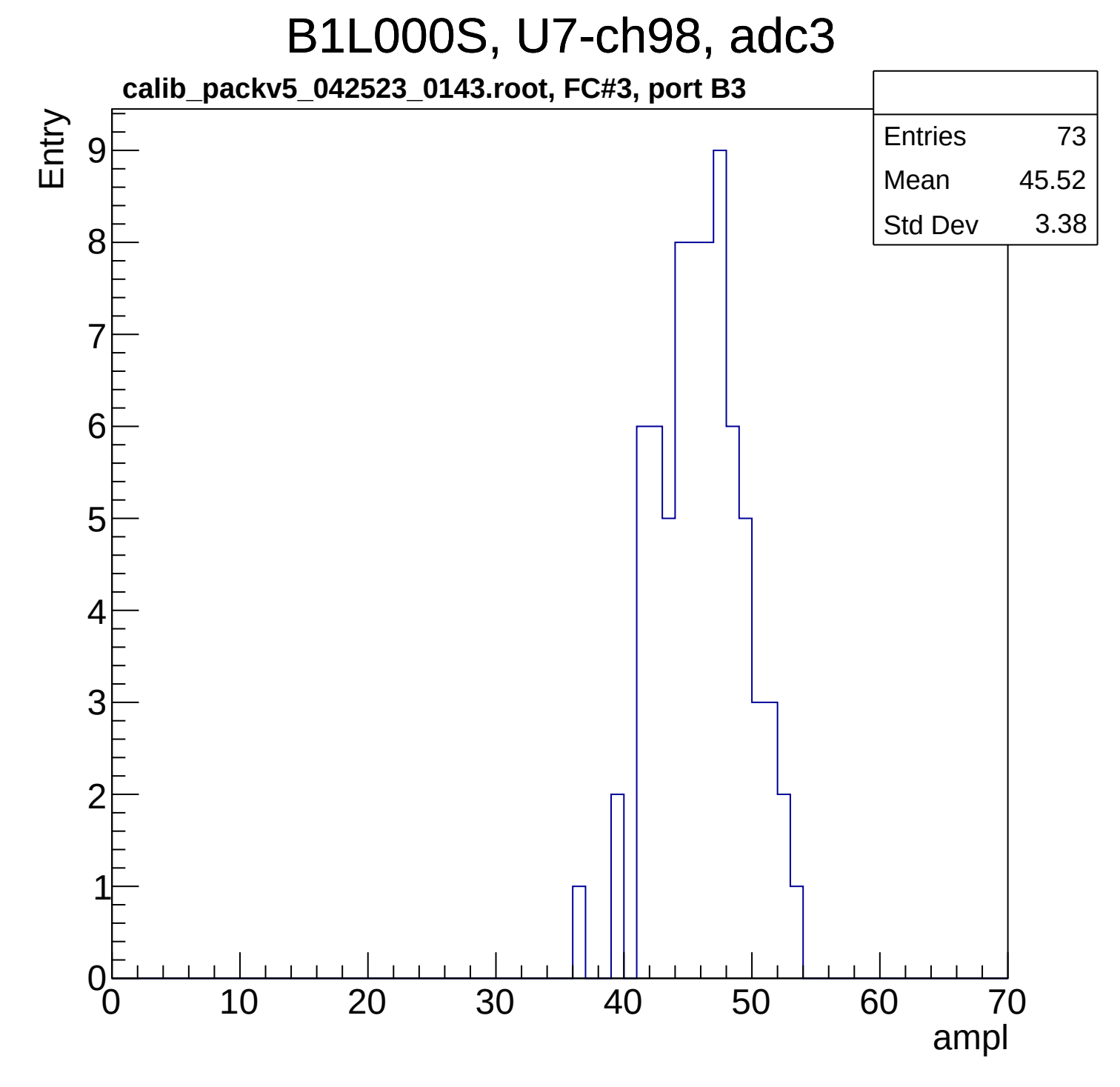
B1L000S, U7-ch98, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 45.52
Std Dev 3.38
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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60
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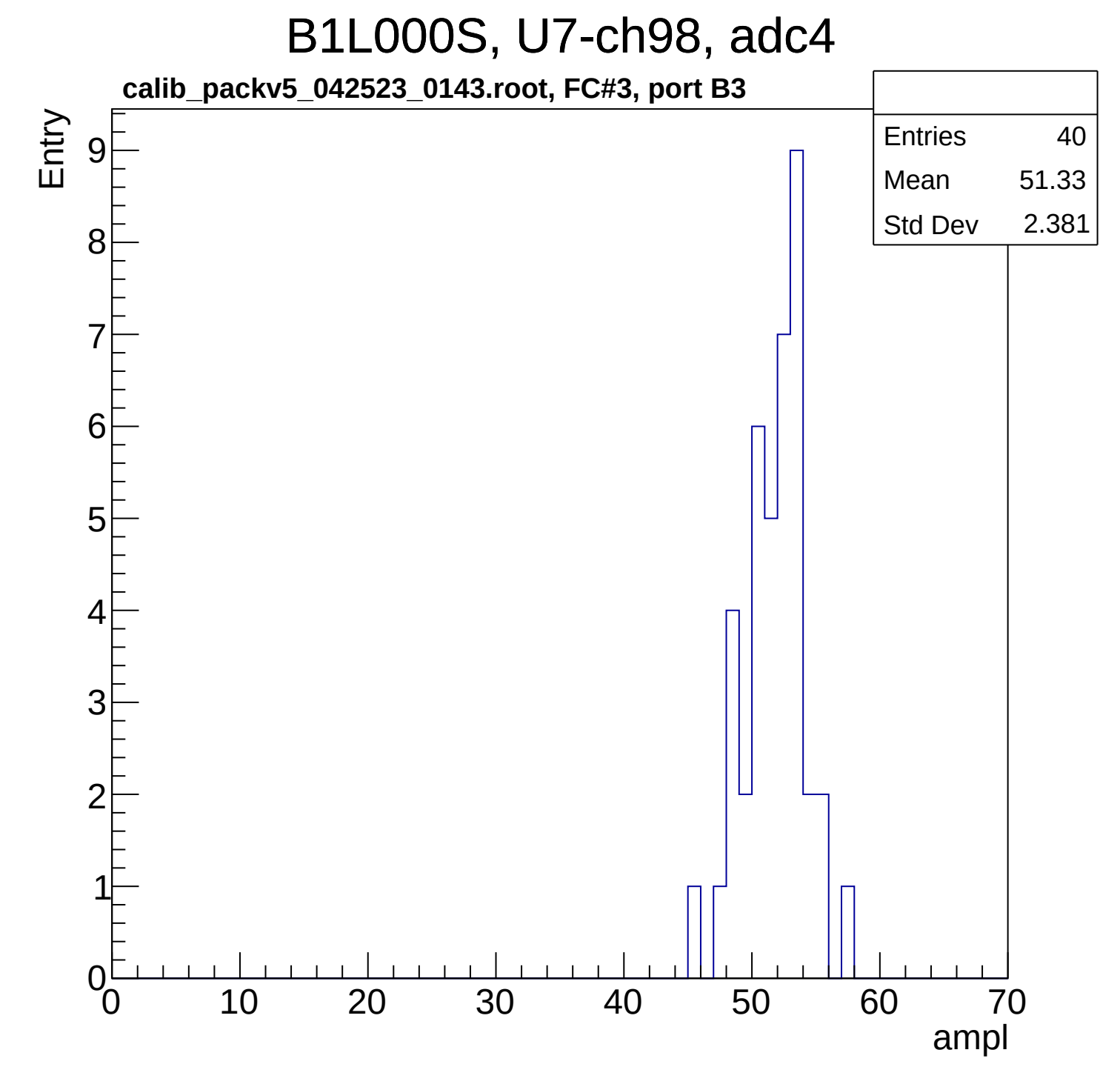
B1L000S, U7-ch98, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 51.33
Std Dev 2.381
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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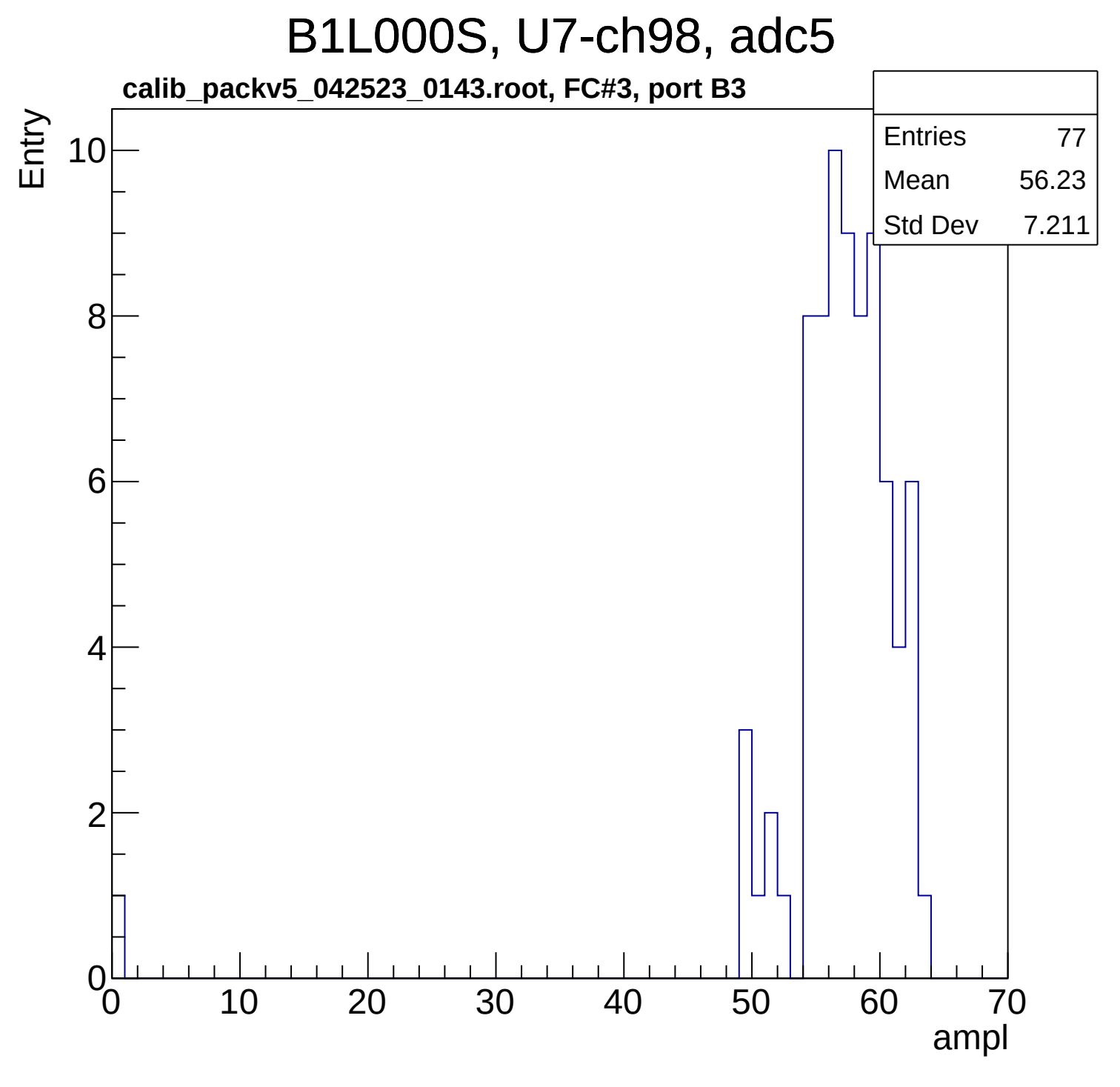
B1L000S, U7-ch98, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 56.23
Std Dev 7.211
Entry
ampl
0
2
4
6
8
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0
10
20
30
40
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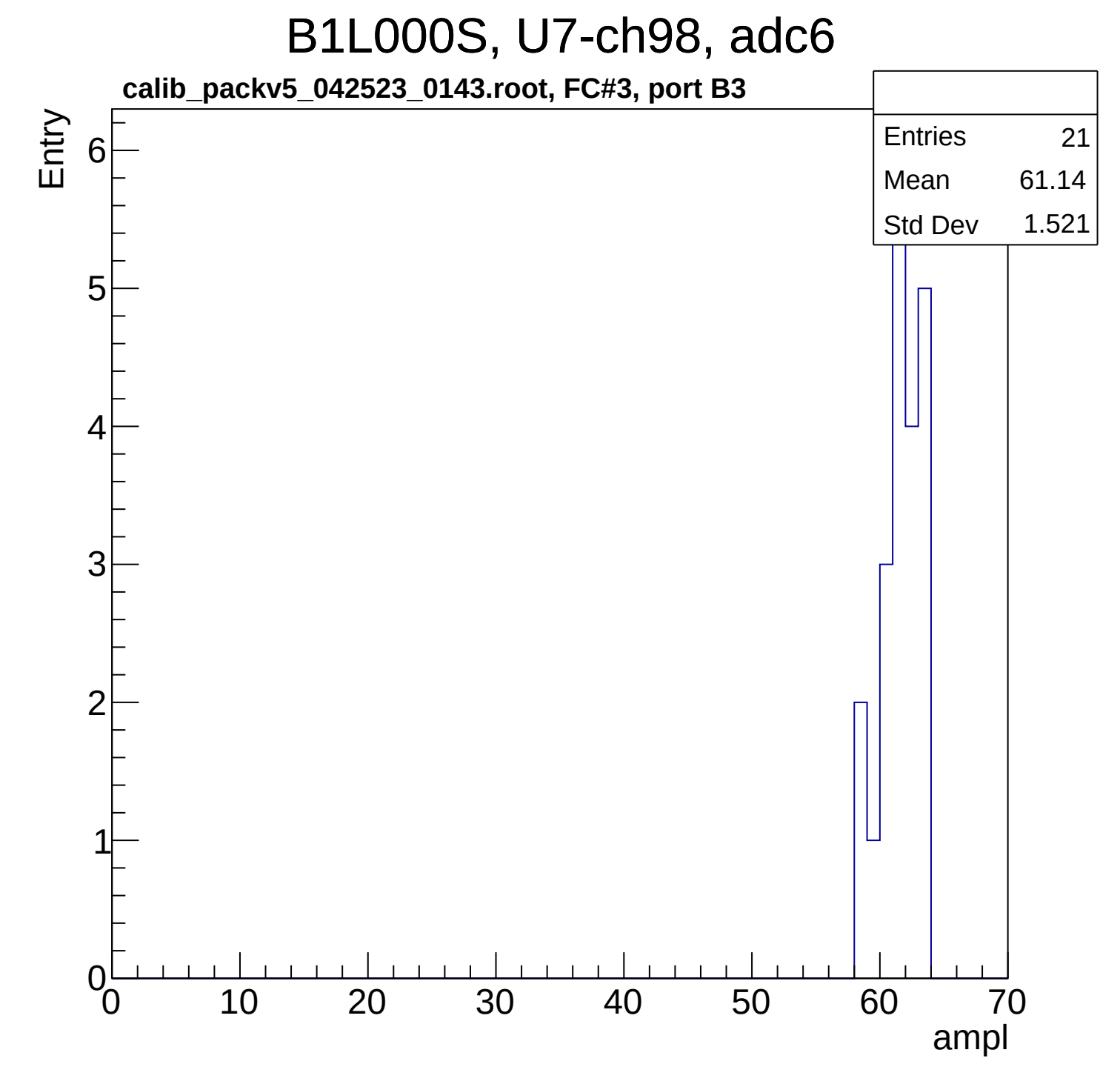
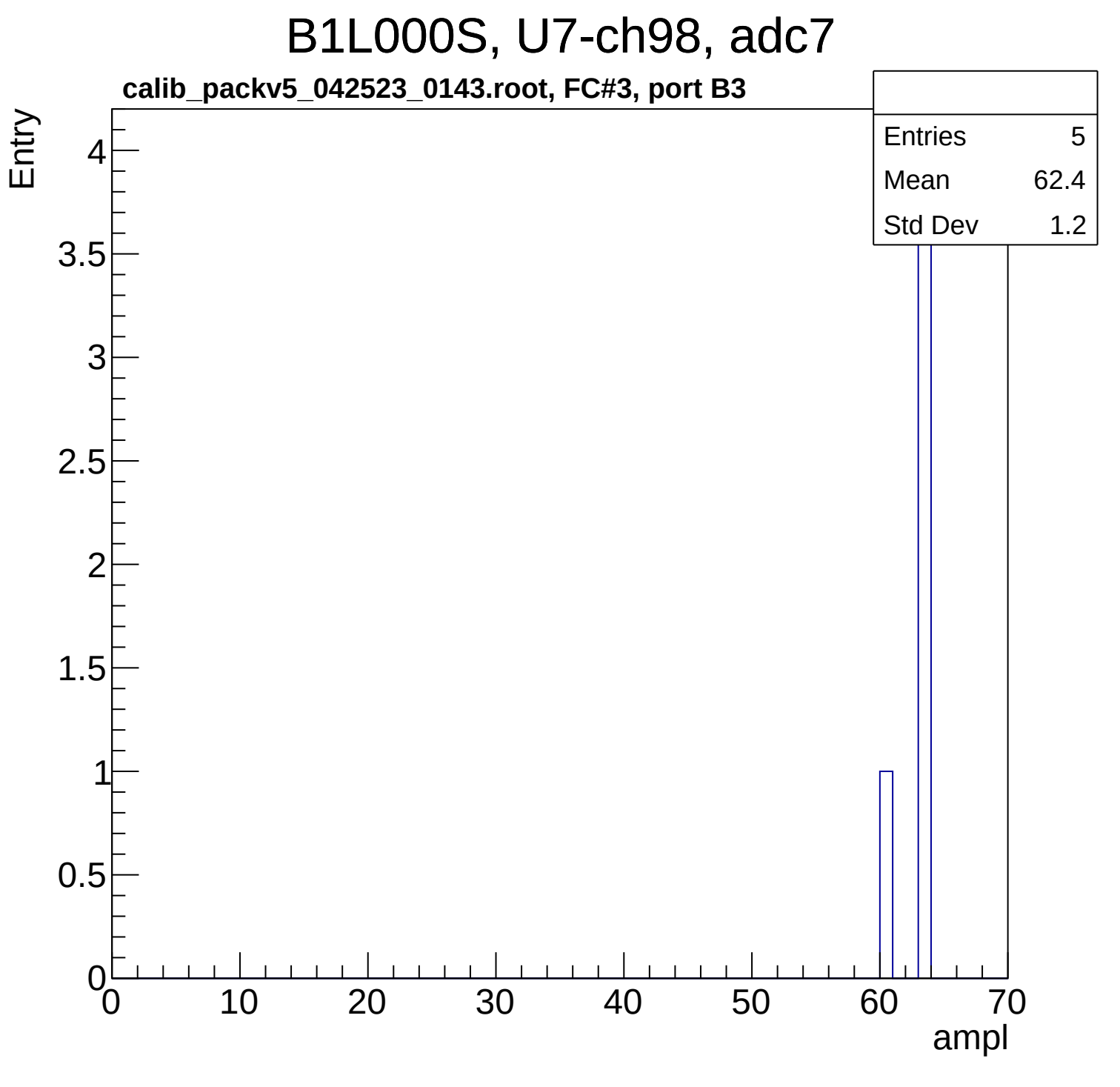
60
70


B1L000S, U7-ch98, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 61.14
Std Dev 1.521
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch98, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 62.4
Std Dev 1.2
Entry
ampl
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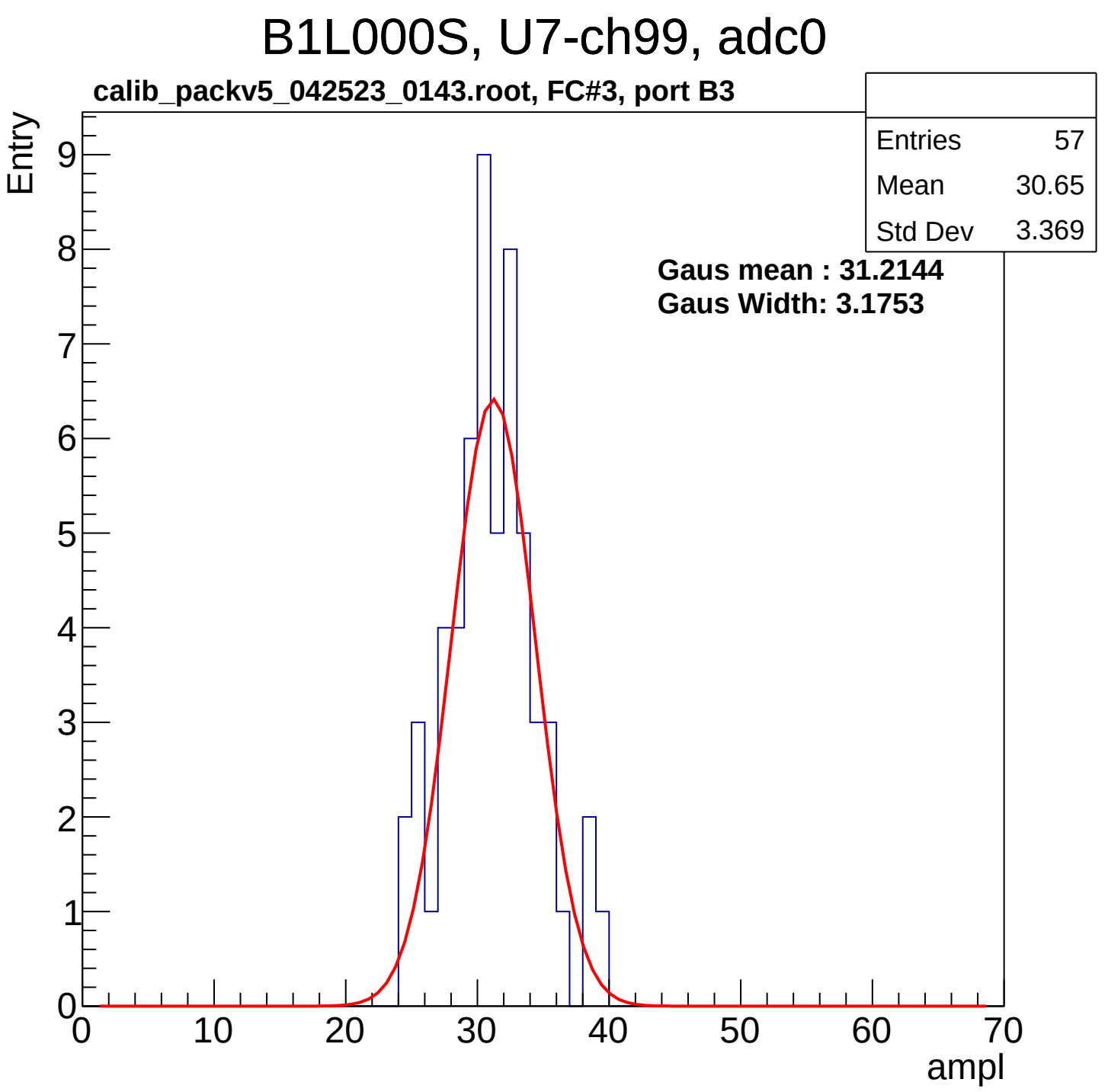
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch99, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 30.65
Std Dev 3.369
Gaus mean : 31.2144
Gaus Width: 3.1753
Entry
ampl
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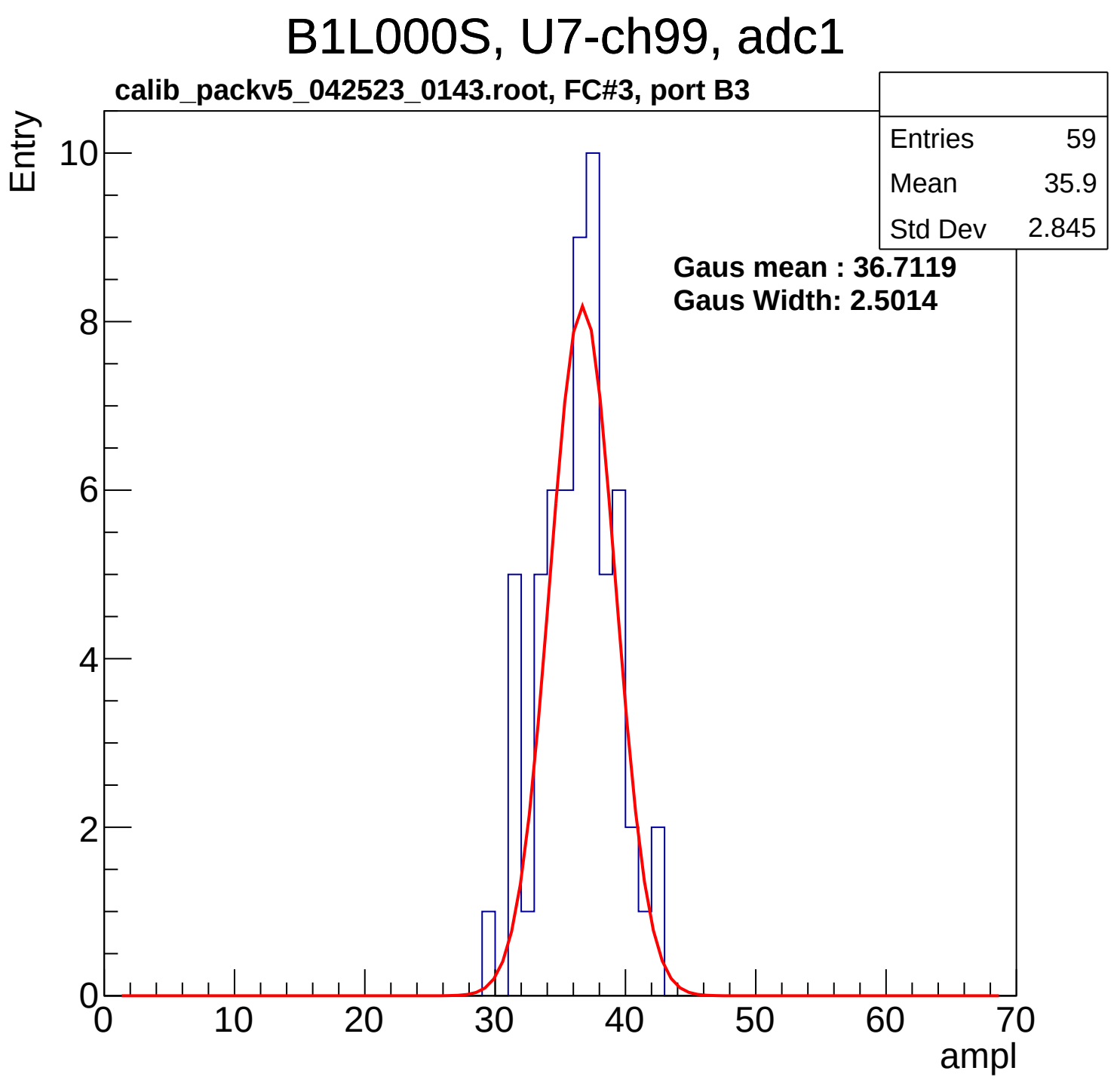
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch99, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.9
Std Dev 2.845
Gaus mean : 36.7119
Gaus Width: 2.5014
Entry
ampl
0
2
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6
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10
0
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20
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60
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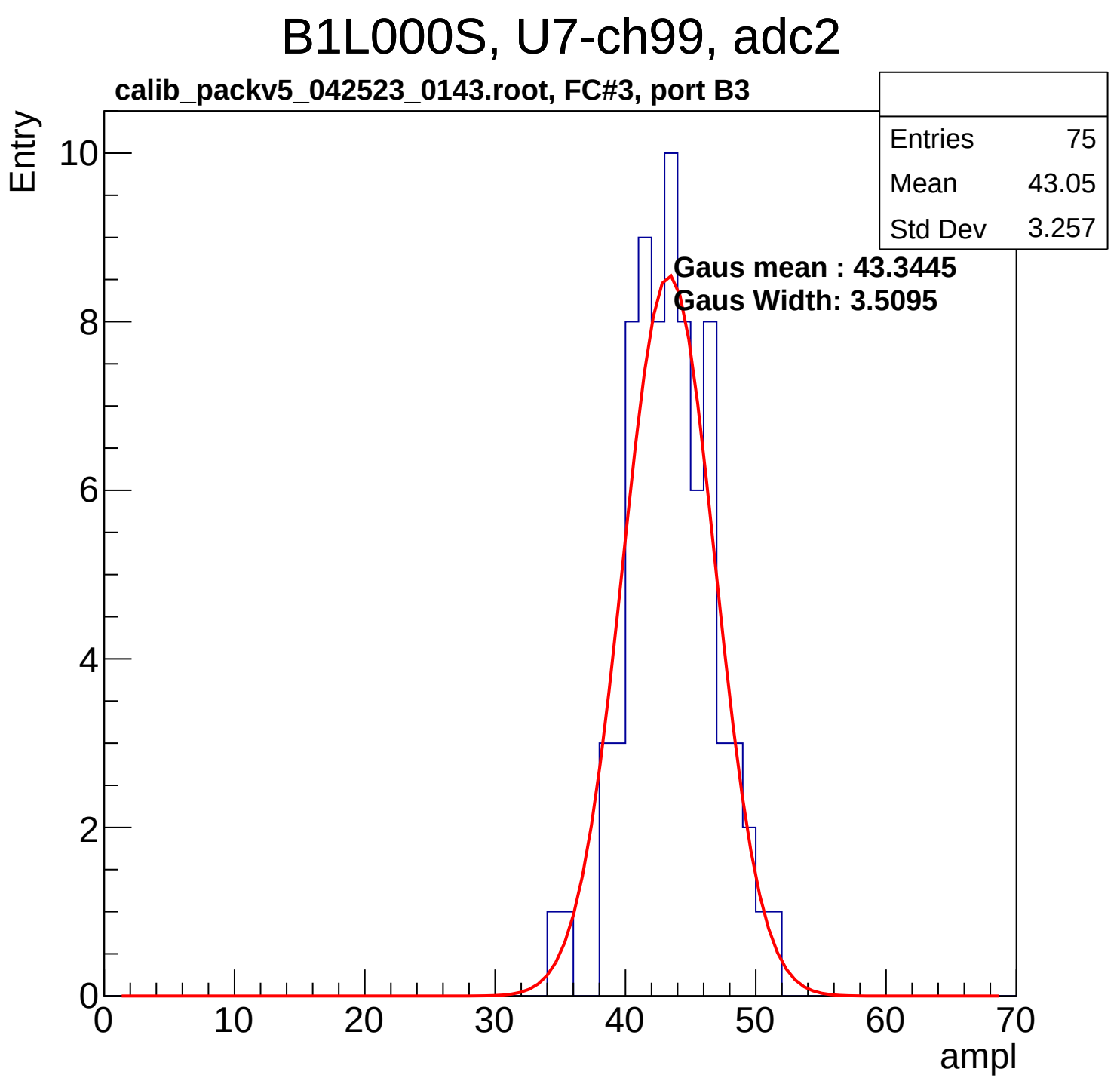
B1L000S, U7-ch99, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 43.05
Std Dev 3.257
Gaus mean : 43.3445
Gaus Width: 3.5095
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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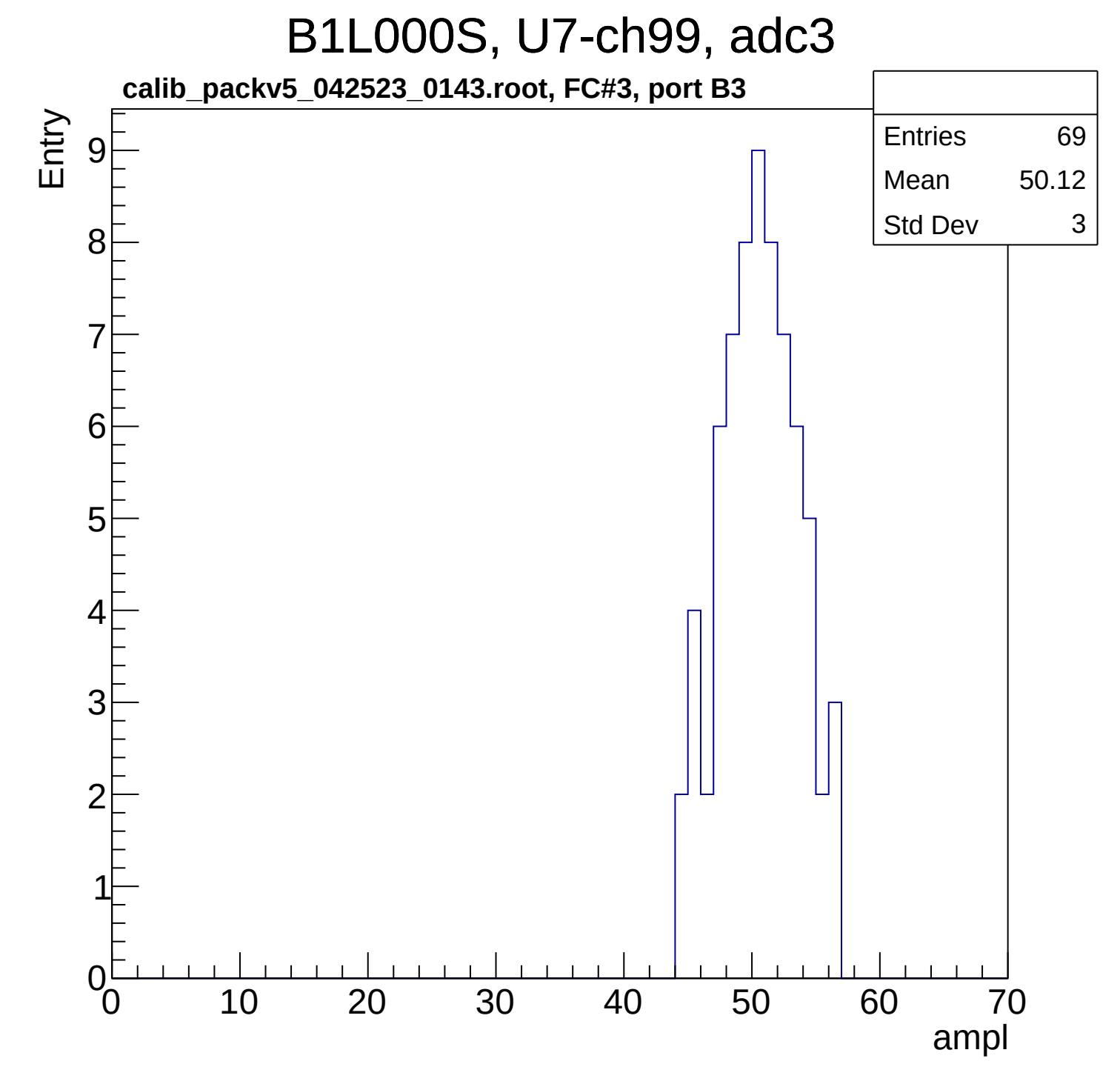
B1L000S, U7-ch99, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 50.12
Std Dev 3
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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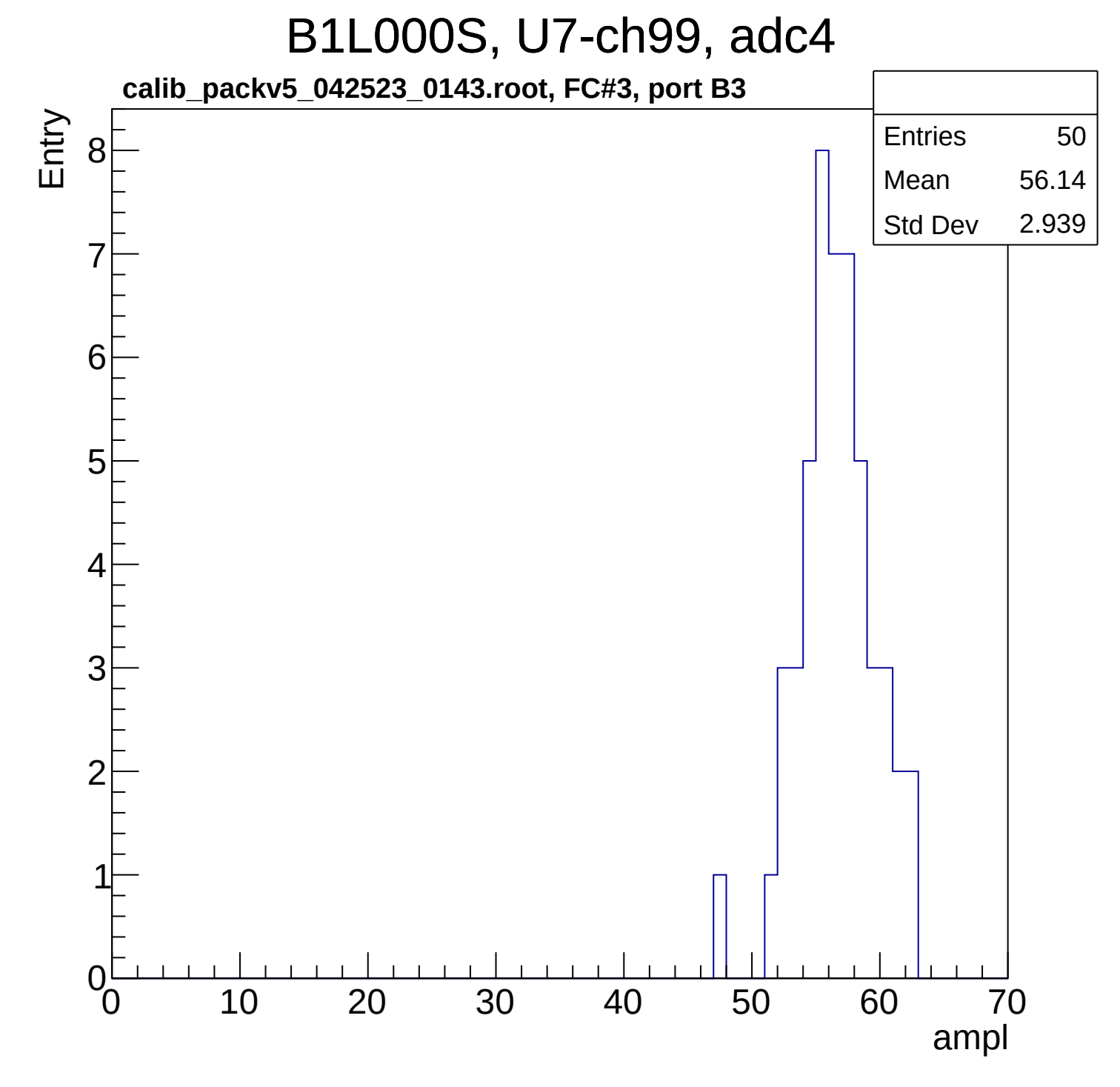
B1L000S, U7-ch99, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 56.14
Std Dev 2.939
Entry
ampl
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5
6
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0
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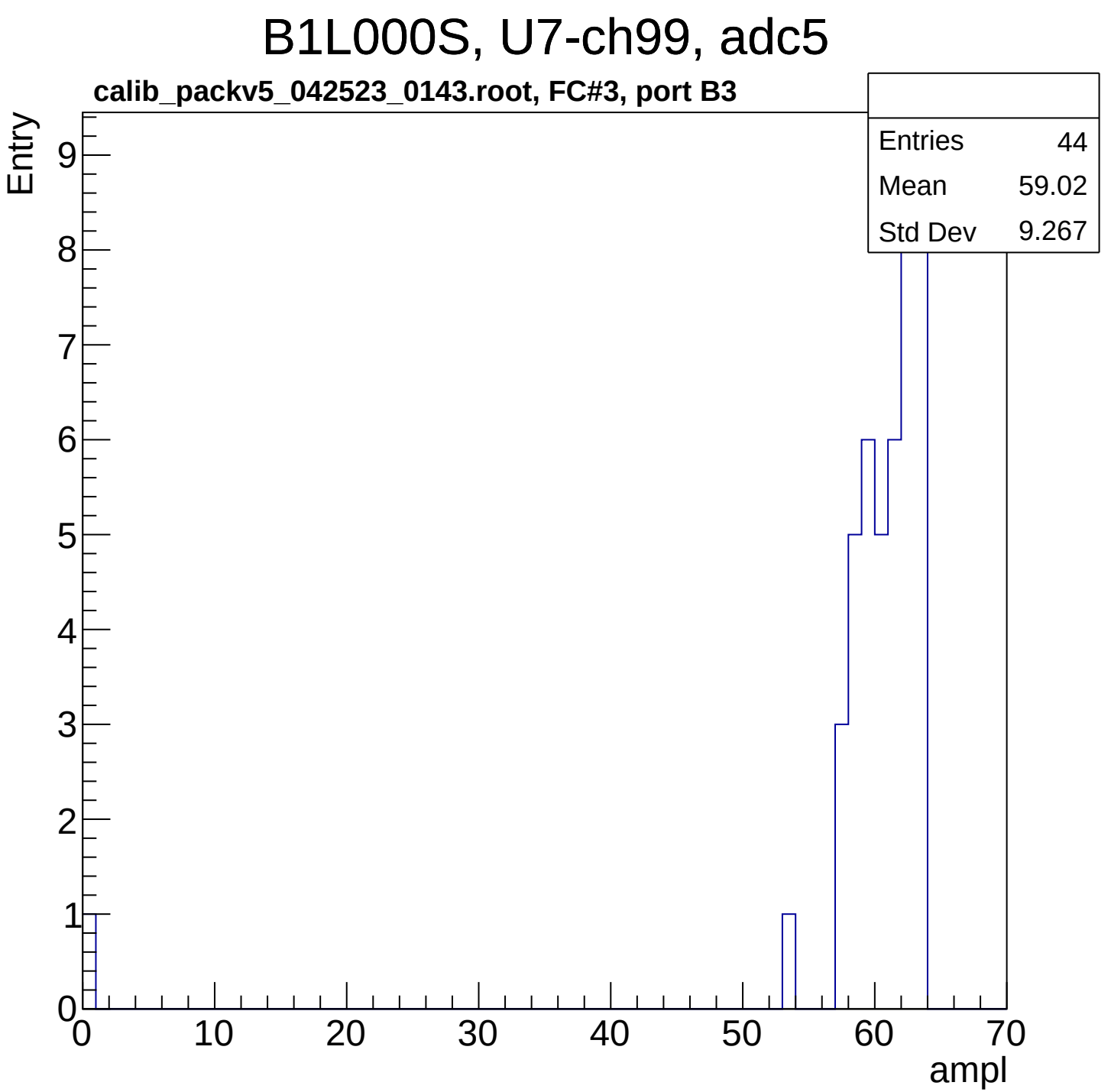
B1L000S, U7-ch99, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 59.02
Std Dev 9.267
Entry
ampl
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2
3
4
5
6
7
8
9
0
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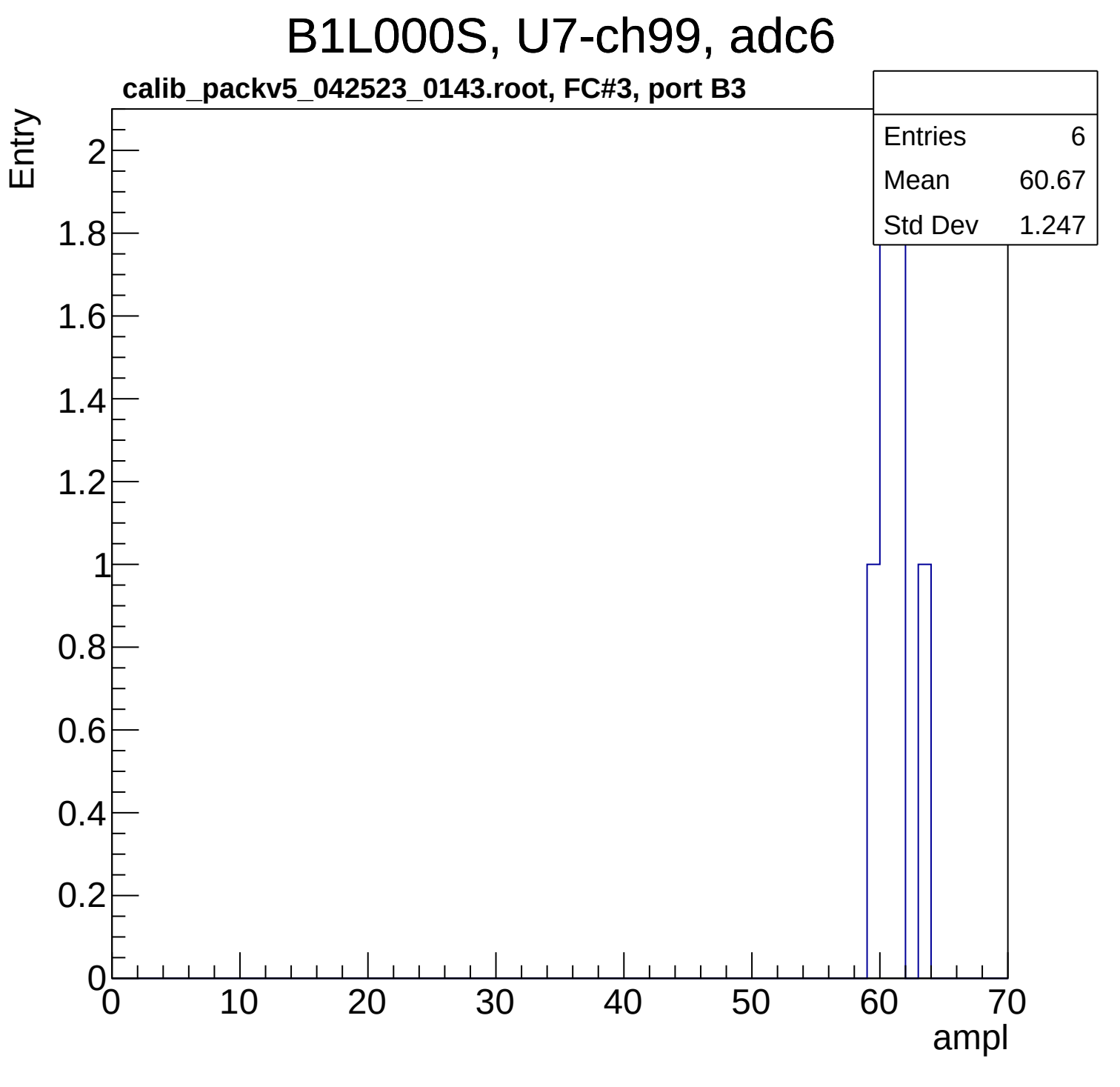
60
70


B1L000S, U7-ch99, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 6
Mean 60.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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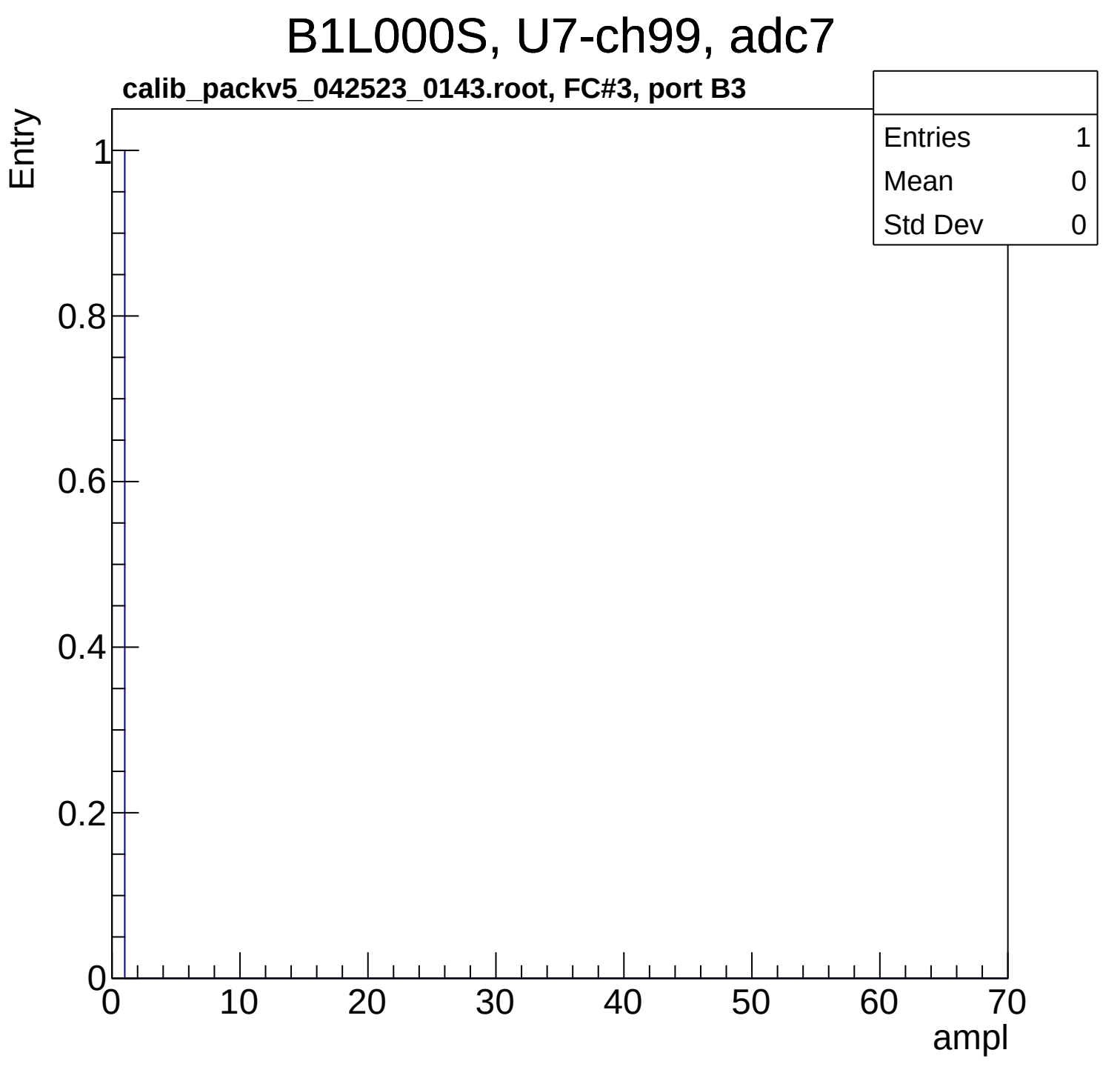
20
30
40
50
60
70


B1L000S, U7-ch99, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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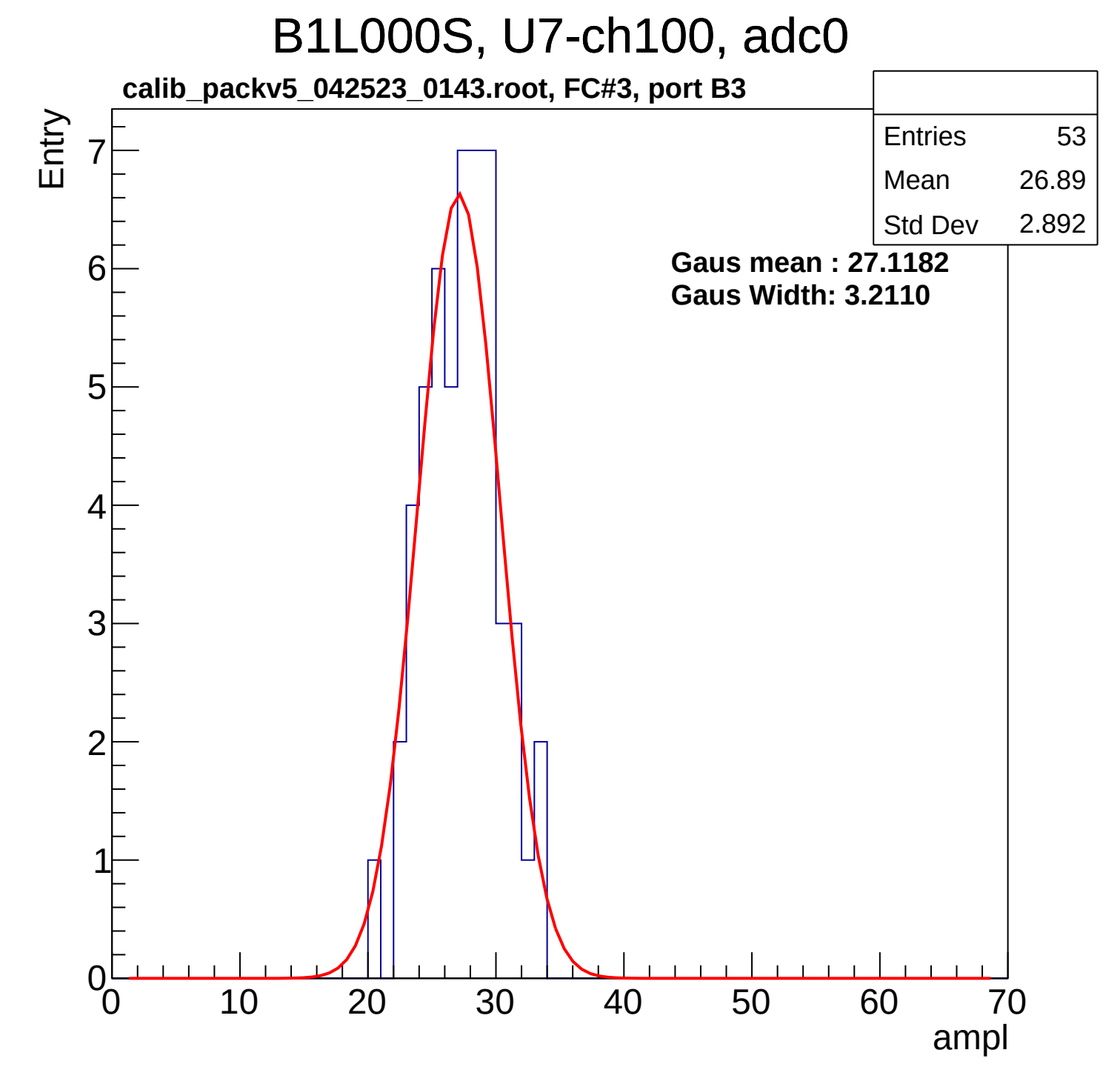
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch100, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 26.89
Std Dev 2.892
Gaus mean : 27.1182
Gaus Width: 3.2110
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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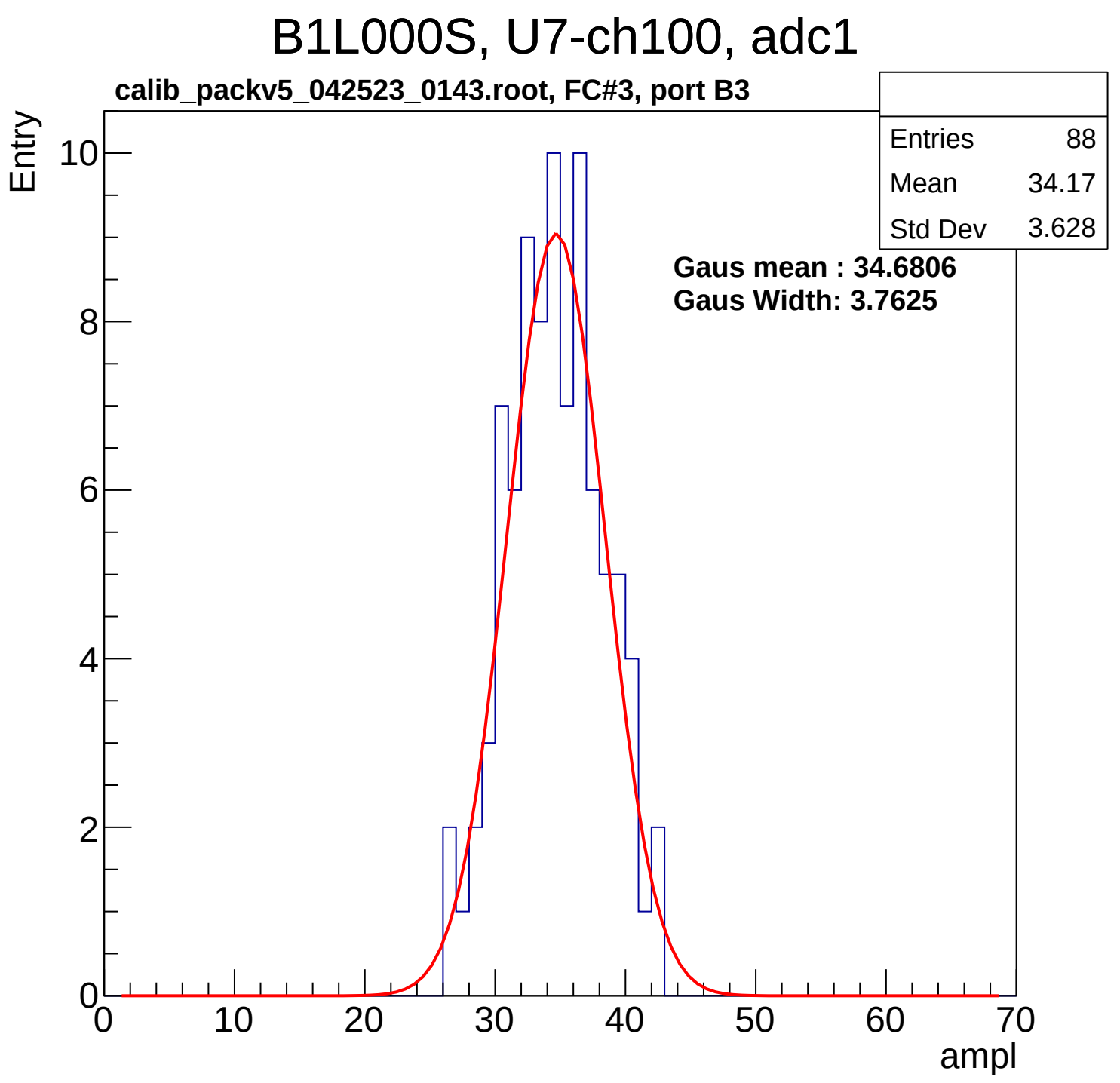
40
50
60
70


B1L000S, U7-ch100, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 88
Mean 34.17
Std Dev 3.628
Gaus mean : 34.6806
Gaus Width: 3.7625
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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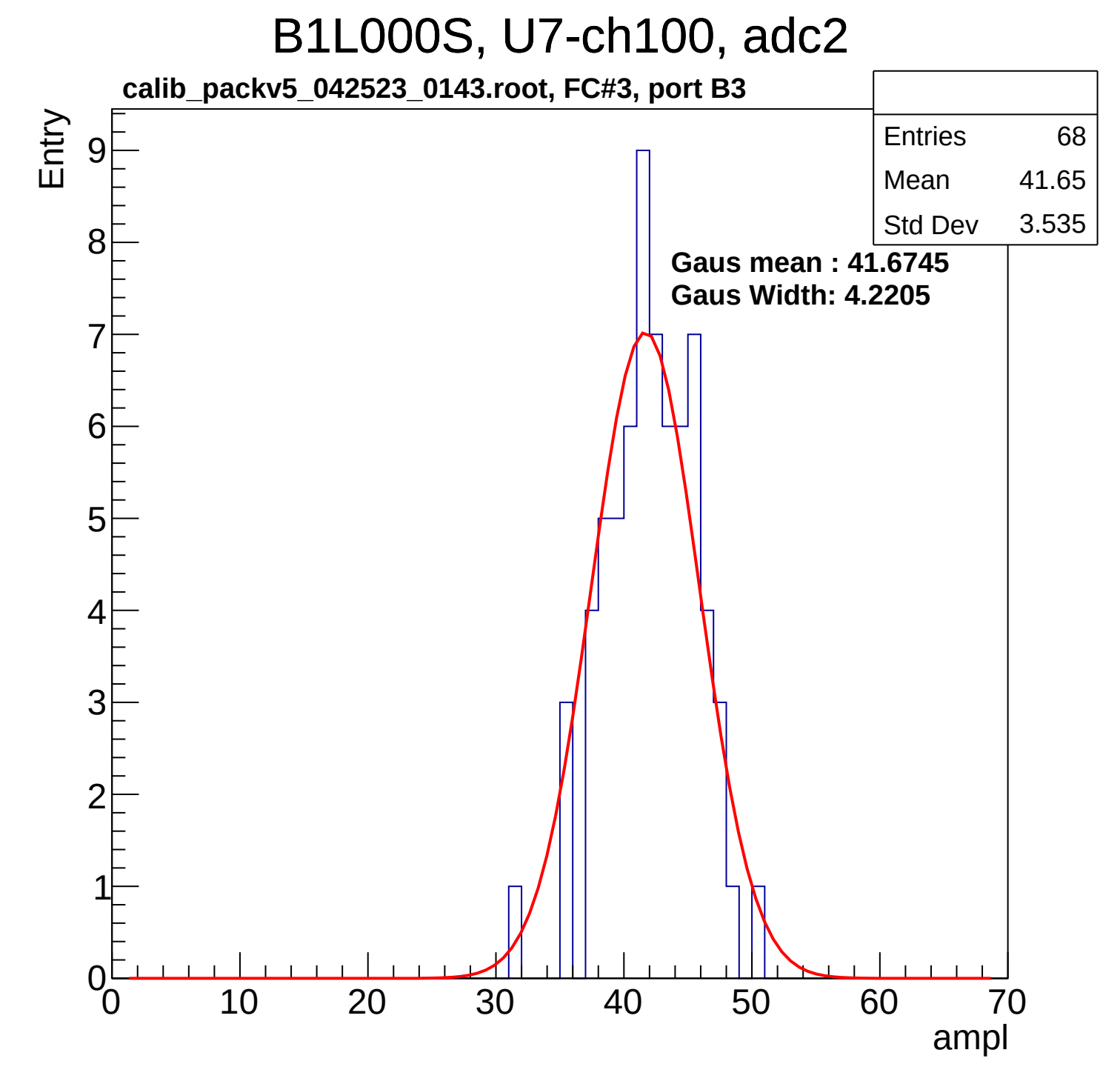
B1L000S, U7-ch100, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 41.65
Std Dev 3.535
Gaus mean : 41.6745
Gaus Width: 4.2205
Entry
ampl
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3
4
5
6
7
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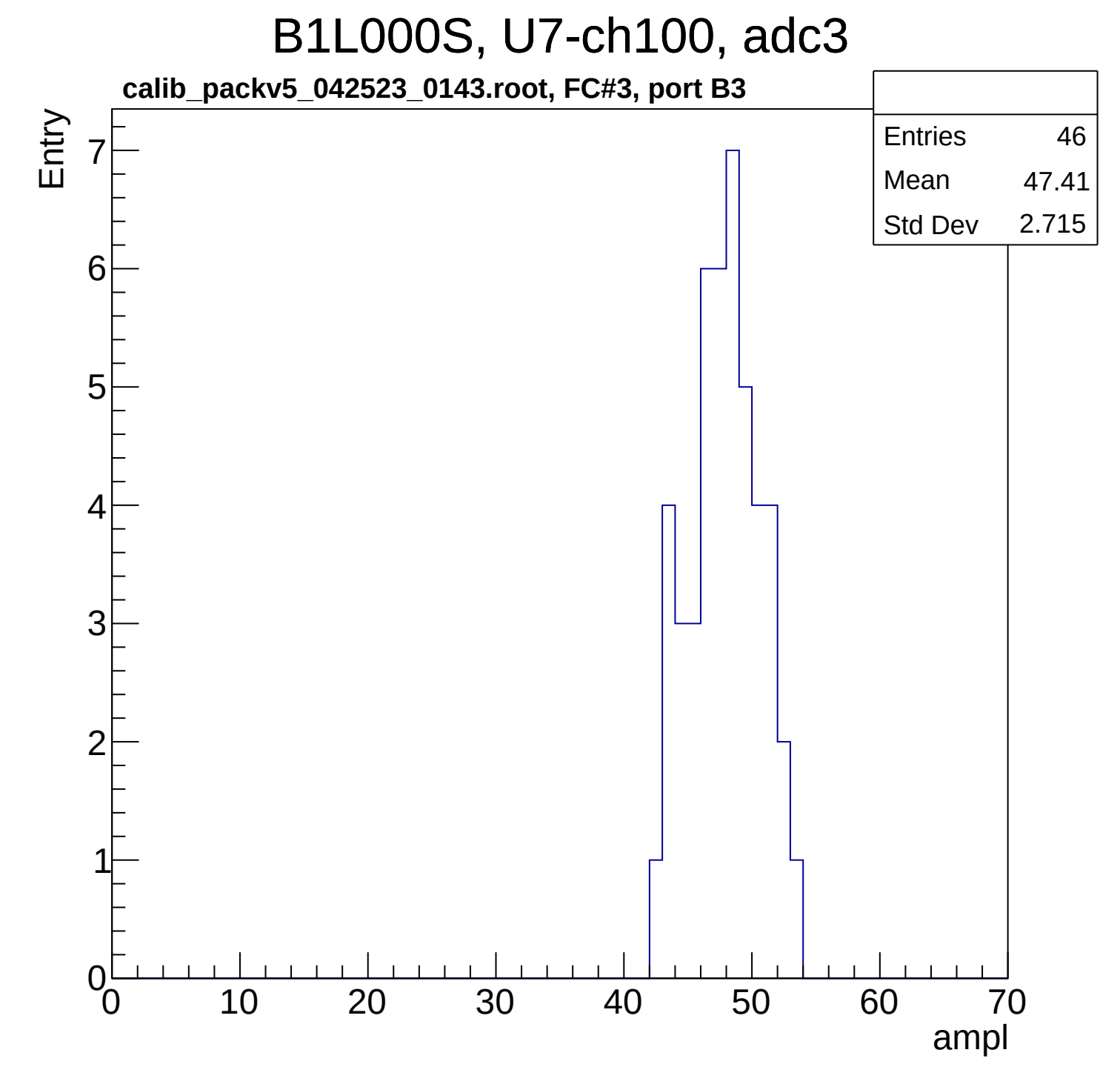
B1L000S, U7-ch100, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 46
Mean 47.41
Std Dev 2.715
Entry
ampl
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2
3
4
5
6
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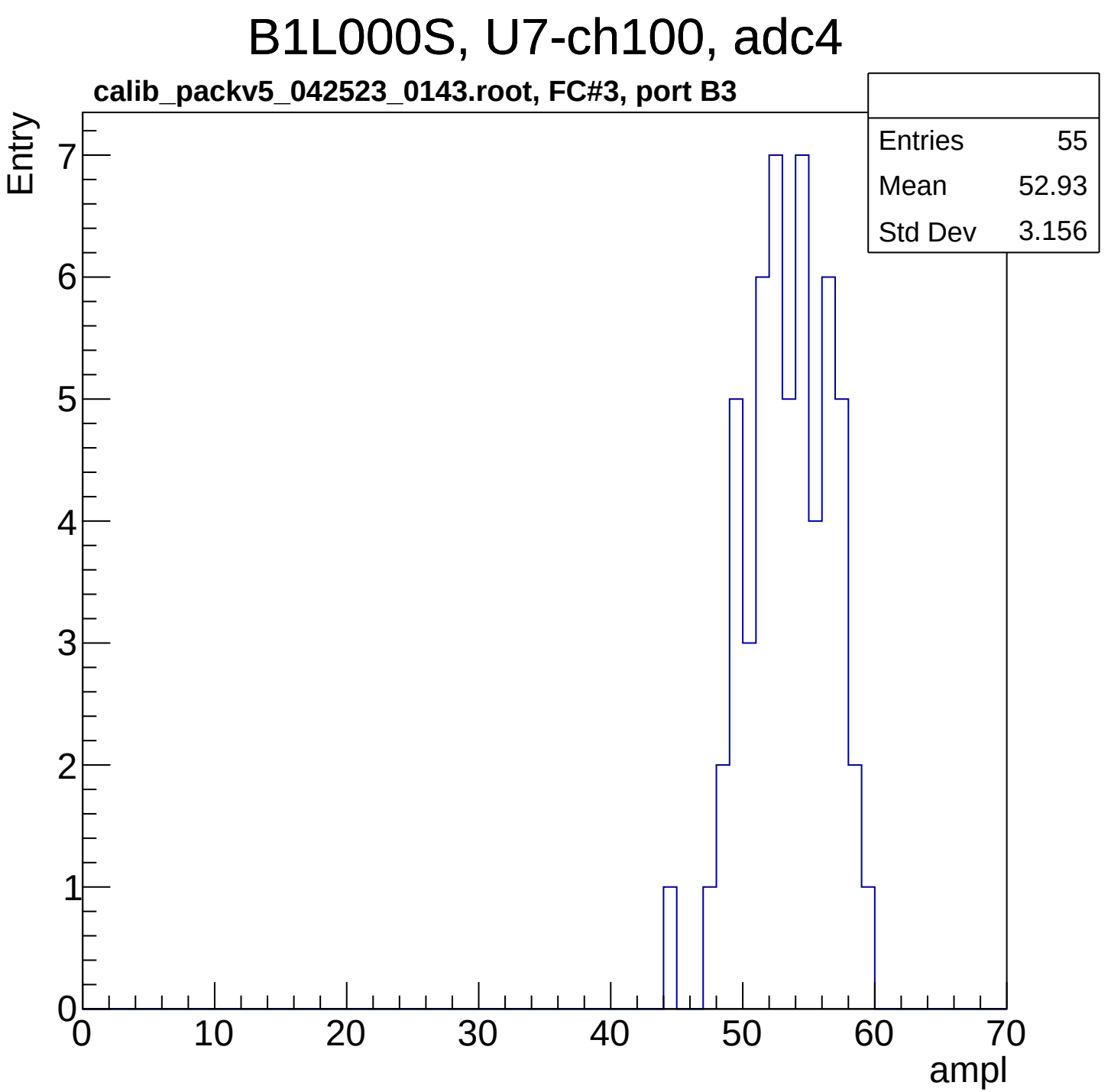
B1L000S, U7-ch100, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 52.93
Std Dev 3.156
Entry
ampl
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40
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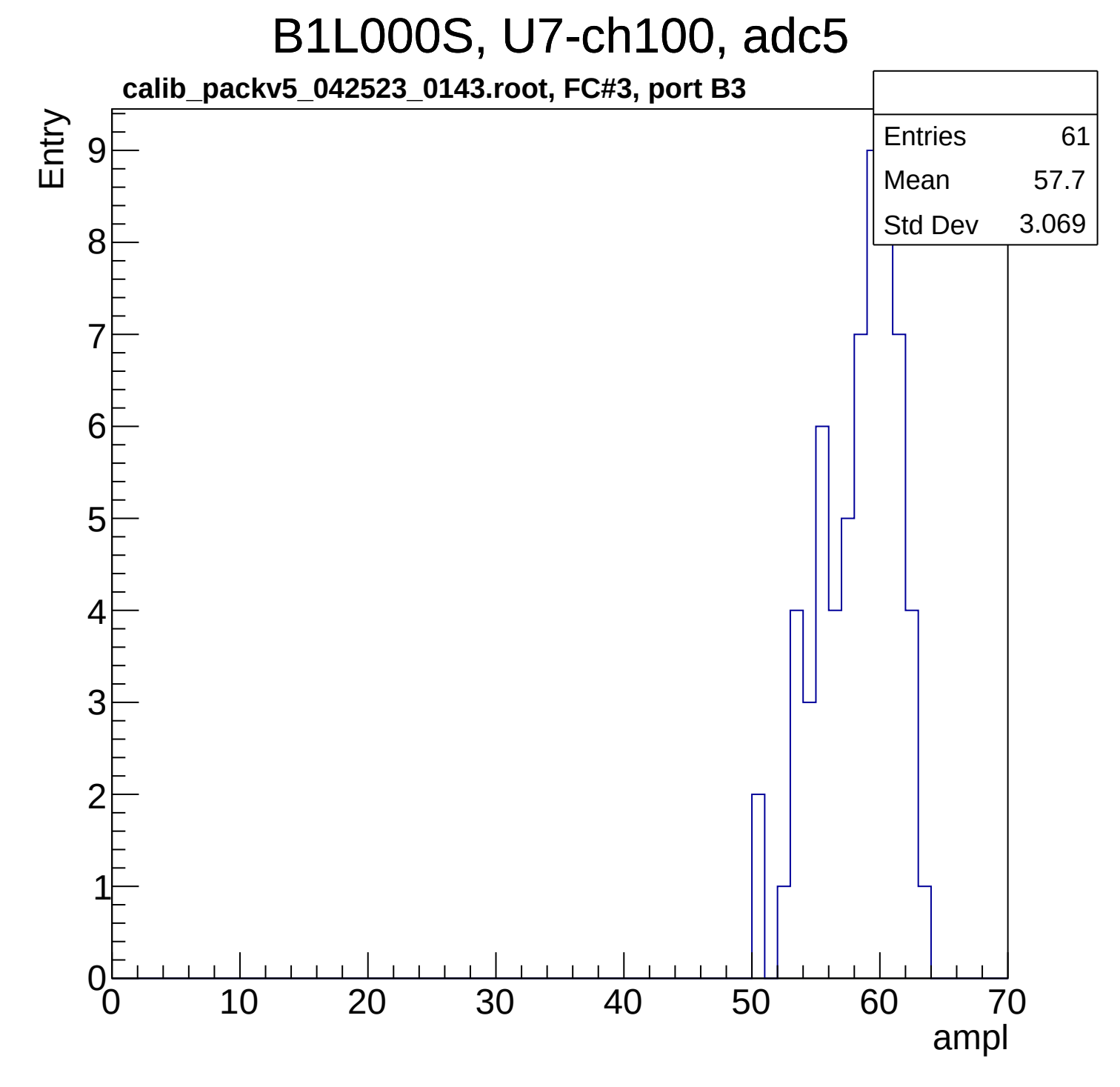
B1L000S, U7-ch100, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 57.7
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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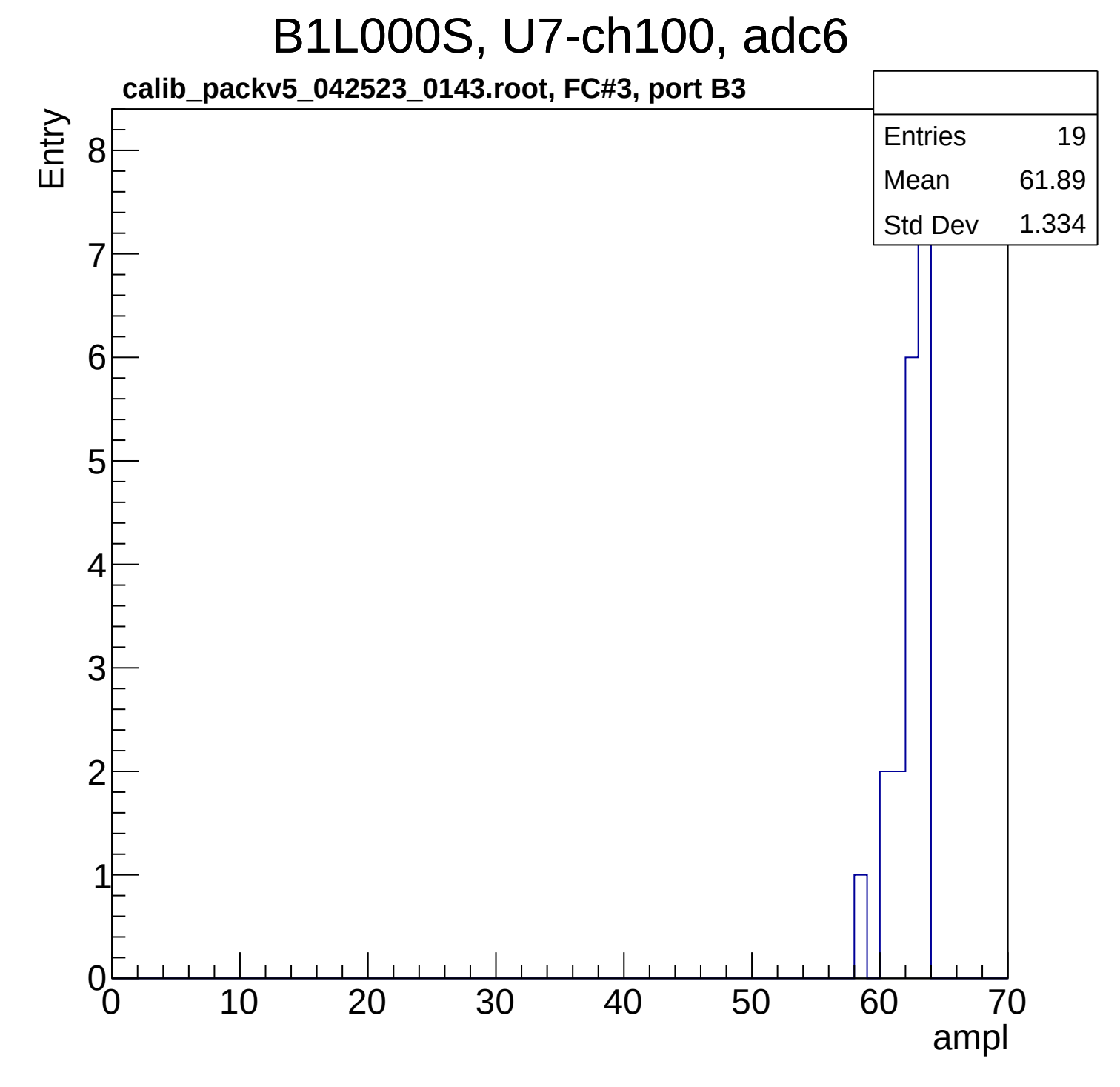
B1L000S, U7-ch100, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 19
Mean 61.89
Std Dev 1.334
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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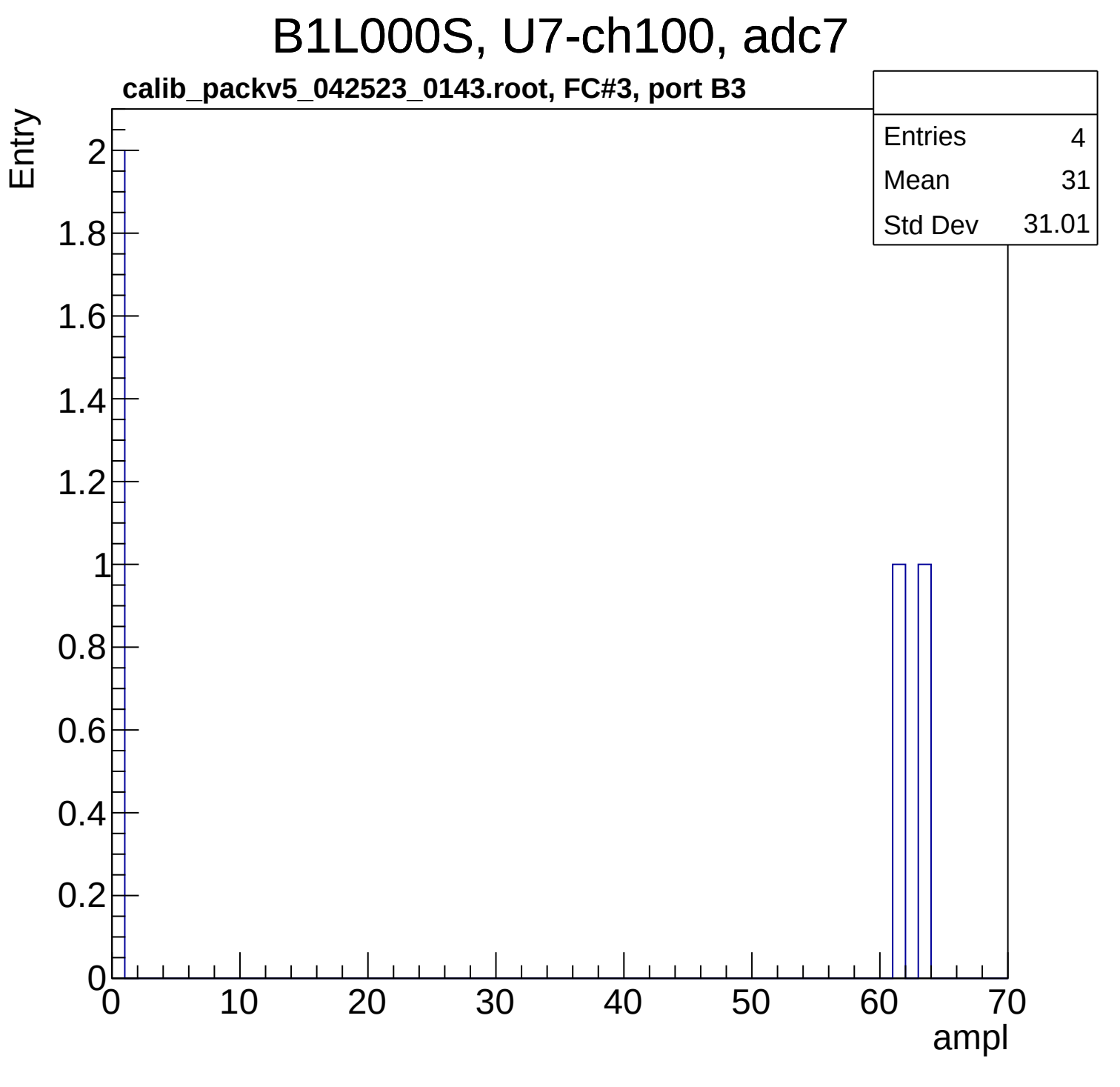
B1L000S, U7-ch100, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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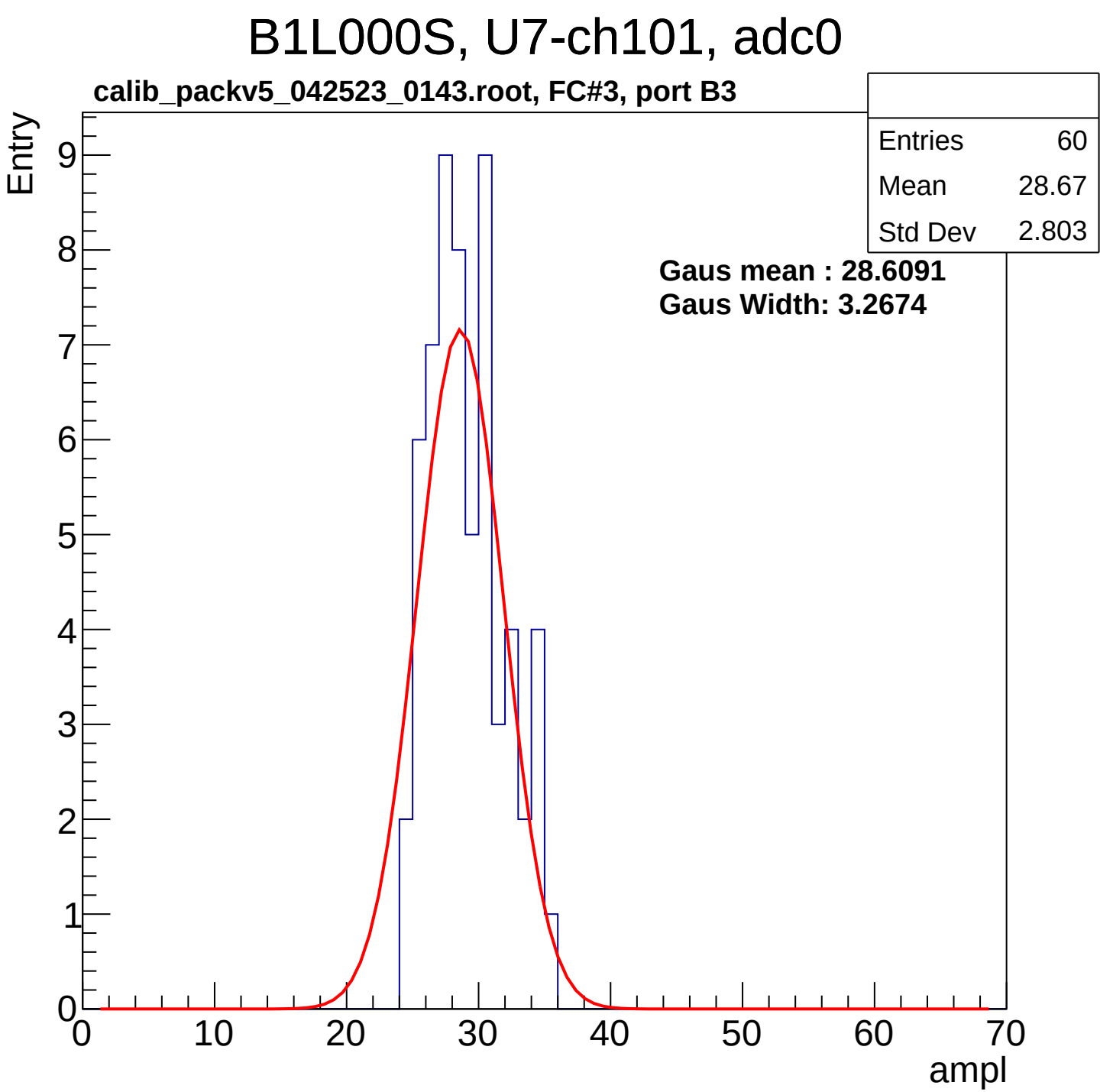
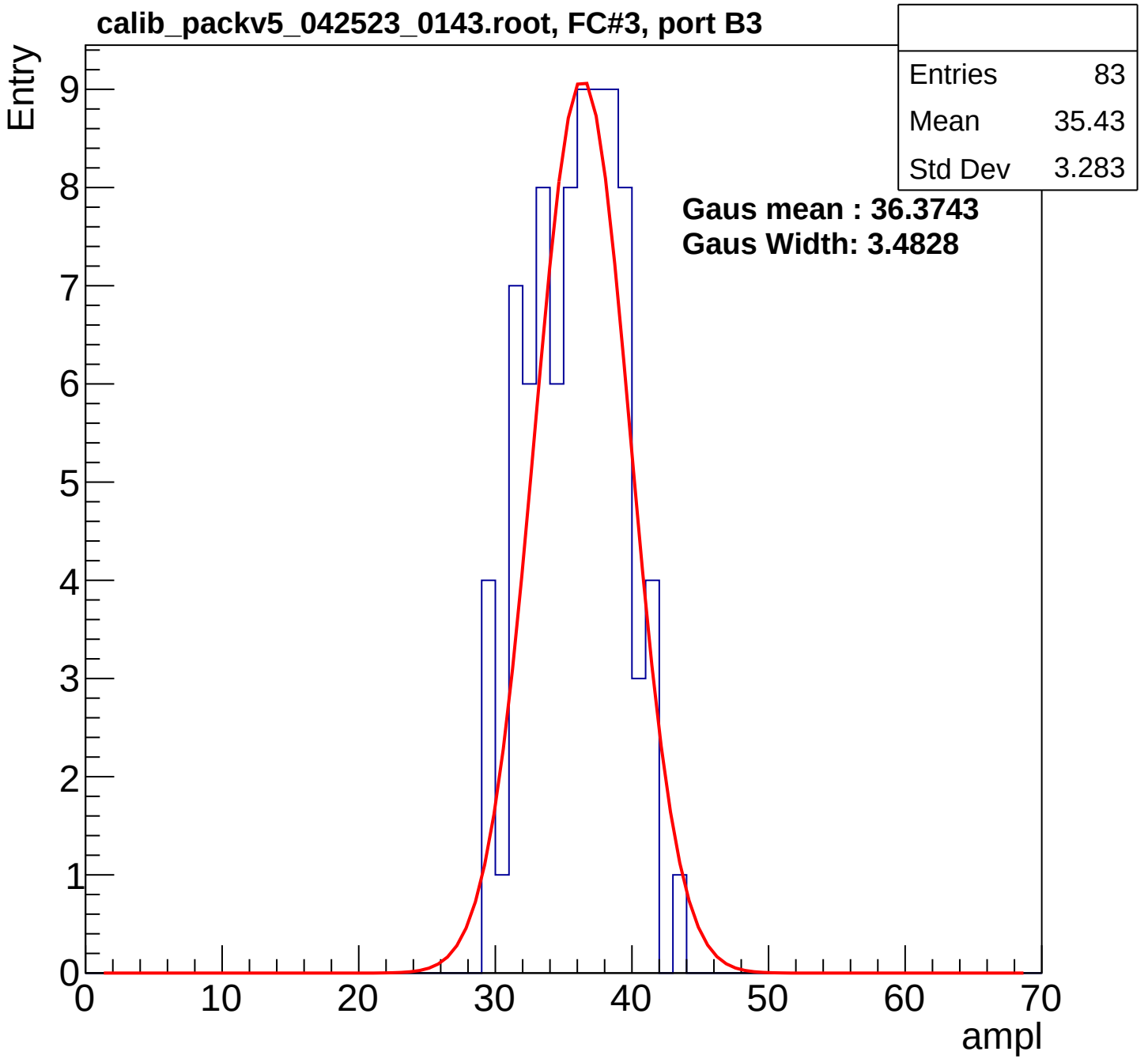

B1L000S, U7-ch101, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 28.67
Std Dev 2.803
Gaus mean : 28.6091
Gaus Width: 3.2674
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch101, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 83
Mean 35.43
Std Dev 3.283
Gaus mean : 36.3743
Gaus Width: 3.4828
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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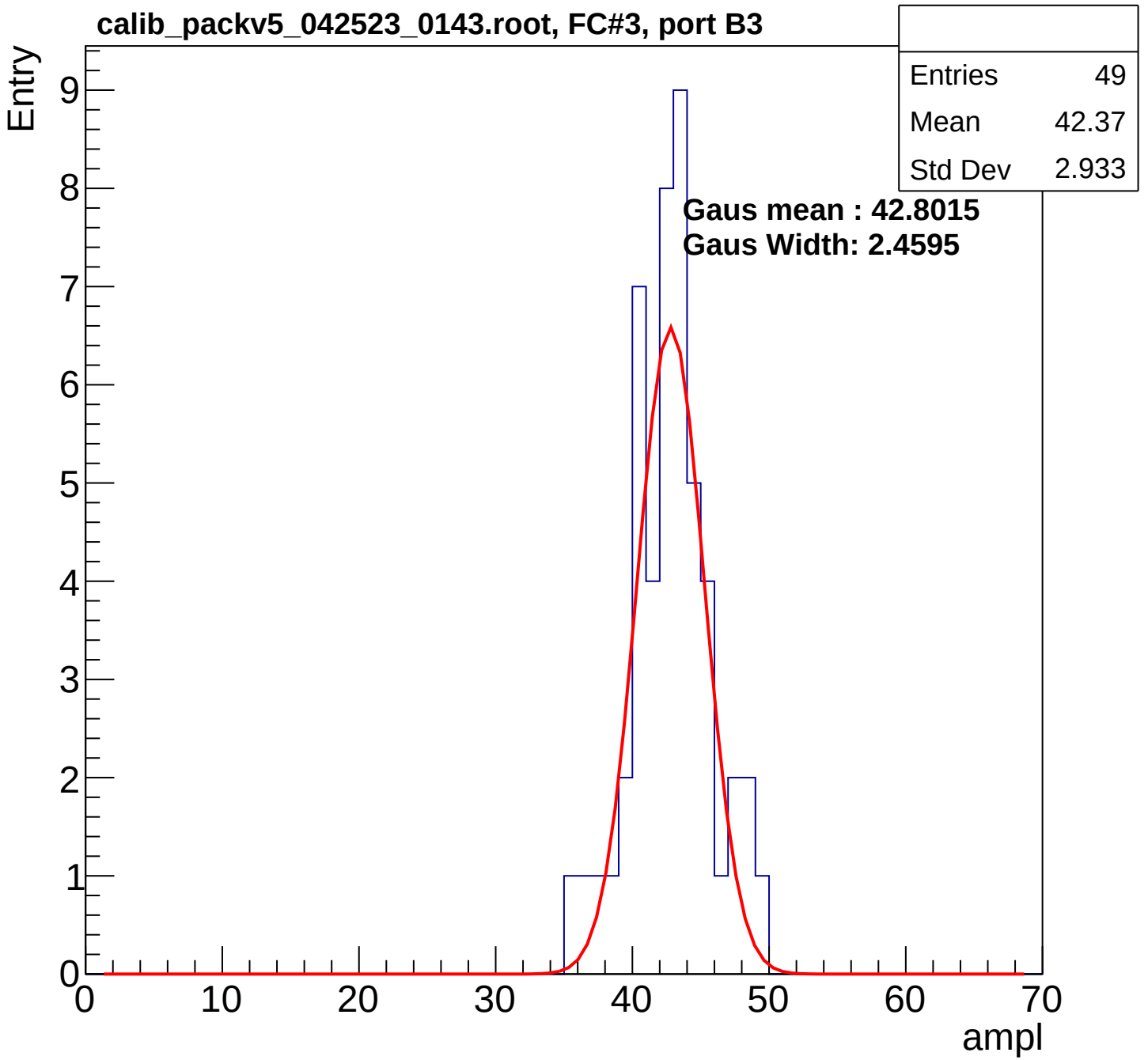
B1L000S, U7-ch101, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 42.37
Std Dev 2.933
Gaus mean : 42.8015
Gaus Width: 2.4595
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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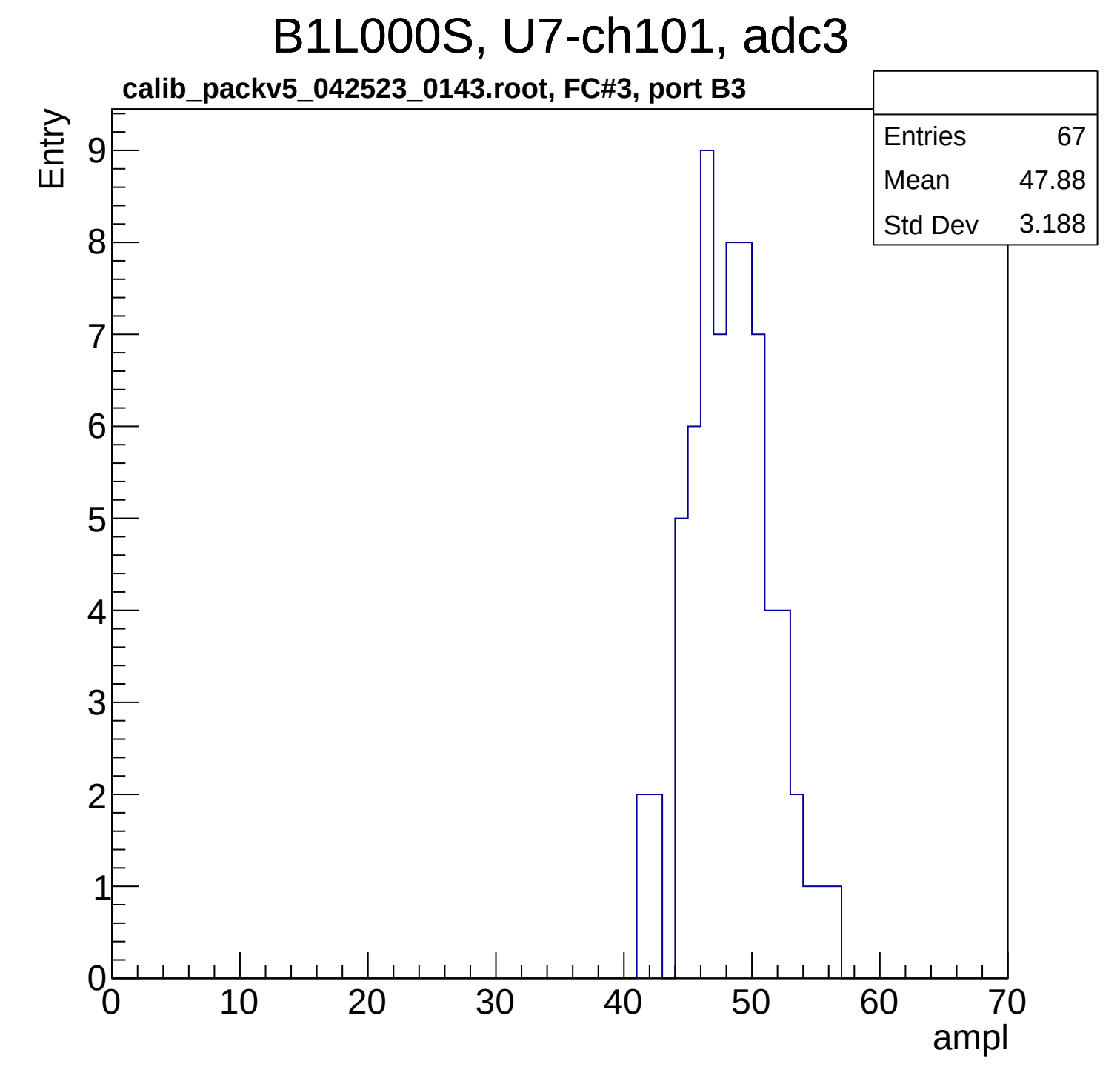
B1L000S, U7-ch101, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 47.88
Std Dev 3.188
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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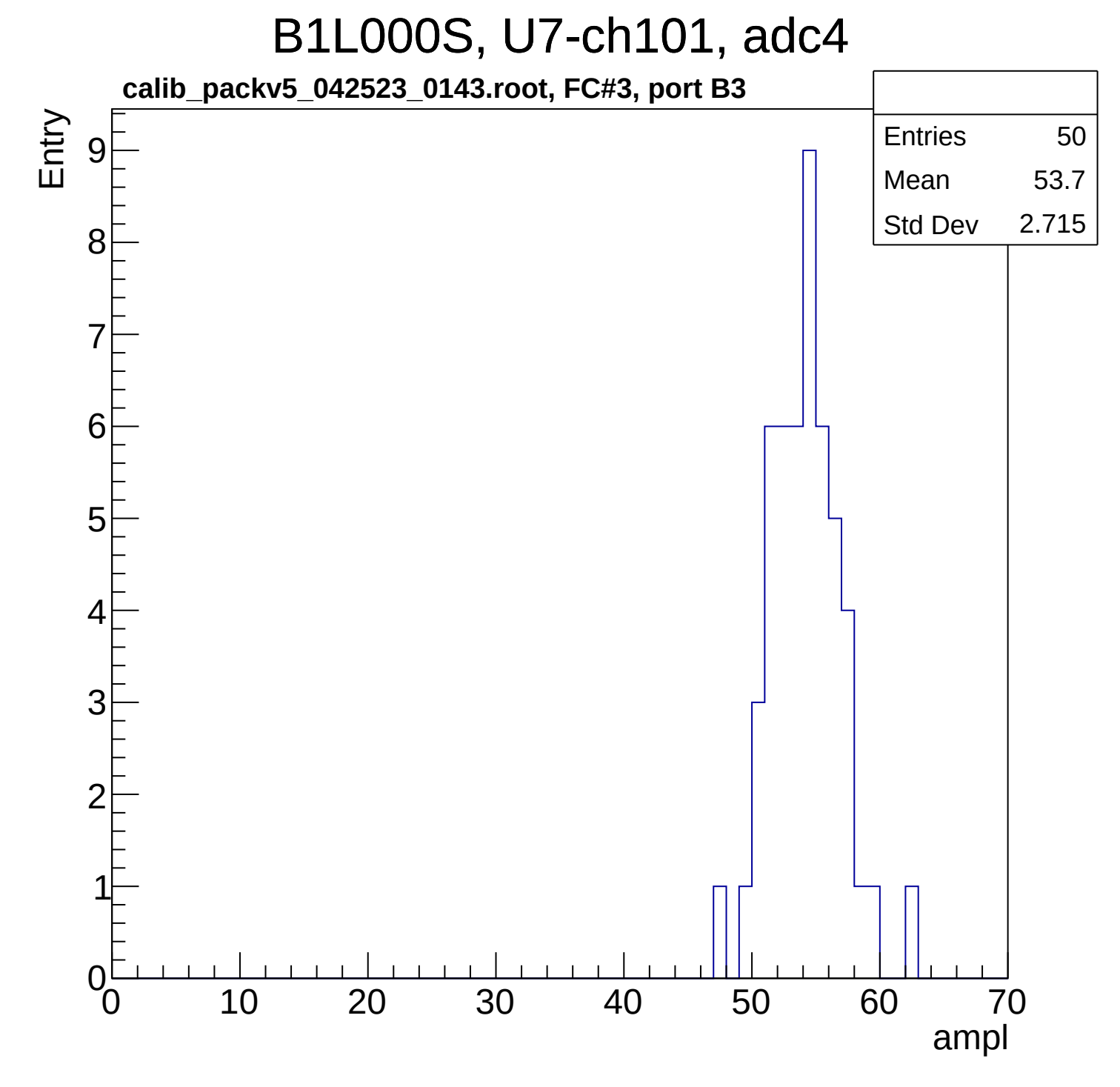
B1L000S, U7-ch101, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 53.7
Std Dev 2.715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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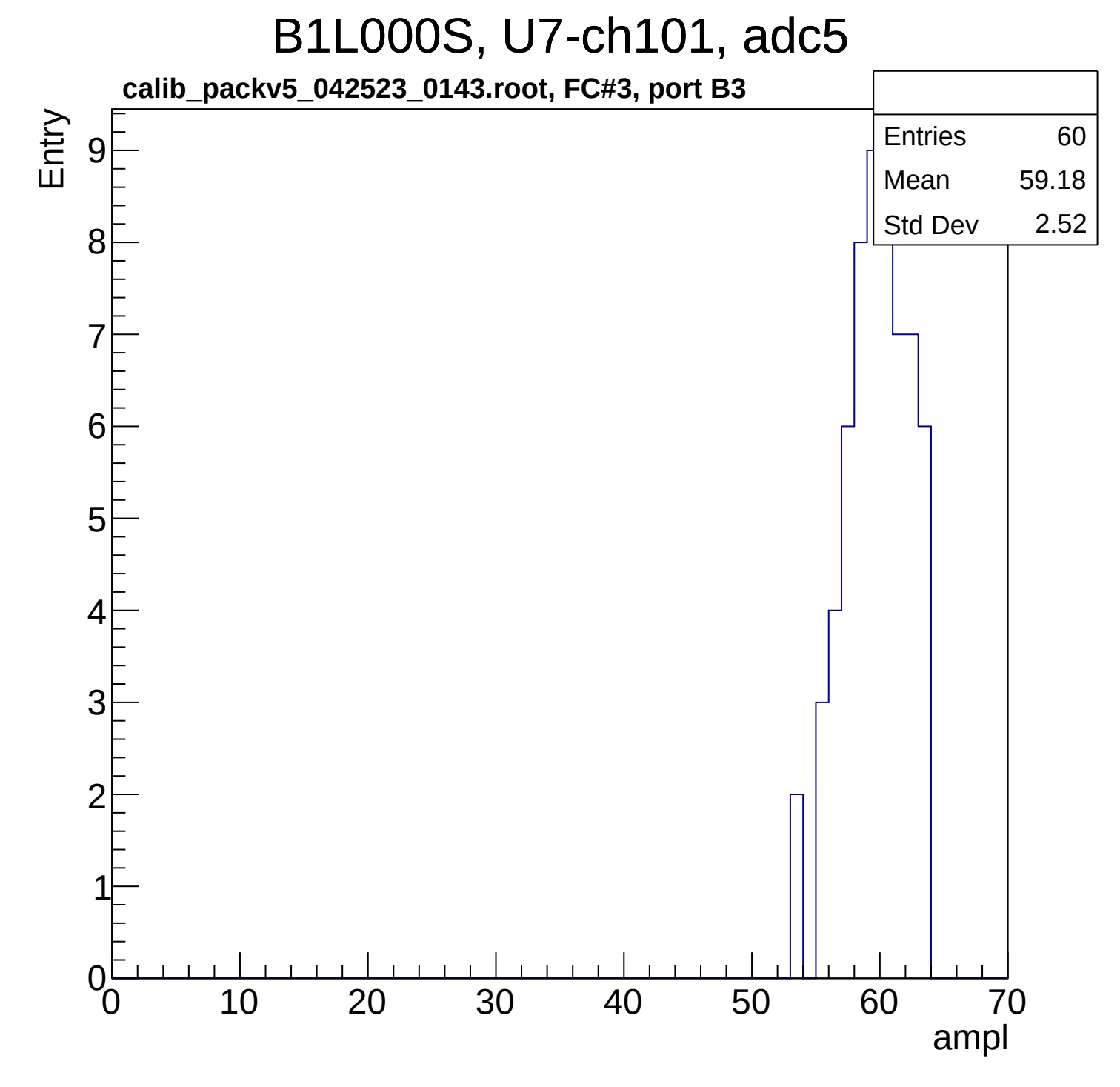
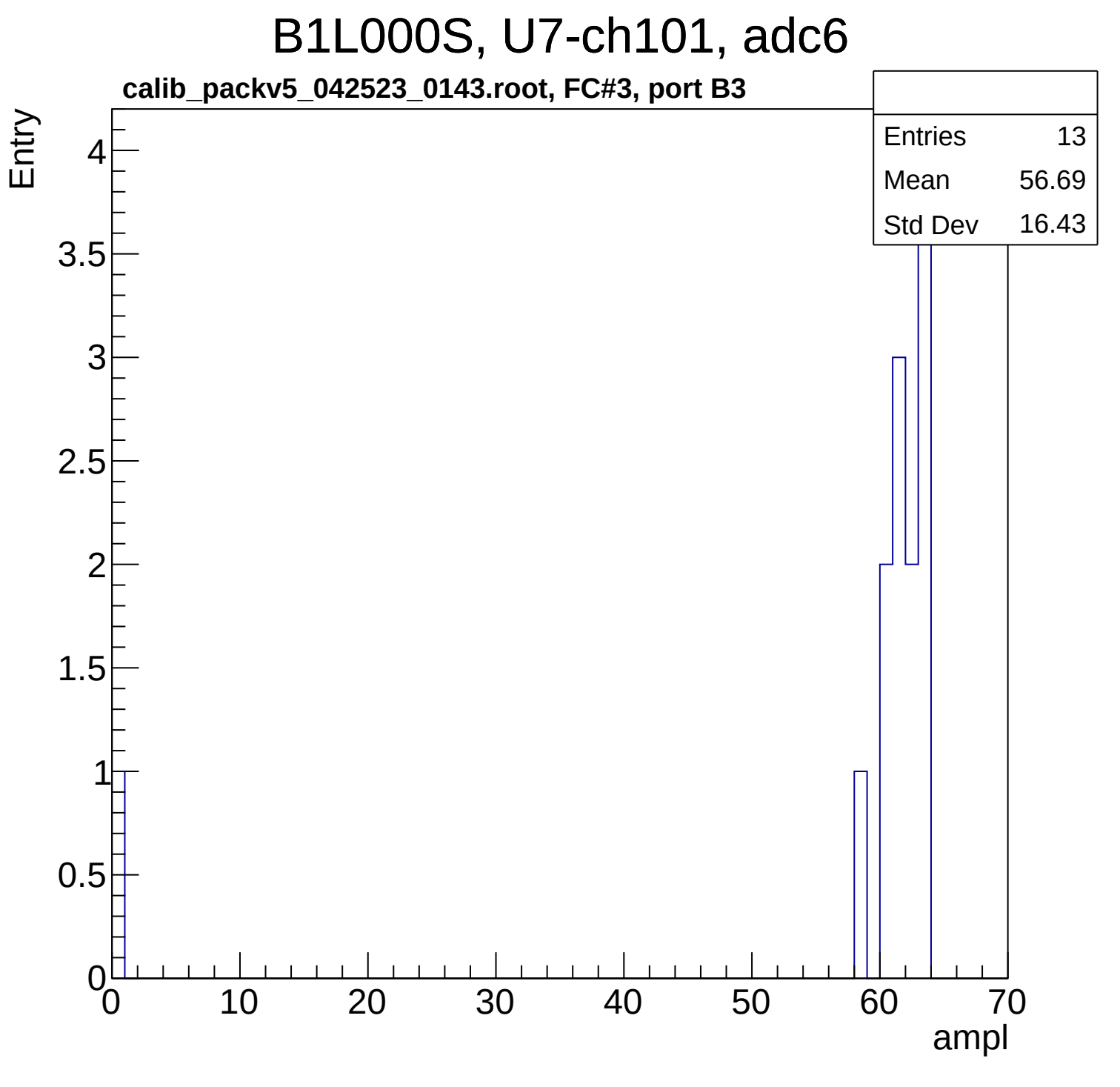

B1L000S, U7-ch101, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 59.18
Std Dev 2.52
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch101, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 56.69
Std Dev 16.43
Entry
ampl
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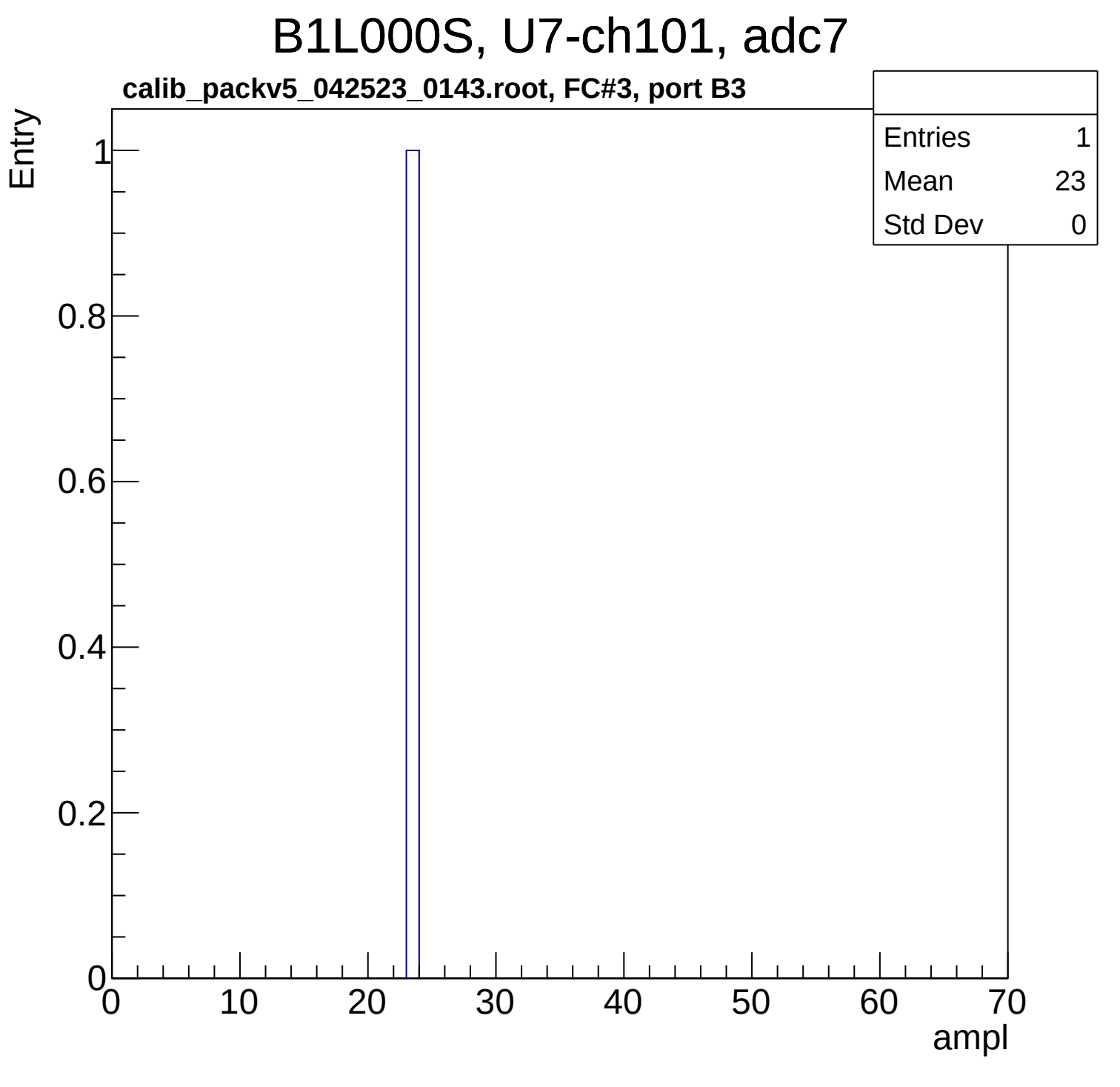
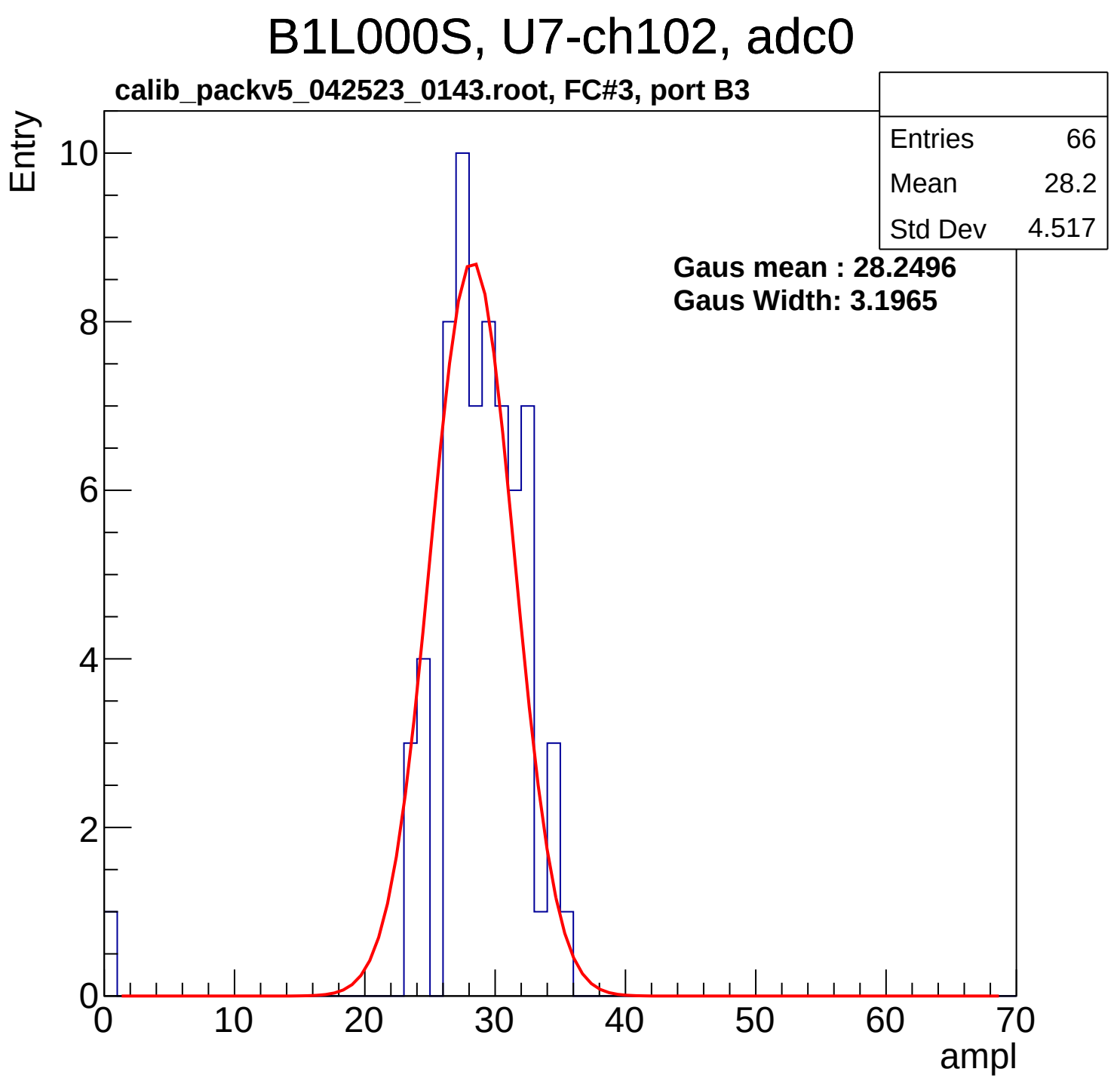
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L000S, U7-ch101, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch102, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.2
Std Dev 4.517
Gaus mean : 28.2496
Gaus Width: 3.1965
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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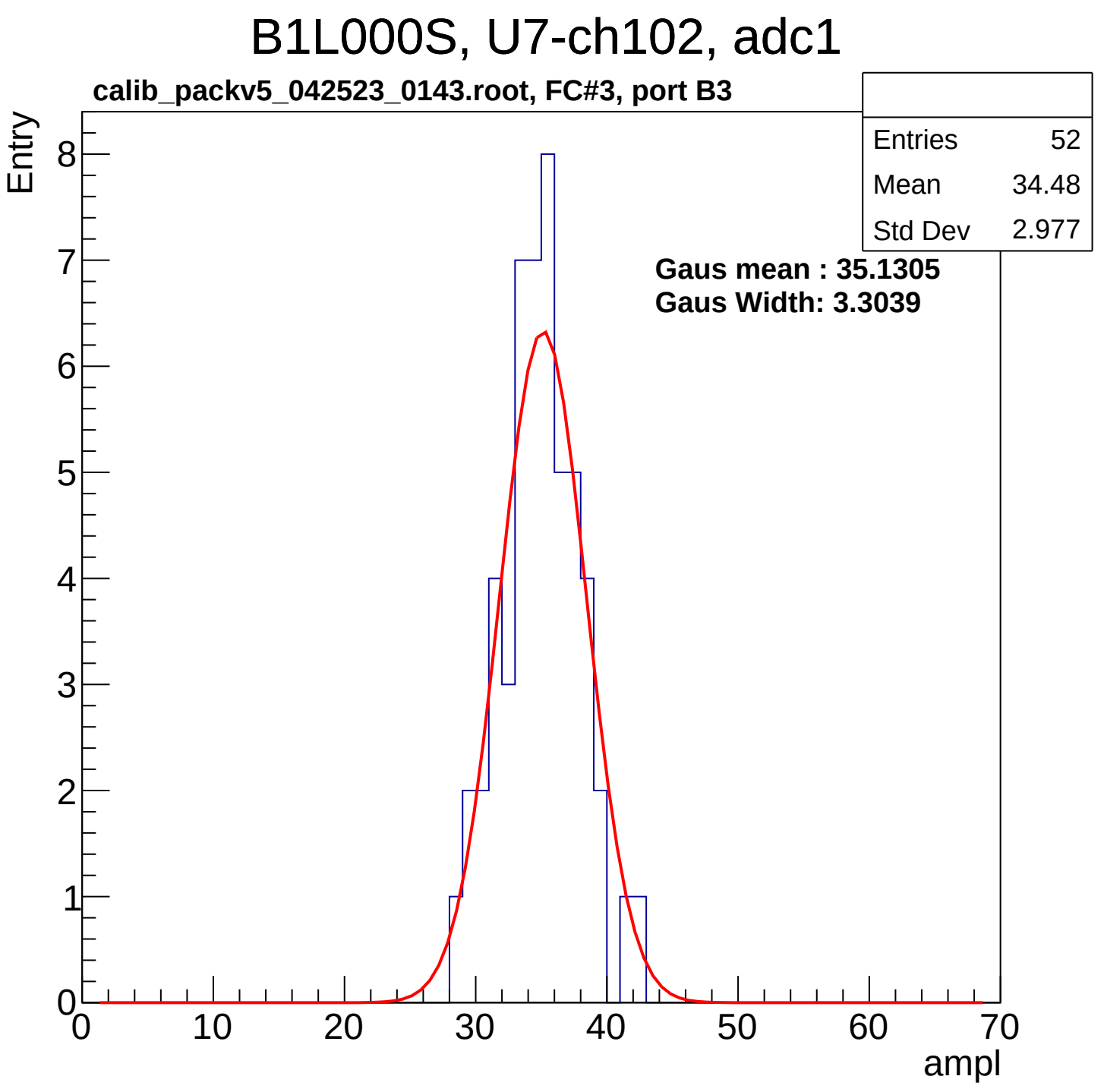
B1L000S, U7-ch102, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 34.48
Std Dev 2.977
Gaus mean : 35.1305
Gaus Width: 3.3039
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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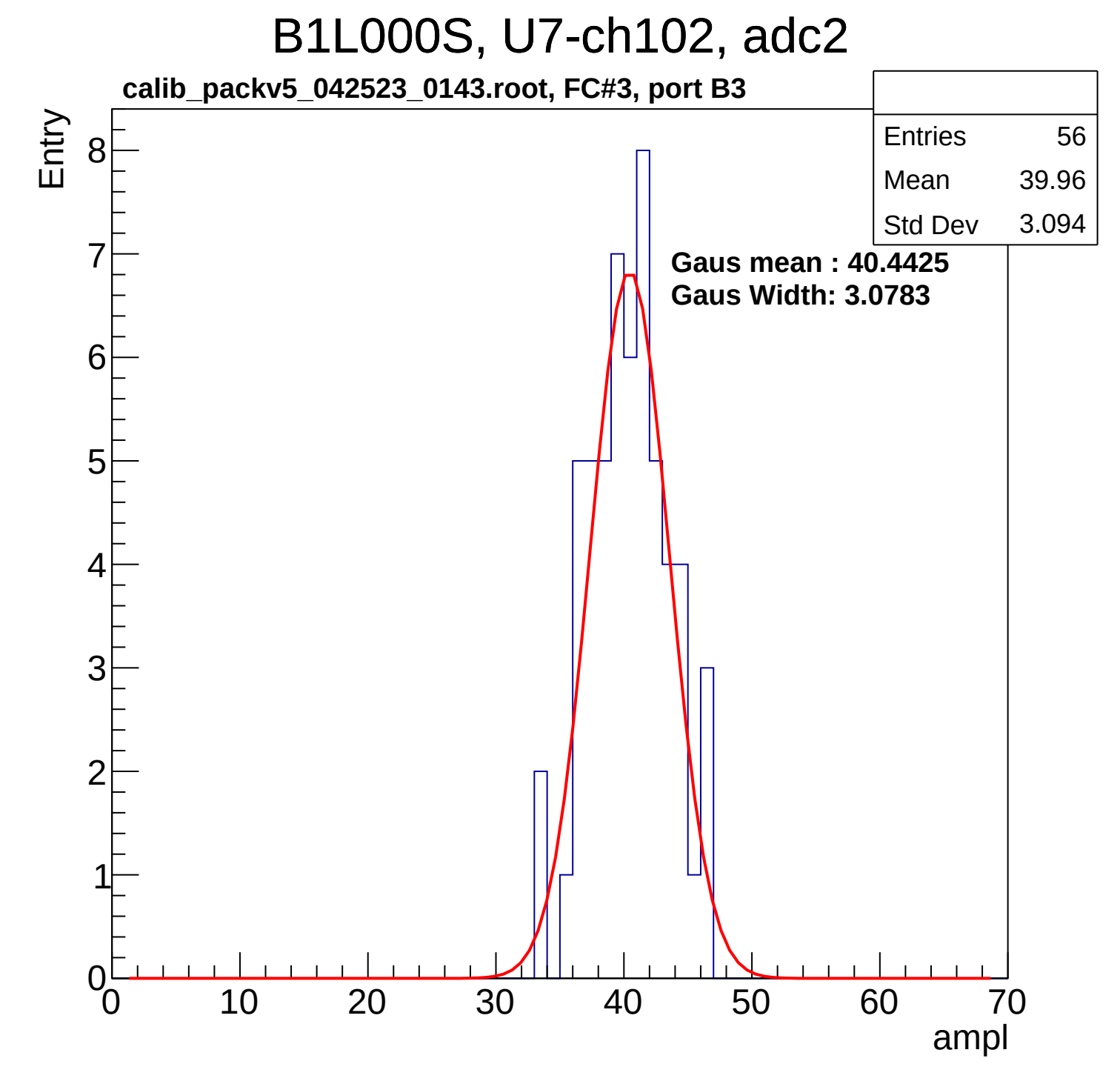
B1L000S, U7-ch102, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 39.96
Std Dev 3.094
Gaus mean : 40.4425
Gaus Width: 3.0783
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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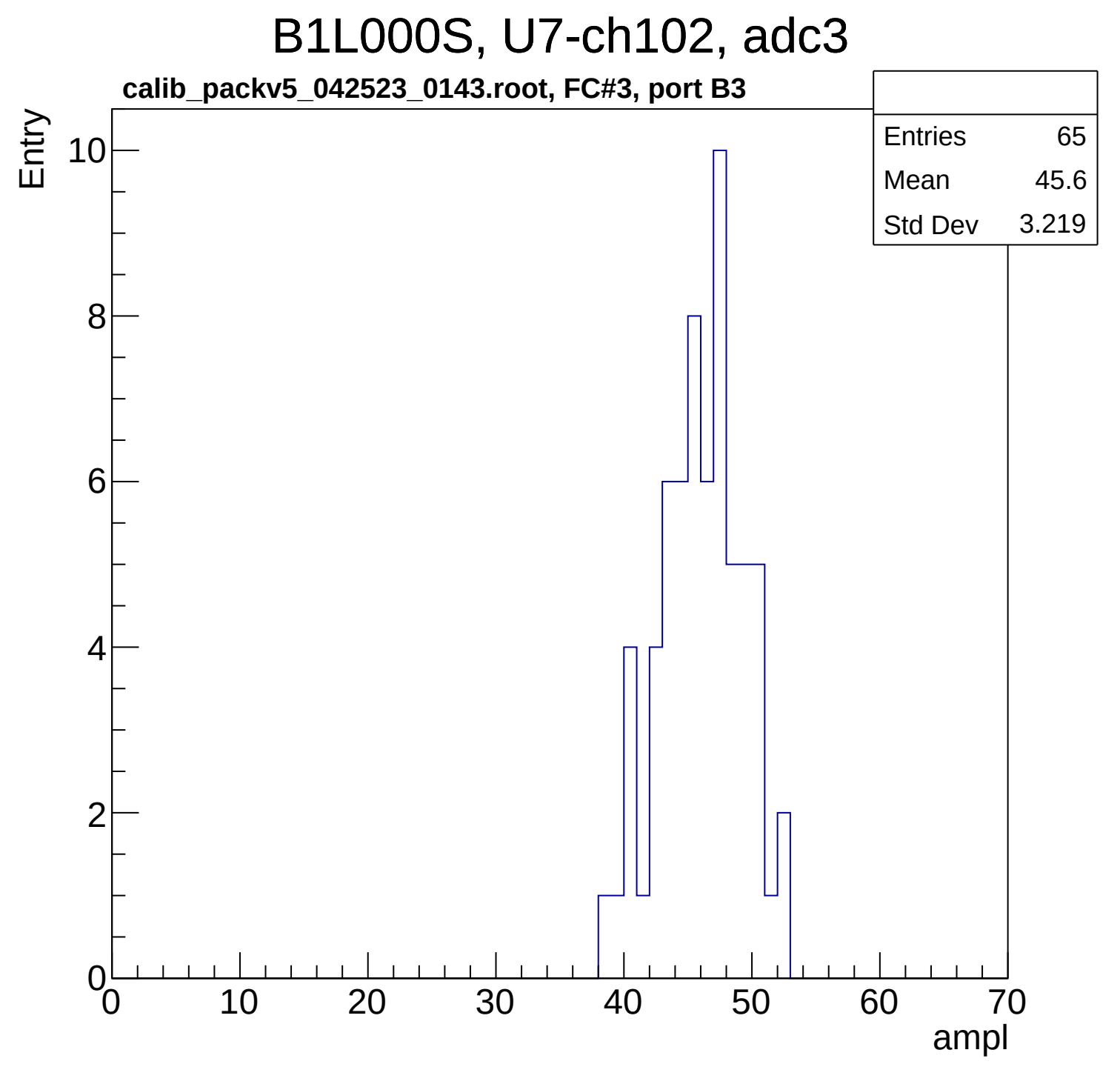
B1L000S, U7-ch102, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 45.6
Std Dev 3.219
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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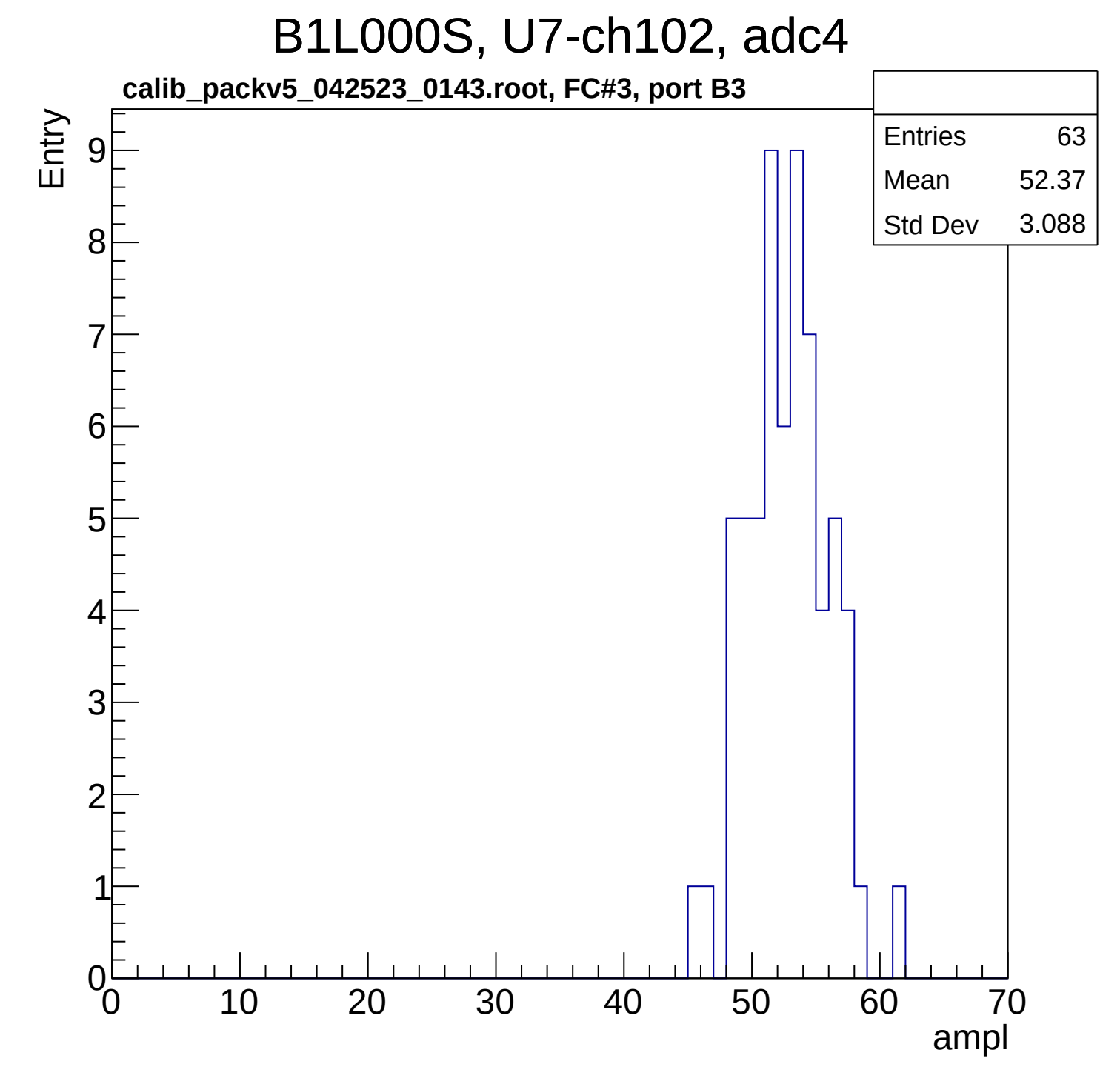
B1L000S, U7-ch102, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 52.37
Std Dev 3.088
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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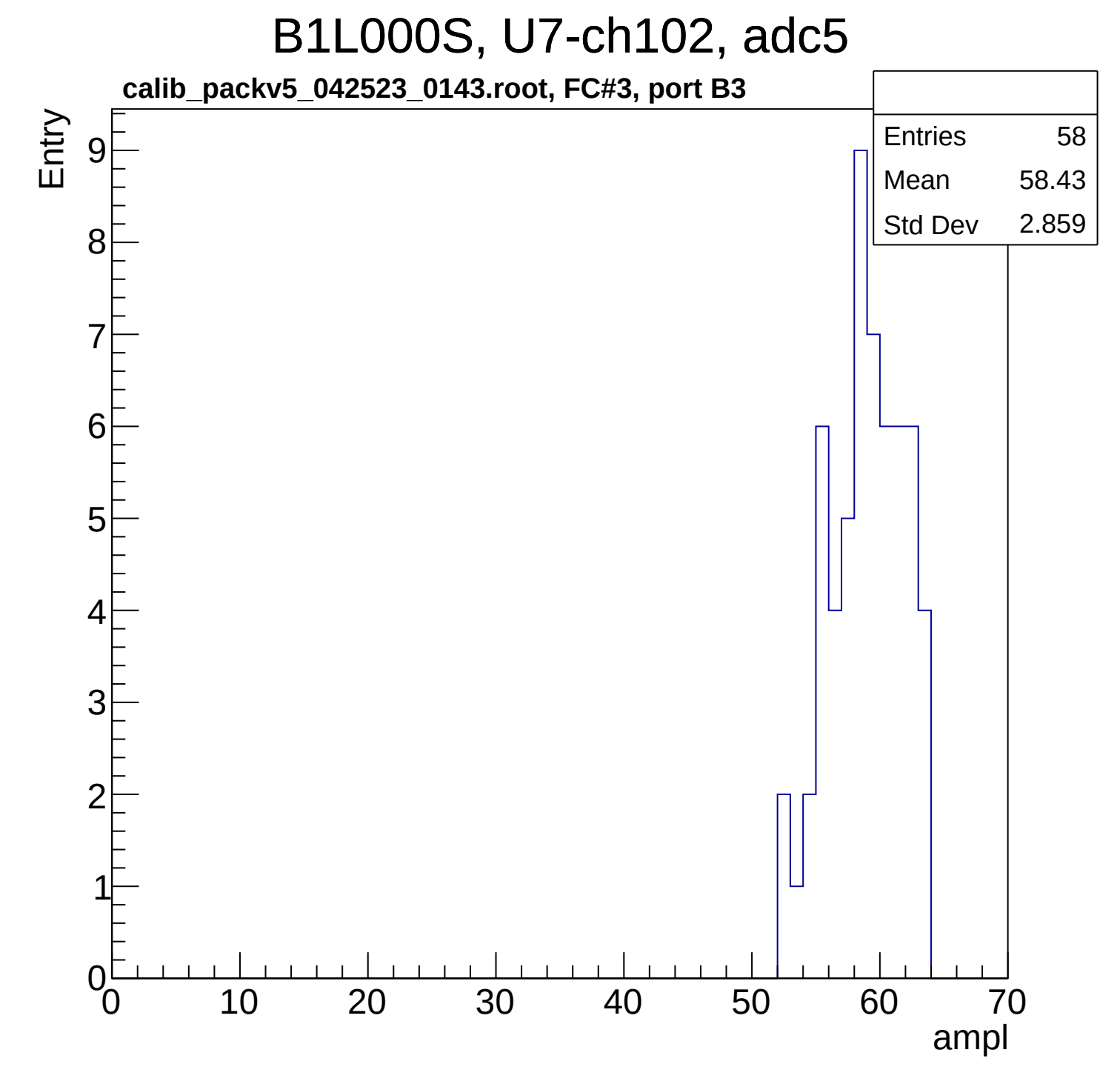
B1L000S, U7-ch102, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 58.43
Std Dev 2.859
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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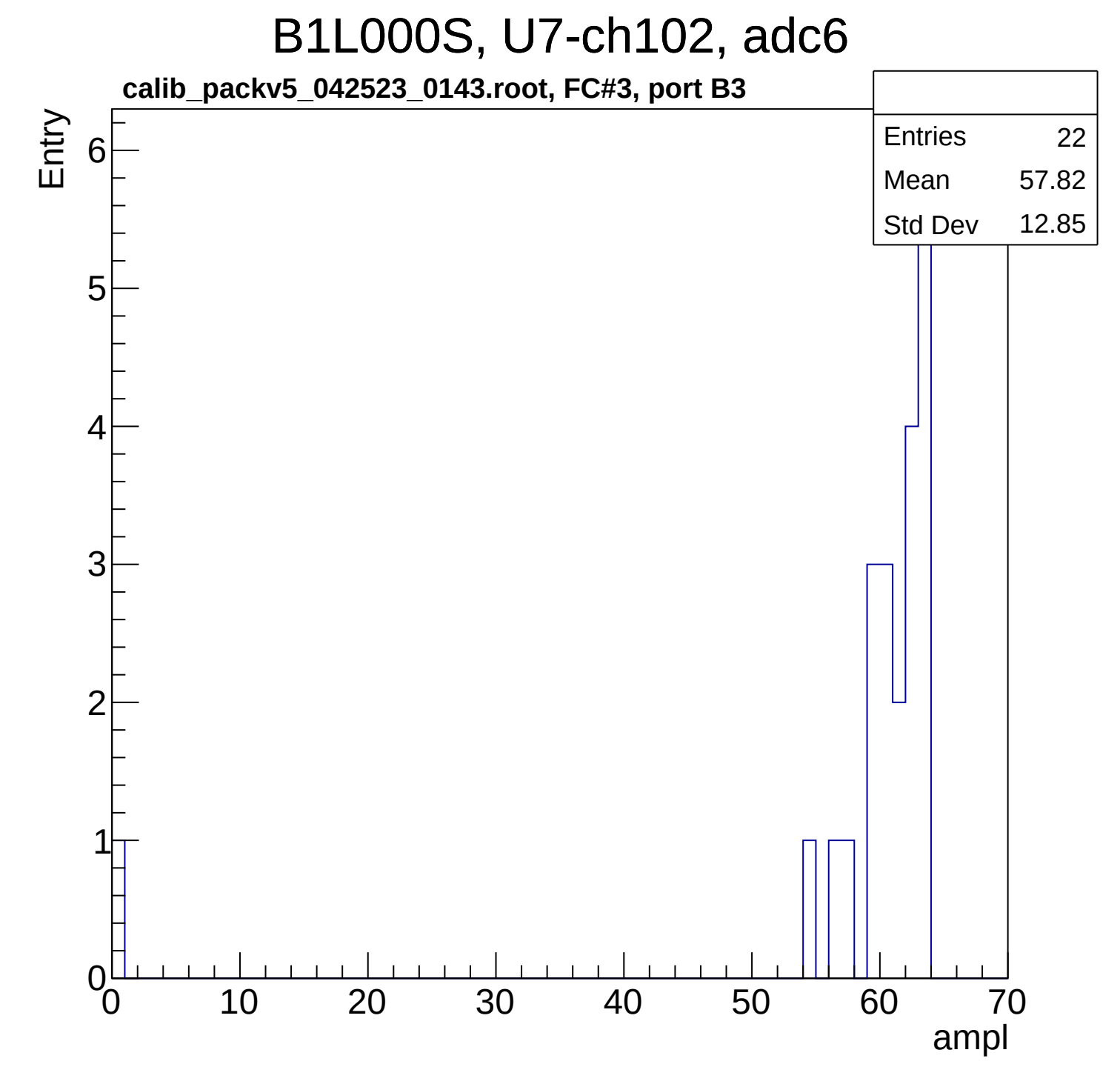
B1L000S, U7-ch102, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 22
Mean 57.82
Std Dev 12.85
Entry
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2
3
4
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6
0
10
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30
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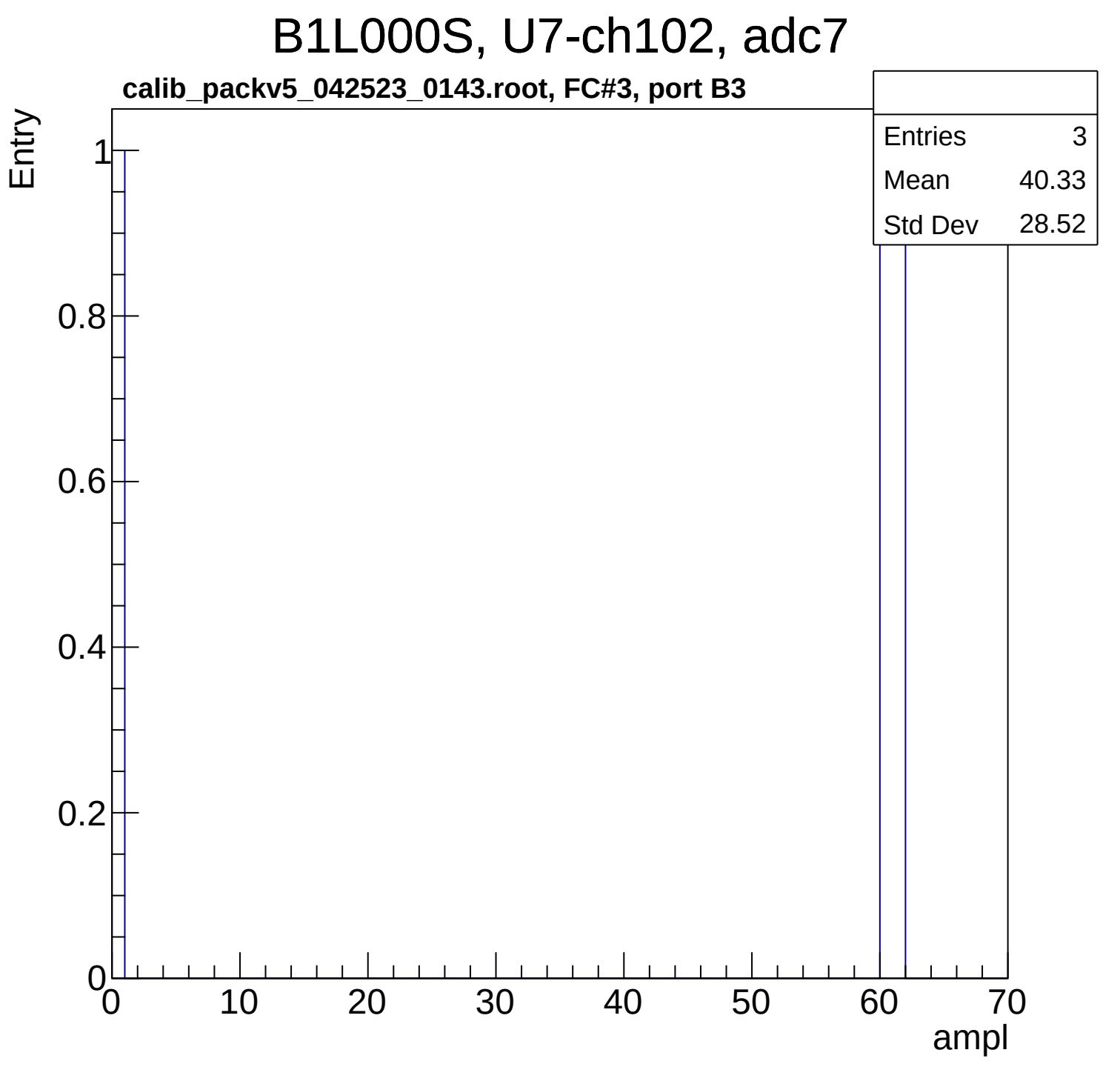
50
60
70
ampl


B1L000S, U7-ch102, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 40.33
Std Dev 28.52
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
50
60
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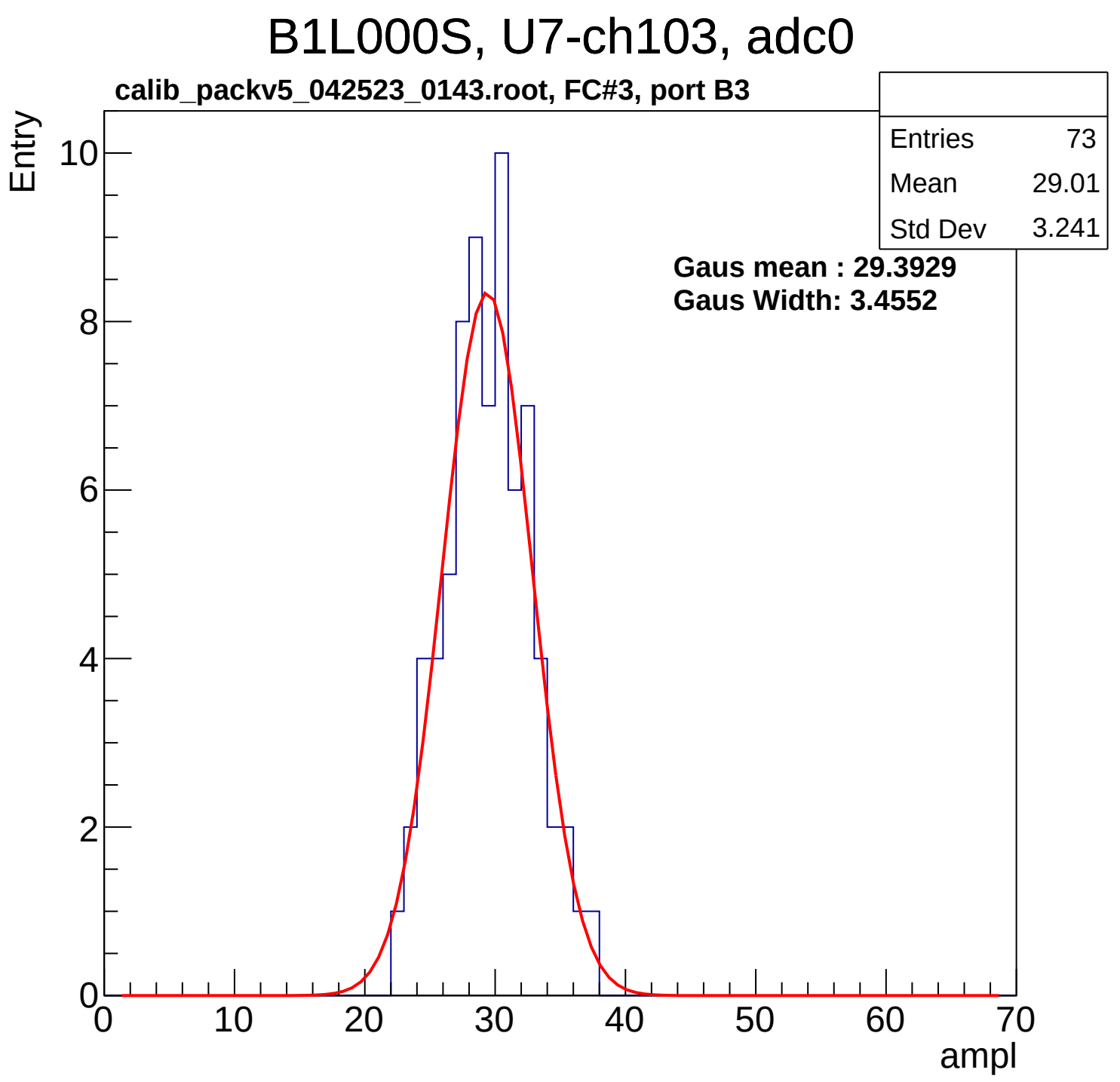
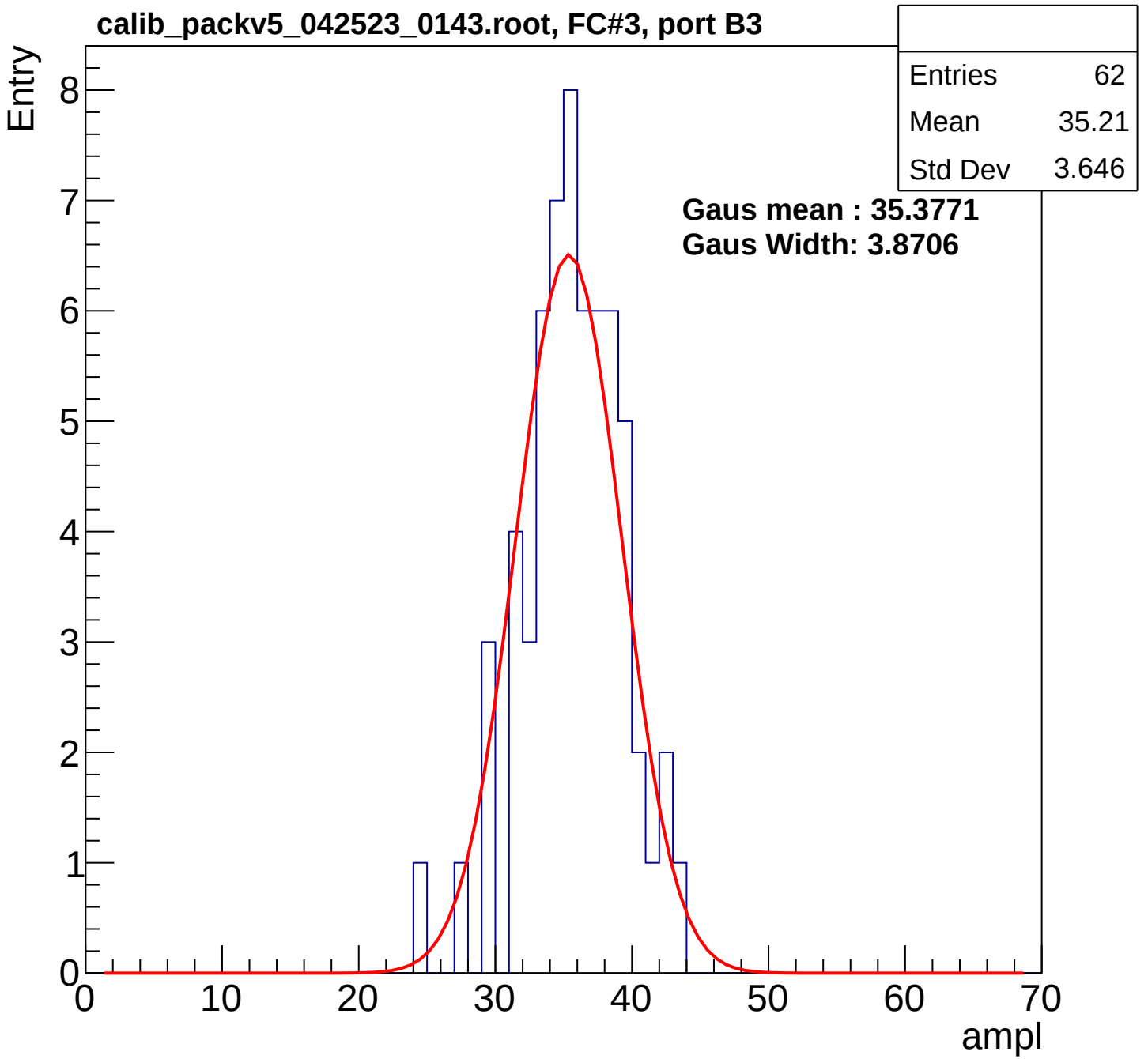

B1L000S, U7-ch103, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 29.01
Std Dev 3.241
Gaus mean : 29.3929
Gaus Width: 3.4552
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch103, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 35.21
Std Dev 3.646
Gaus mean : 35.3771
Gaus Width: 3.8706
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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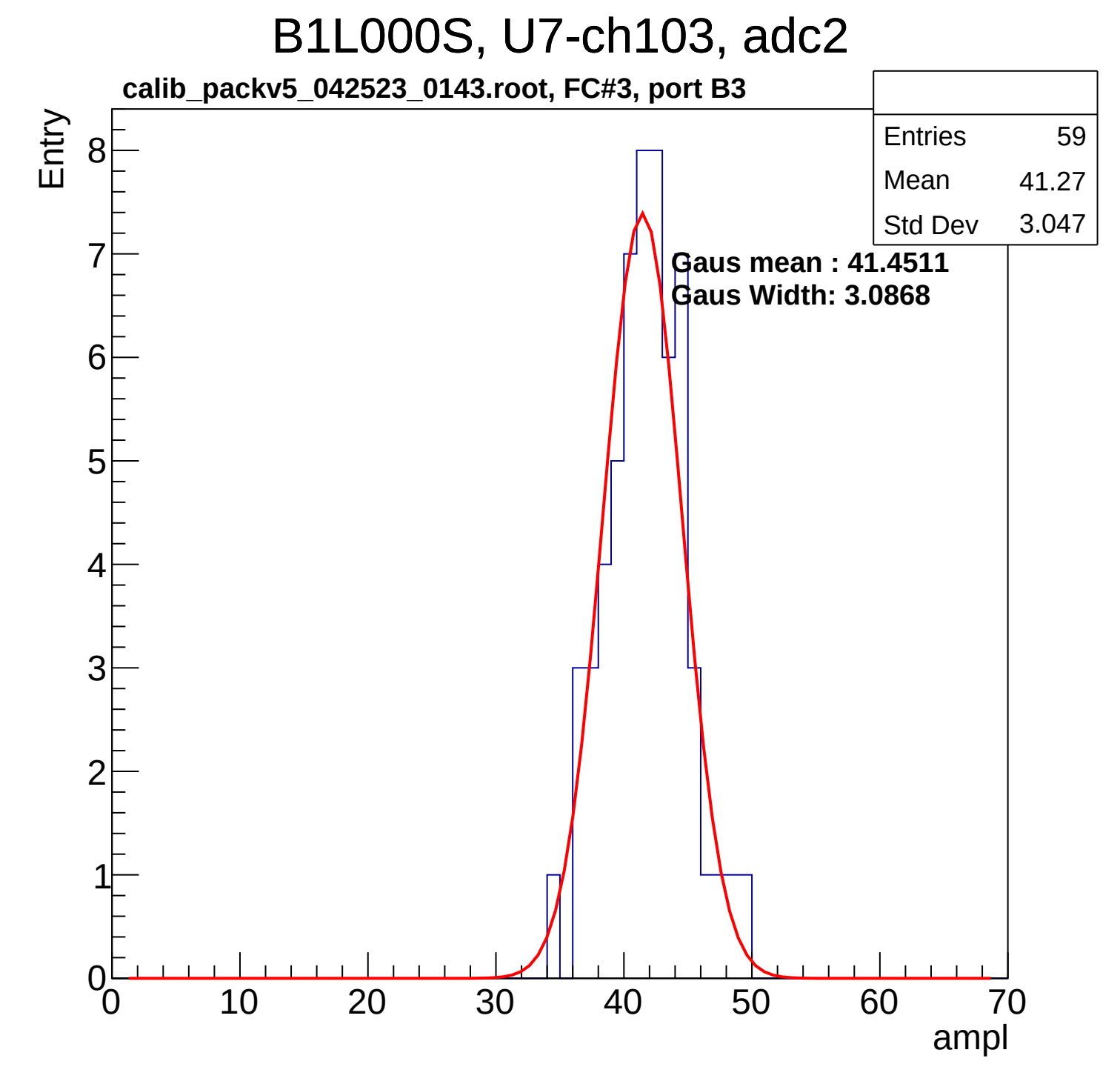
B1L000S, U7-ch103, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 41.27
Std Dev 3.047
Gaus mean : 41.4511
Gaus Width: 3.0868
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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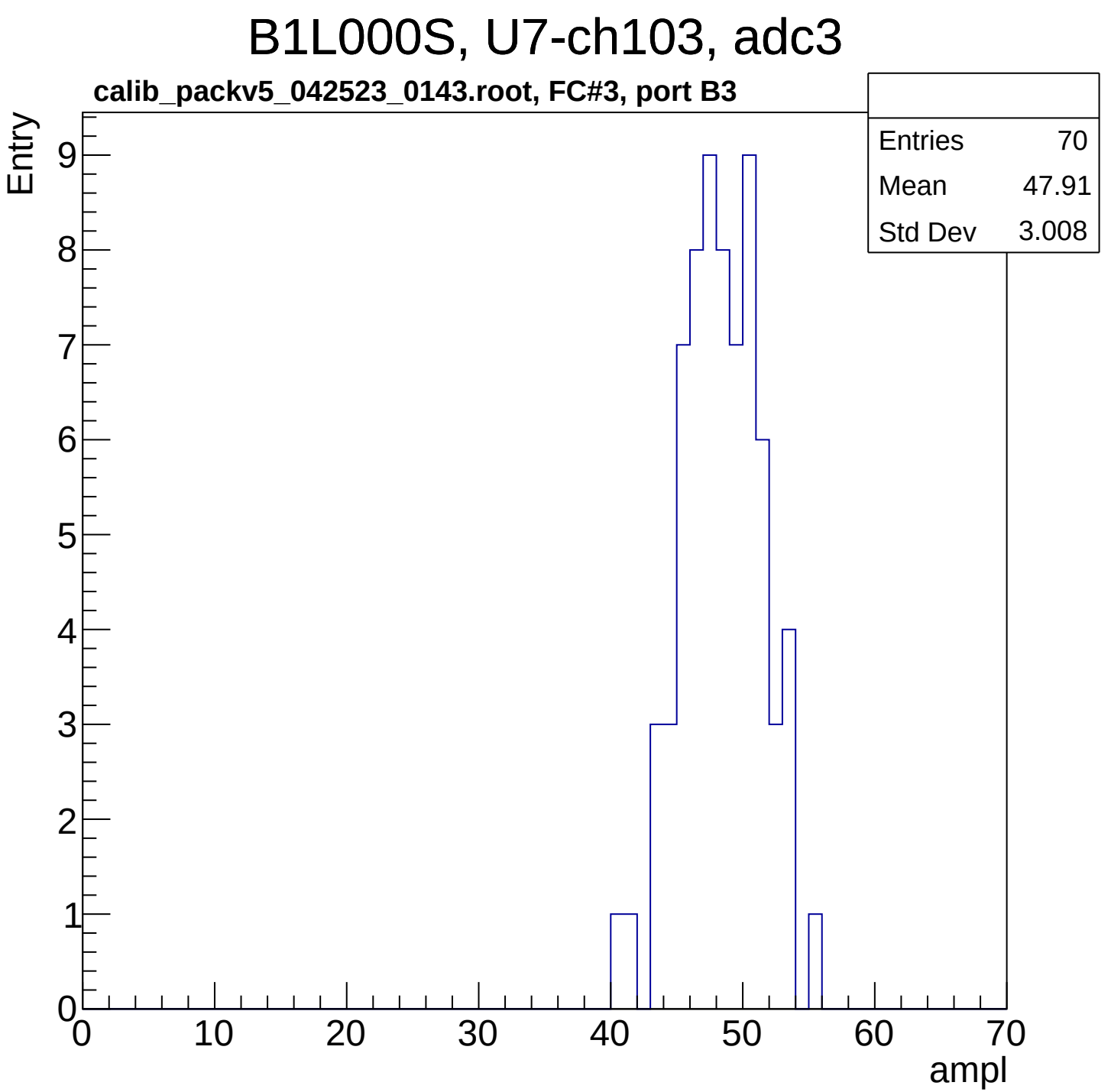
B1L000S, U7-ch103, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 47.91
Std Dev 3.008
Entry
ampl
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3
4
5
6
7
8
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0
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60
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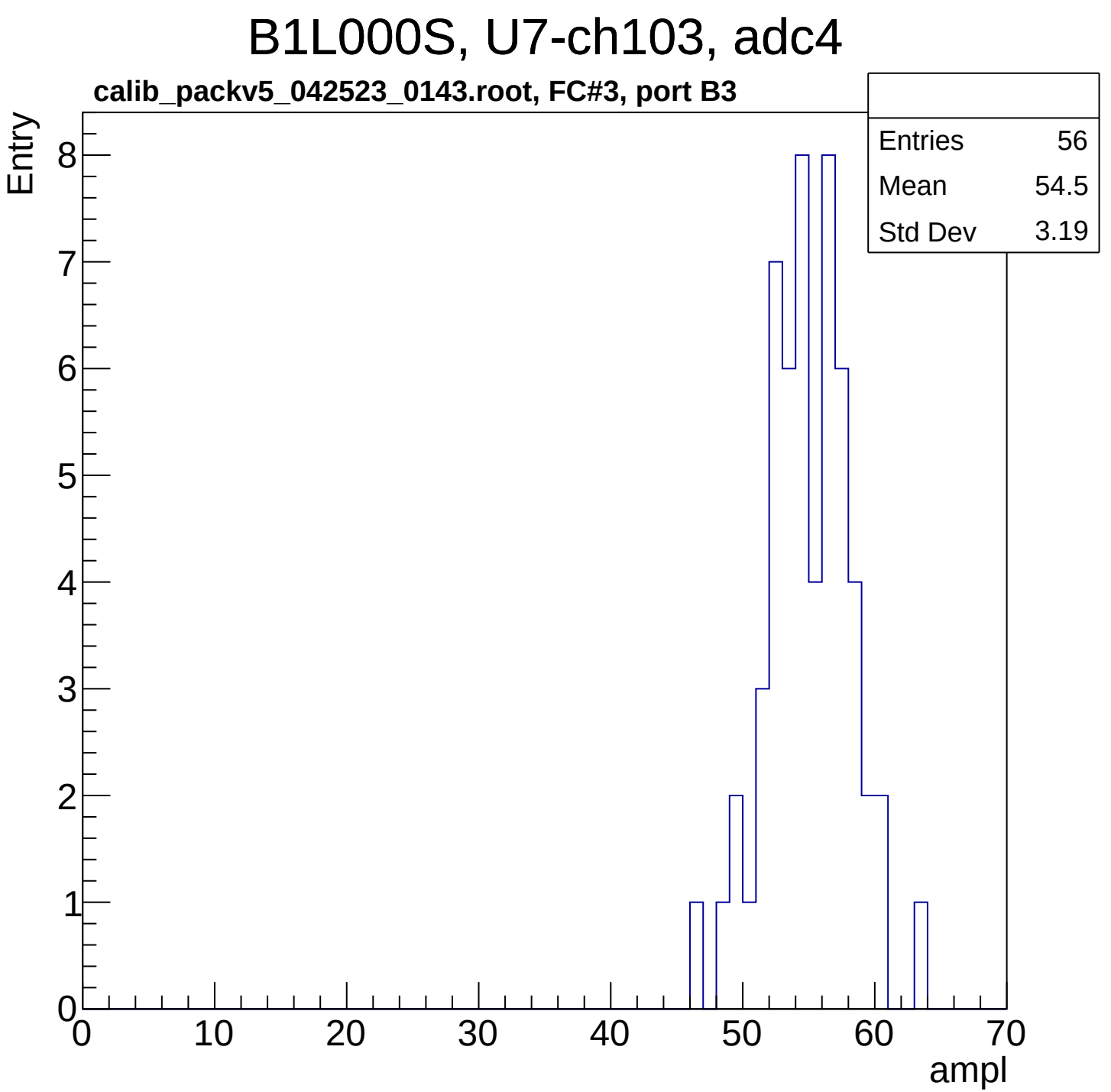
B1L000S, U7-ch103, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 54.5
Std Dev 3.19
Entry
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2
3
4
5
6
7
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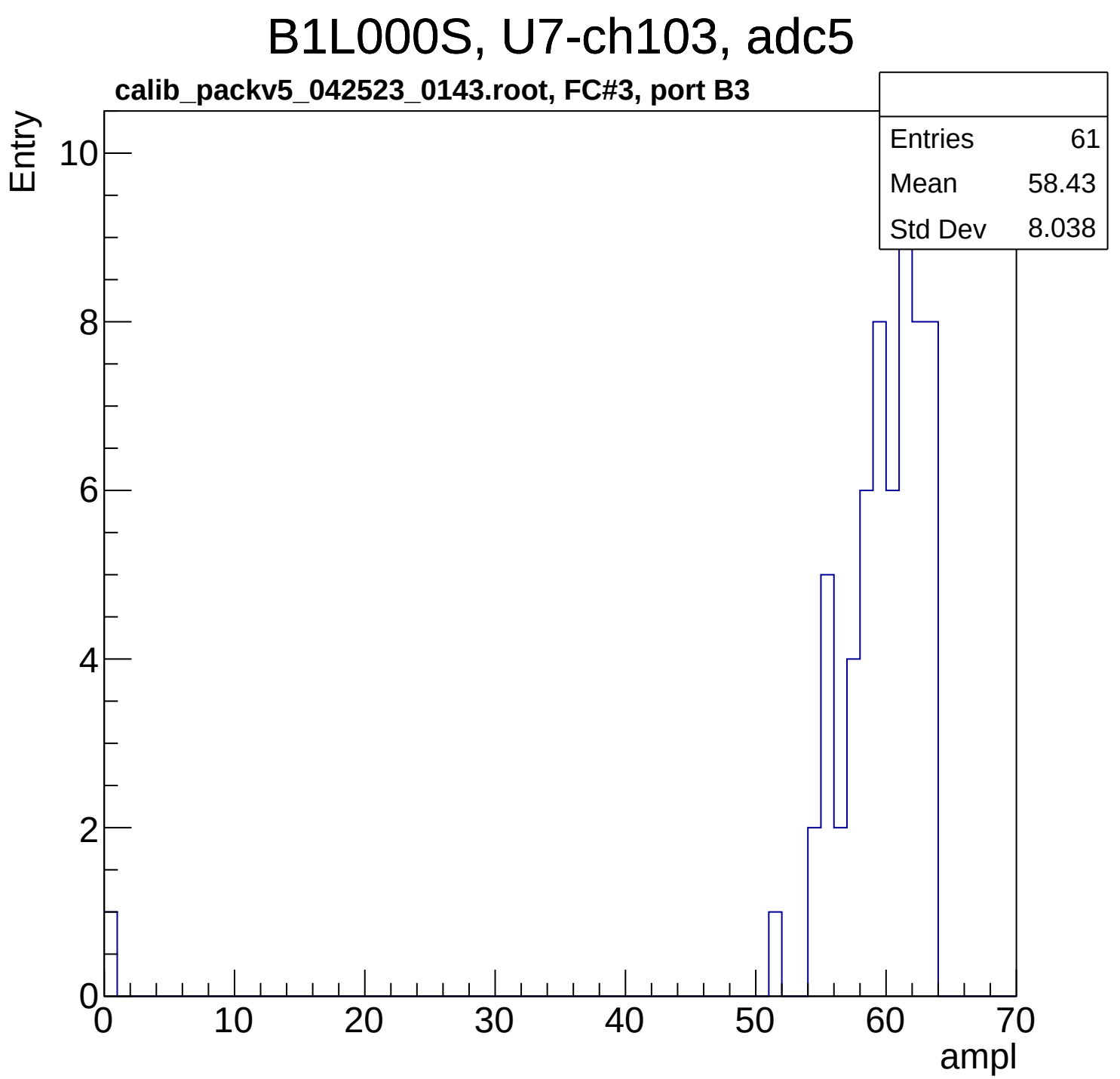
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch103, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.43
Std Dev 8.038
Entry
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2
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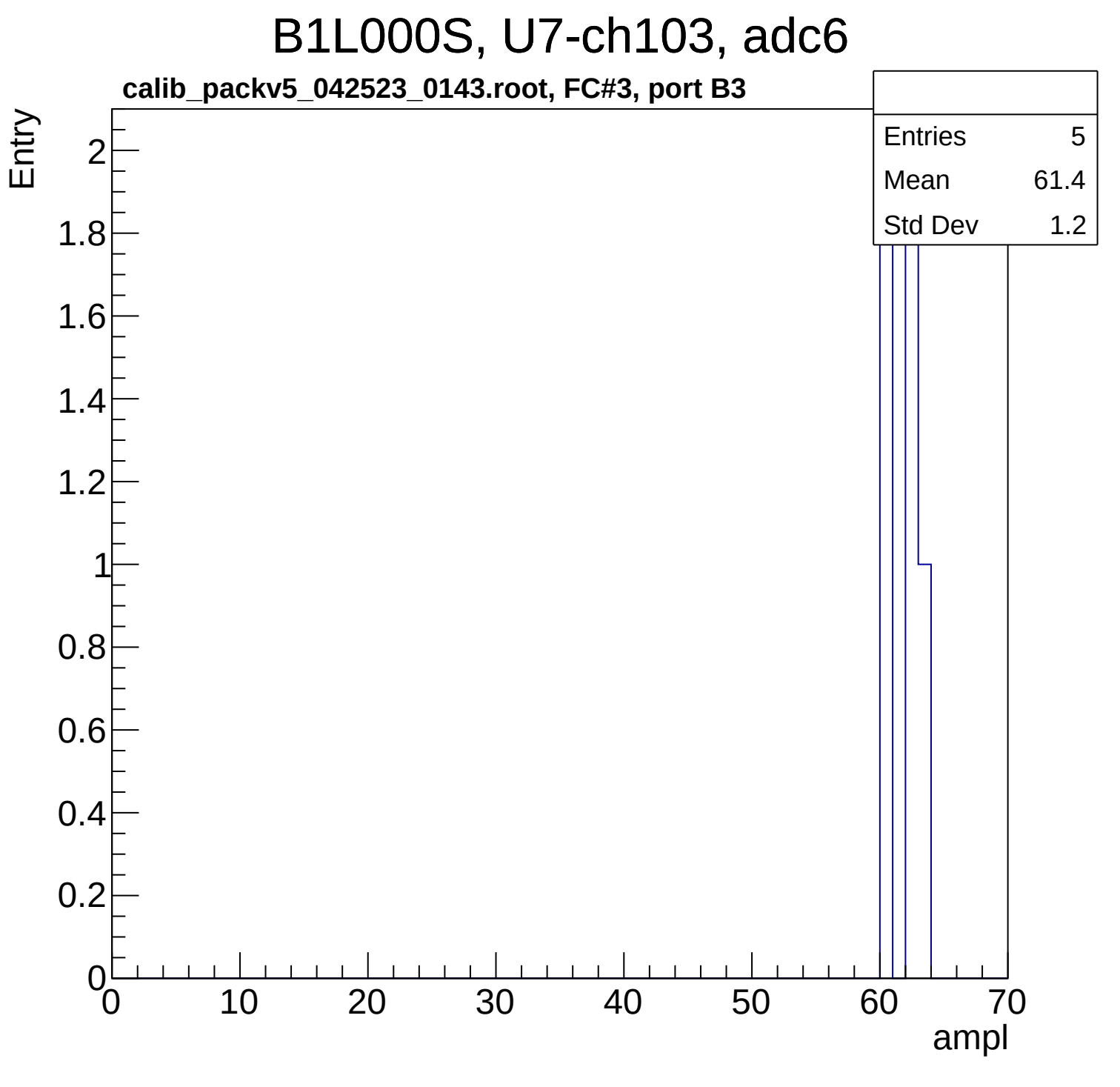
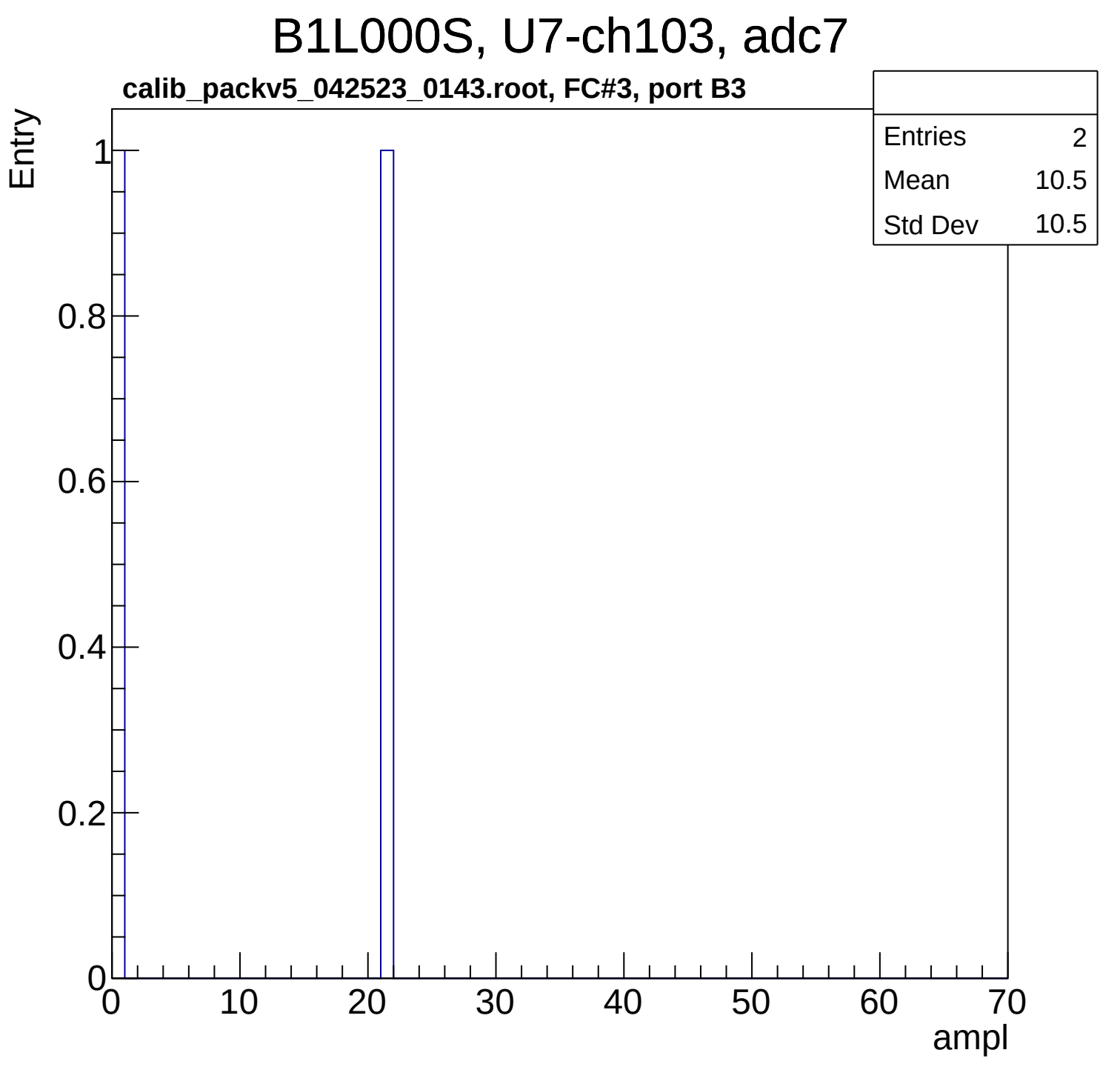
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch103, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 61.4
Std Dev 1.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch103, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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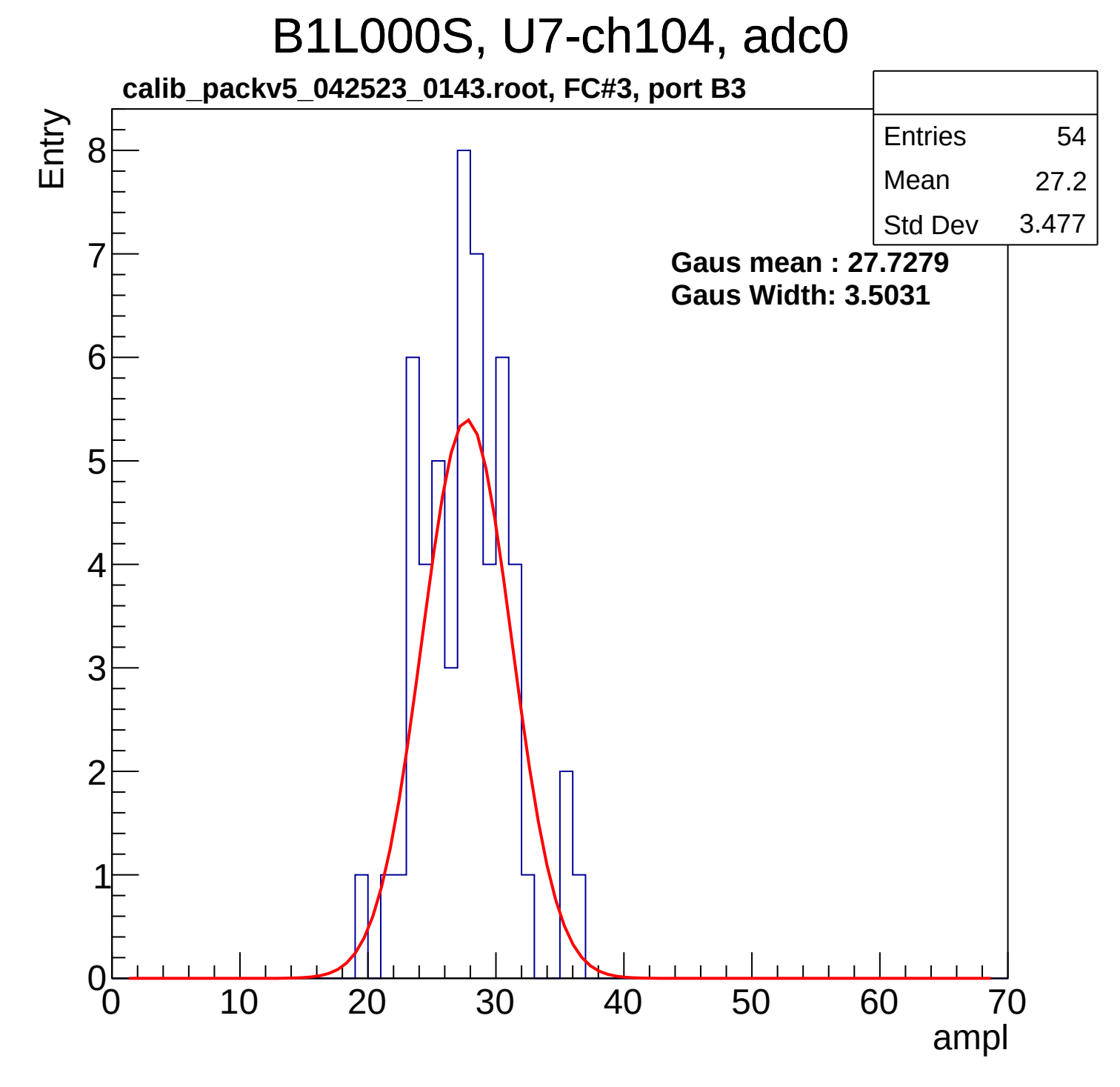
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch104, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 27.2
Std Dev 3.477
Gaus mean : 27.7279
Gaus Width: 3.5031
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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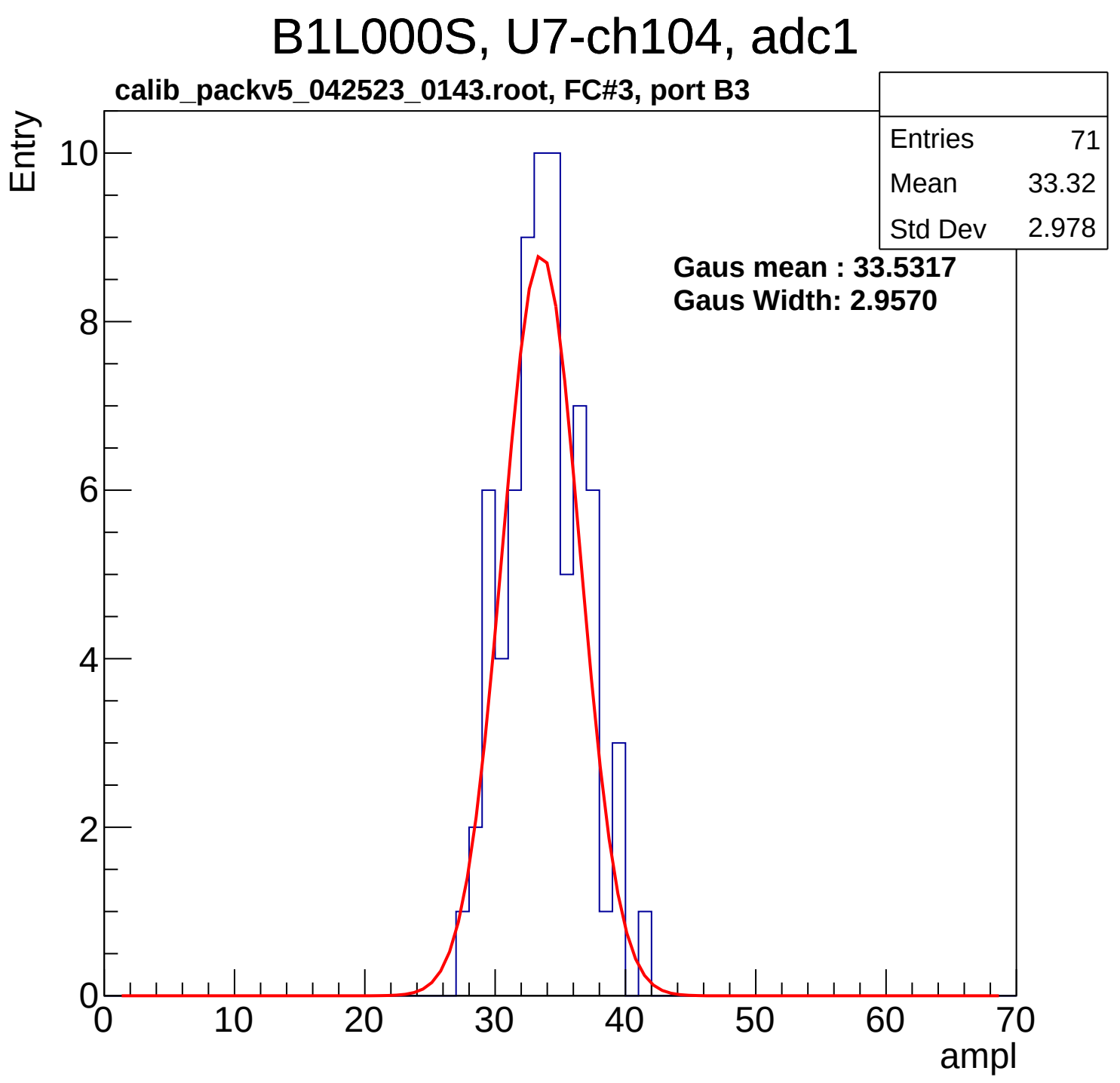
40
50
60
70


B1L000S, U7-ch104, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 33.32
Std Dev 2.978
Gaus mean : 33.5317
Gaus Width: 2.9570
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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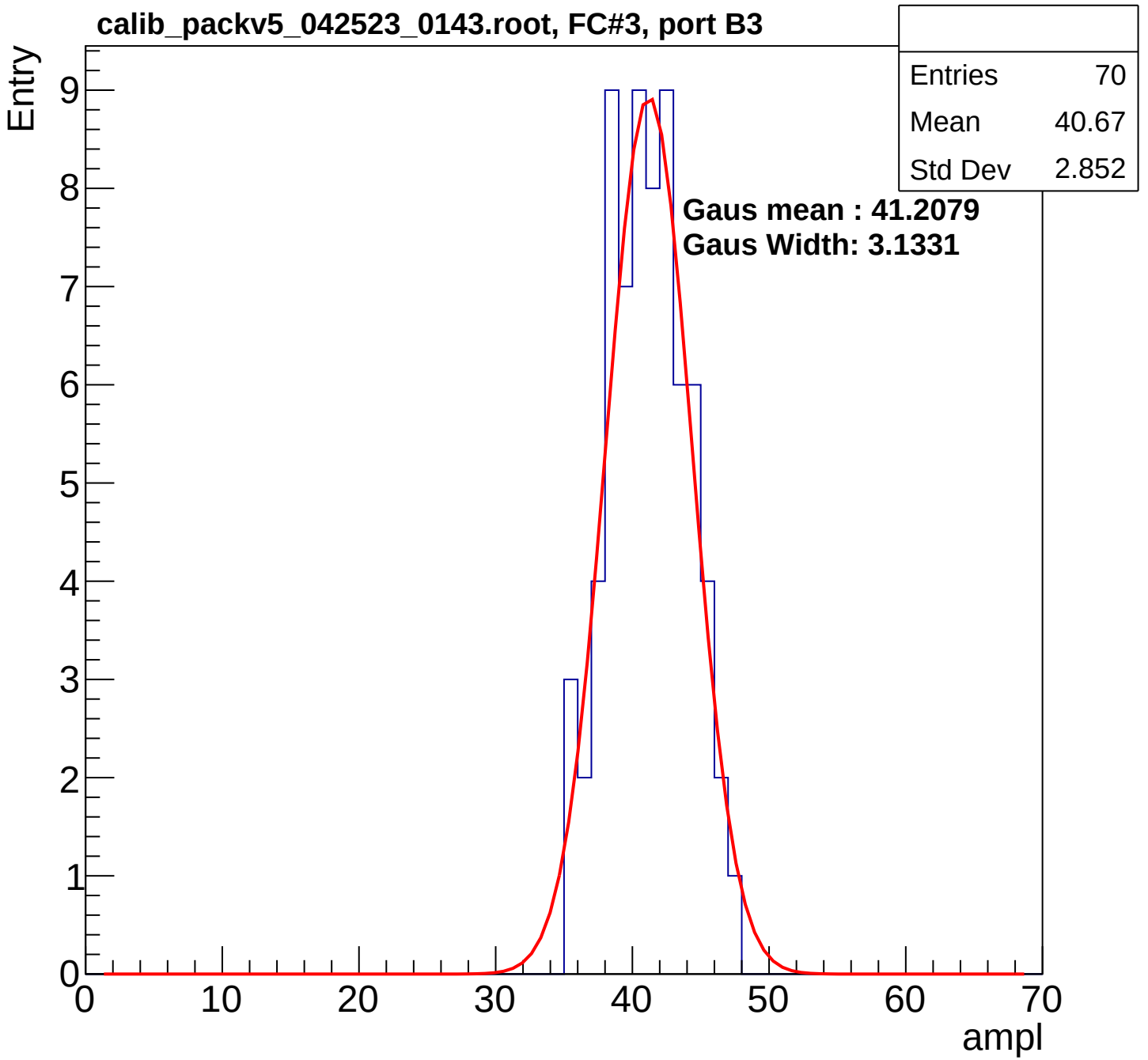
B1L000S, U7-ch104, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 40.67
Std Dev 2.852
Gaus mean : 41.2079
Gaus Width: 3.1331
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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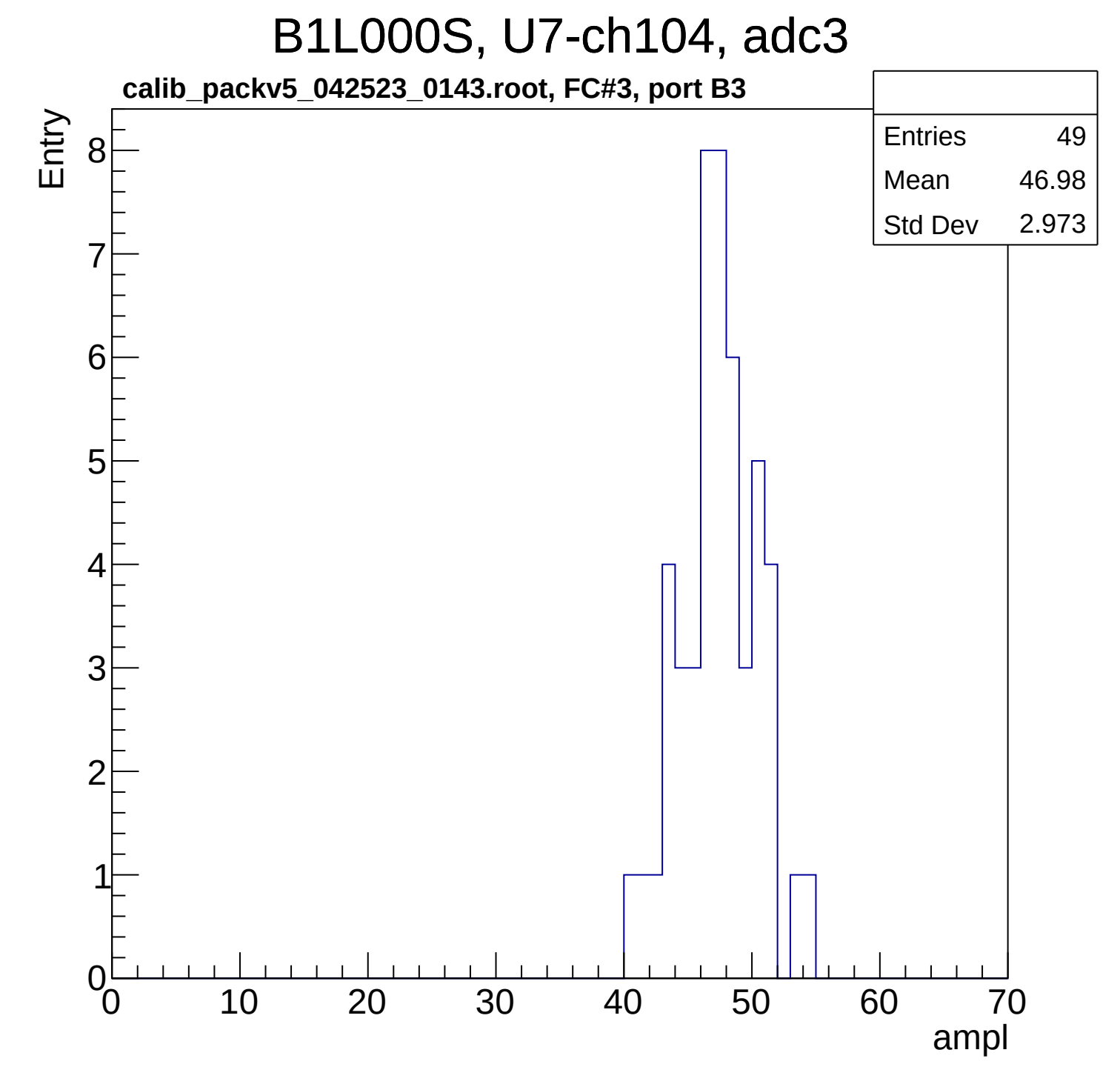
B1L000S, U7-ch104, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 46.98
Std Dev 2.973
Entry
ampl
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5
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7
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0
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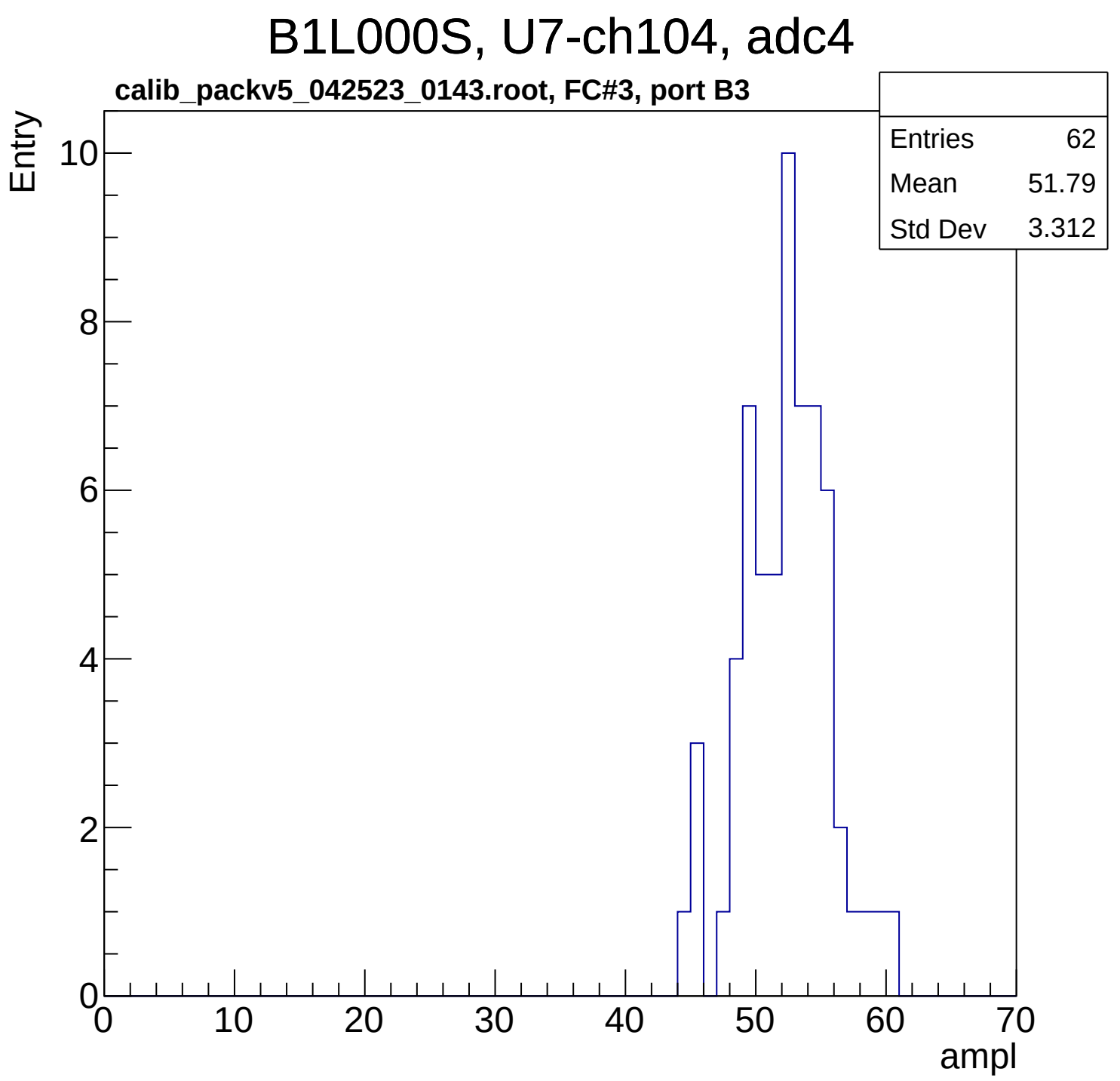
40
50
60
70


B1L000S, U7-ch104, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 51.79
Std Dev 3.312
Entry
0
2
4
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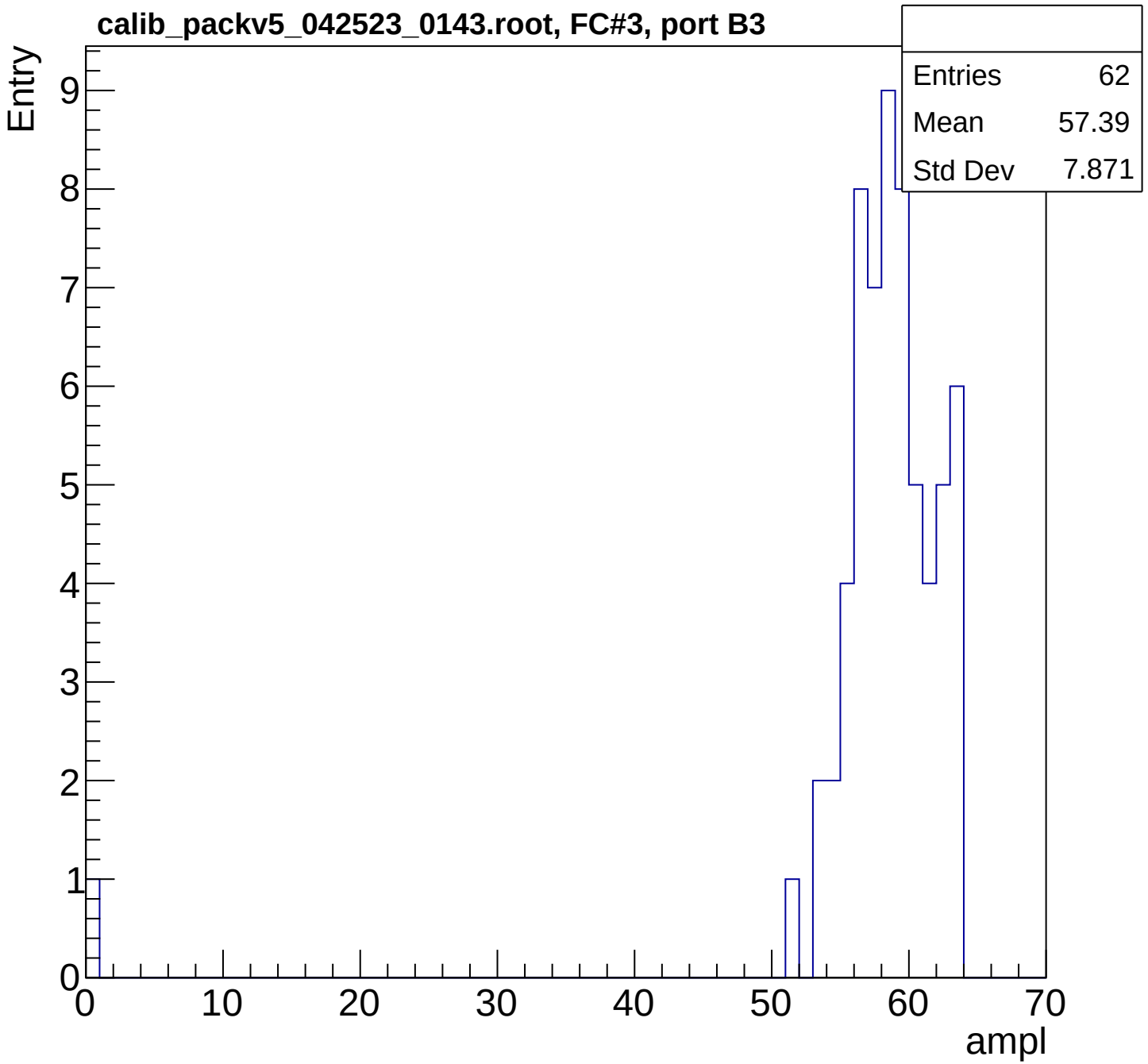
70
ampl


B1L000S, U7-ch104, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 57.39
Std Dev 7.871
Entry
ampl
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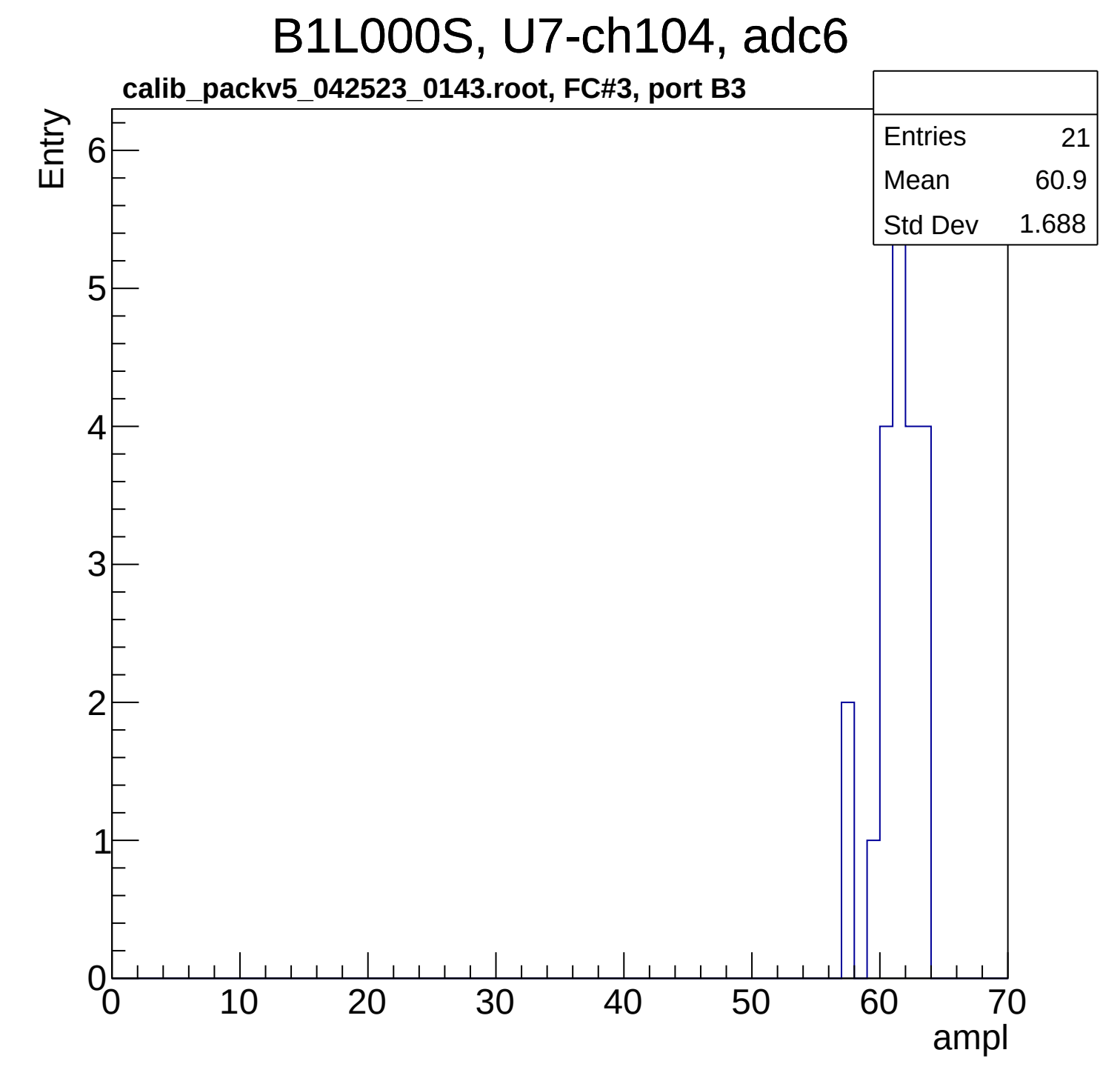
B1L000S, U7-ch104, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 60.9
Std Dev 1.688
Entry
0
1
2
3
4
5
6
0
10
20
30
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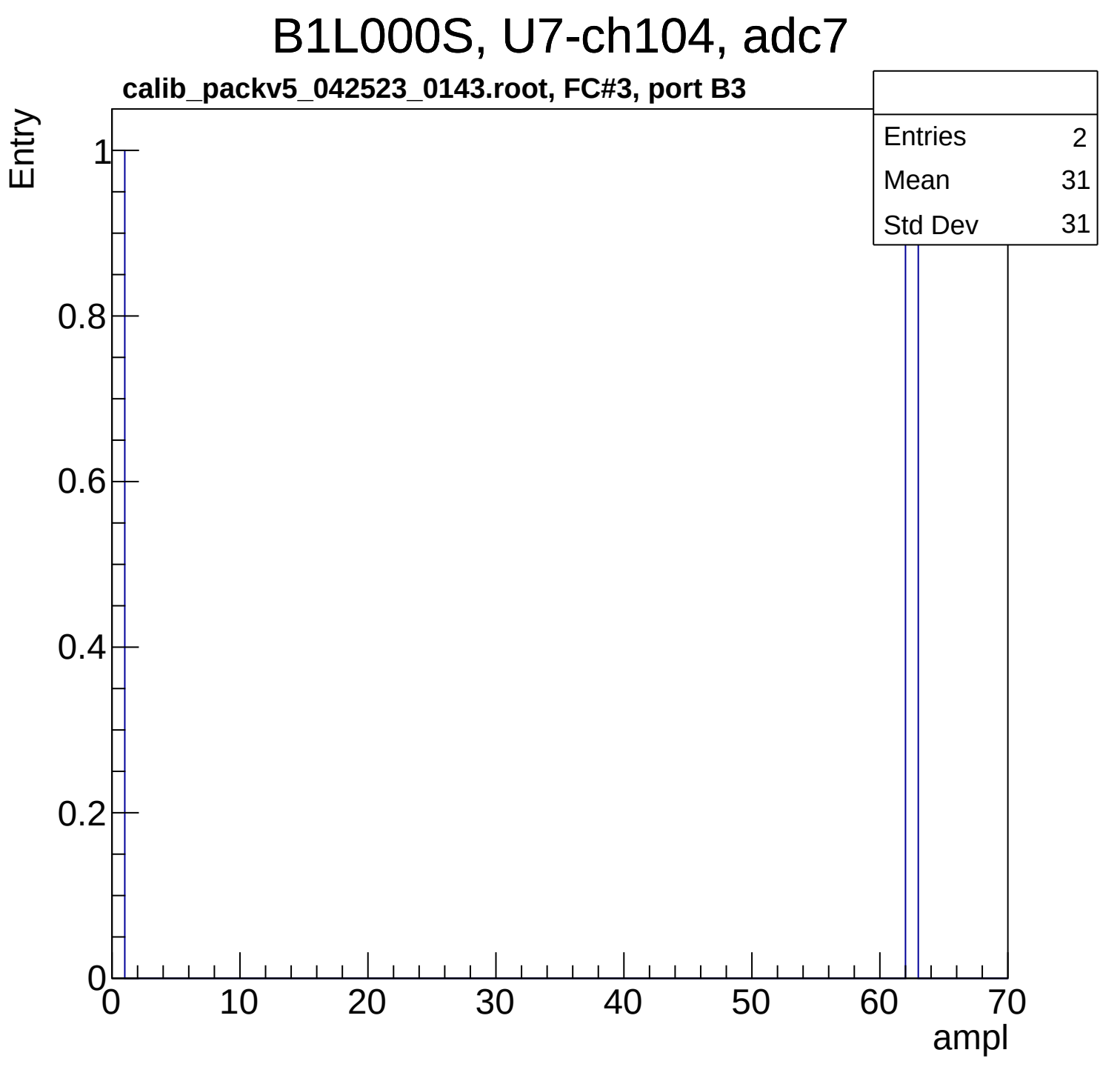
50
60
70
ampl


B1L000S, U7-ch104, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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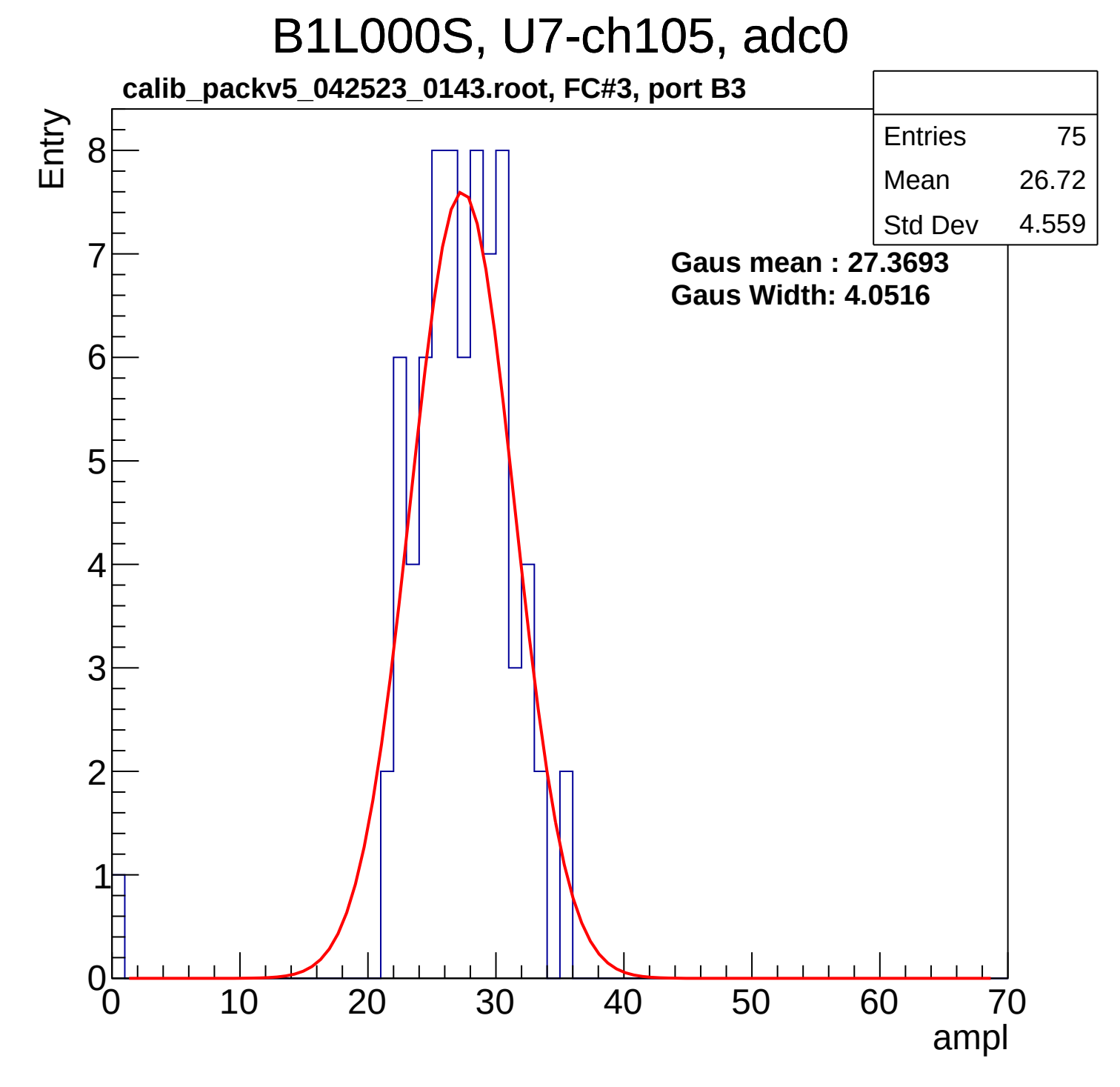
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch105, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 26.72
Std Dev 4.559
Gaus mean : 27.3693
Gaus Width: 4.0516
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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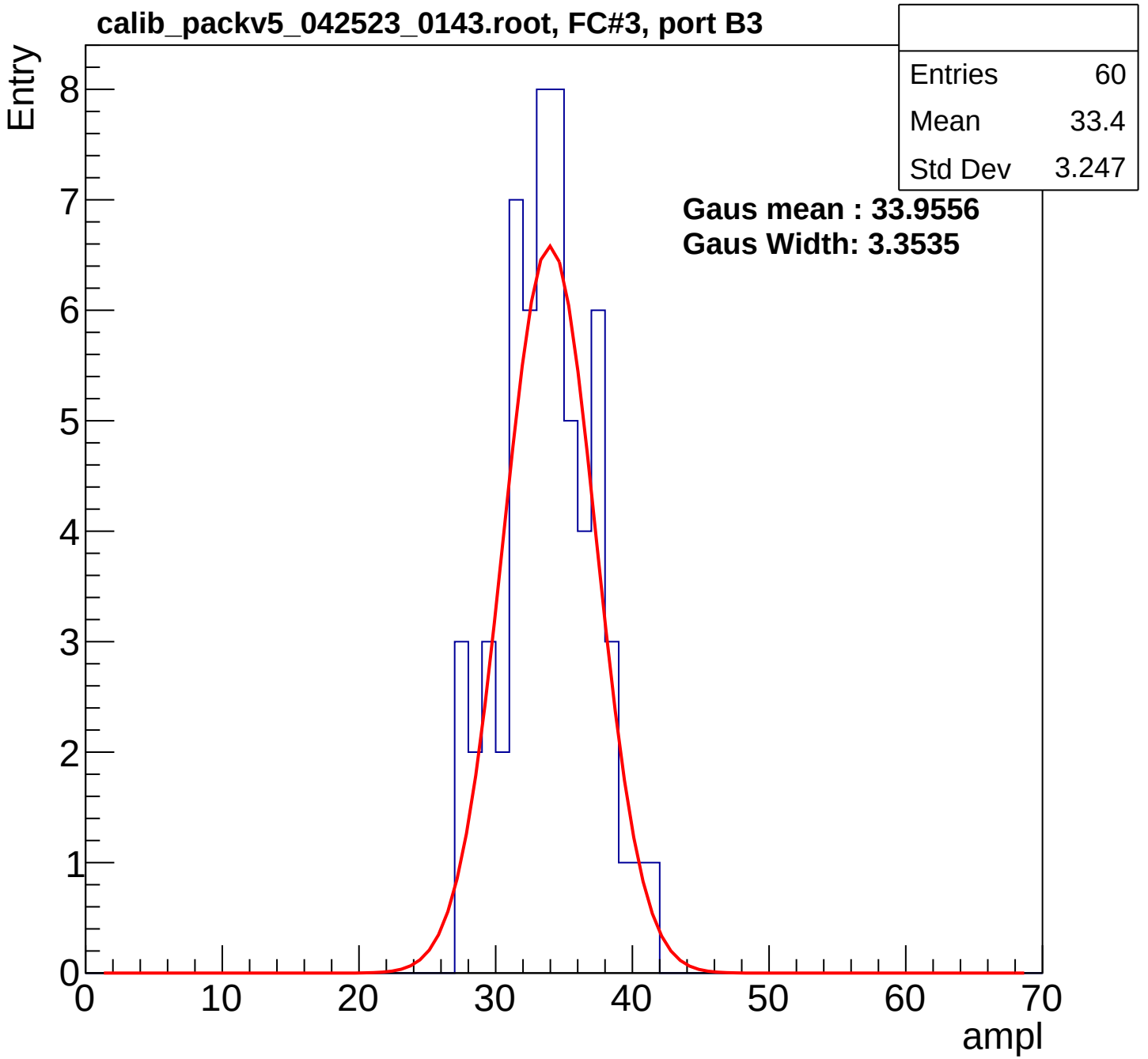
B1L000S, U7-ch105, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 33.4
Std Dev 3.247
Gaus mean : 33.9556
Gaus Width: 3.3535
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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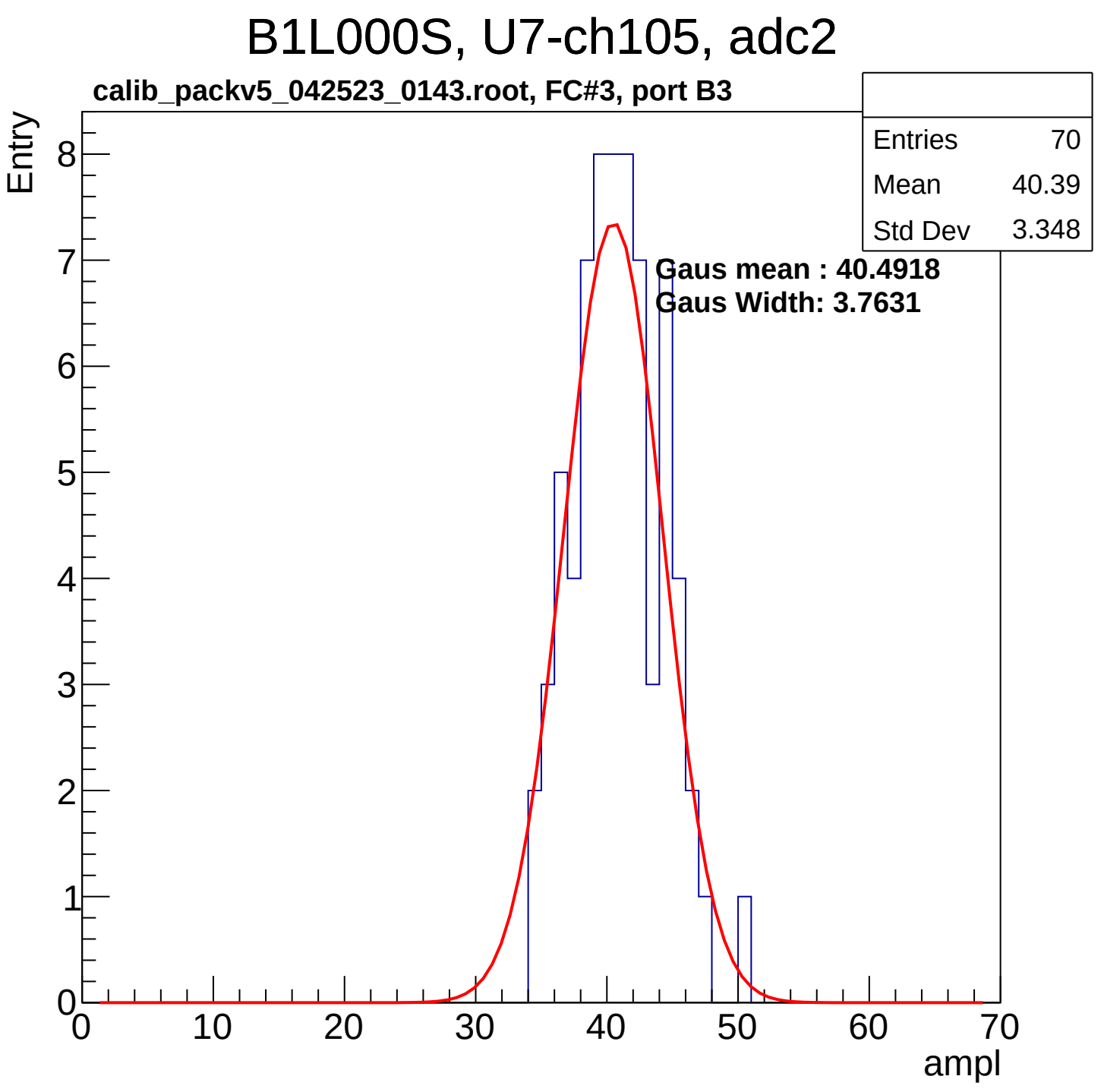
B1L000S, U7-ch105, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 40.39
Std Dev 3.348
Gaus mean : 40.4918
Gaus Width: 3.7631
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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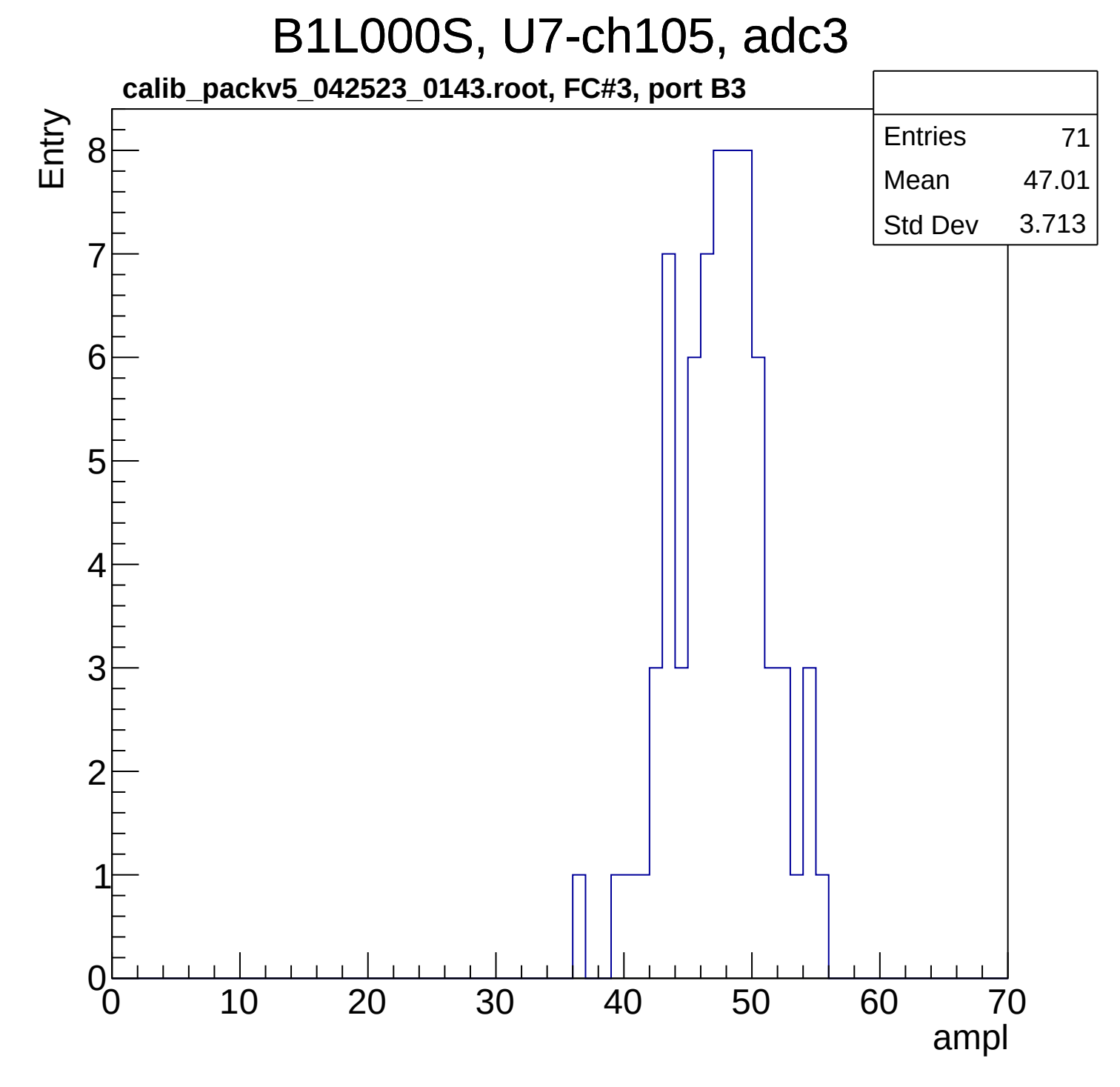
B1L000S, U7-ch105, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 47.01
Std Dev 3.713
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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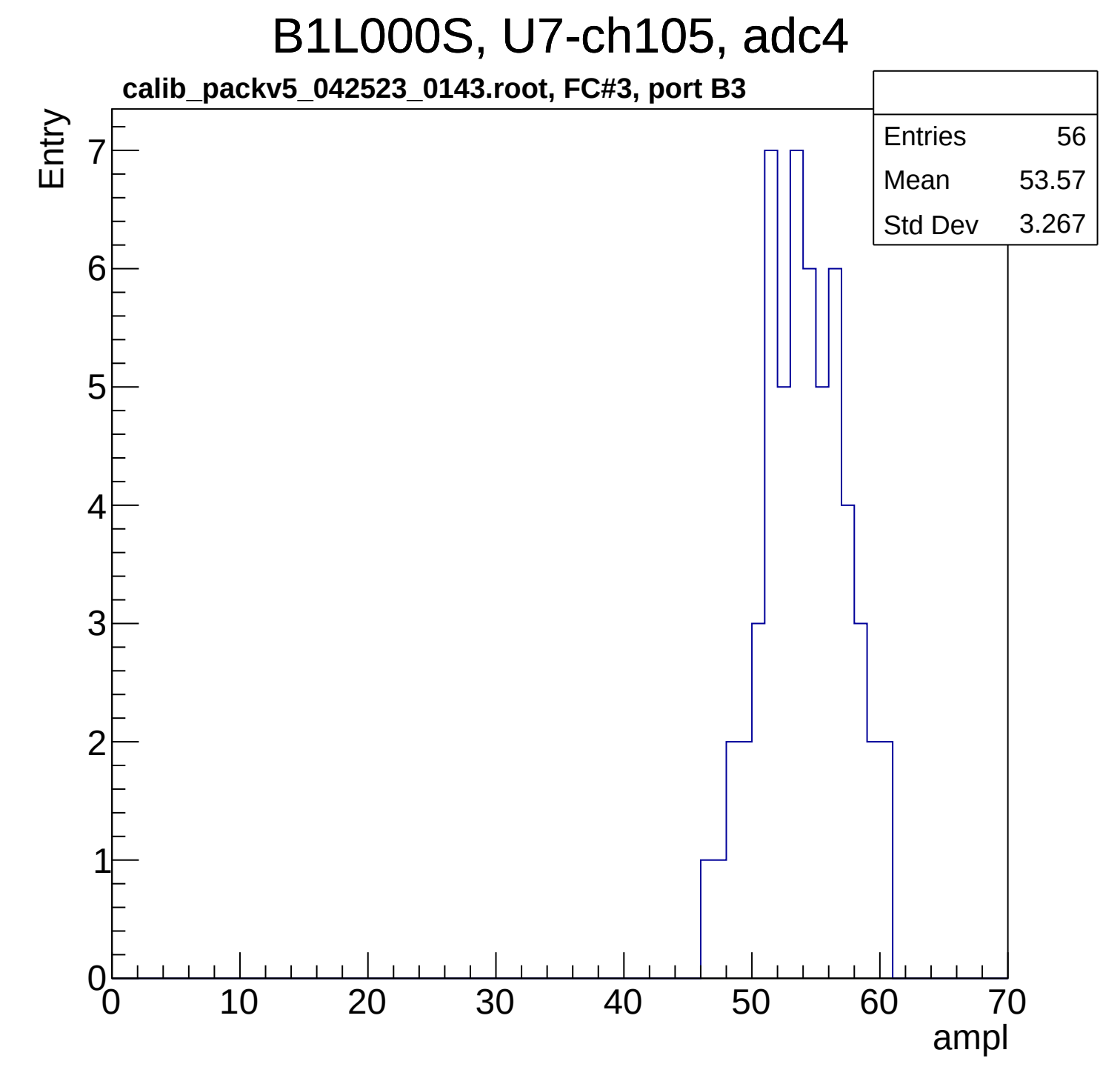
B1L000S, U7-ch105, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 53.57
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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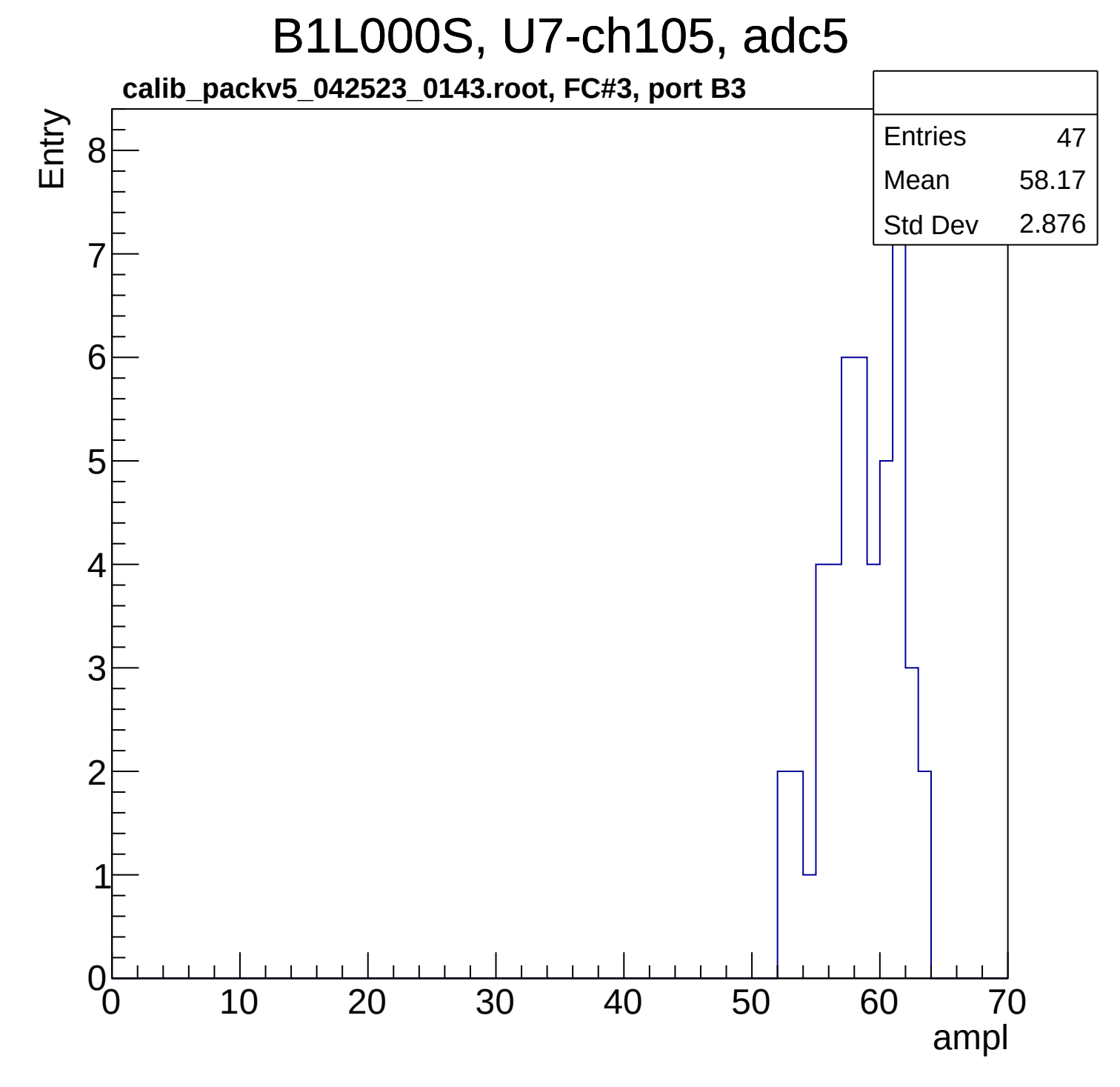
B1L000S, U7-ch105, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 58.17
Std Dev 2.876
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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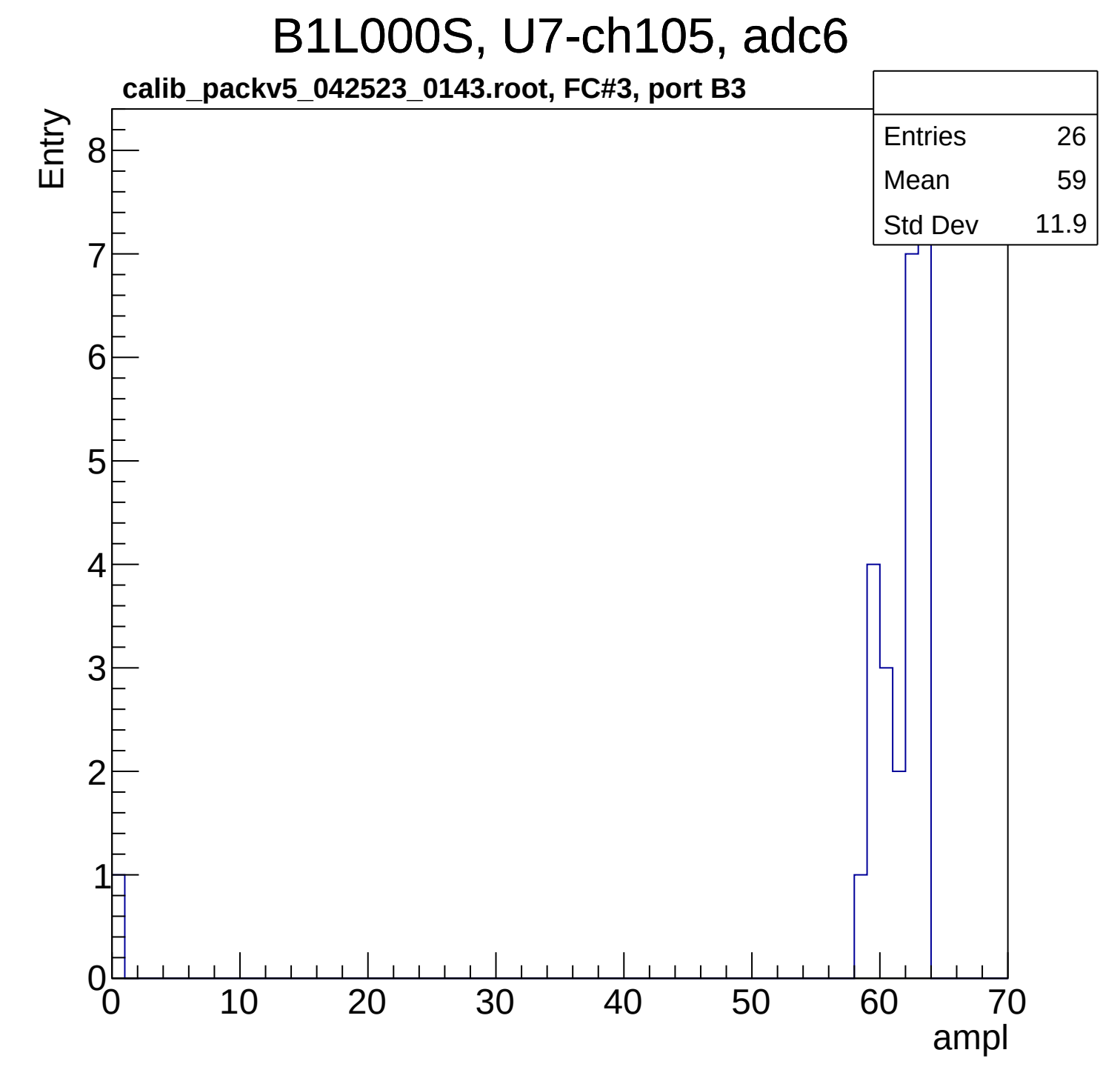
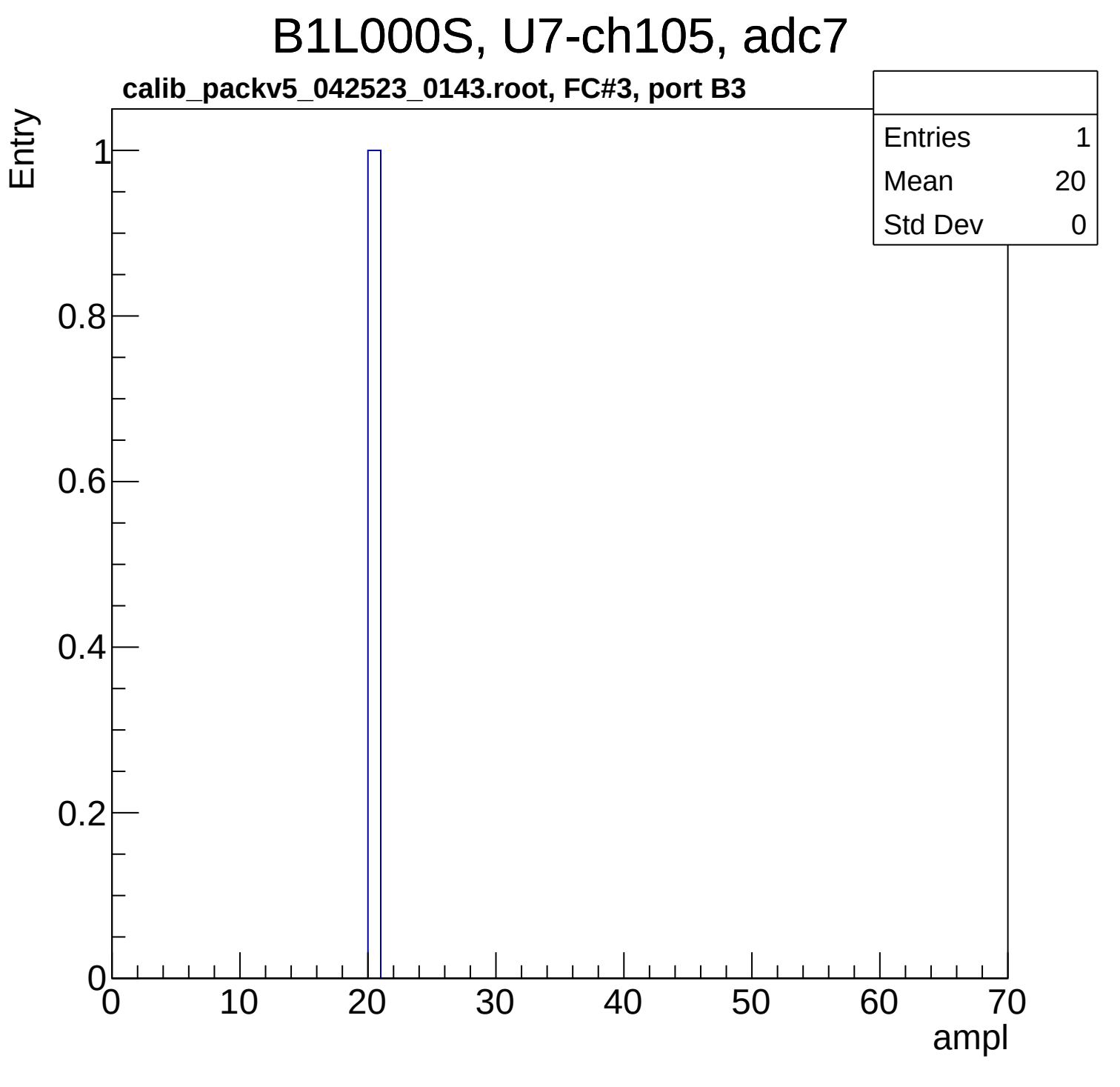

B1L000S, U7-ch105, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 26
Mean 59
Std Dev 11.9
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch105, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 20
Std Dev 0
Entry
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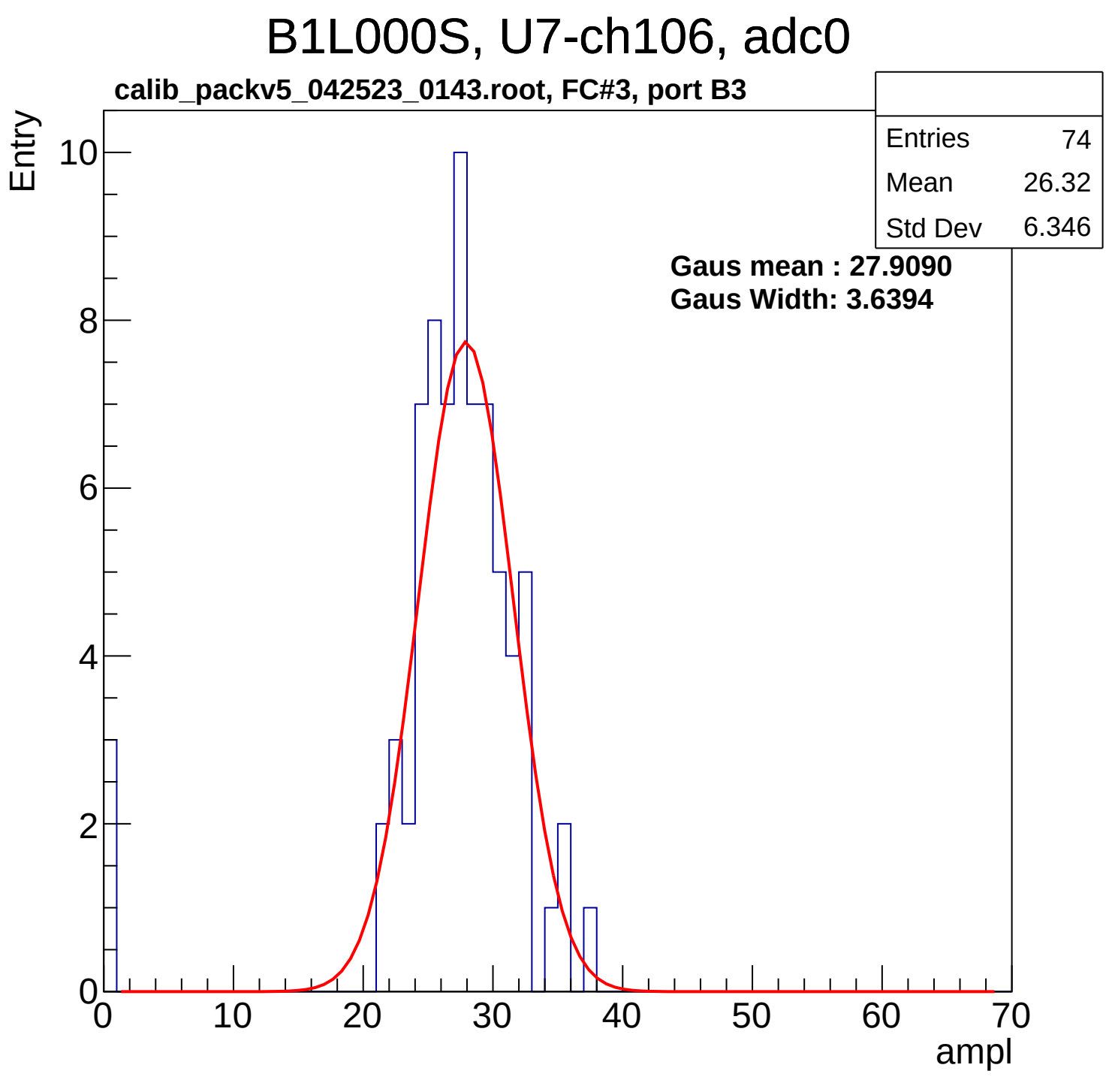
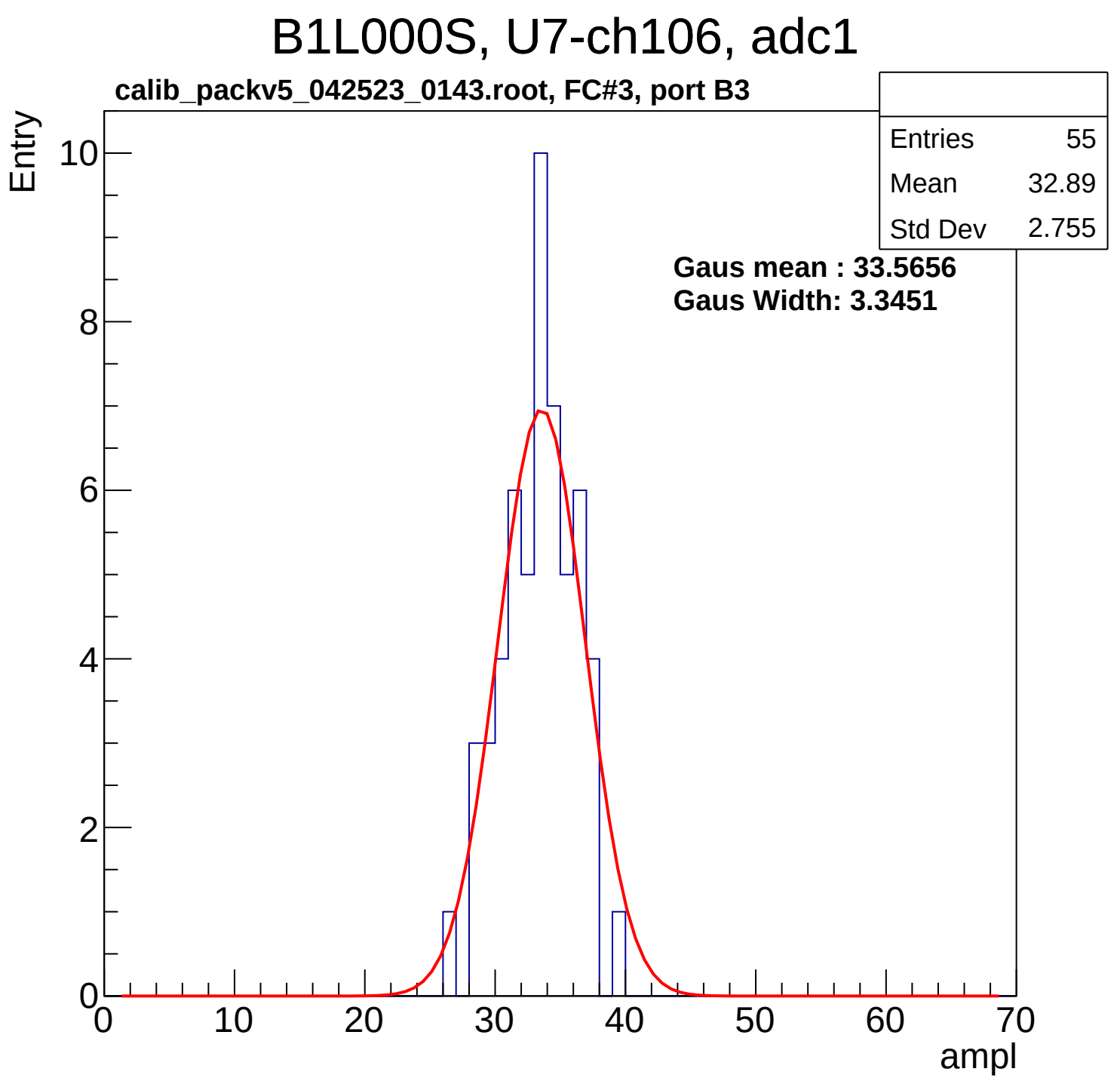
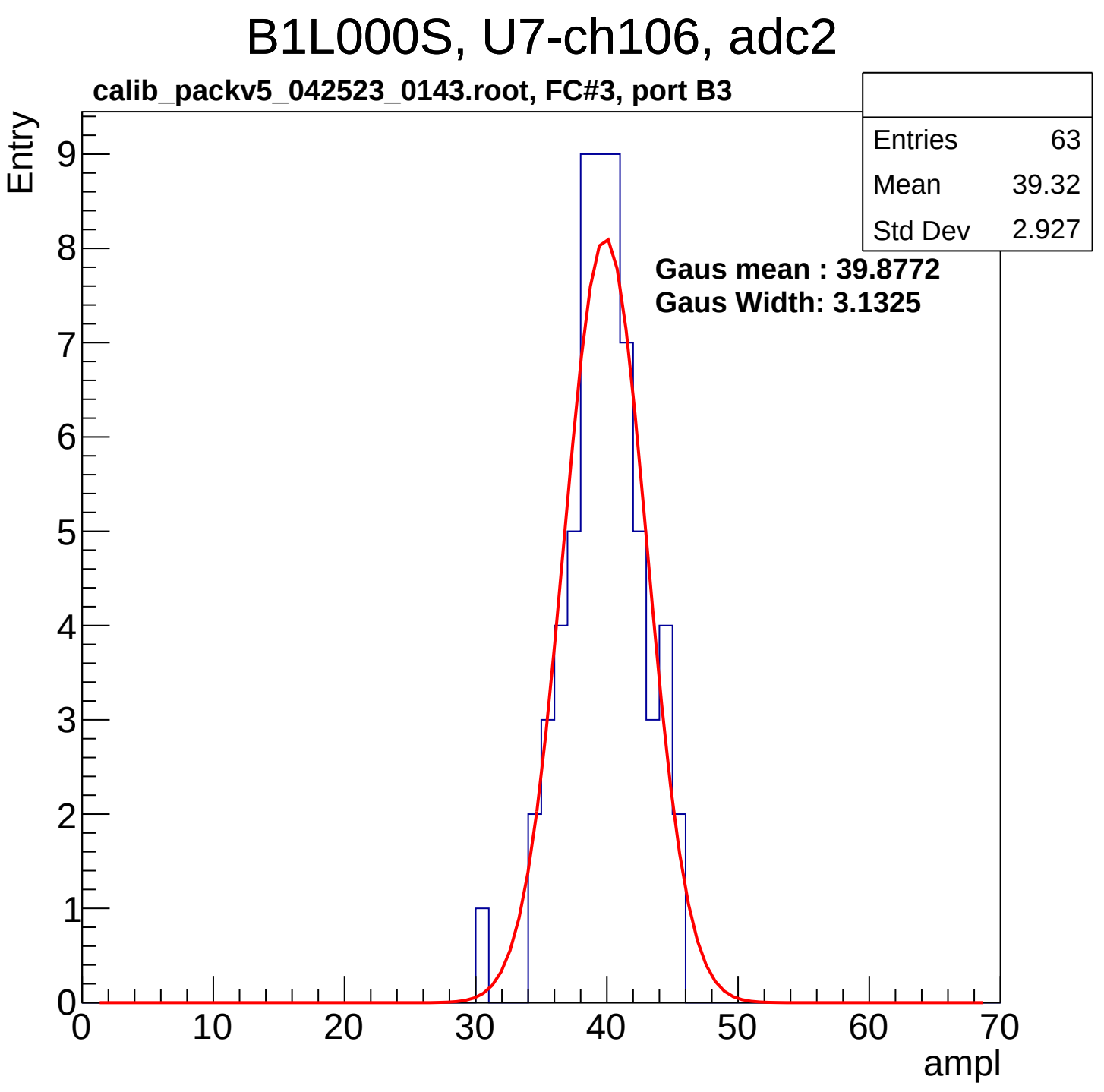
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch106, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 26.32
Std Dev 6.346
Gaus mean : 27.9090
Gaus Width: 3.6394
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch106, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 32.89
Std Dev 2.755
Gaus mean : 33.5656
Gaus Width: 3.3451
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch106, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 39.32
Std Dev 2.927
Gaus mean : 39.8772
Gaus Width: 3.1325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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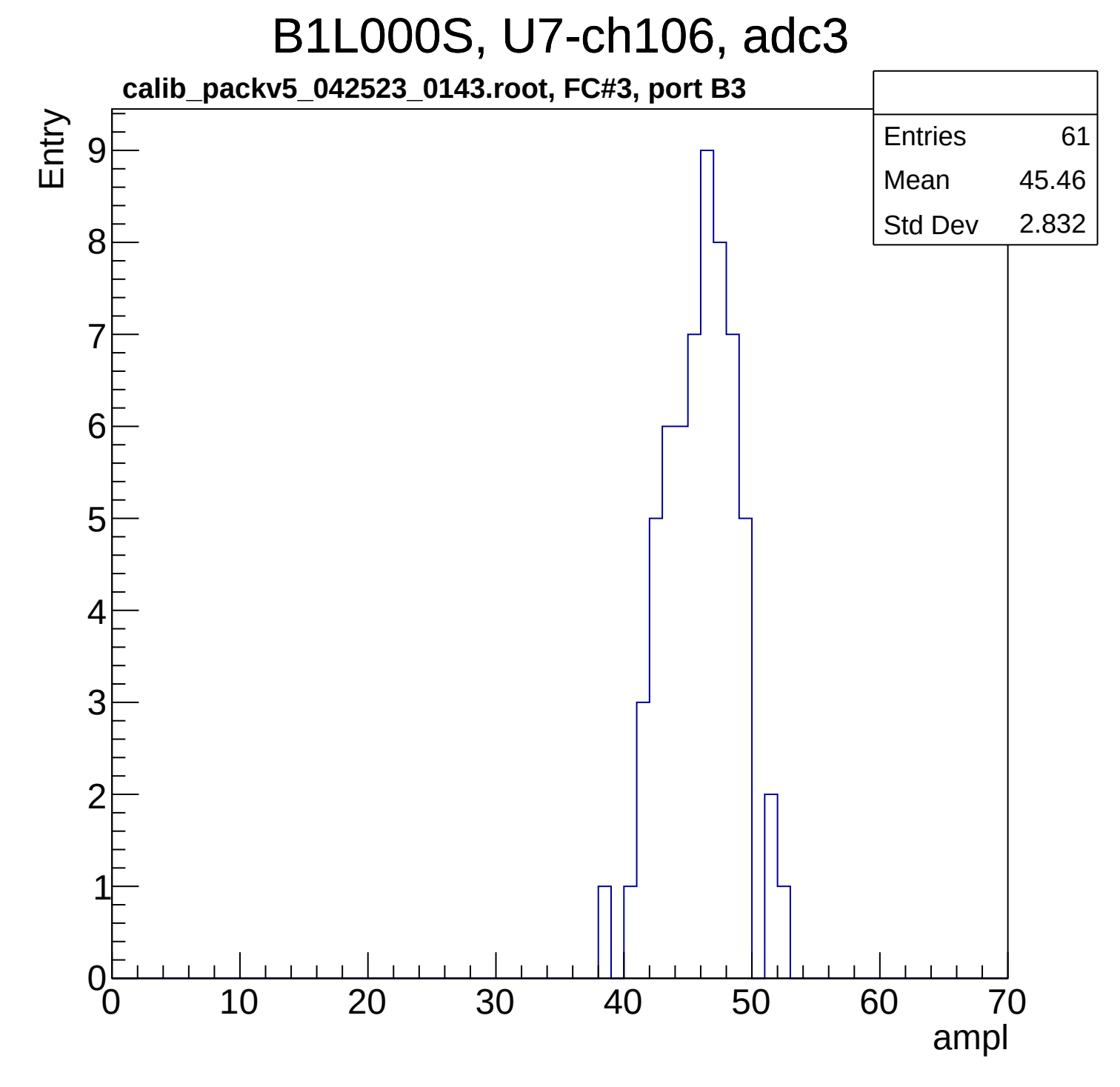
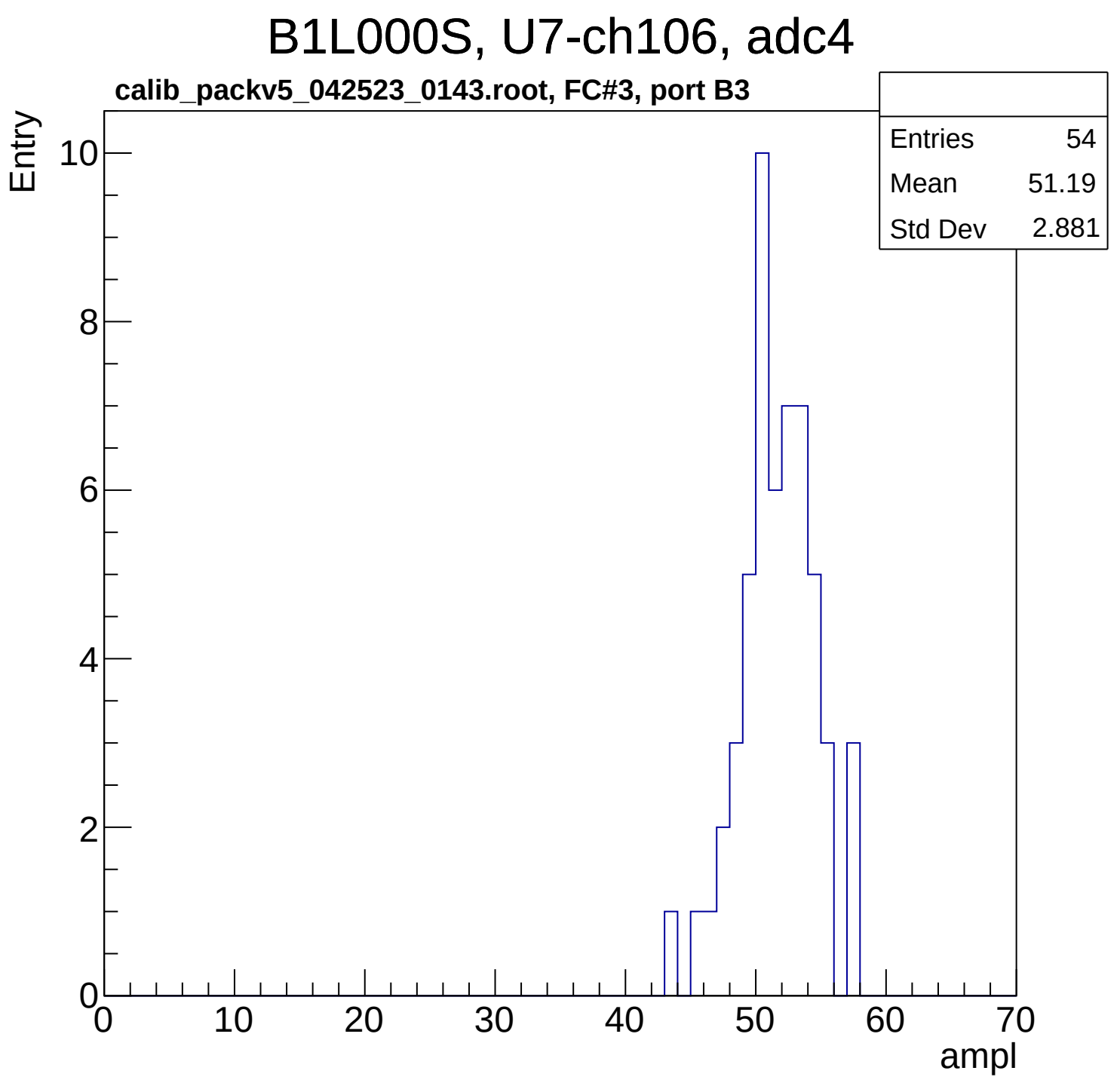
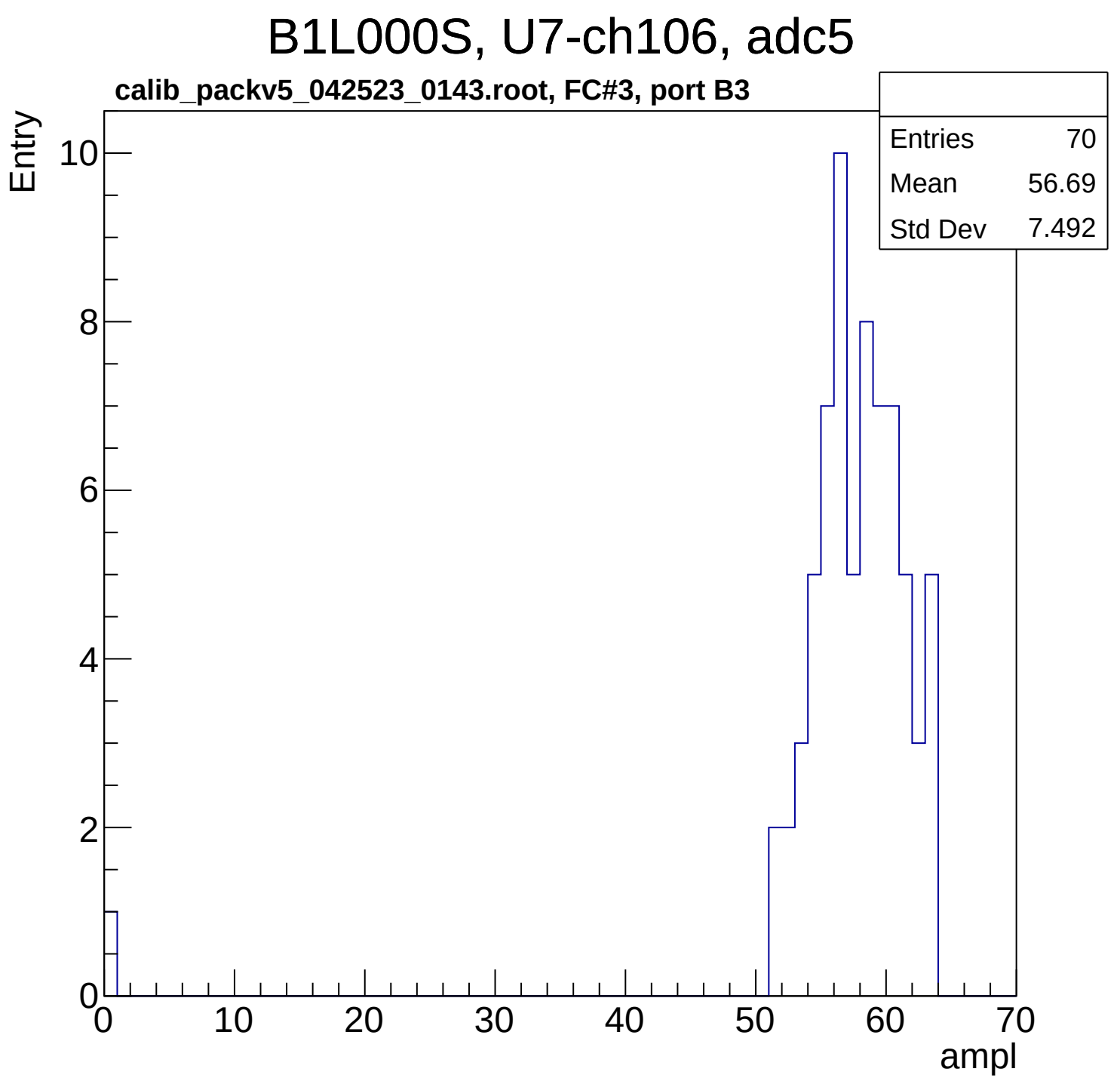

B1L000S, U7-ch106, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 45.46
Std Dev 2.832
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch106, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 51.19
Std Dev 2.881
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch106, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 56.69
Std Dev 7.492
Entry
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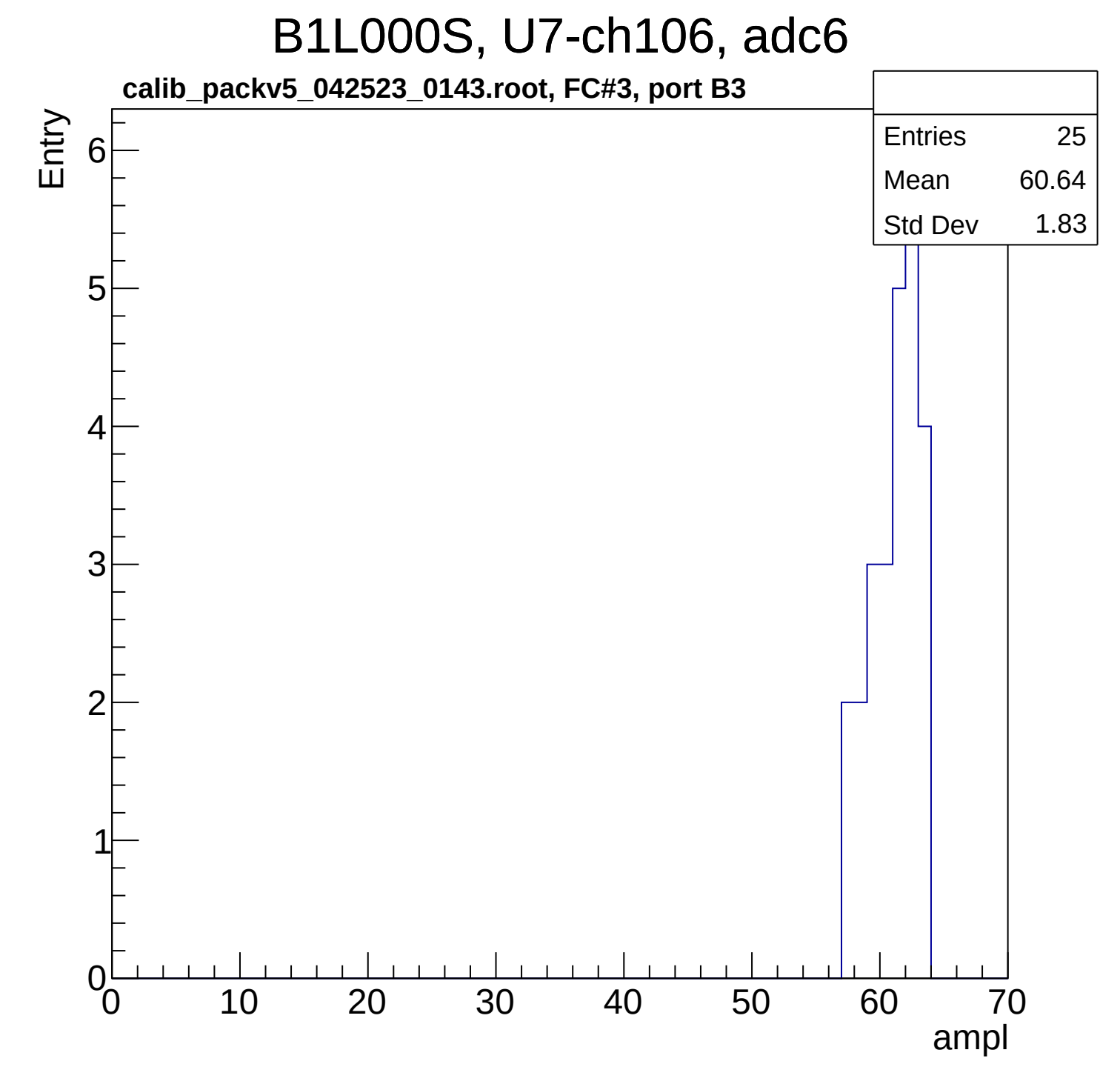
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch106, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 25
Mean 60.64
Std Dev 1.83
Entry
0
1
2
3
4
5
6
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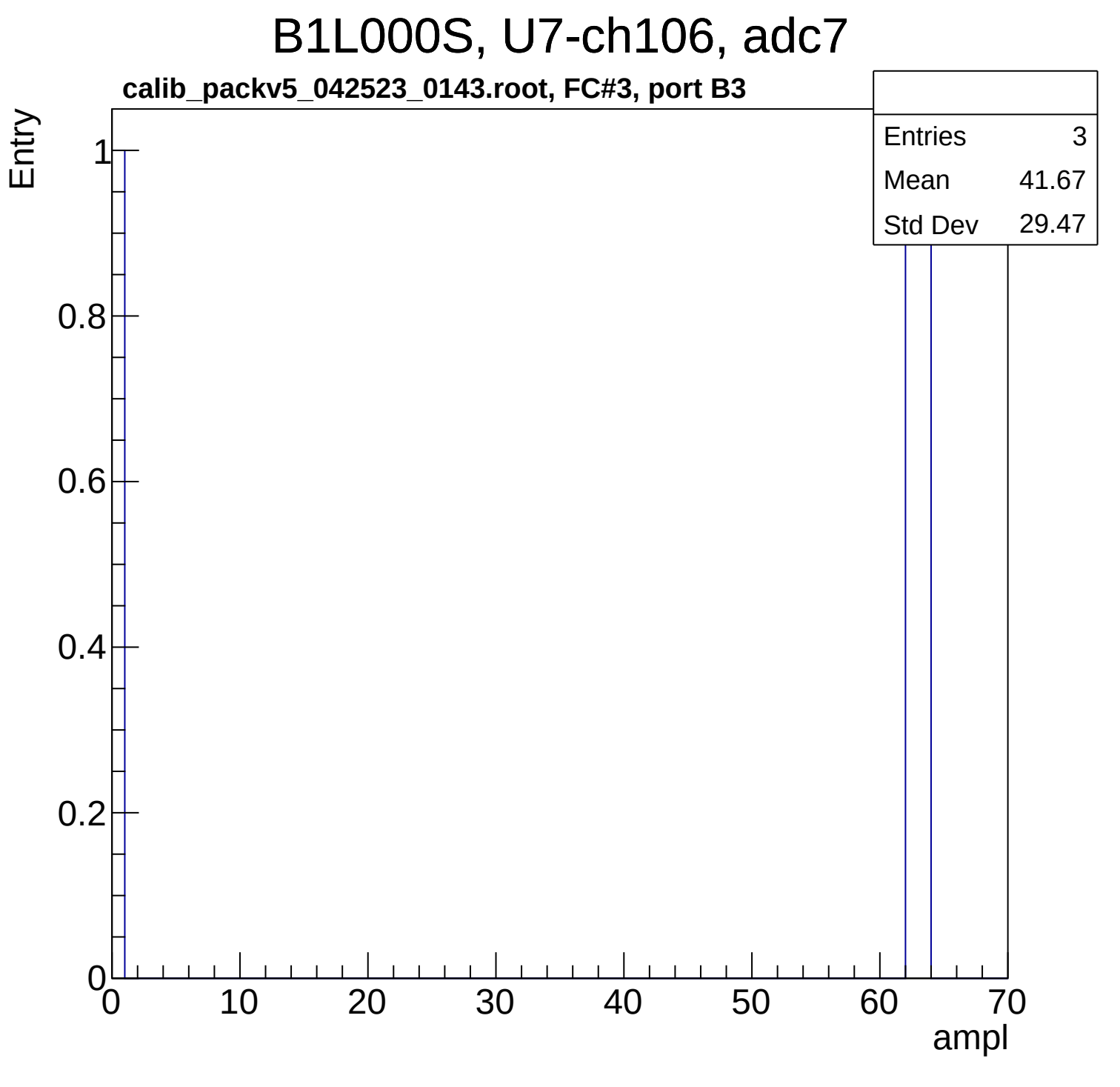
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch106, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
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10
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30
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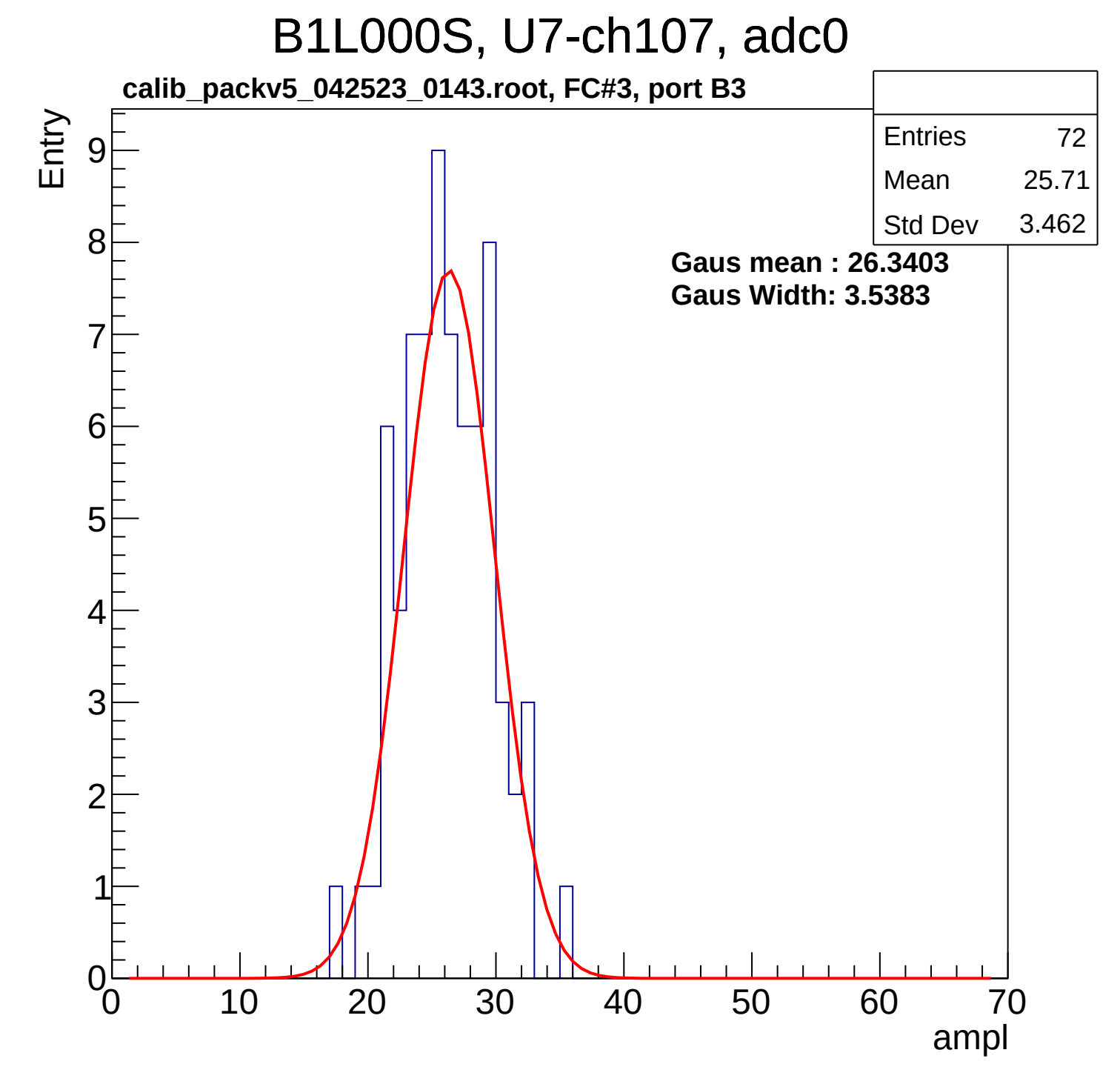
50
60
70
ampl


B1L000S, U7-ch107, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 25.71
Std Dev 3.462
Gaus mean : 26.3403
Gaus Width: 3.5383
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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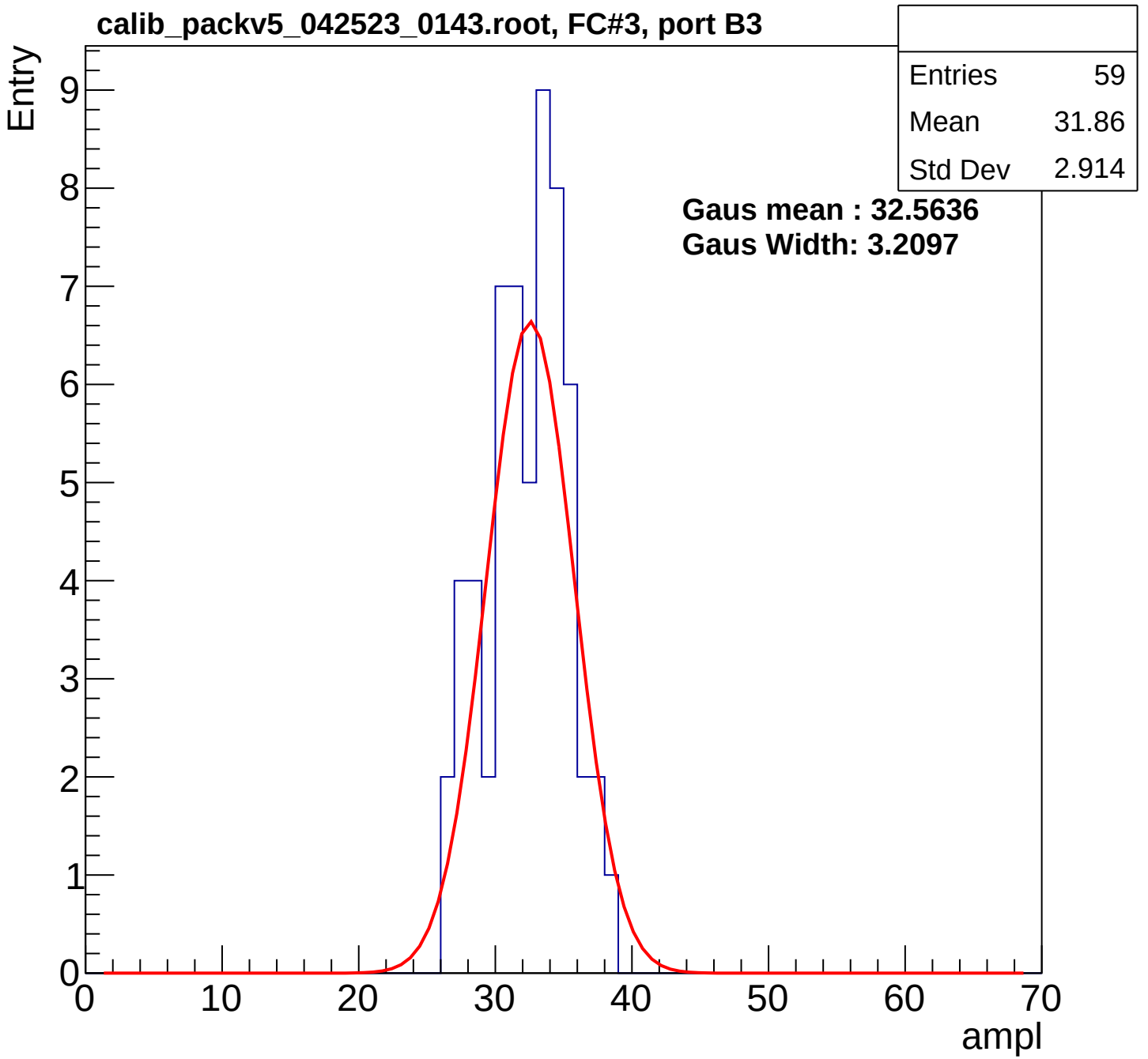
B1L000S, U7-ch107, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 31.86
Std Dev 2.914
Gaus mean : 32.5636
Gaus Width: 3.2097
Entry
ampl
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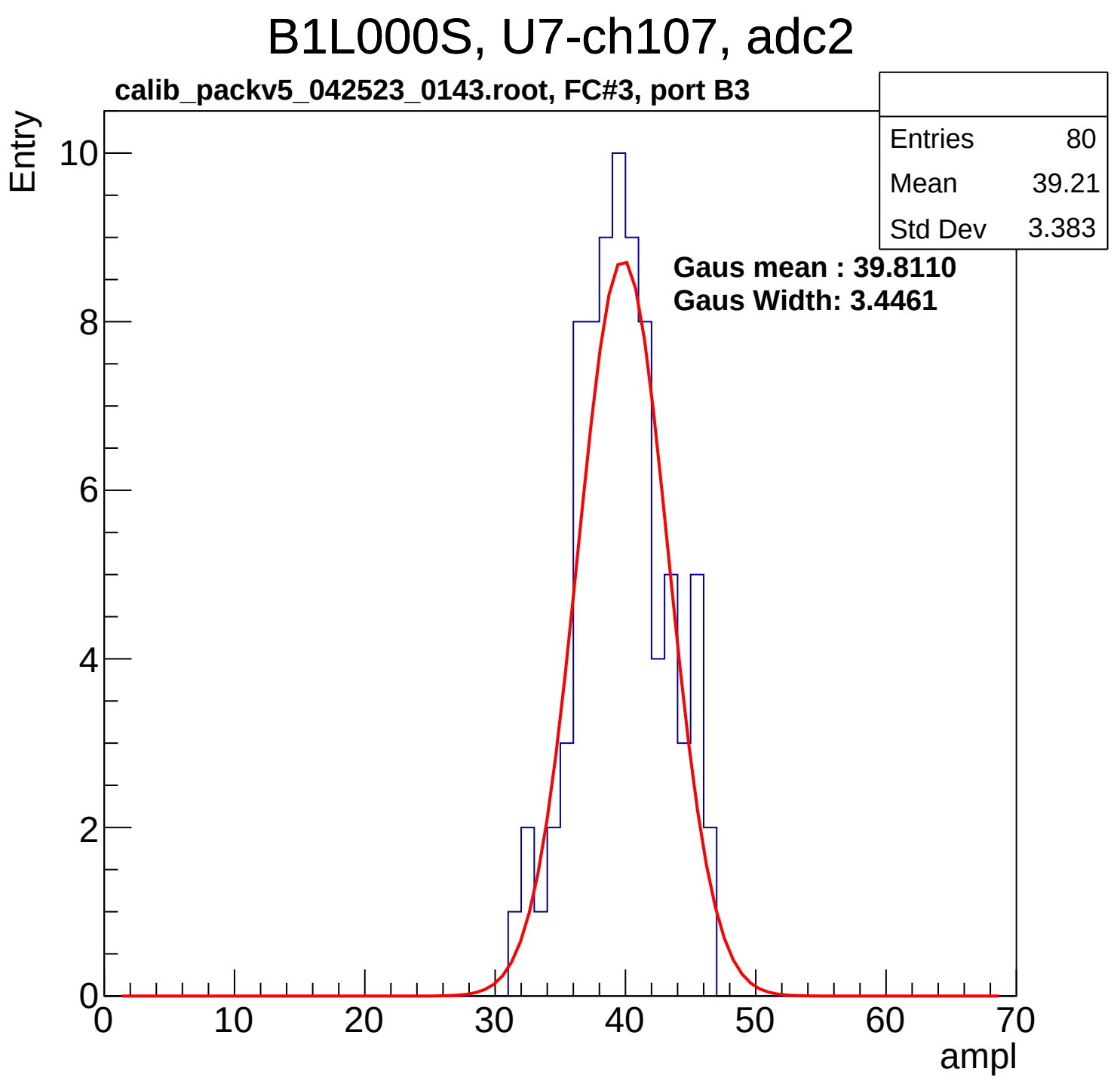
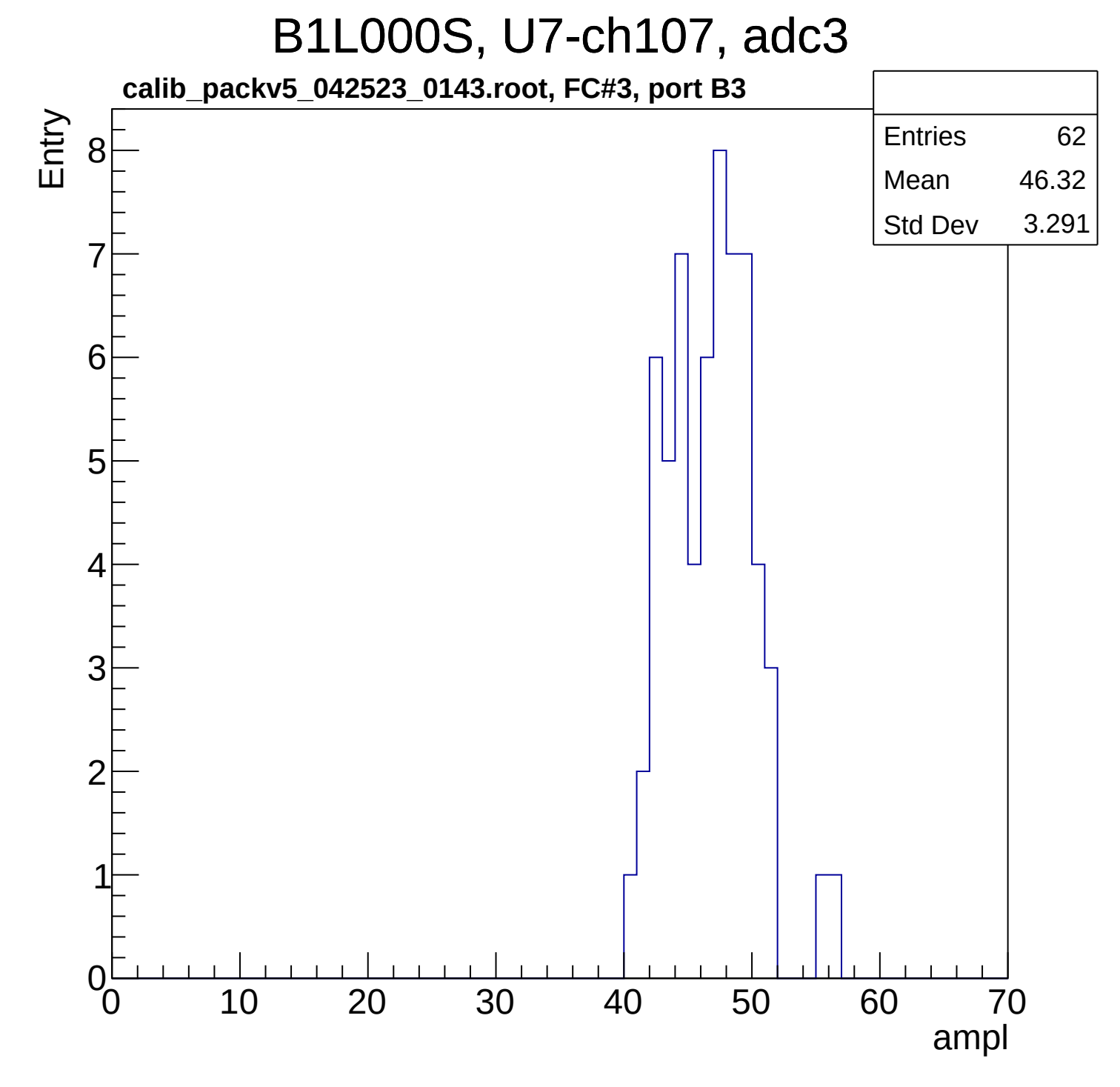
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch107, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 39.21
Std Dev 3.383
Gaus mean : 39.8110
Gaus Width: 3.4461
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch107, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.32
Std Dev 3.291
Entry
ampl
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3
4
5
6
7
8
0
10
20
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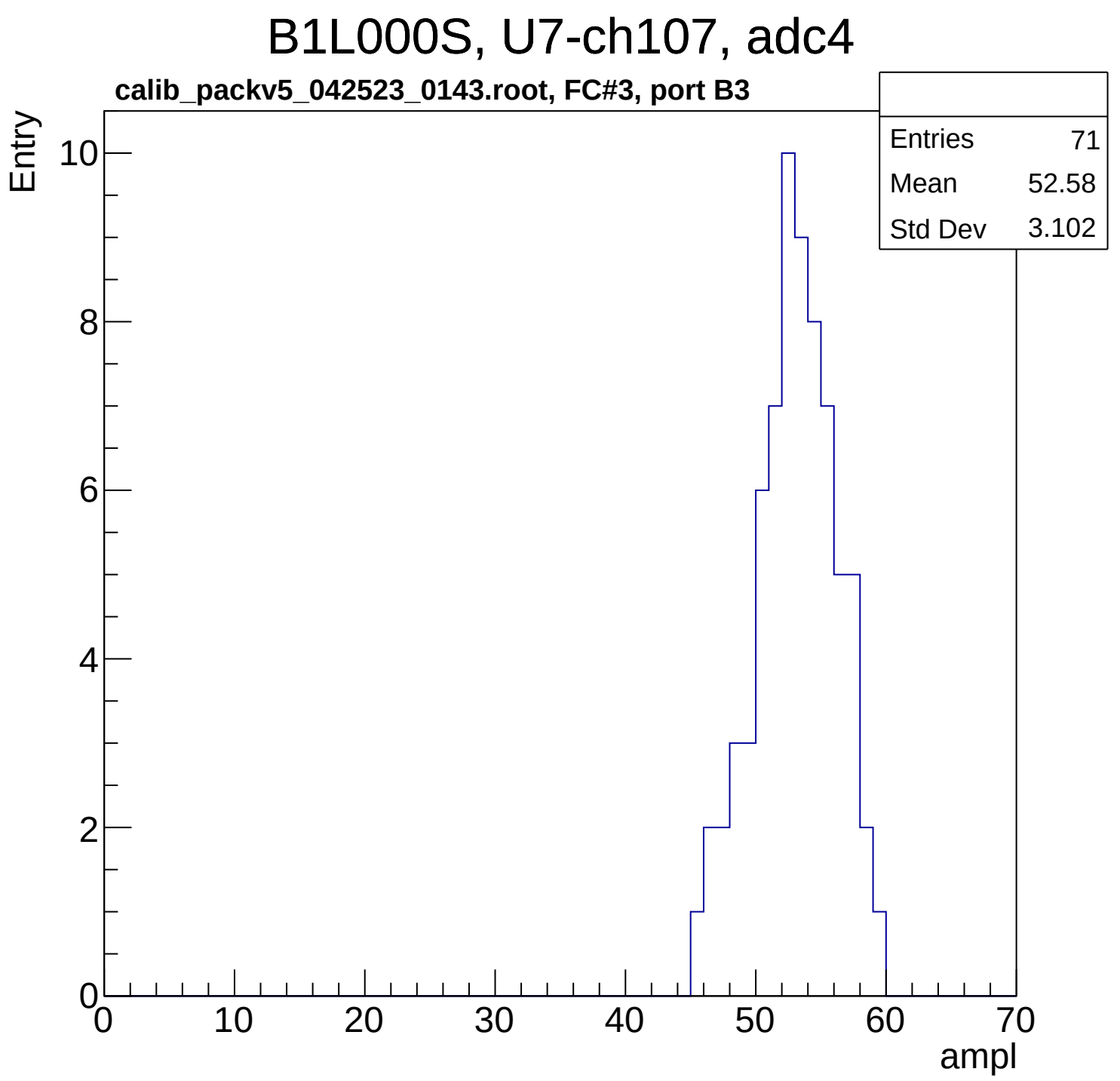
40
50
60
70


B1L000S, U7-ch107, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 52.58
Std Dev 3.102
Entry
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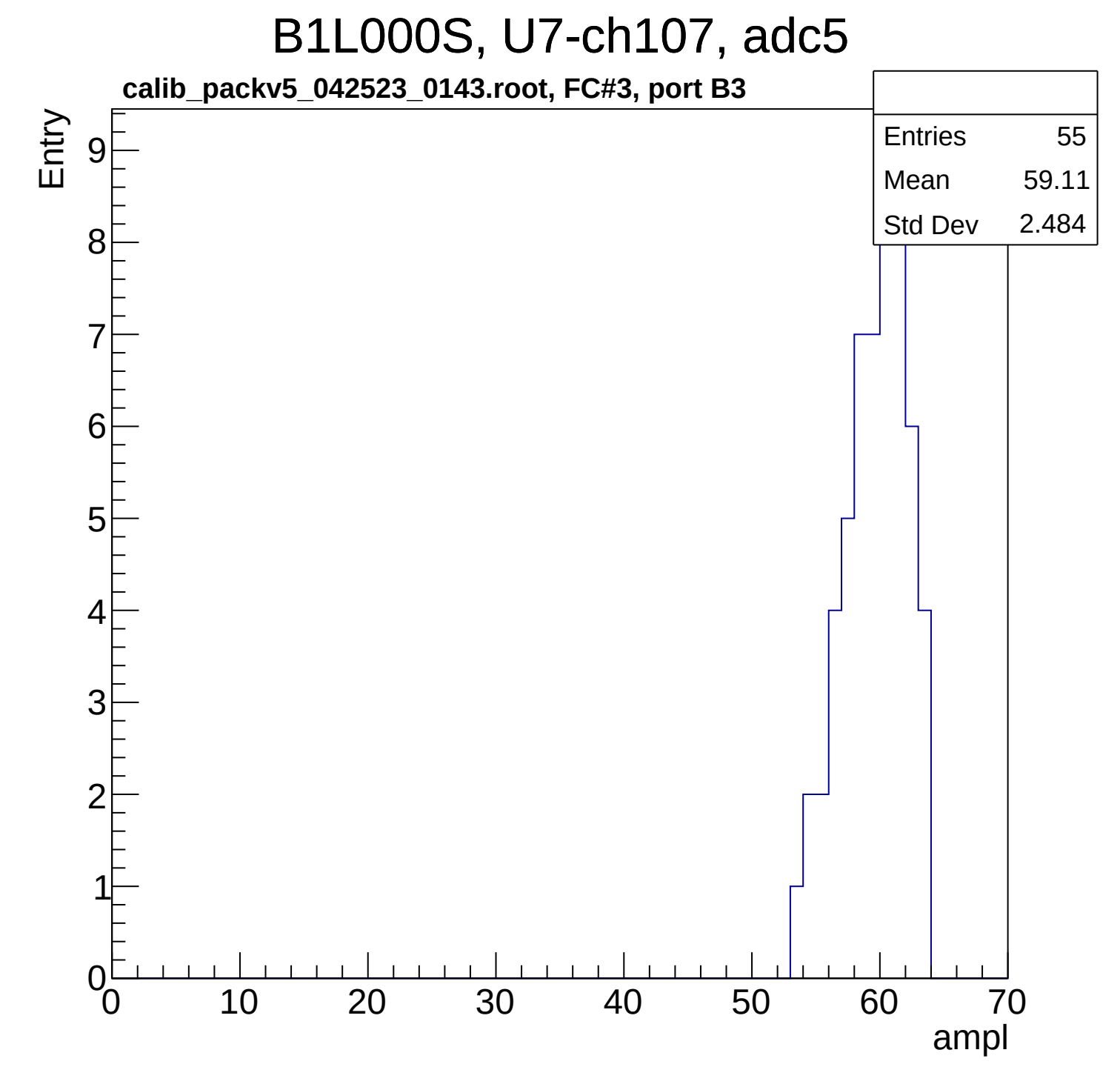
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch107, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 59.11
Std Dev 2.484
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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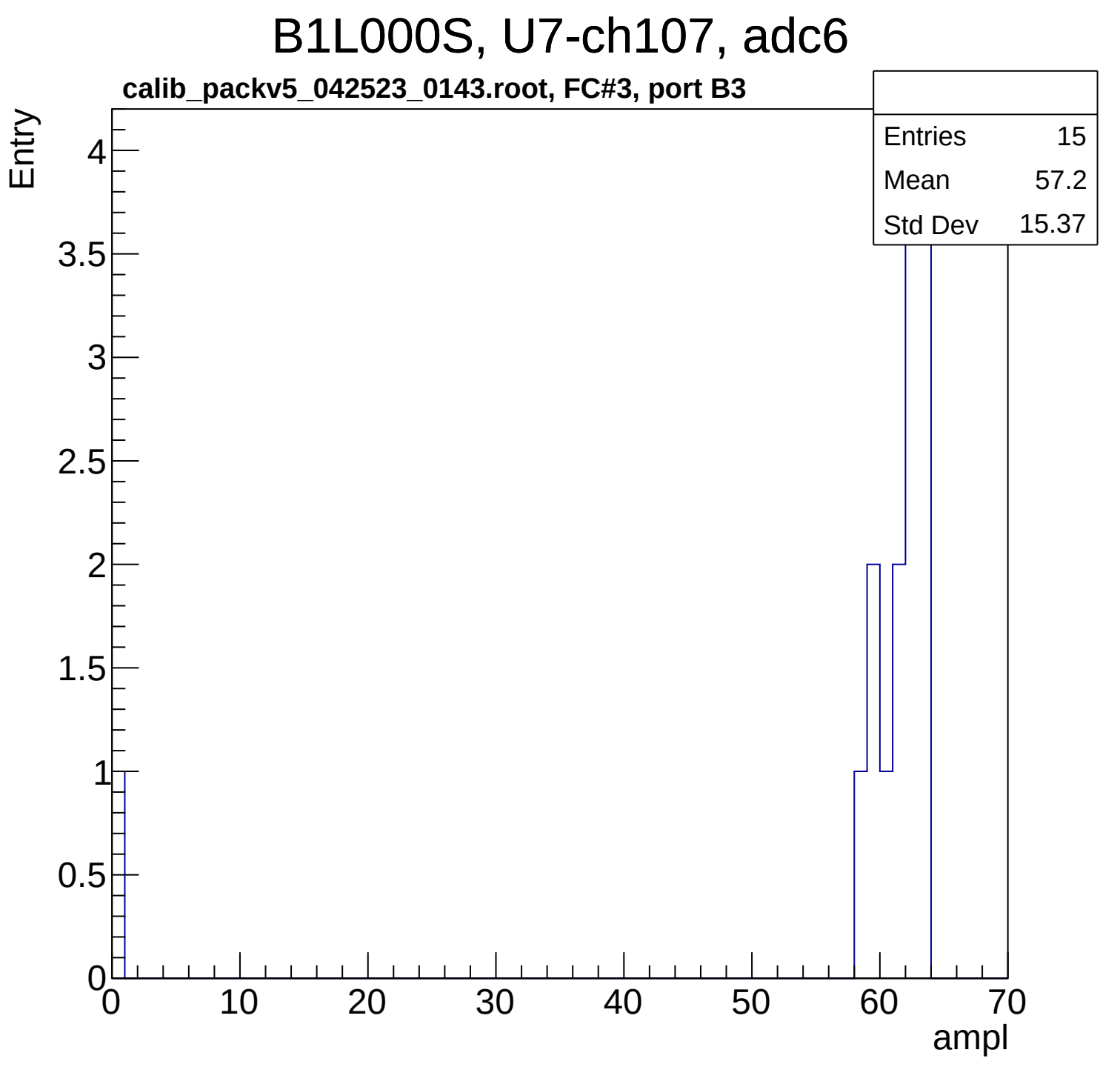
40
50
60
70


B1L000S, U7-ch107, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 15
Mean 57.2
Std Dev 15.37
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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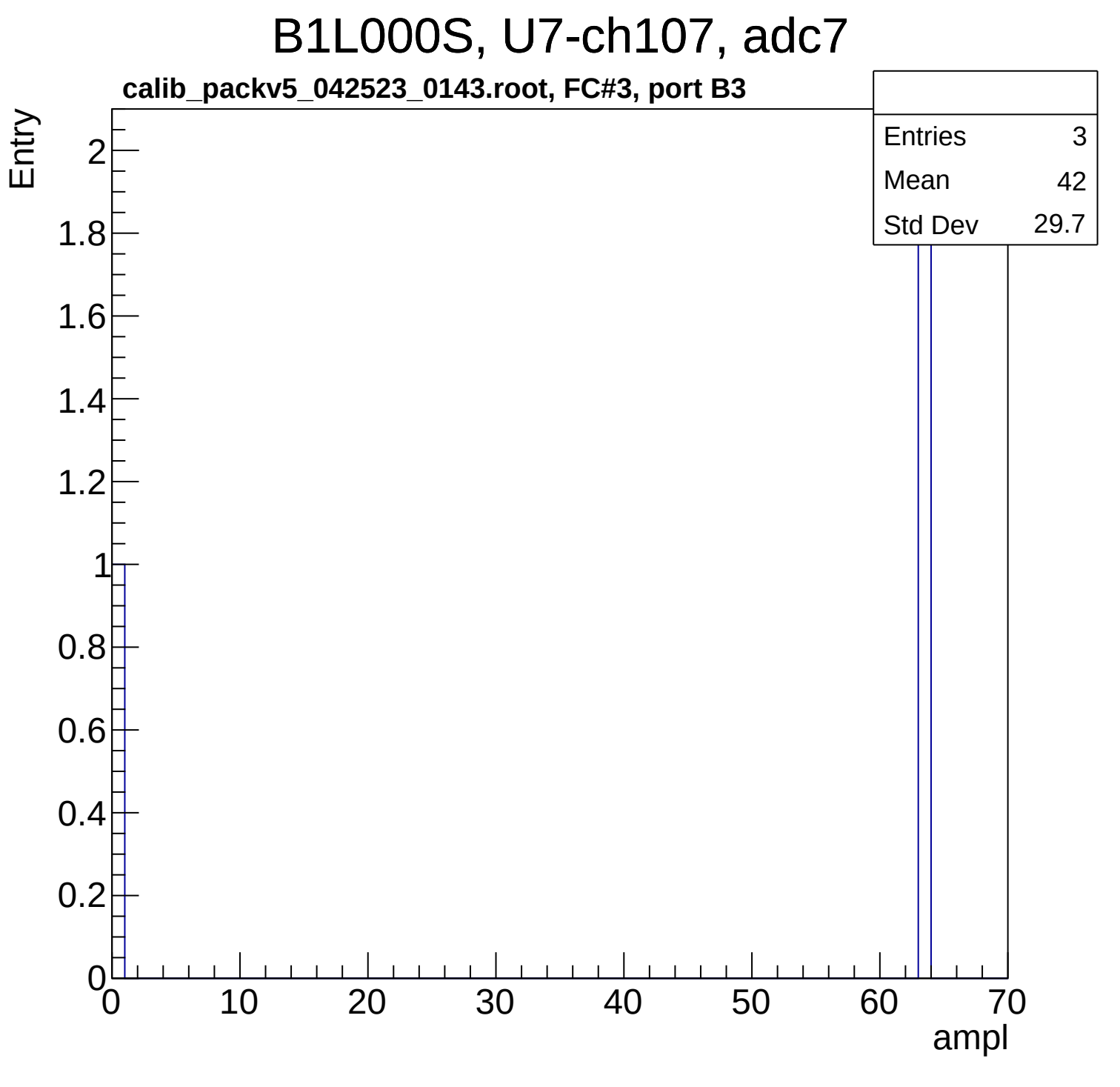
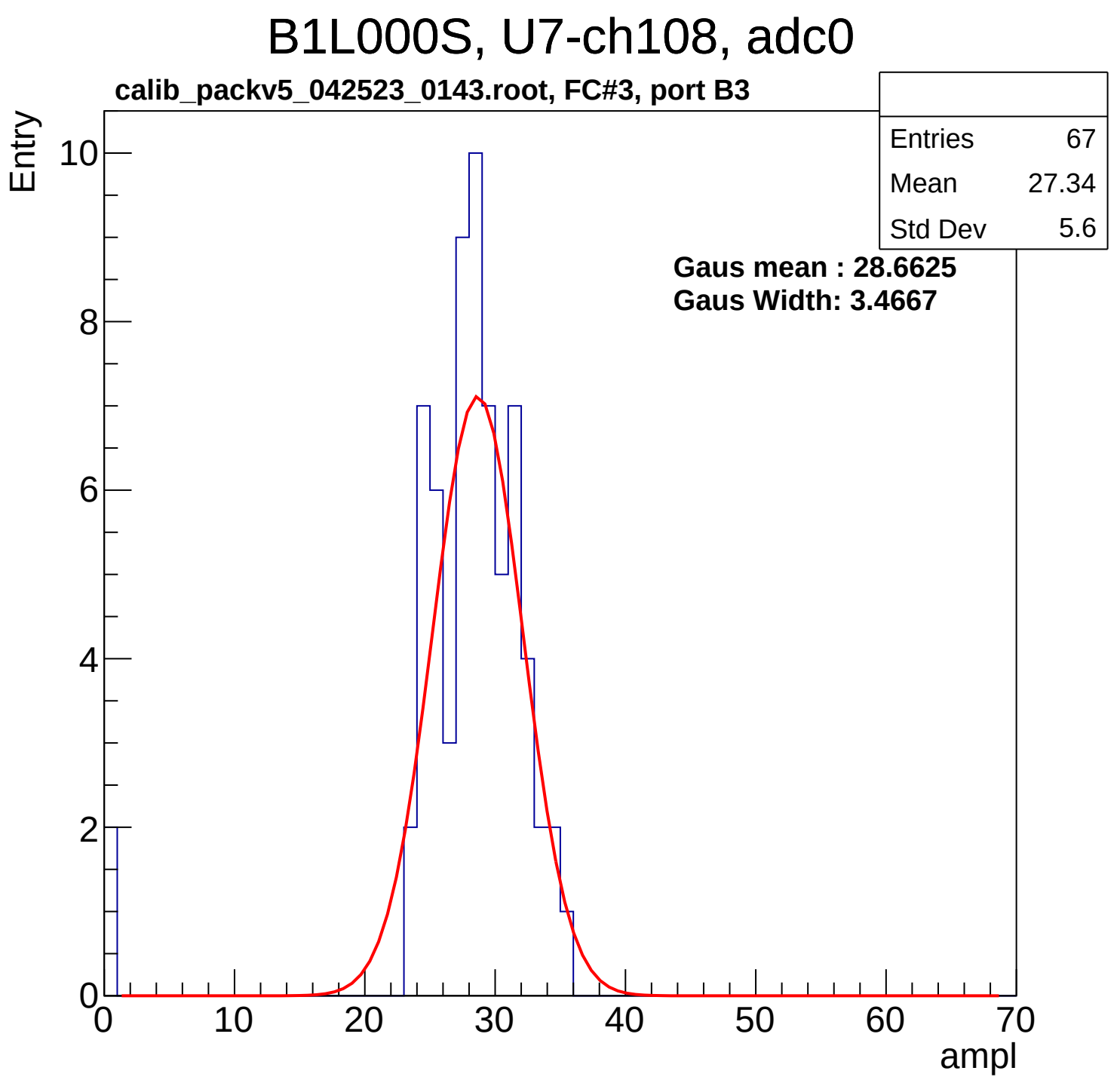

B1L000S, U7-ch107, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch108, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 27.34
Std Dev 5.6
Gaus mean : 28.6625
Gaus Width: 3.4667
Entry
ampl
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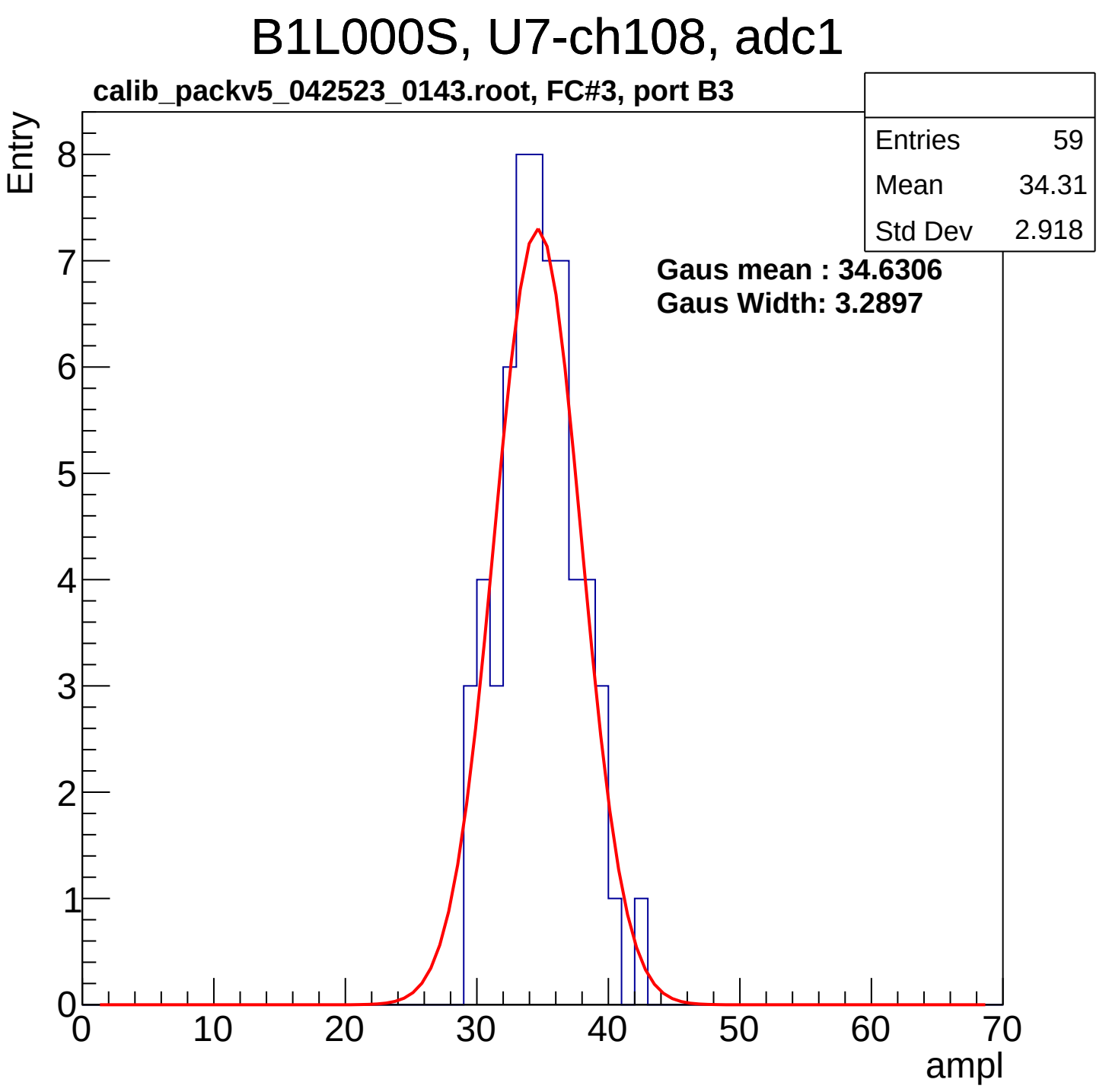
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch108, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 34.31
Std Dev 2.918
Gaus mean : 34.6306
Gaus Width: 3.2897
Entry
ampl
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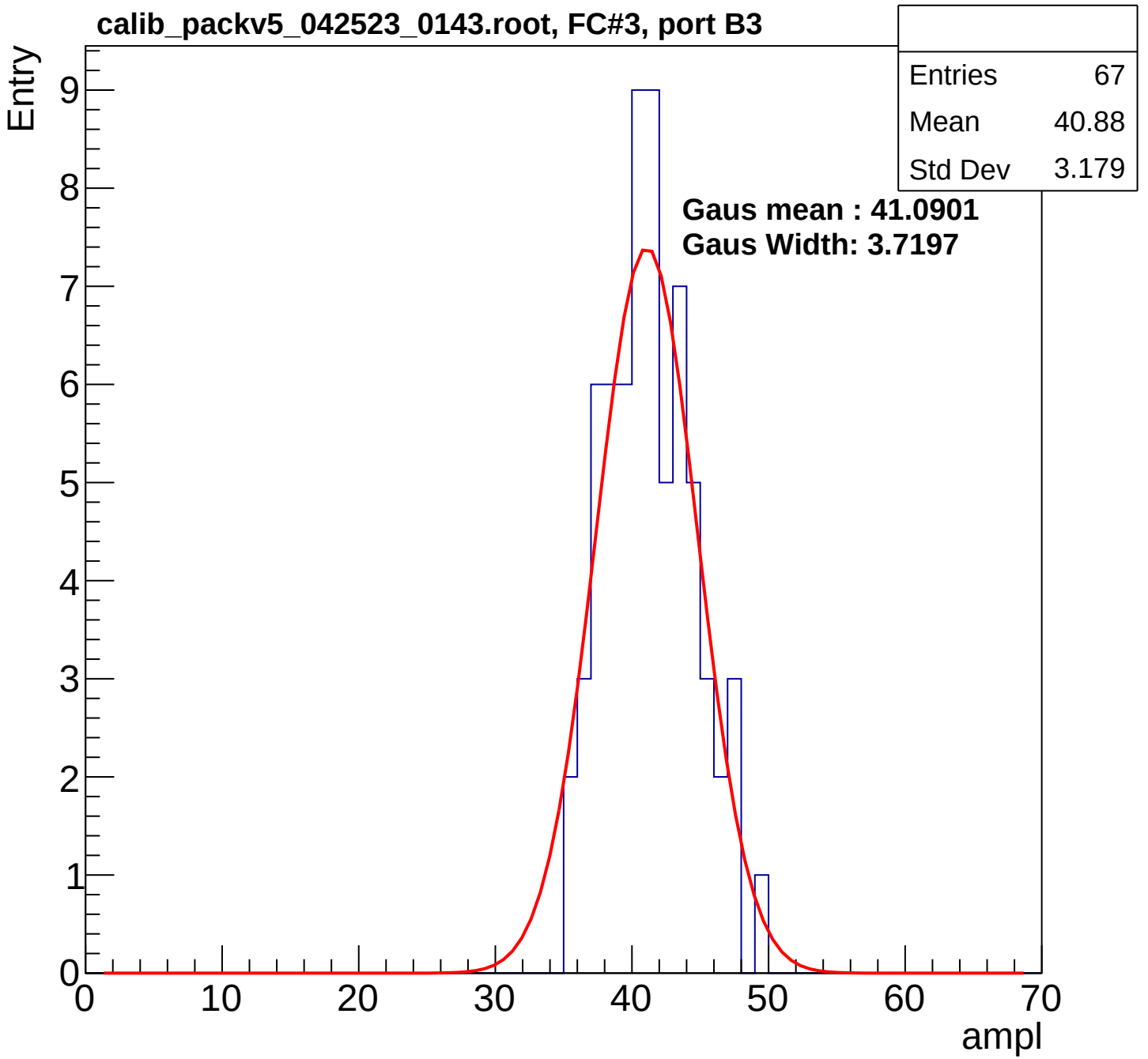
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch108, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.88
Std Dev 3.179
Gaus mean : 41.0901
Gaus Width: 3.7197
Entry
ampl
0
1
2
3
4
5
6
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60
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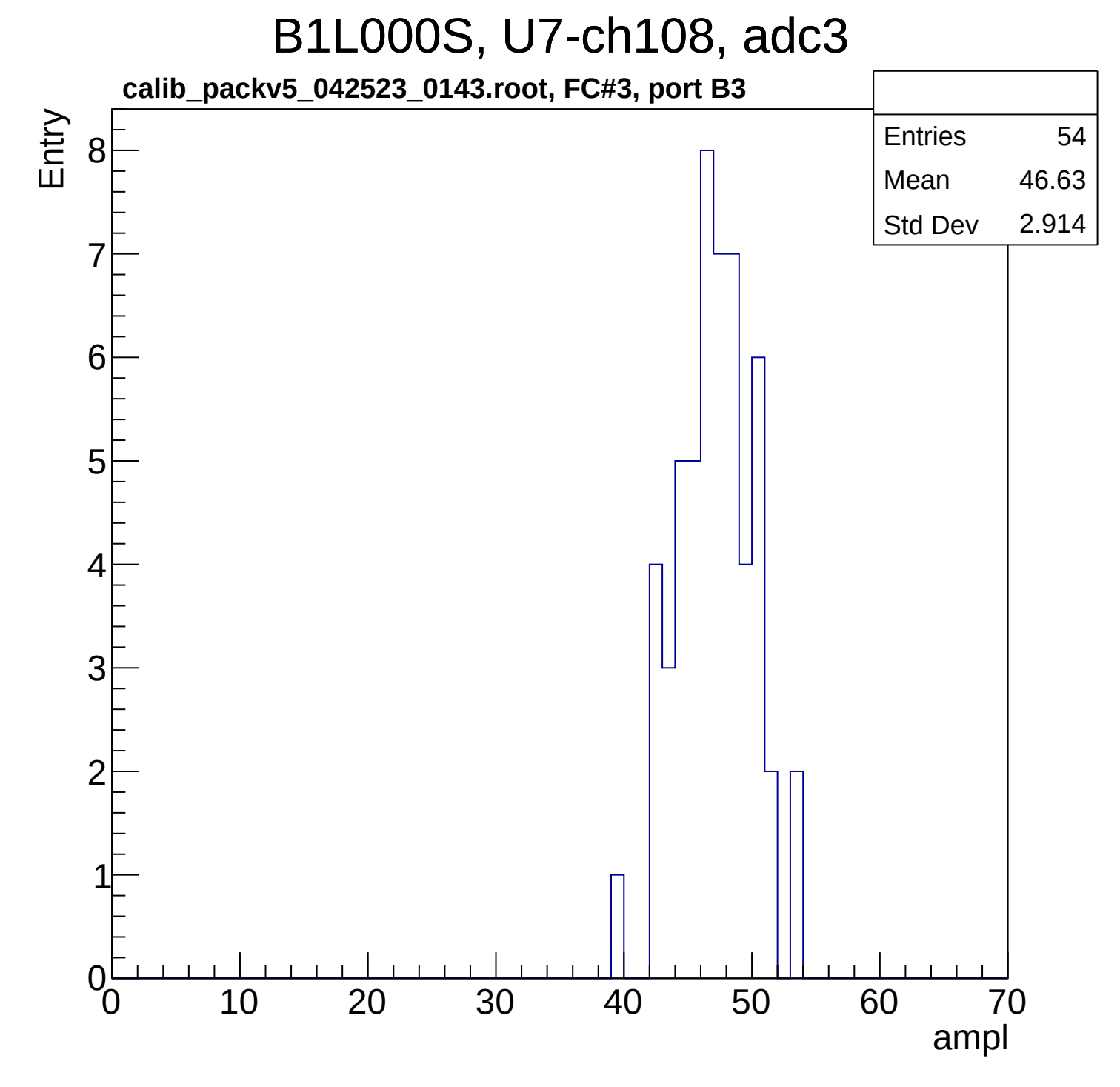
B1L000S, U7-ch108, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 46.63
Std Dev 2.914
Entry
ampl
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1
2
3
4
5
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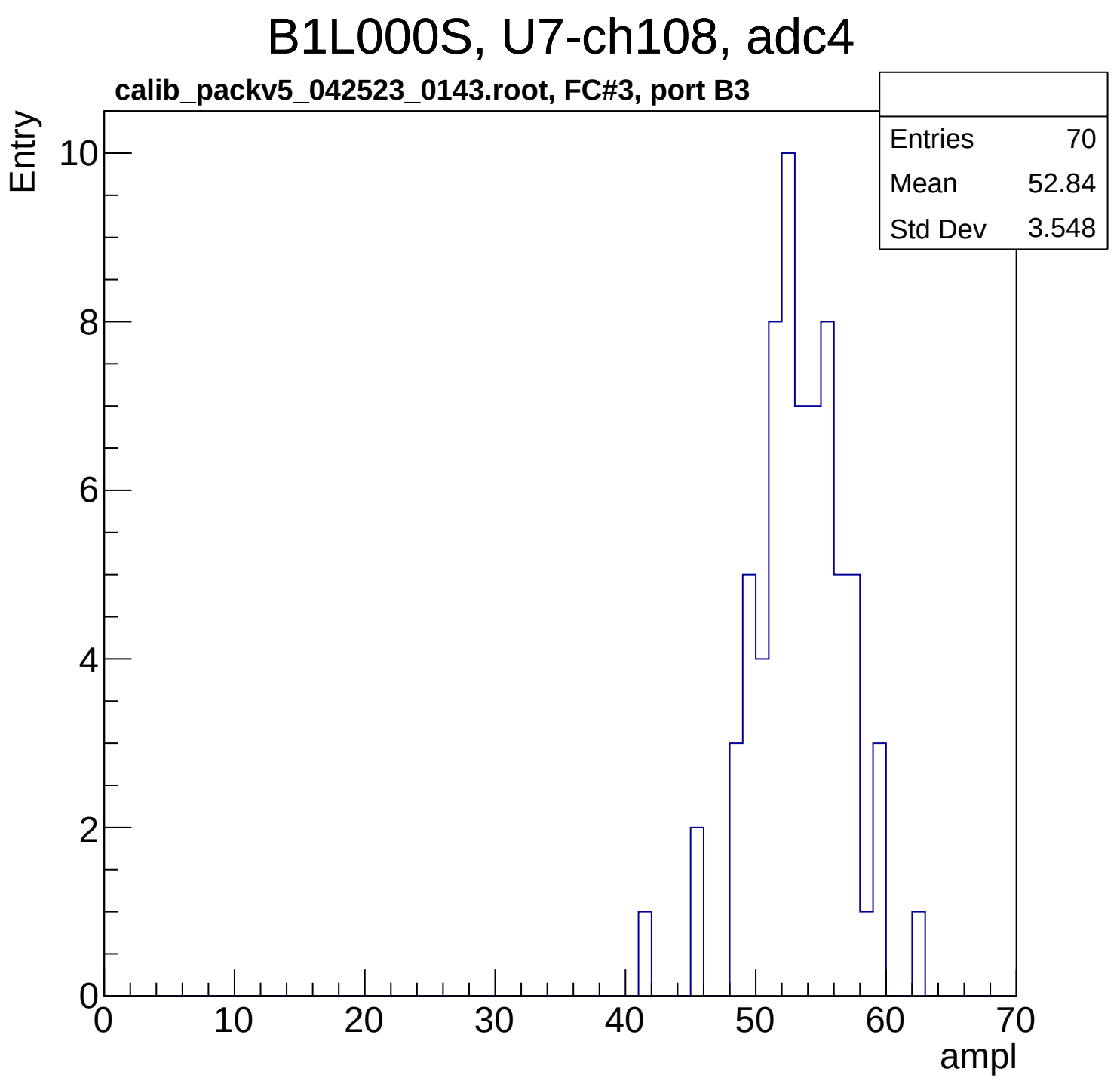
B1L000S, U7-ch108, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 52.84
Std Dev 3.548
Entry
0
2
4
6
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10
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10
20
30
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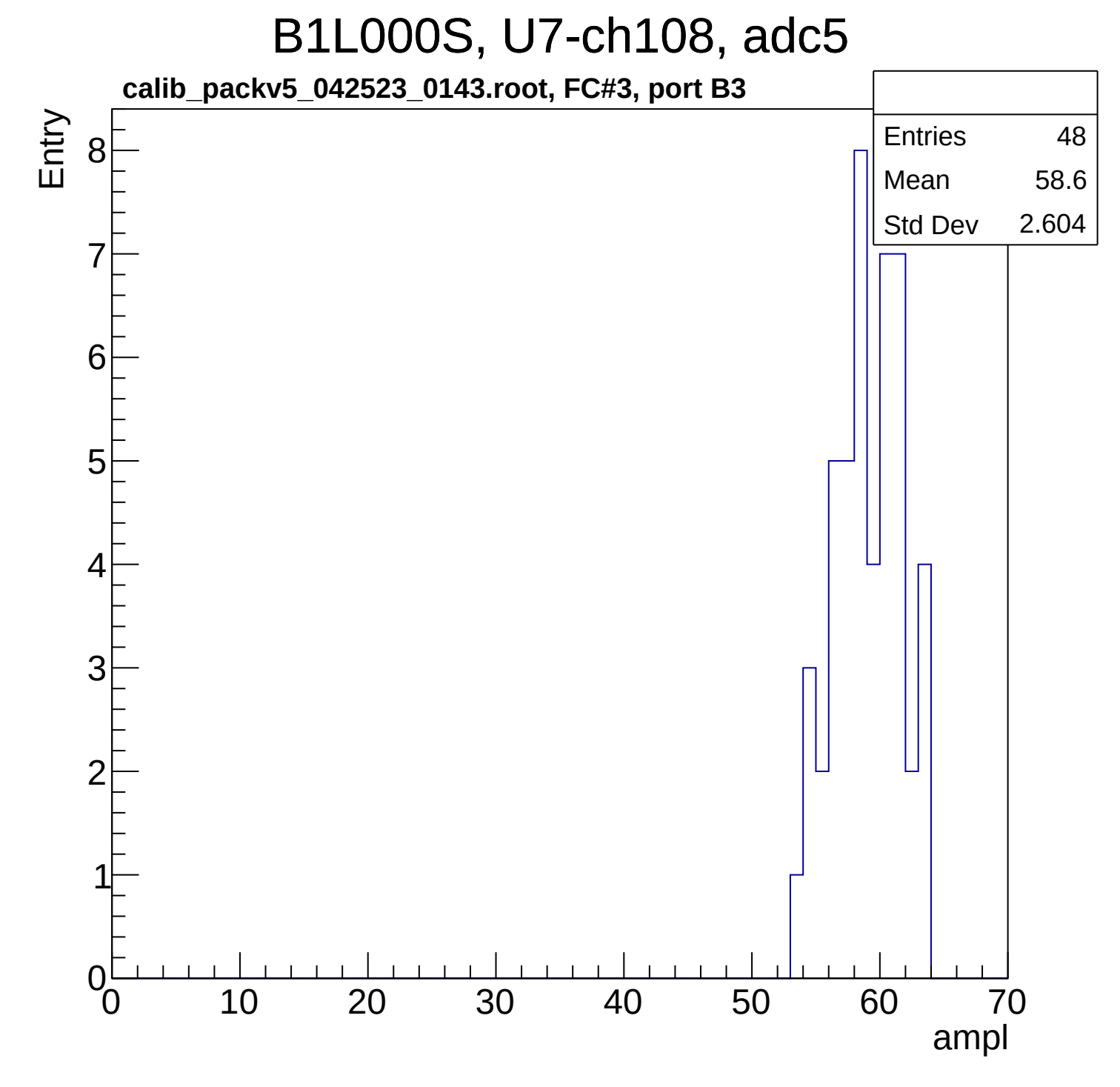
50
60
70
ampl


B1L000S, U7-ch108, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 58.6
Std Dev 2.604
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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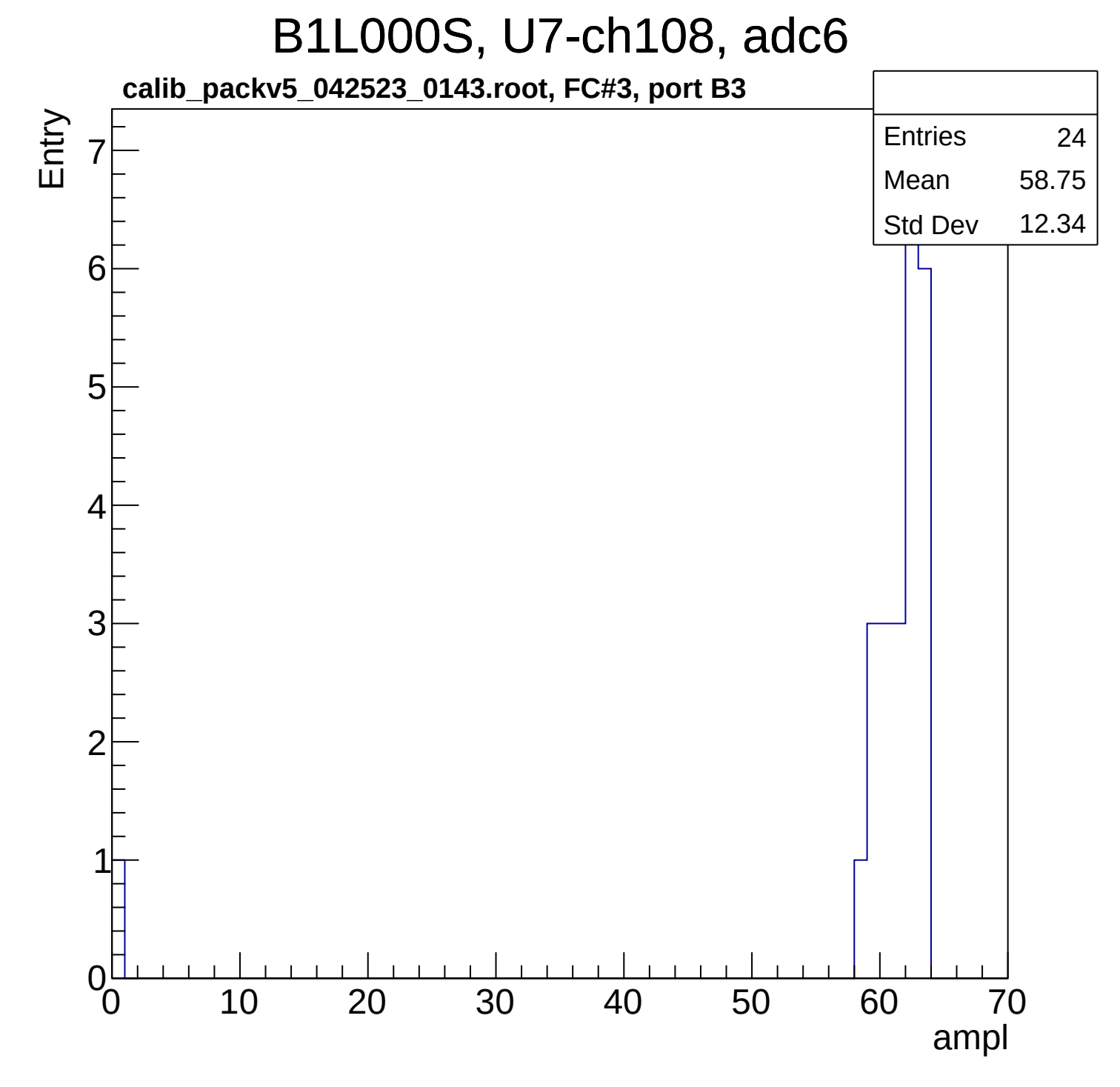
B1L000S, U7-ch108, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 24
Mean 58.75
Std Dev 12.34
Entry
ampl
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1
2
3
4
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0
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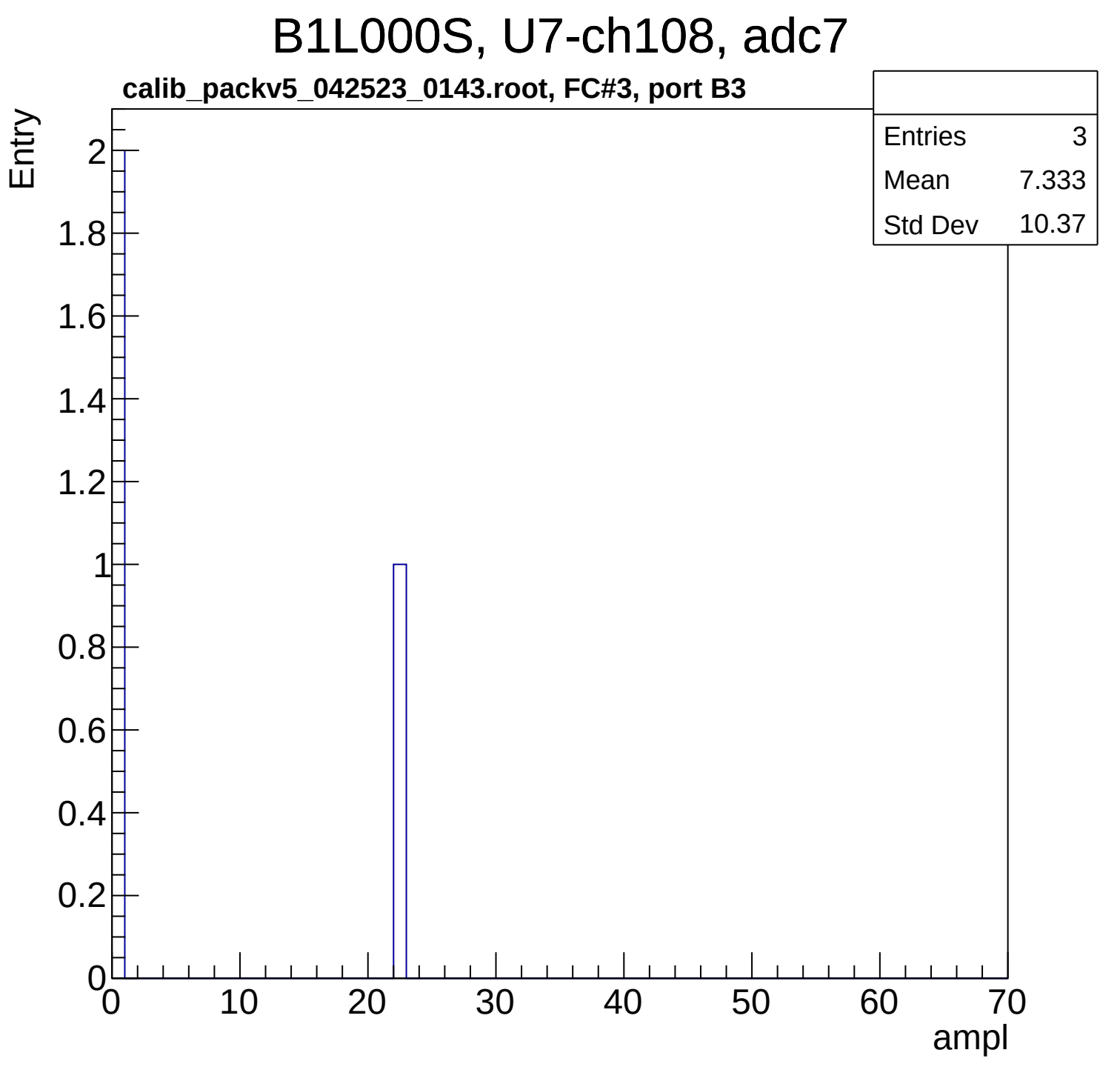
B1L000S, U7-ch108, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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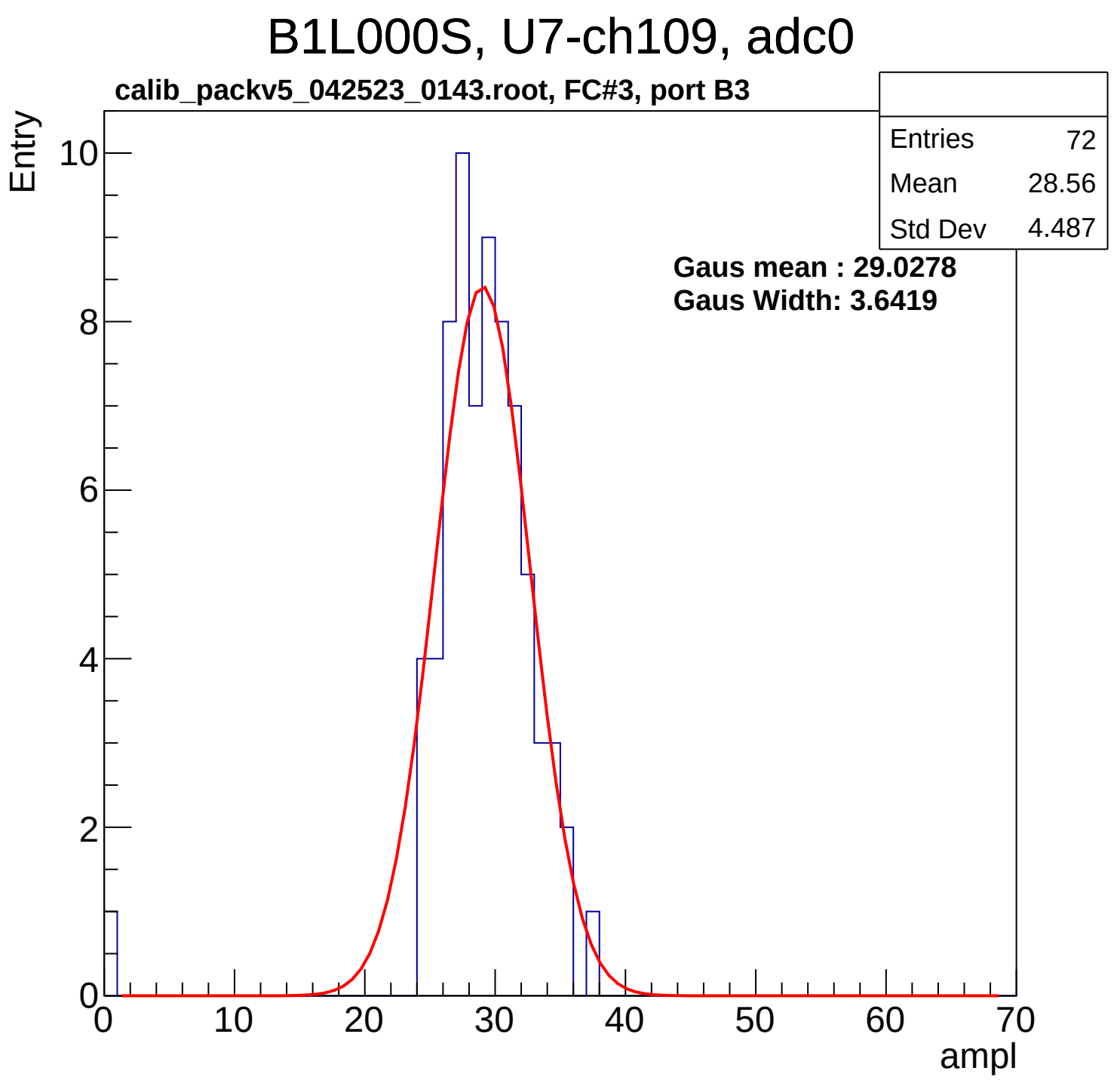
B1L000S, U7-ch109, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 28.56
Std Dev 4.487
Gaus mean : 29.0278
Gaus Width: 3.6419
Entry
ampl
0
2
4
6
8
10
0
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20
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40
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60
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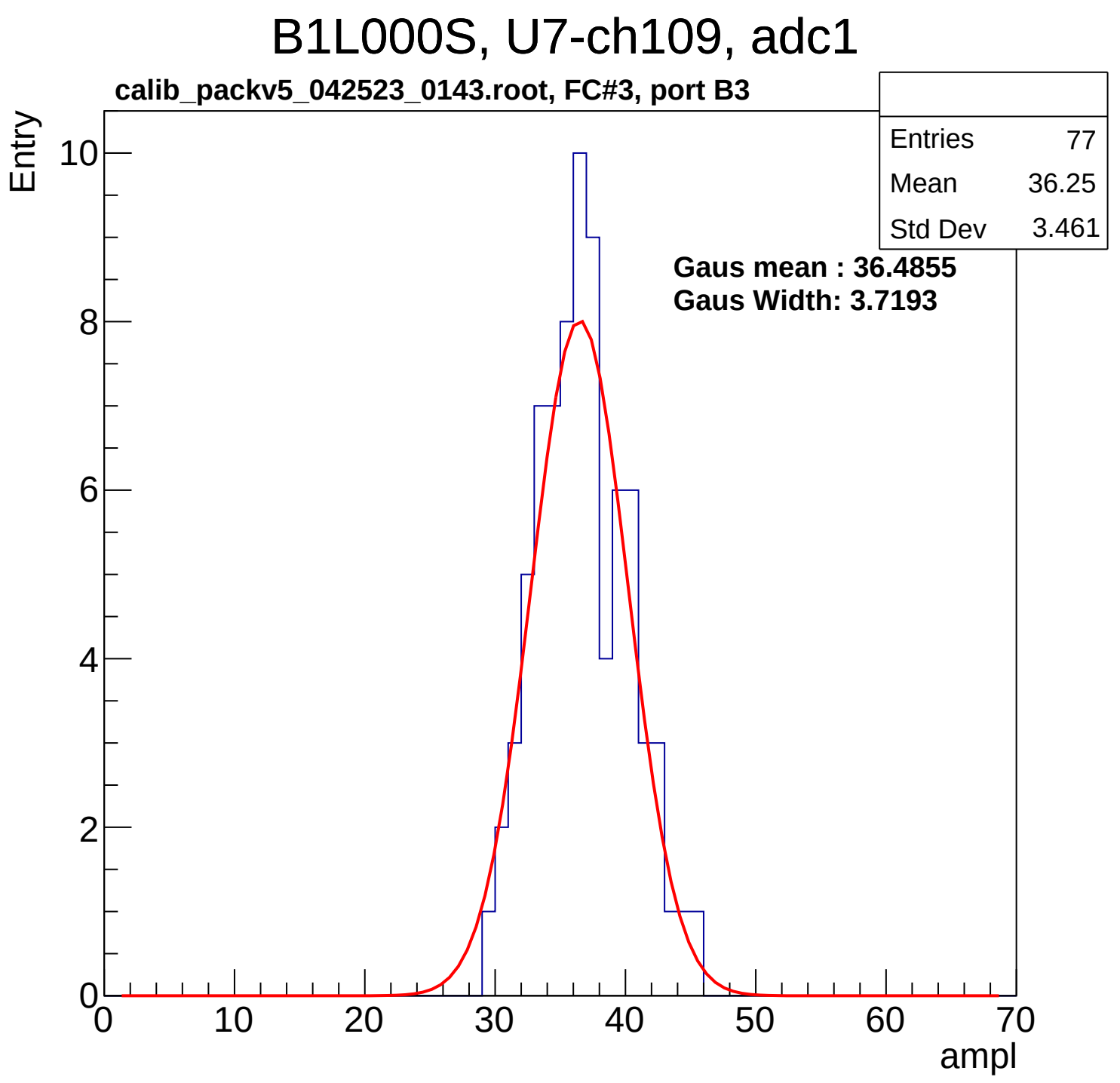
B1L000S, U7-ch109, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 36.25
Std Dev 3.461
Gaus mean : 36.4855
Gaus Width: 3.7193
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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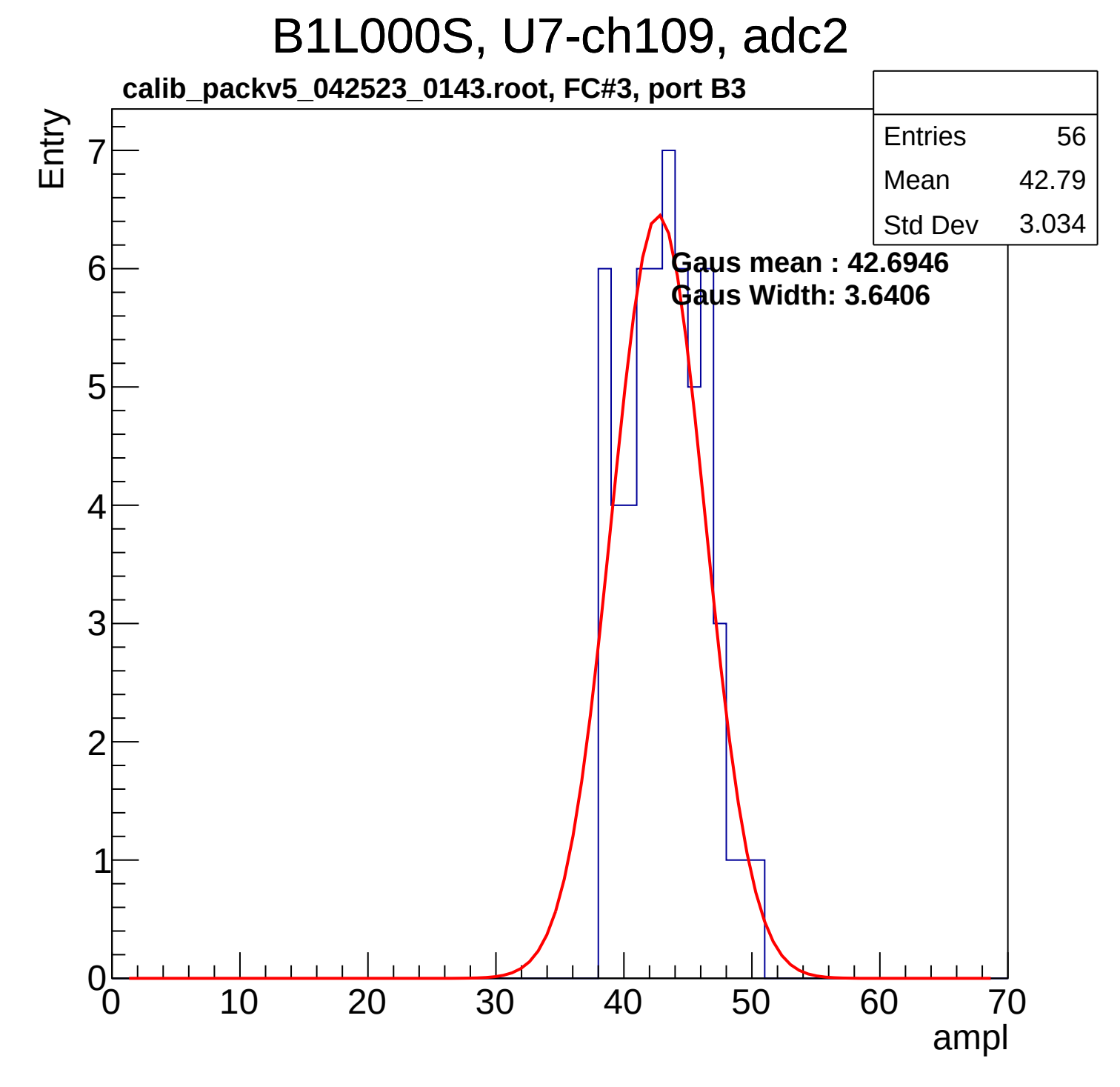
B1L000S, U7-ch109, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 42.79
Std Dev 3.034
Gaus mean : 42.6946
Gaus Width: 3.6406
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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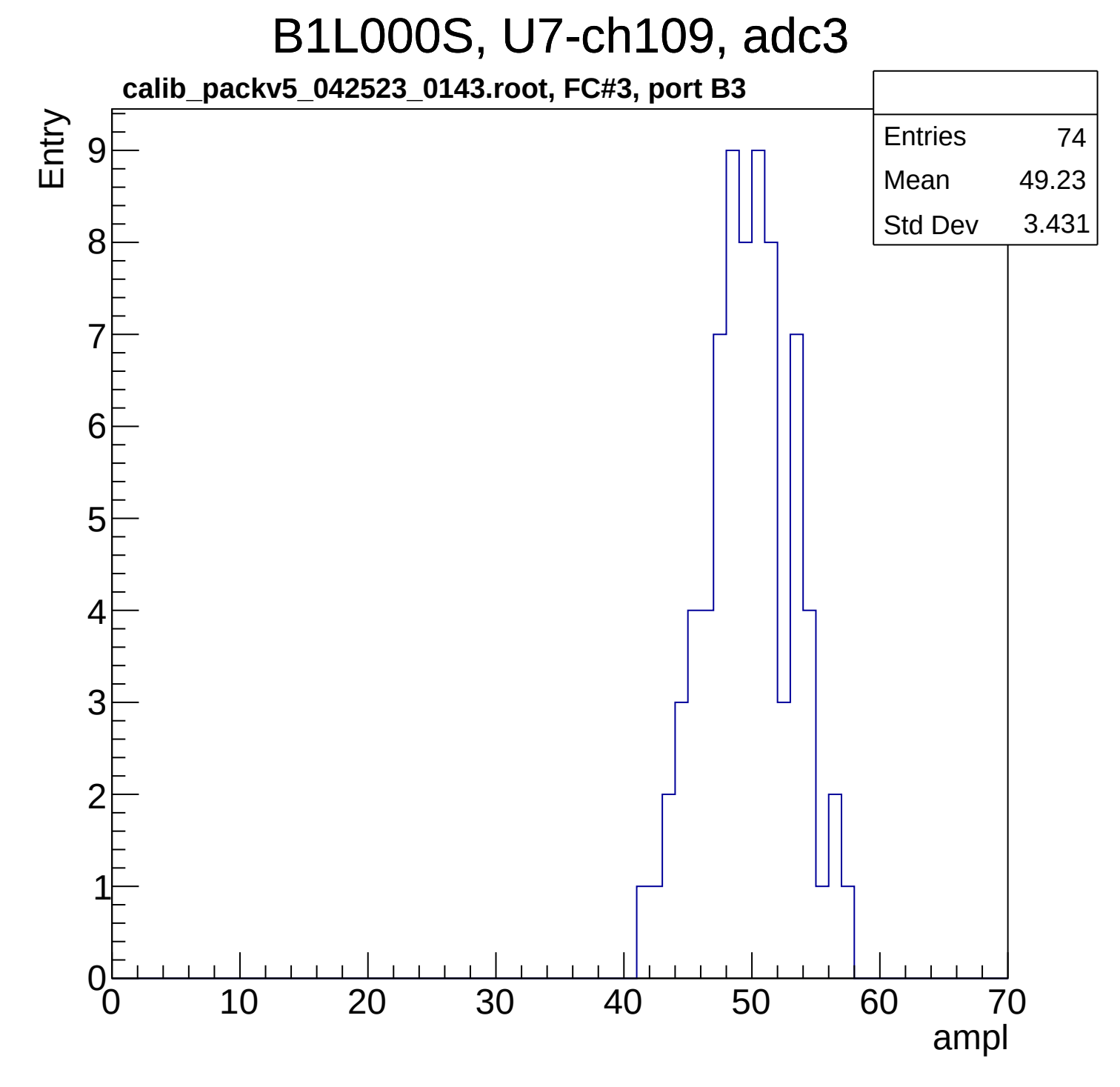
B1L000S, U7-ch109, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 49.23
Std Dev 3.431
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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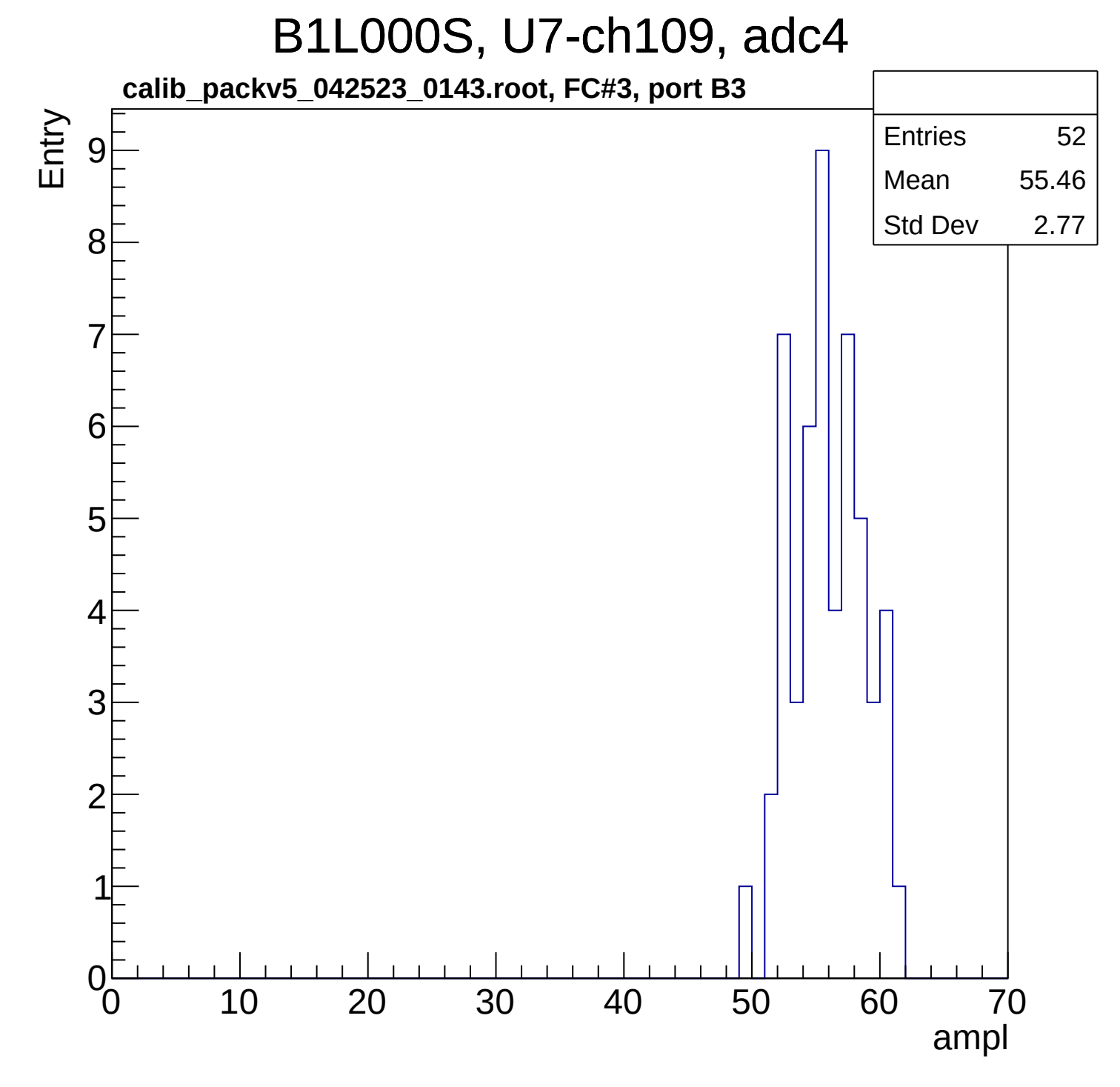
B1L000S, U7-ch109, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 55.46
Std Dev 2.77
Entry
ampl
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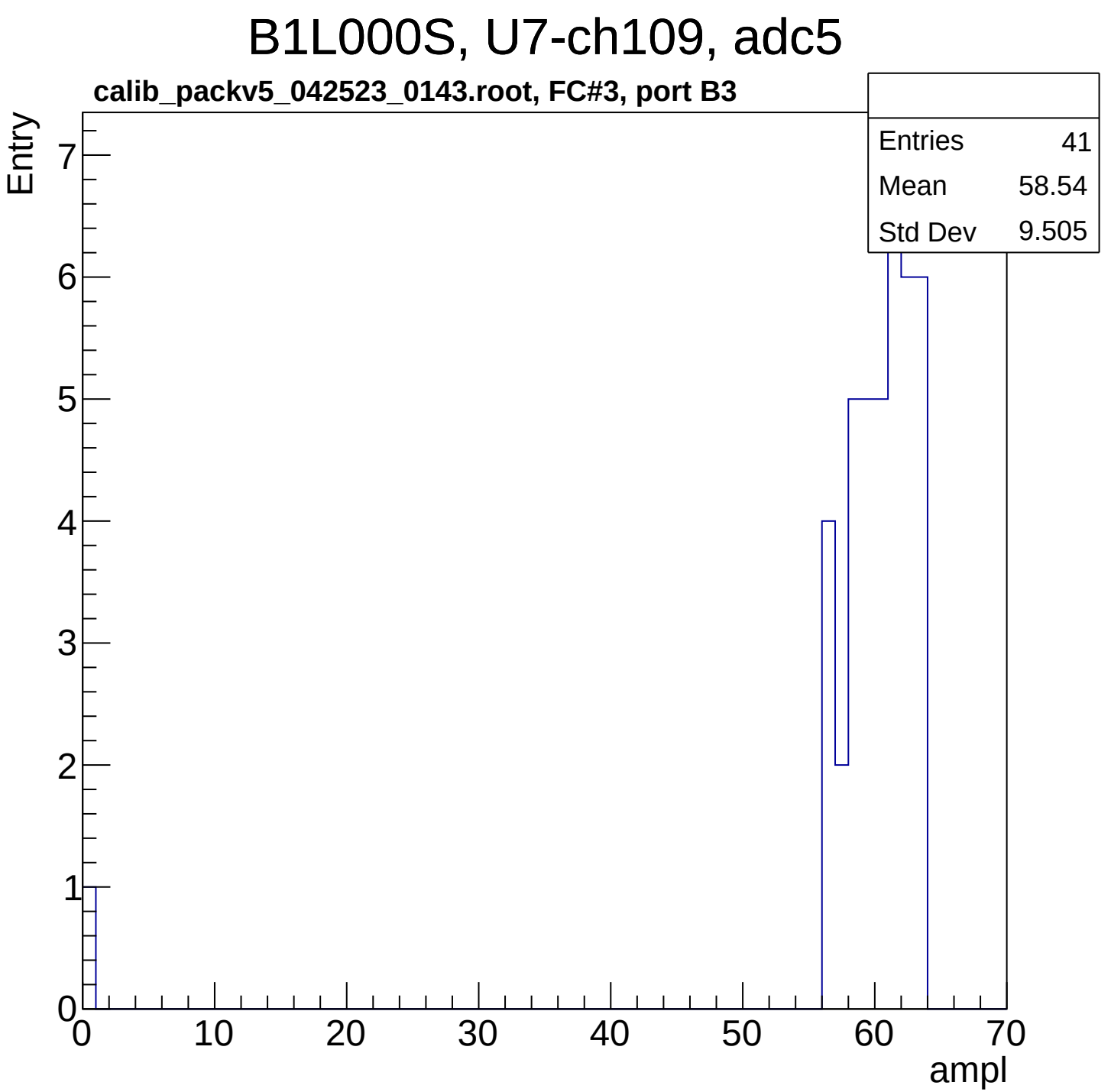
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch109, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 41
Mean 58.54
Std Dev 9.505
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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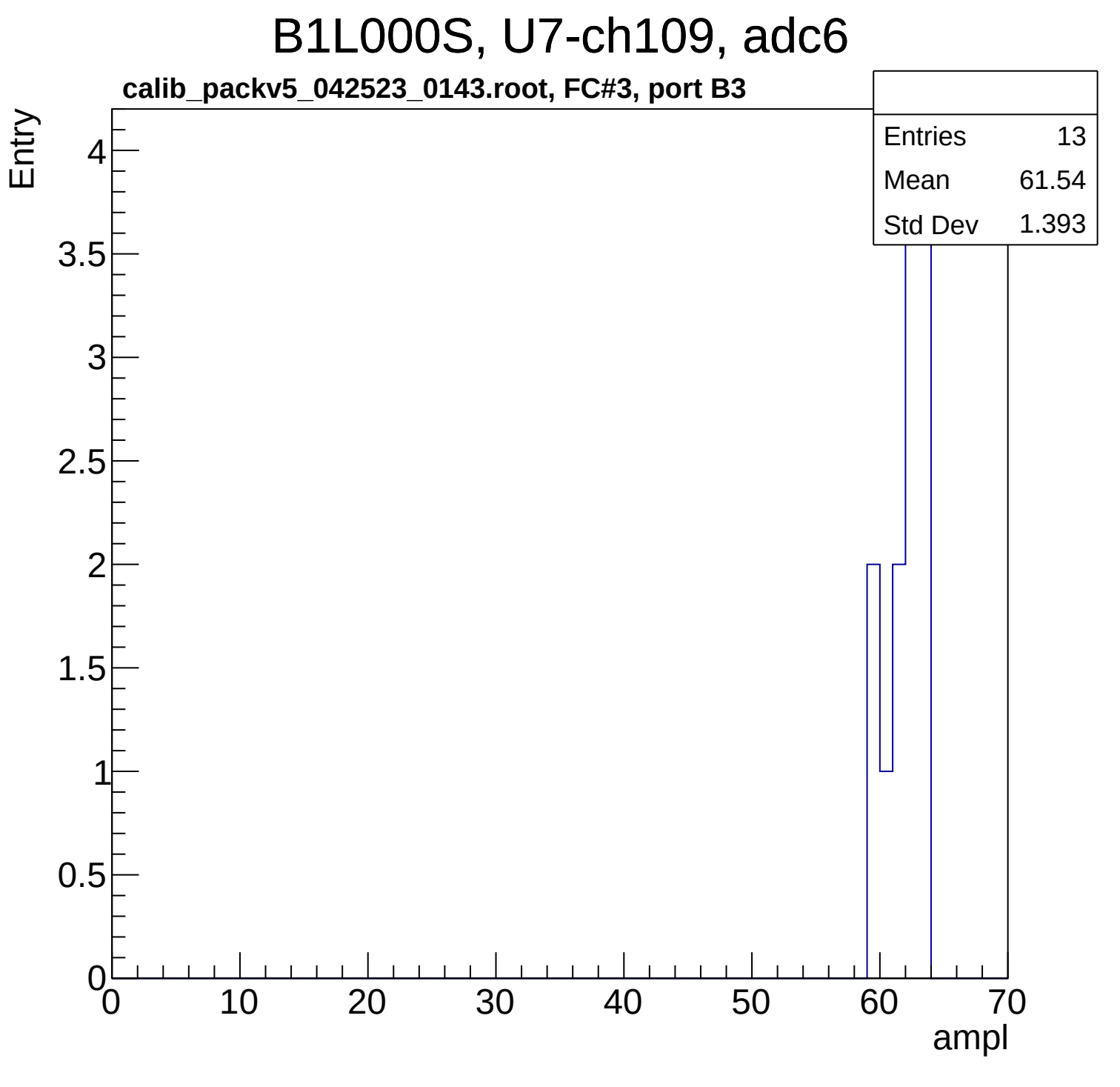
B1L000S, U7-ch109, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 61.54
Std Dev 1.393
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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20
30
40
50
60
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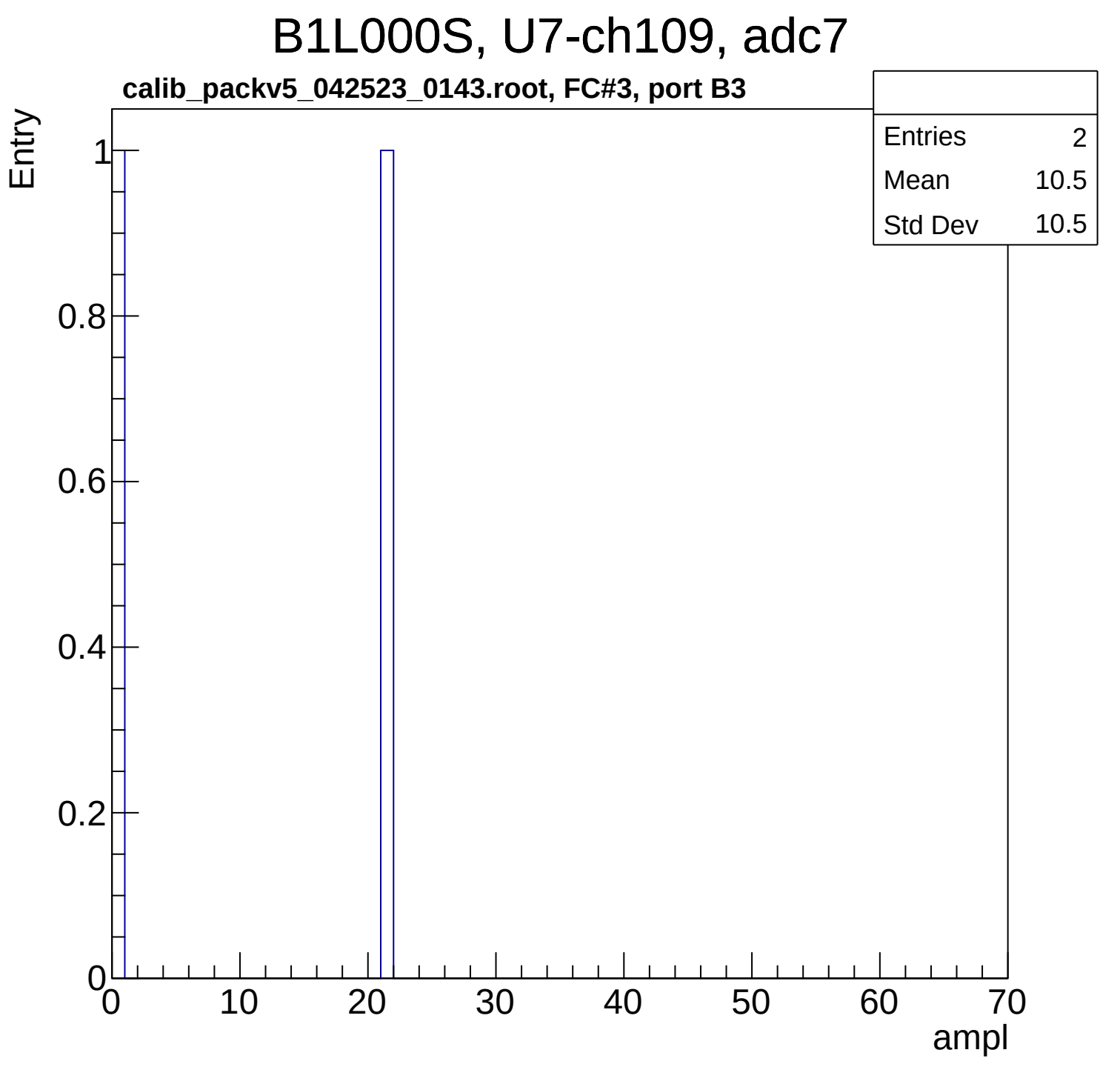
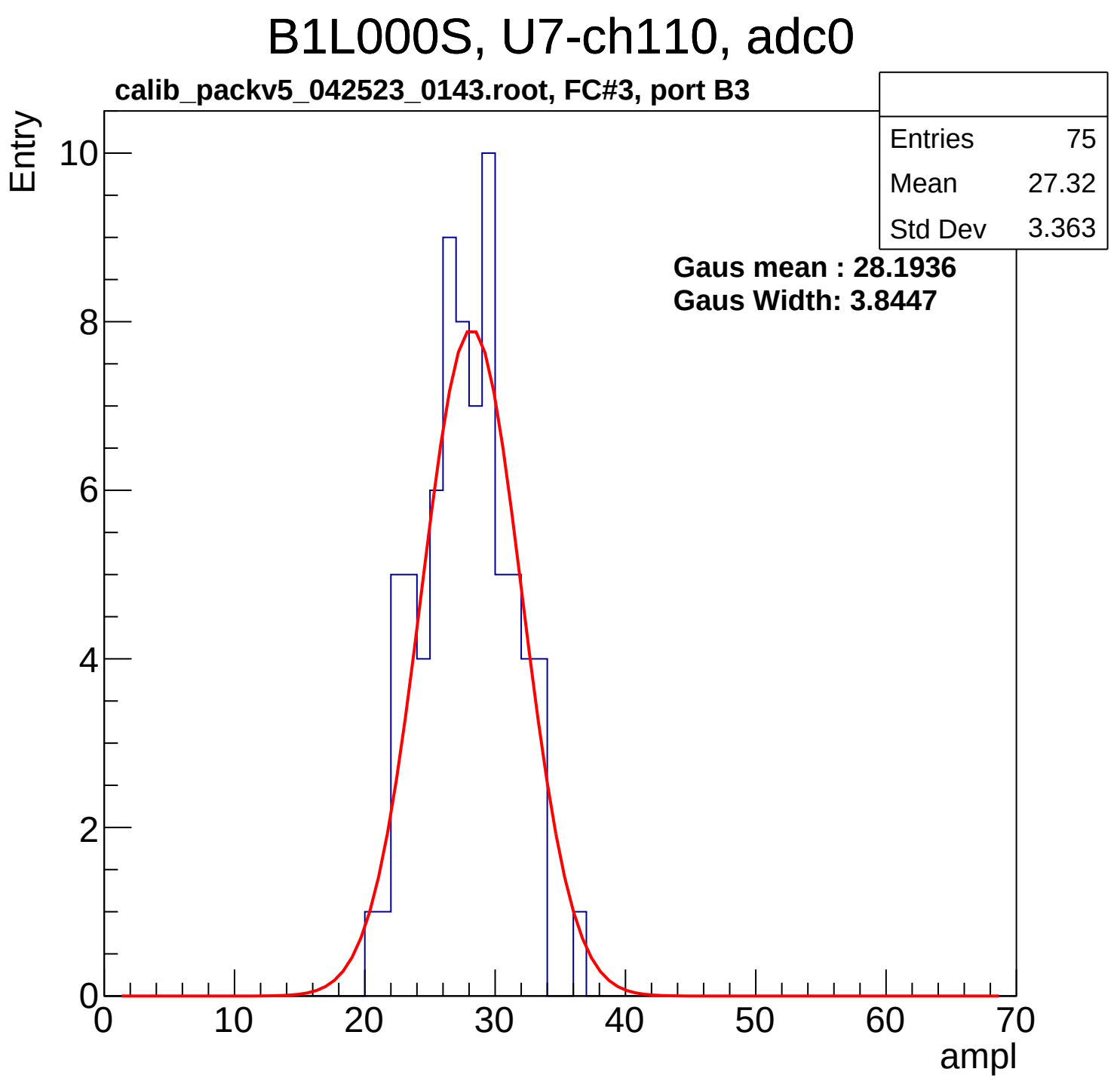
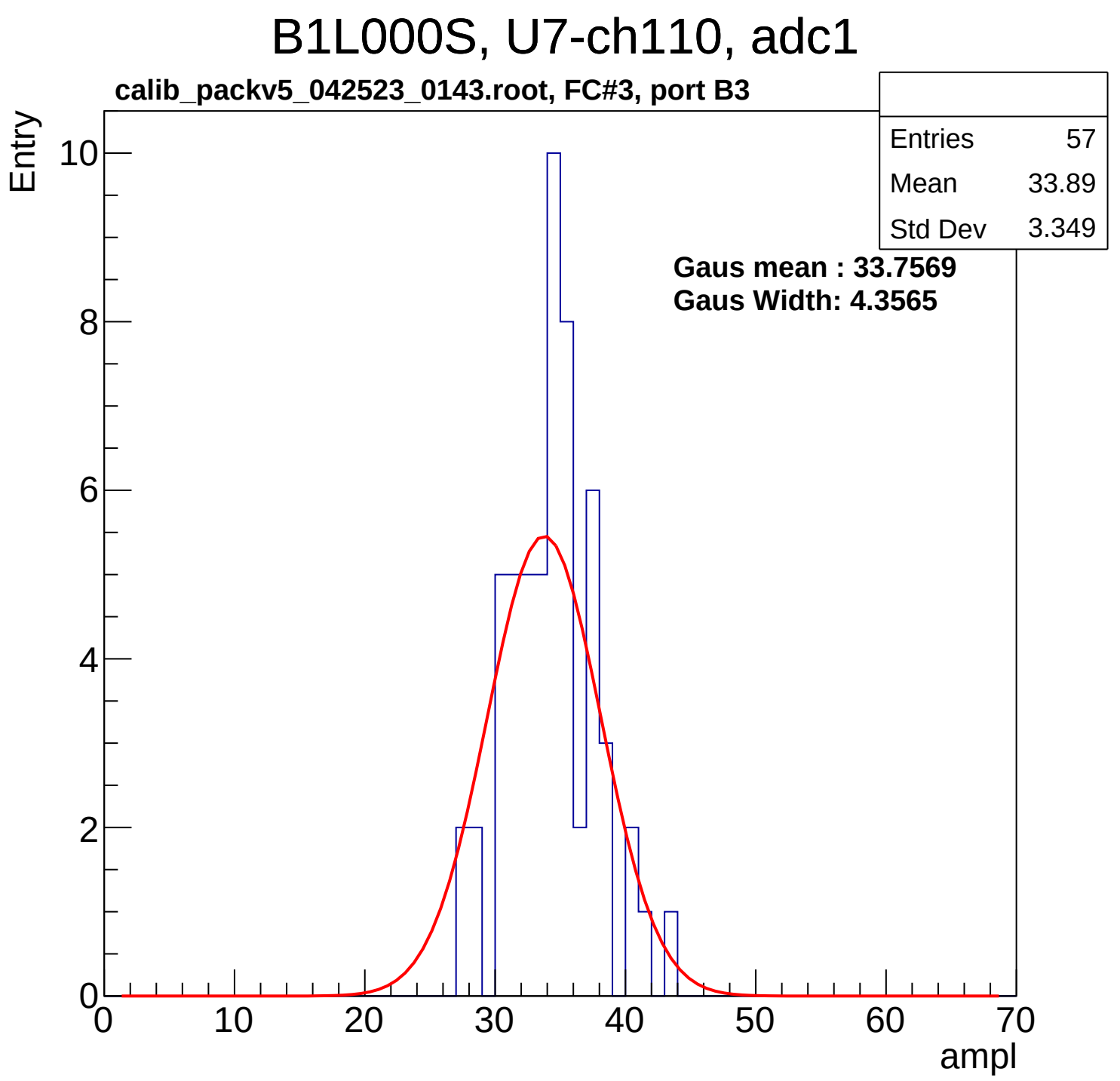

B1L000S, U7-ch109, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch110, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 27.32
Std Dev 3.363
Gaus mean : 28.1936
Gaus Width: 3.8447
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch110, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 33.89
Std Dev 3.349
Gaus mean : 33.7569
Gaus Width: 4.3565
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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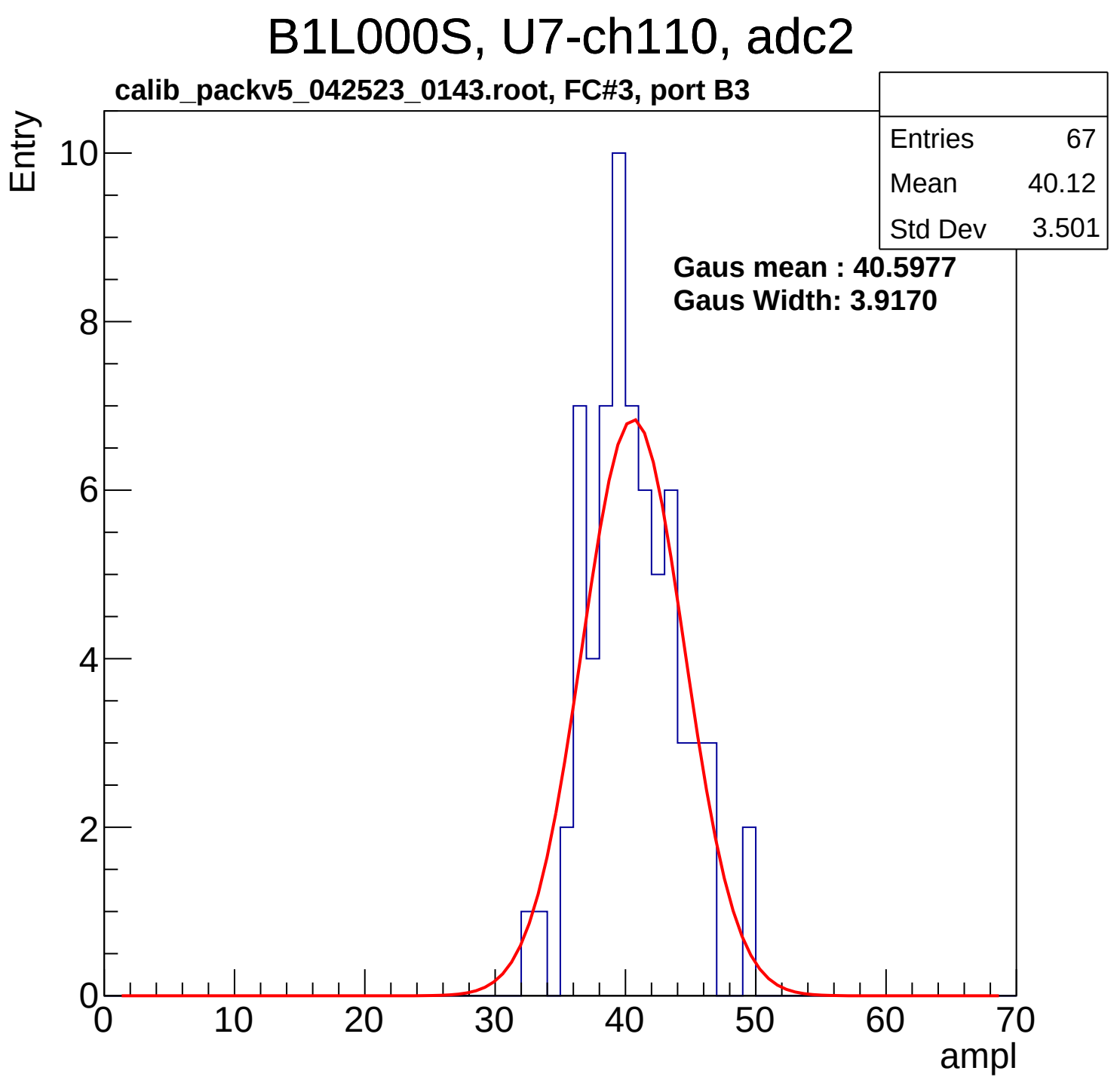
B1L000S, U7-ch110, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 40.12
Std Dev 3.501
Gaus mean : 40.5977
Gaus Width: 3.9170
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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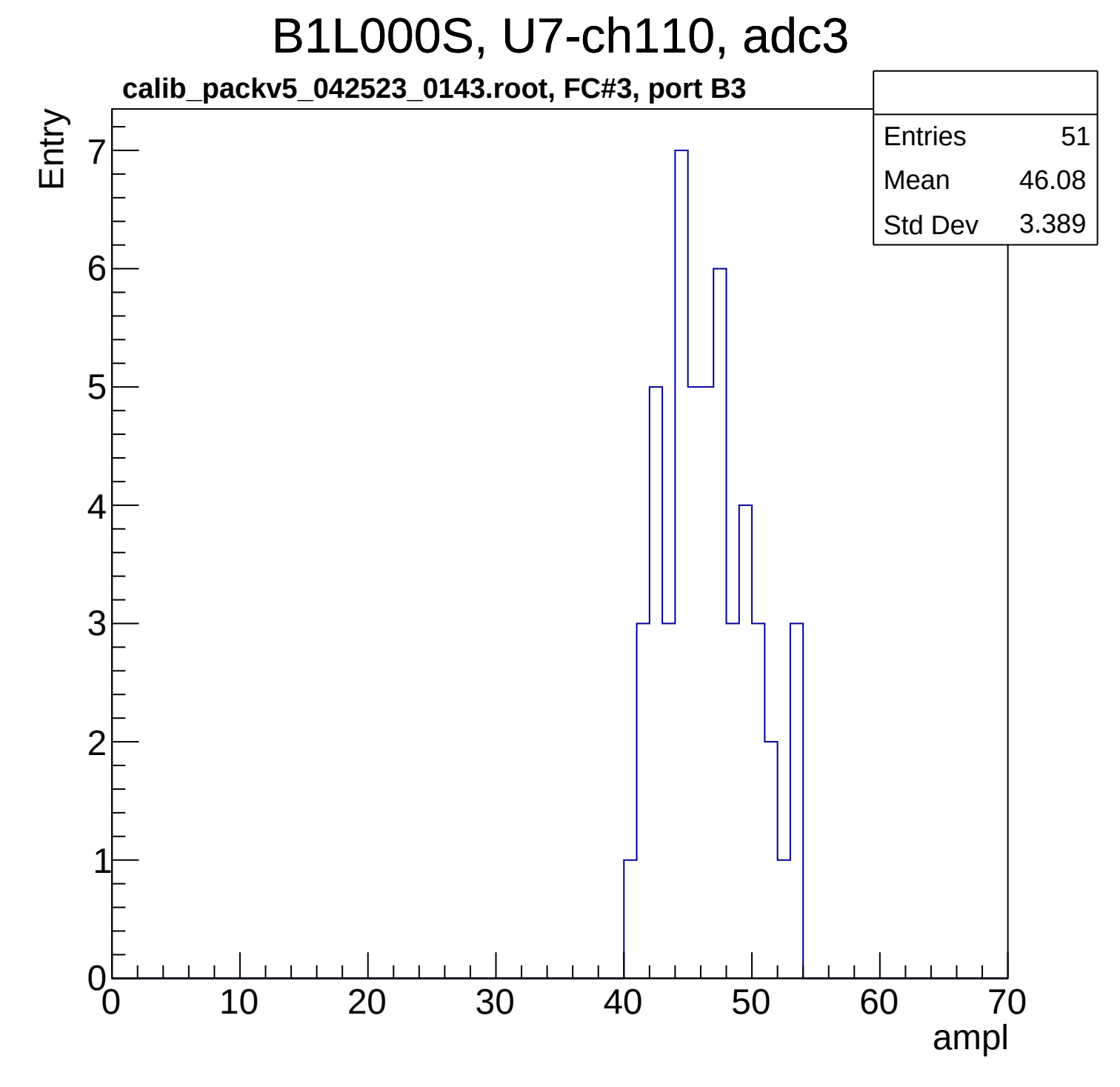
B1L000S, U7-ch110, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 46.08
Std Dev 3.389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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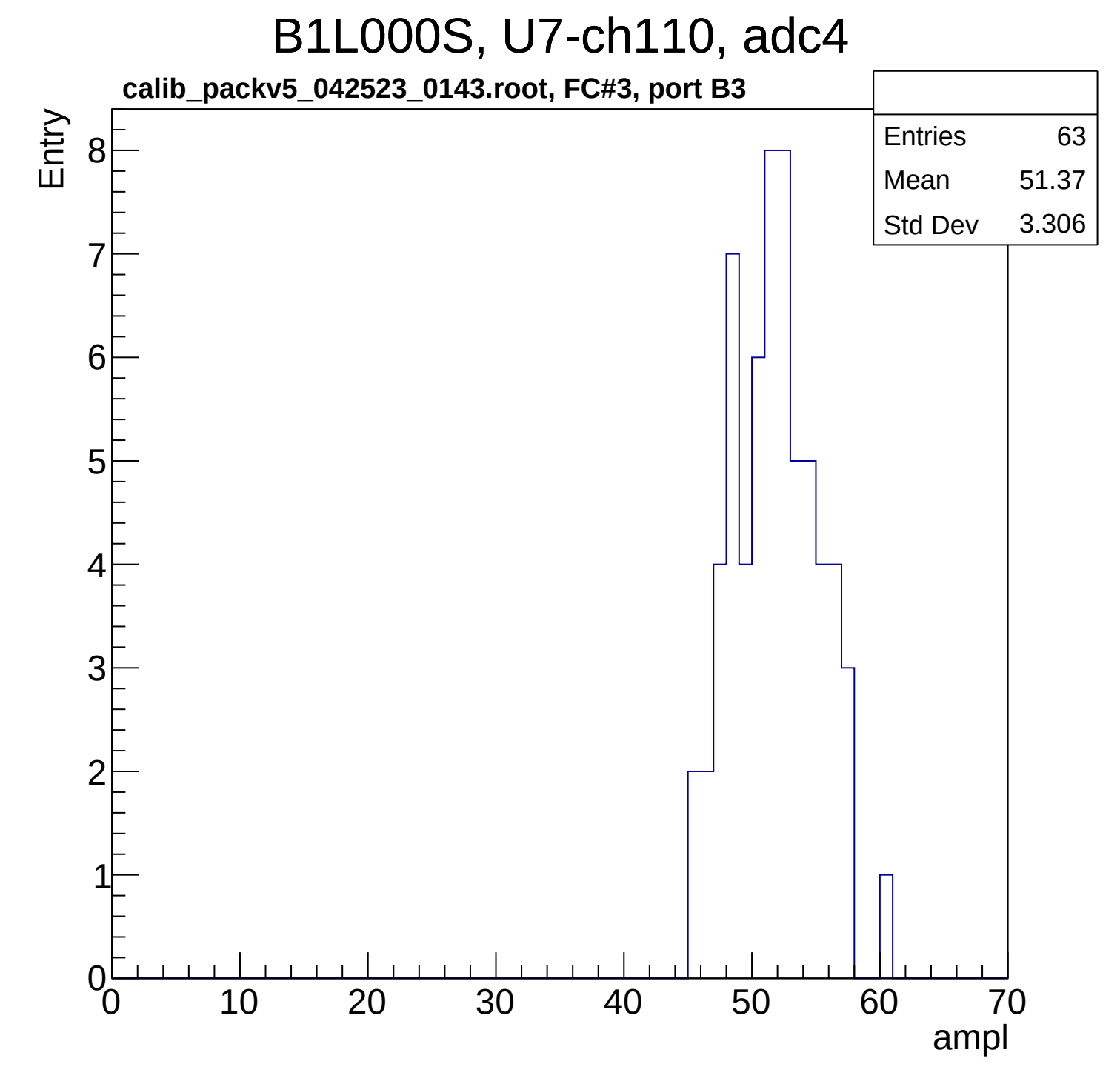
B1L000S, U7-ch110, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 51.37
Std Dev 3.306
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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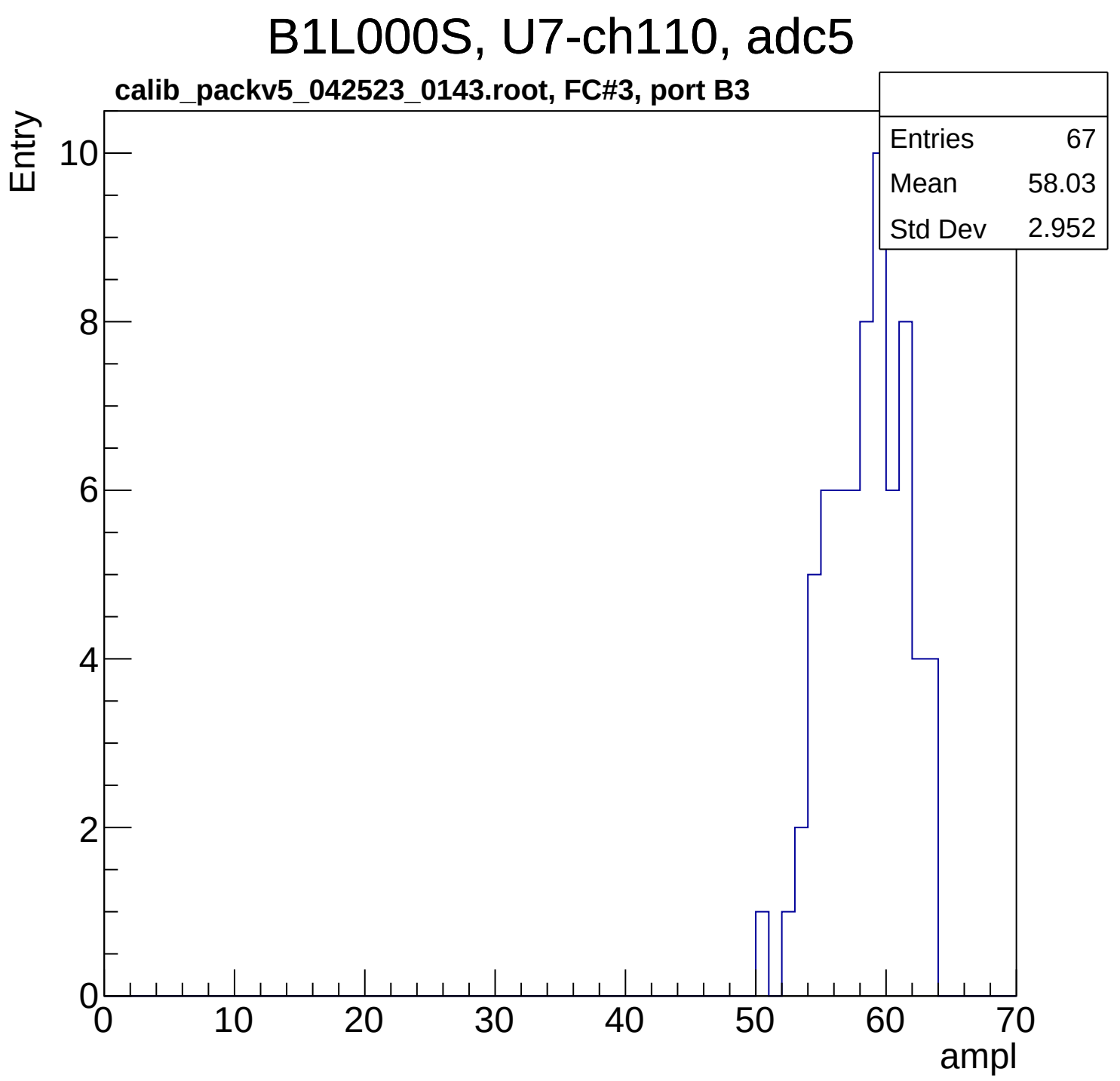
B1L000S, U7-ch110, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 58.03
Std Dev 2.952
Entry
ampl
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2
4
6
8
10
0
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20
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40
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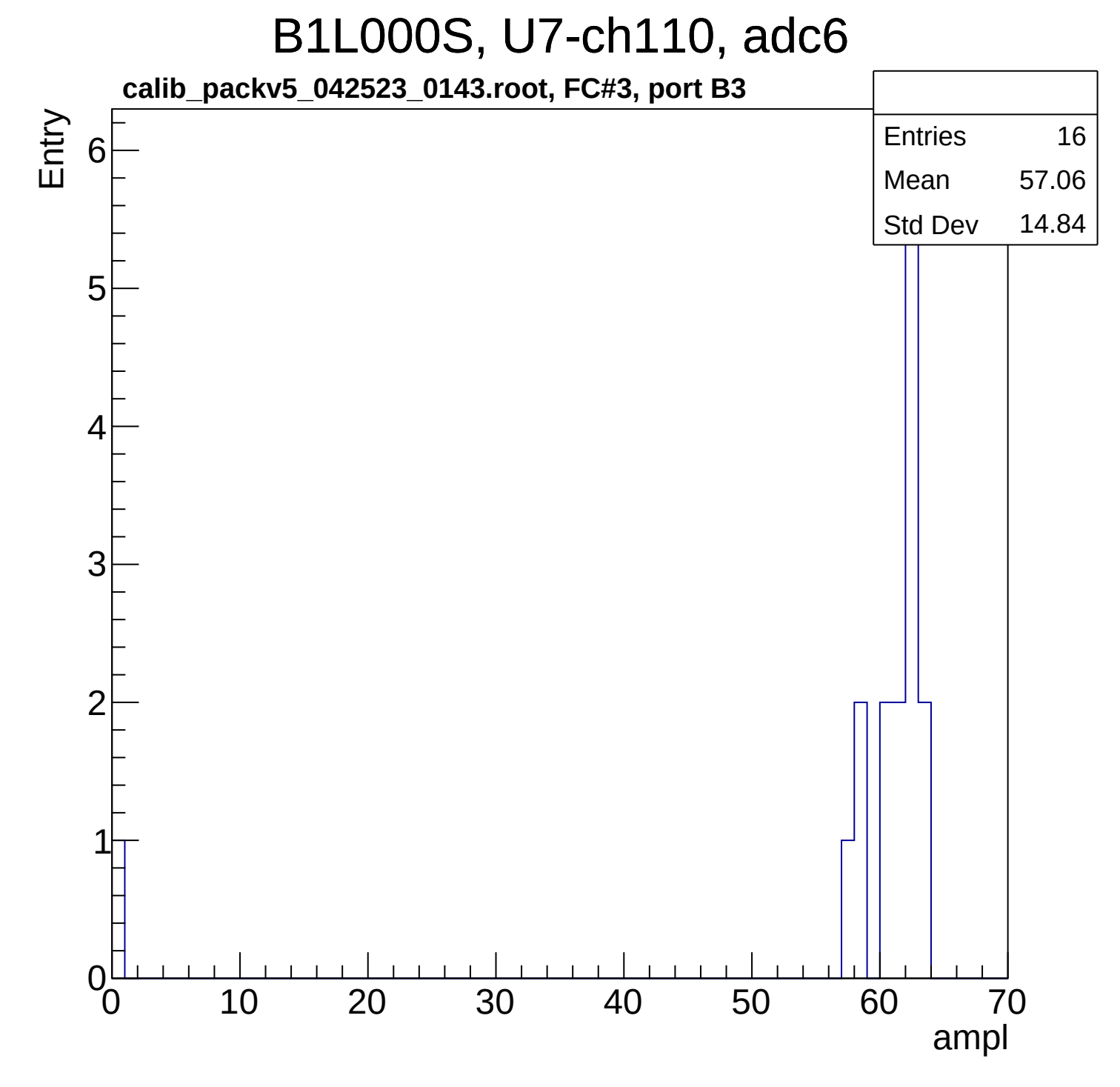
60
70


B1L000S, U7-ch110, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.06
Std Dev 14.84
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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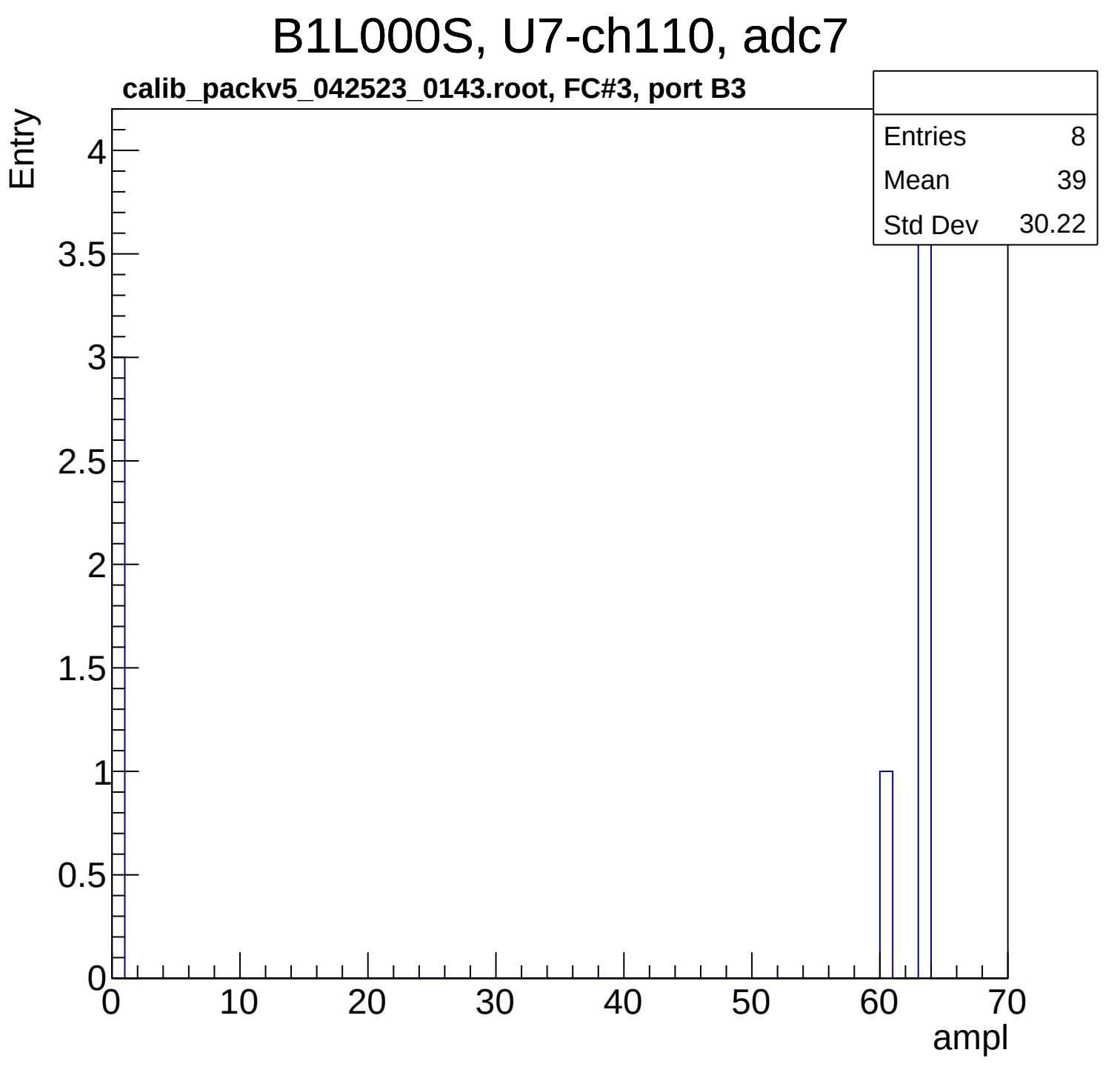
B1L000S, U7-ch110, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 8
Mean 39
Std Dev 30.22
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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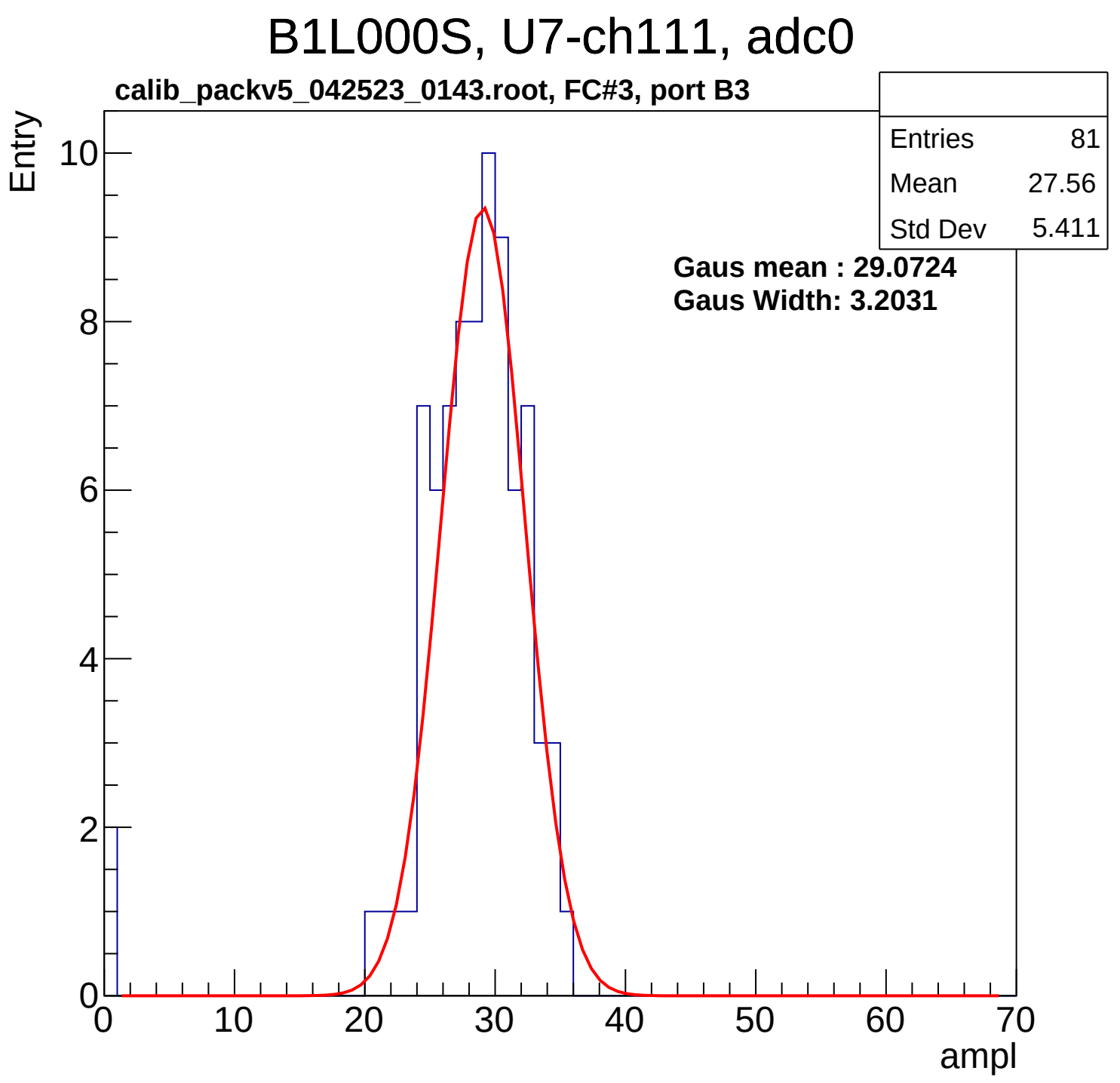
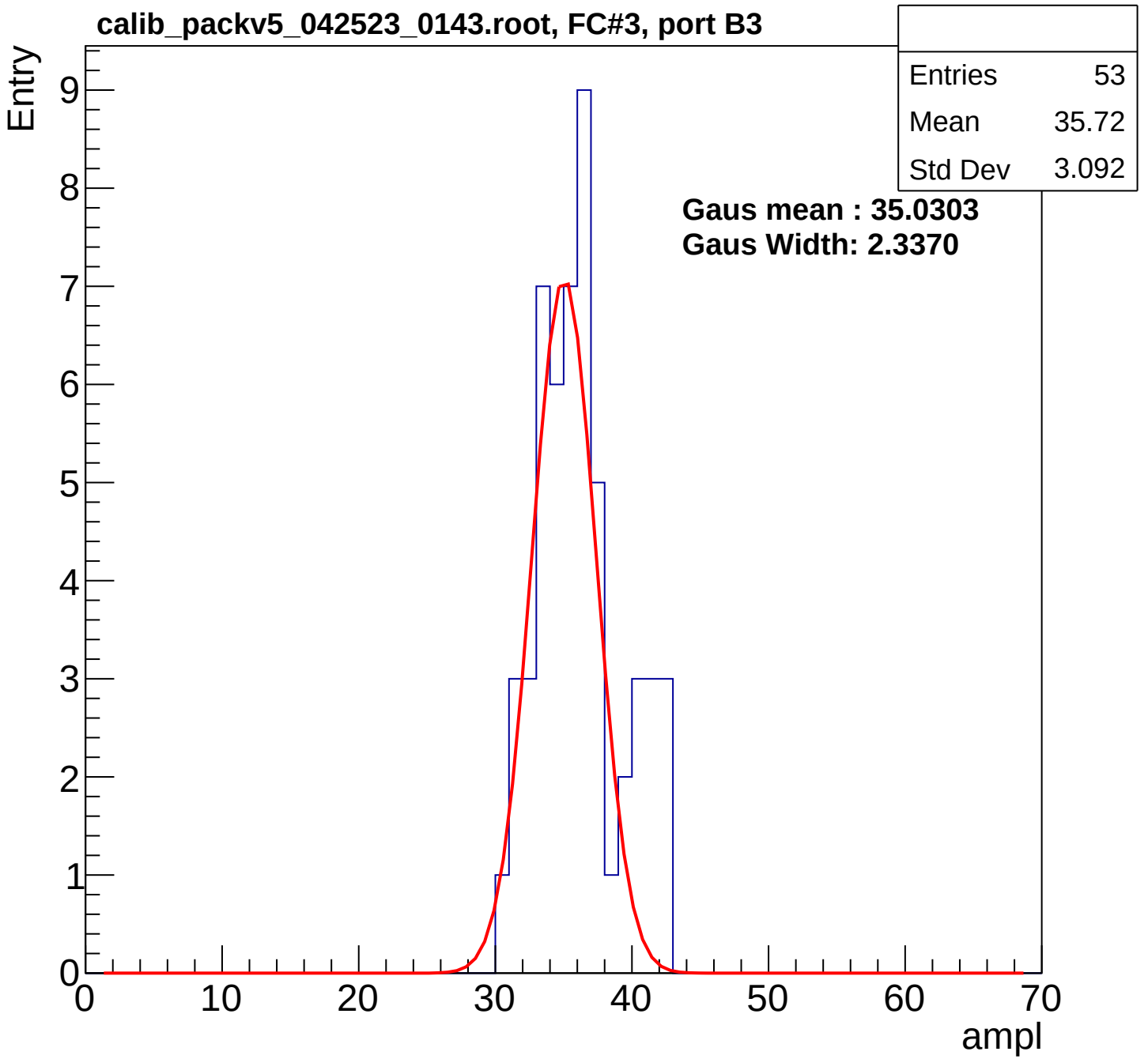
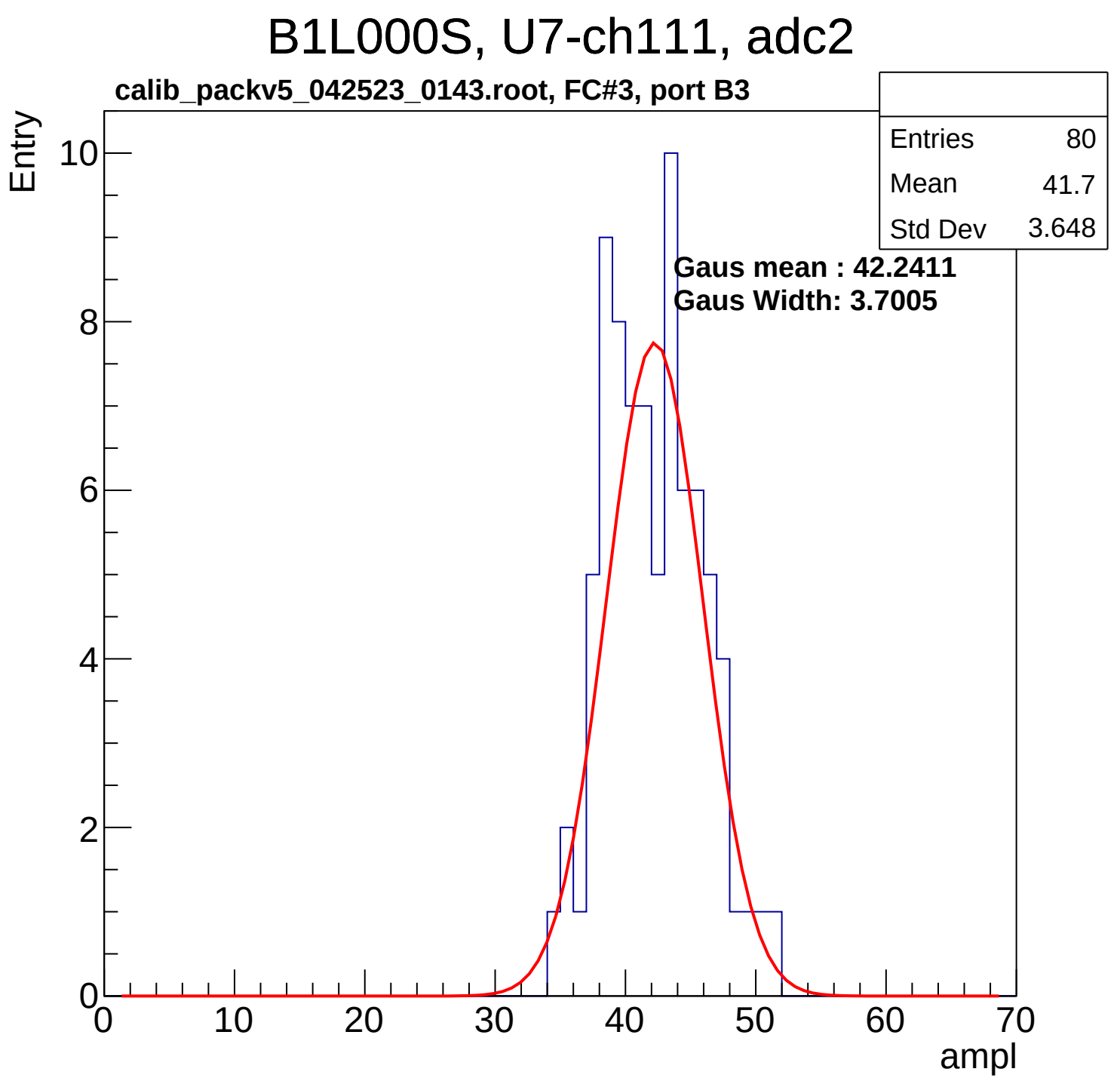
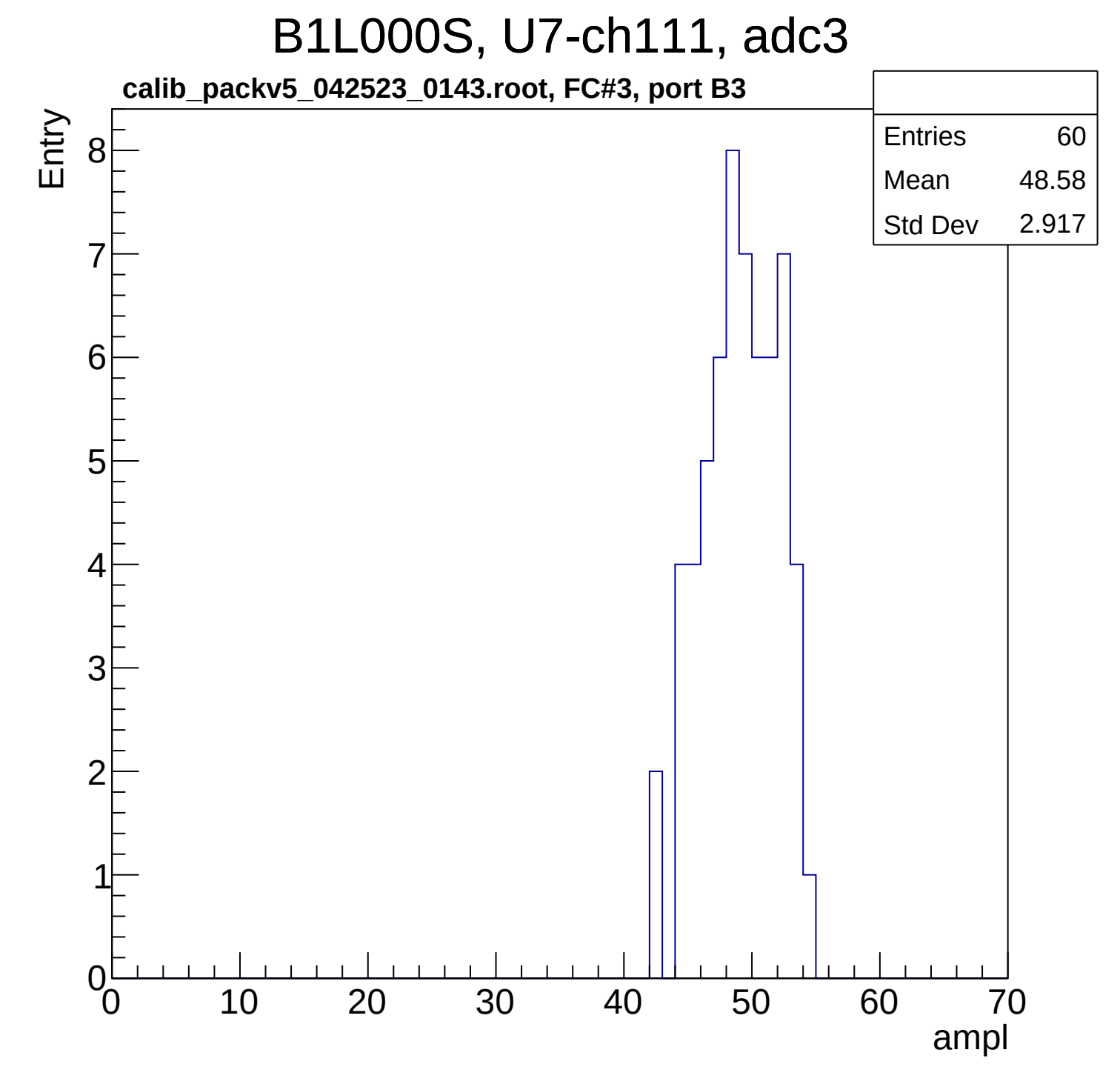

B1L000S, U7-ch111, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 27.56
Std Dev 5.411
Gaus mean : 29.0724
Gaus Width: 3.2031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch111, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 35.72
Std Dev 3.092
Gaus mean : 35.0303
Gaus Width: 2.3370
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch111, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 80
Mean 41.7
Std Dev 3.648
Gaus mean : 42.2411
Gaus Width: 3.7005
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch111, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.58
Std Dev 2.917
Entry
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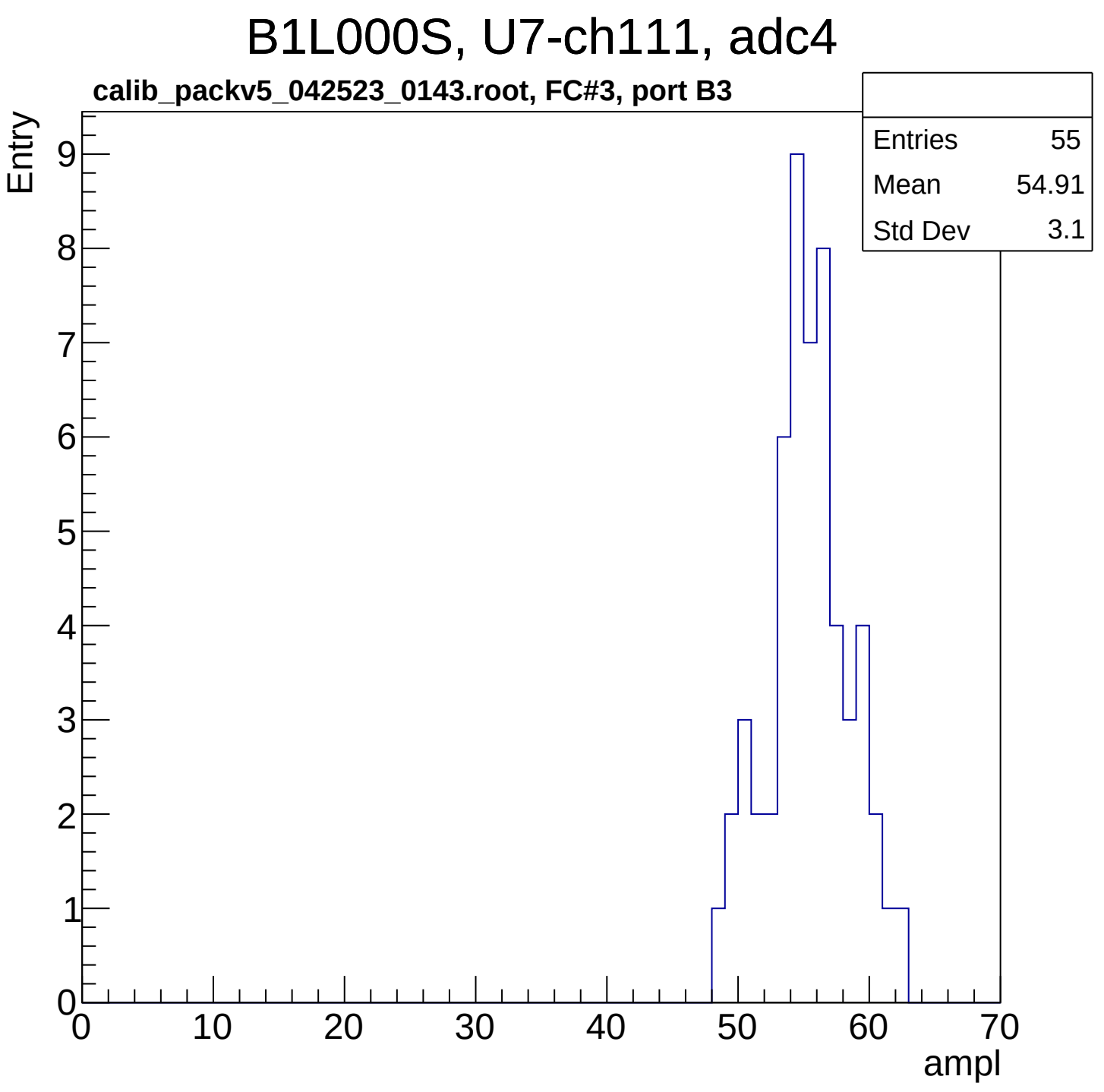
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch111, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 54.91
Std Dev 3.1
Entry
0
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2
3
4
5
6
7
8
9
0
10
20
30
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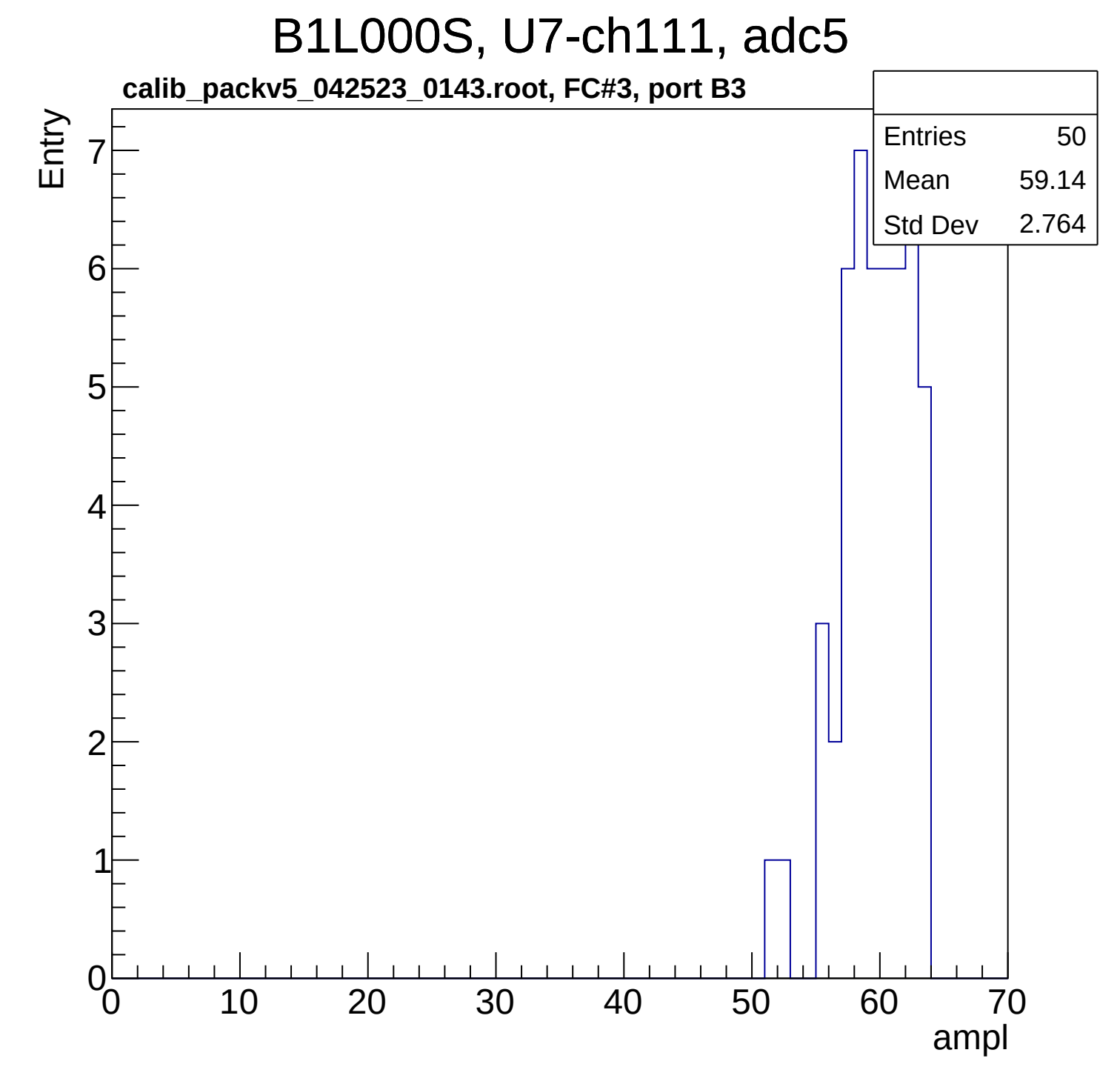
50
60
70
ampl


B1L000S, U7-ch111, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 59.14
Std Dev 2.764
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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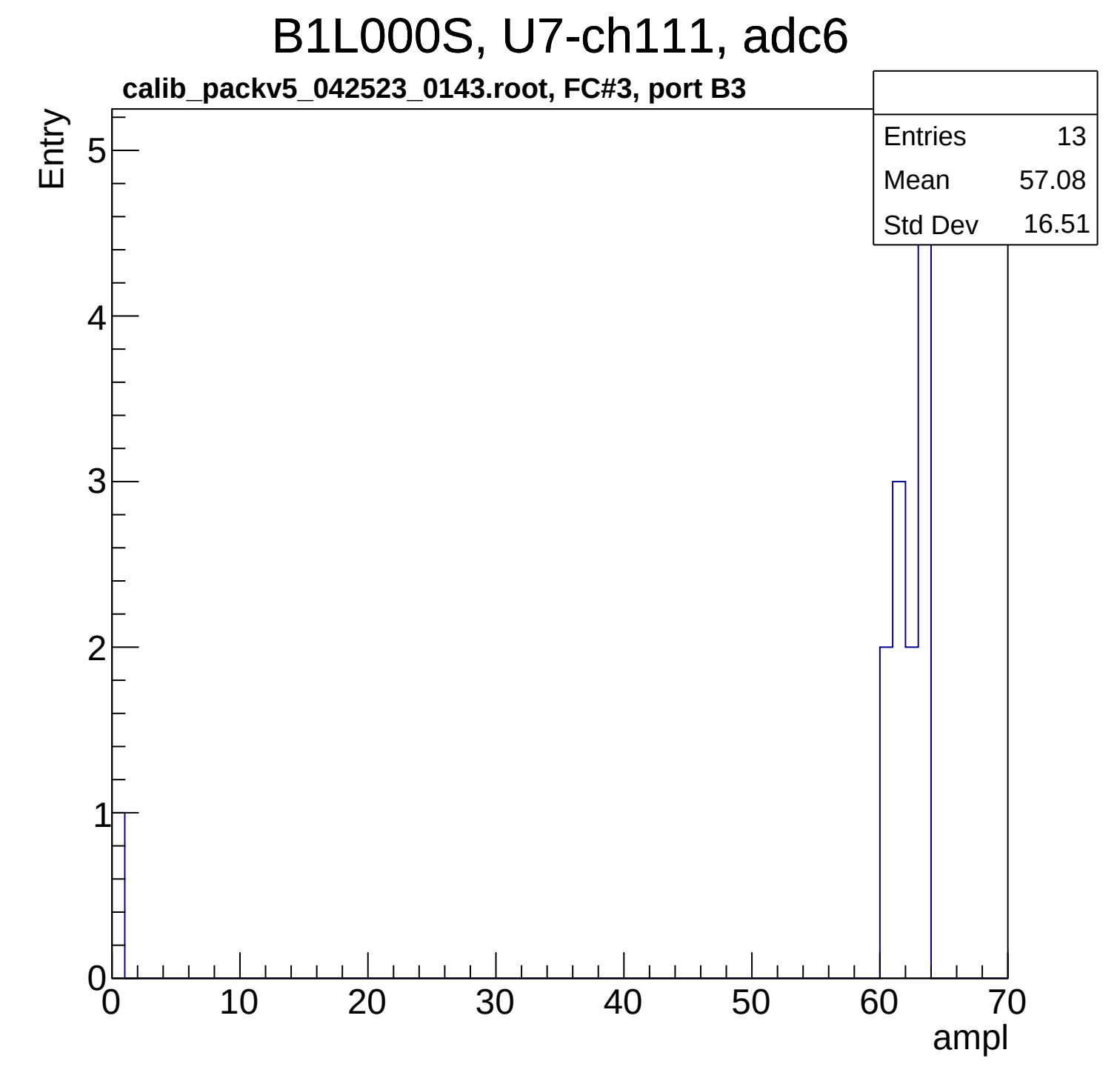
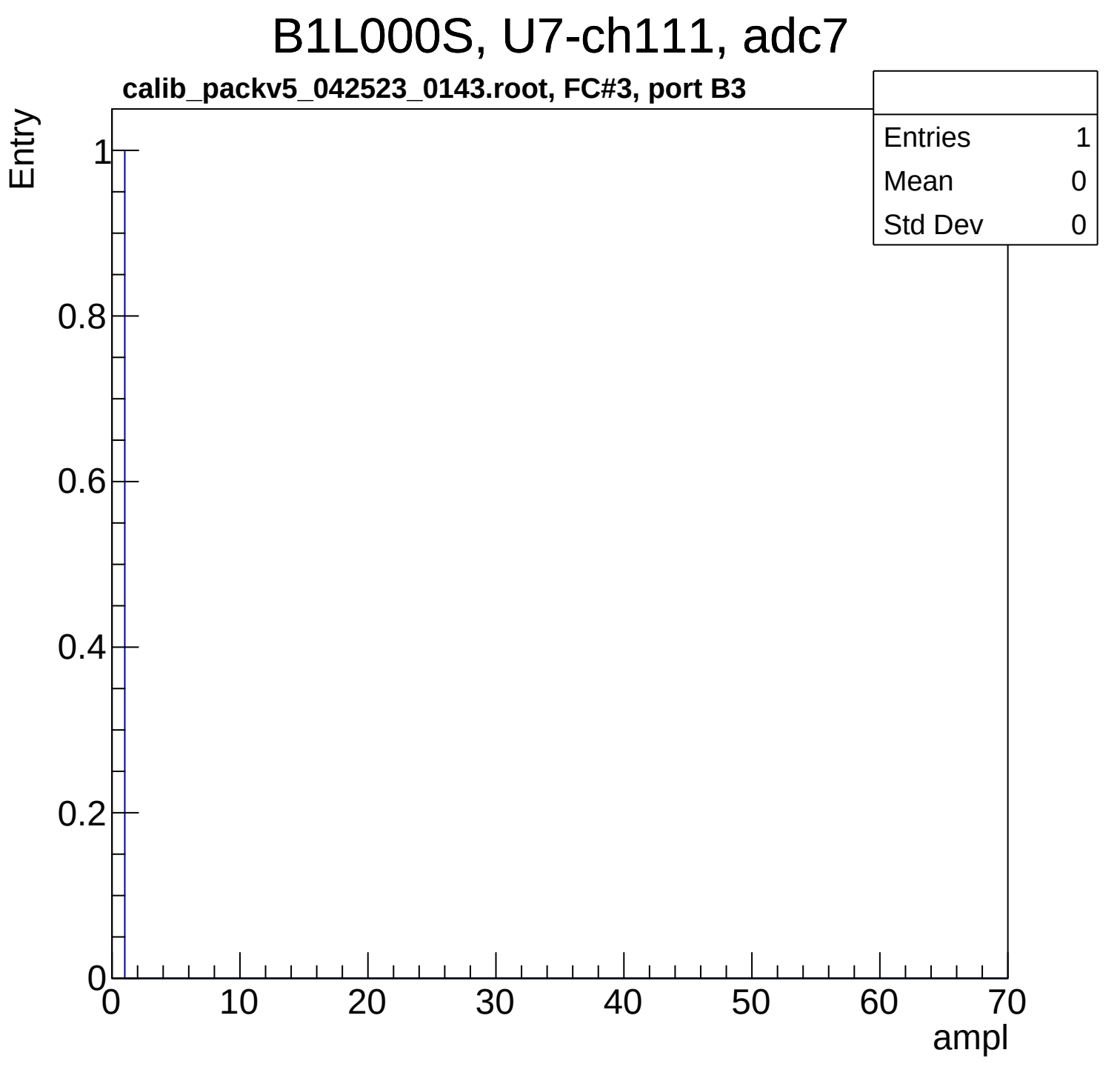

B1L000S, U7-ch111, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 13
Mean 57.08
Std Dev 16.51
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch111, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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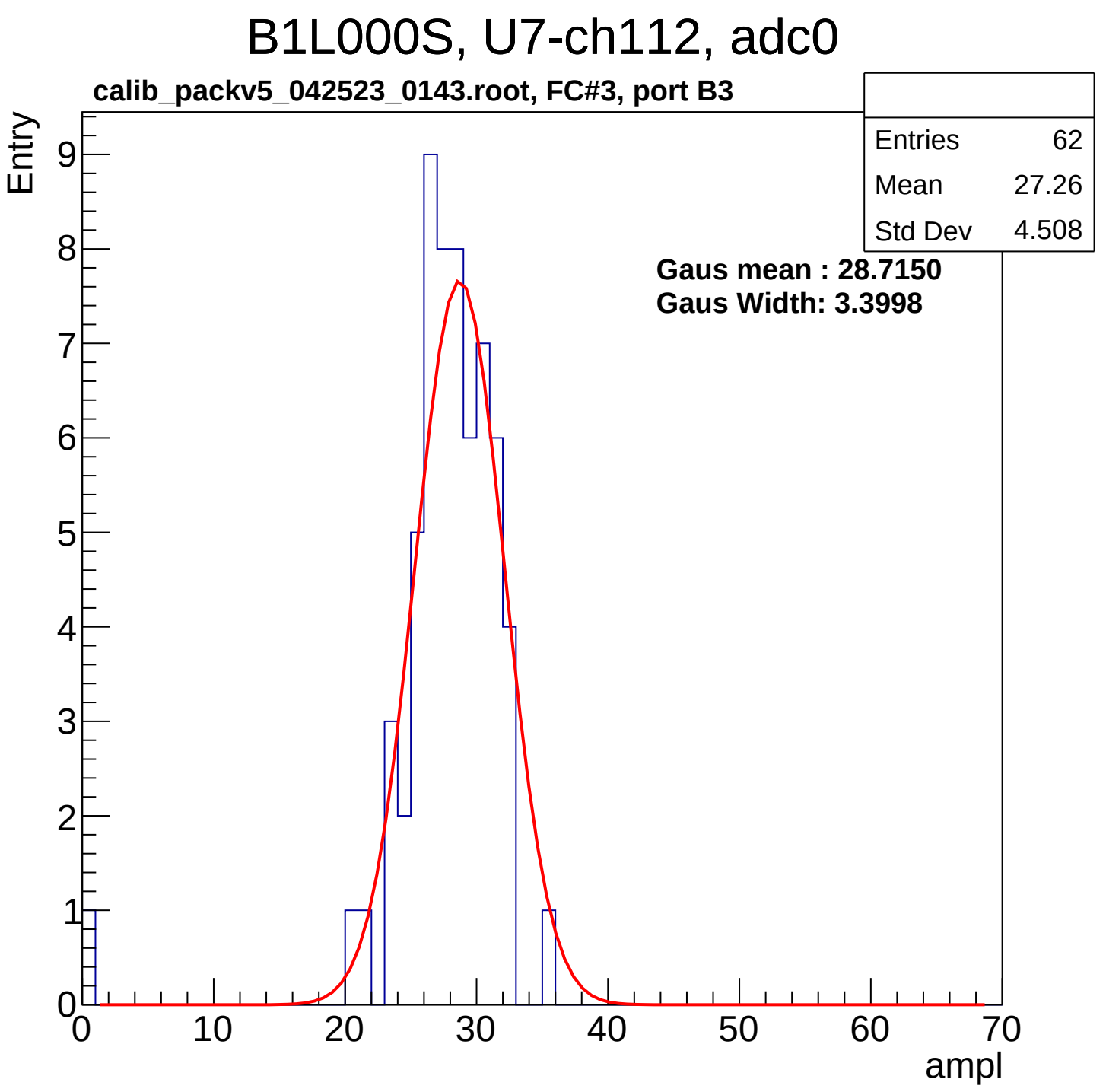
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch112, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 27.26
Std Dev 4.508
Gaus mean : 28.7150
Gaus Width: 3.3998
Entry
ampl
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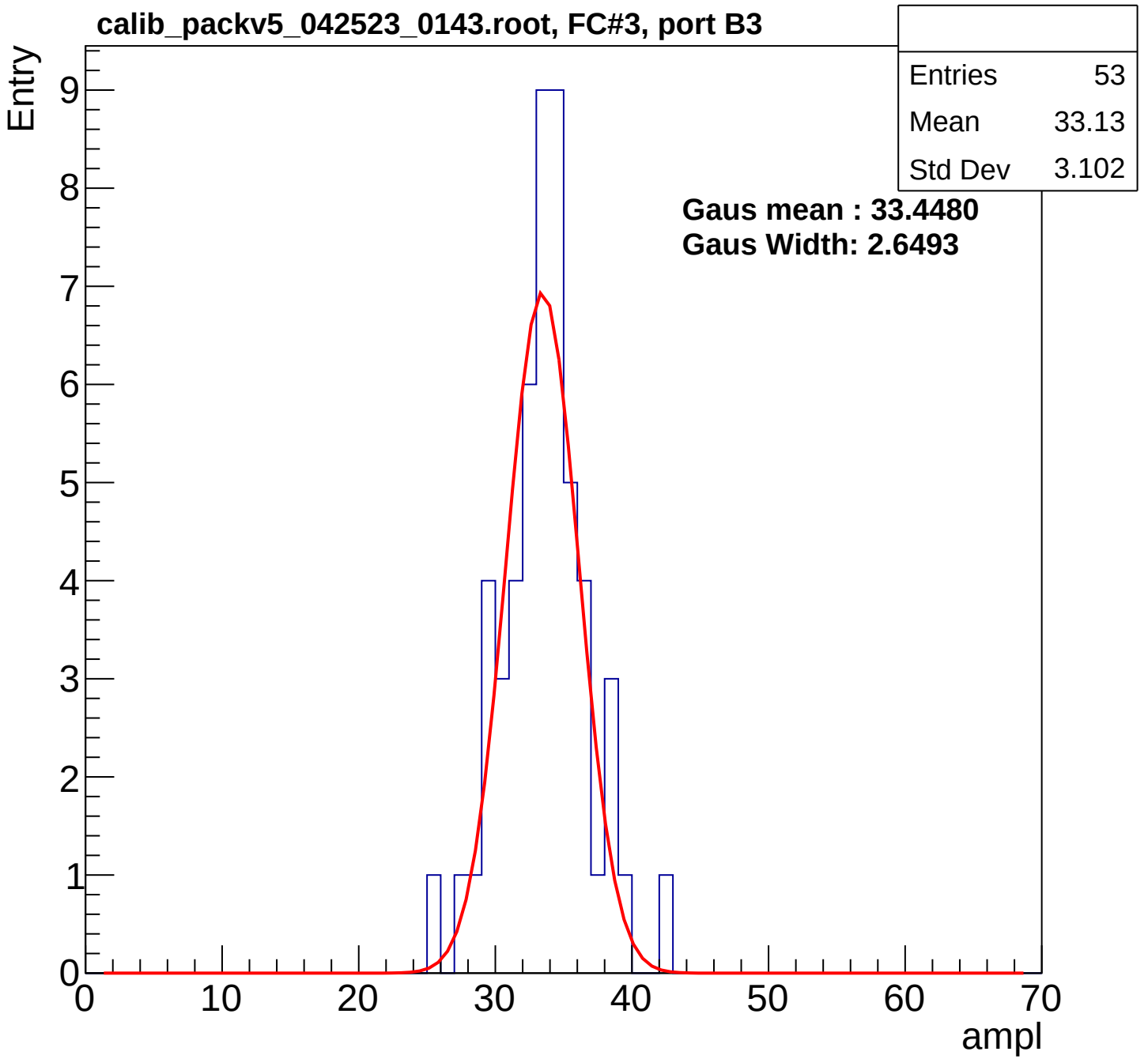
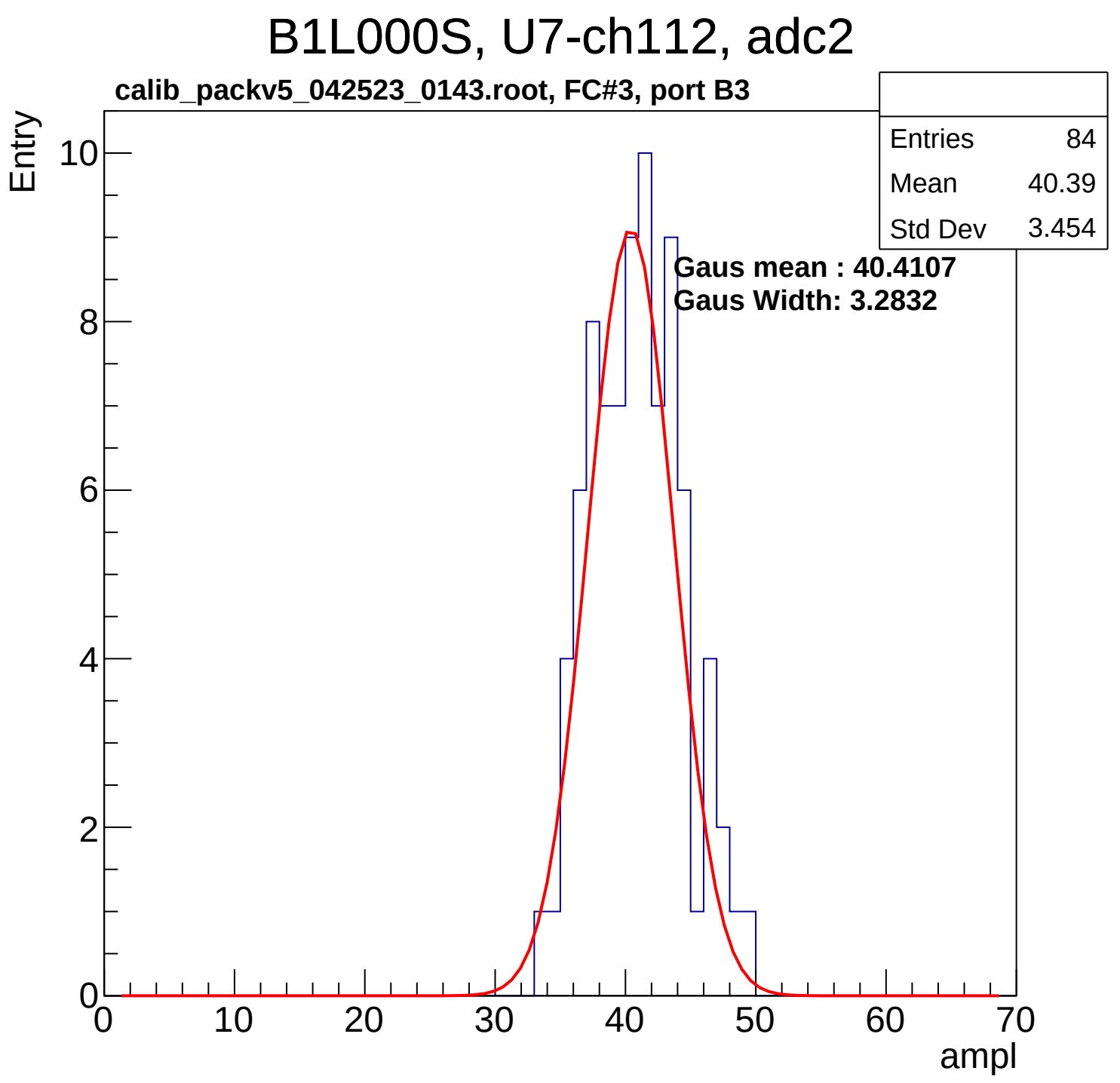
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch112, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 33.13
Std Dev 3.102
Gaus mean : 33.4480
Gaus Width: 2.6493
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch112, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 84
Mean 40.39
Std Dev 3.454
Gaus mean : 40.4107
Gaus Width: 3.2832
Entry
ampl
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2
4
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0
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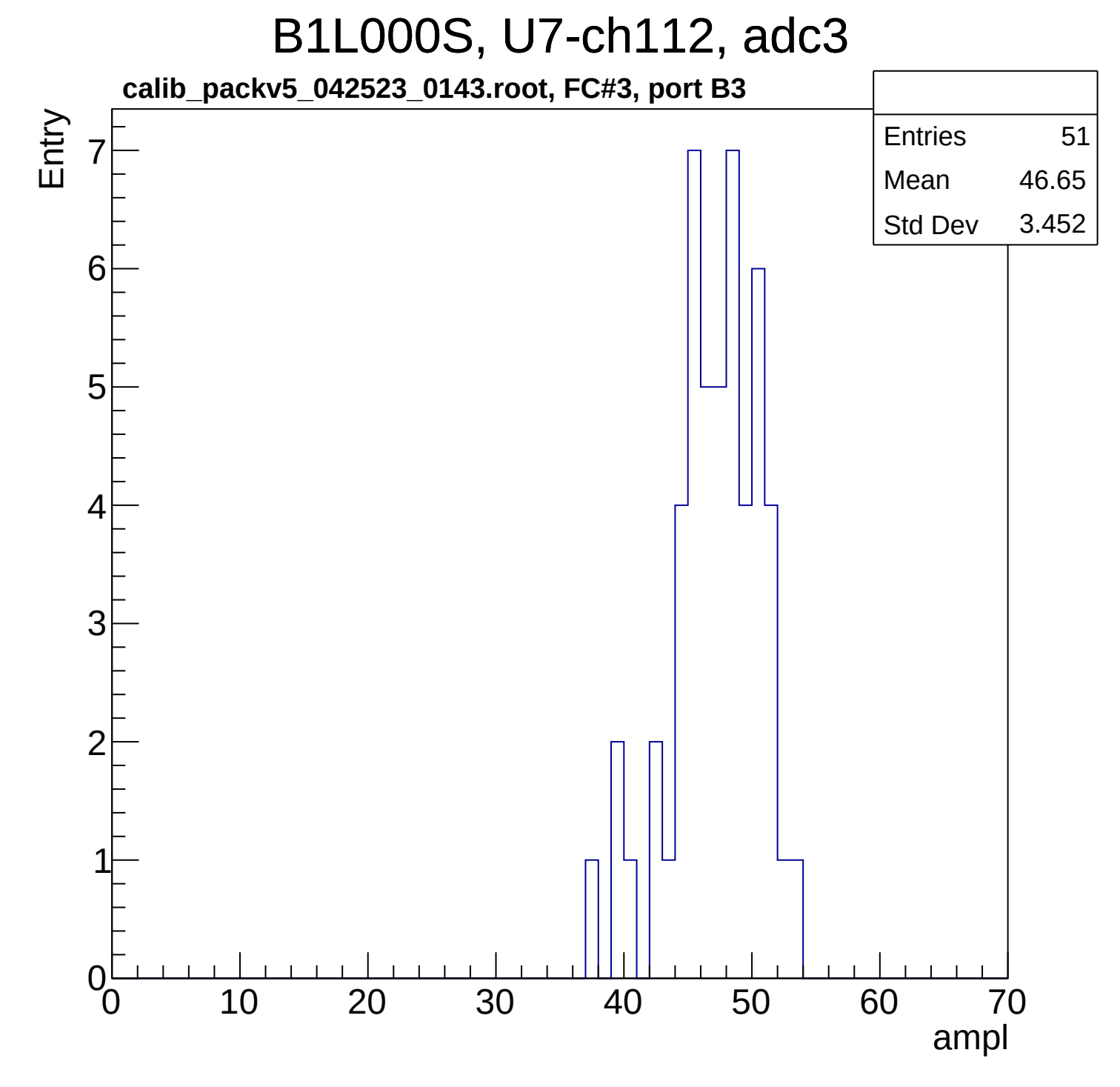
B1L000S, U7-ch112, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 46.65
Std Dev 3.452
Entry
ampl
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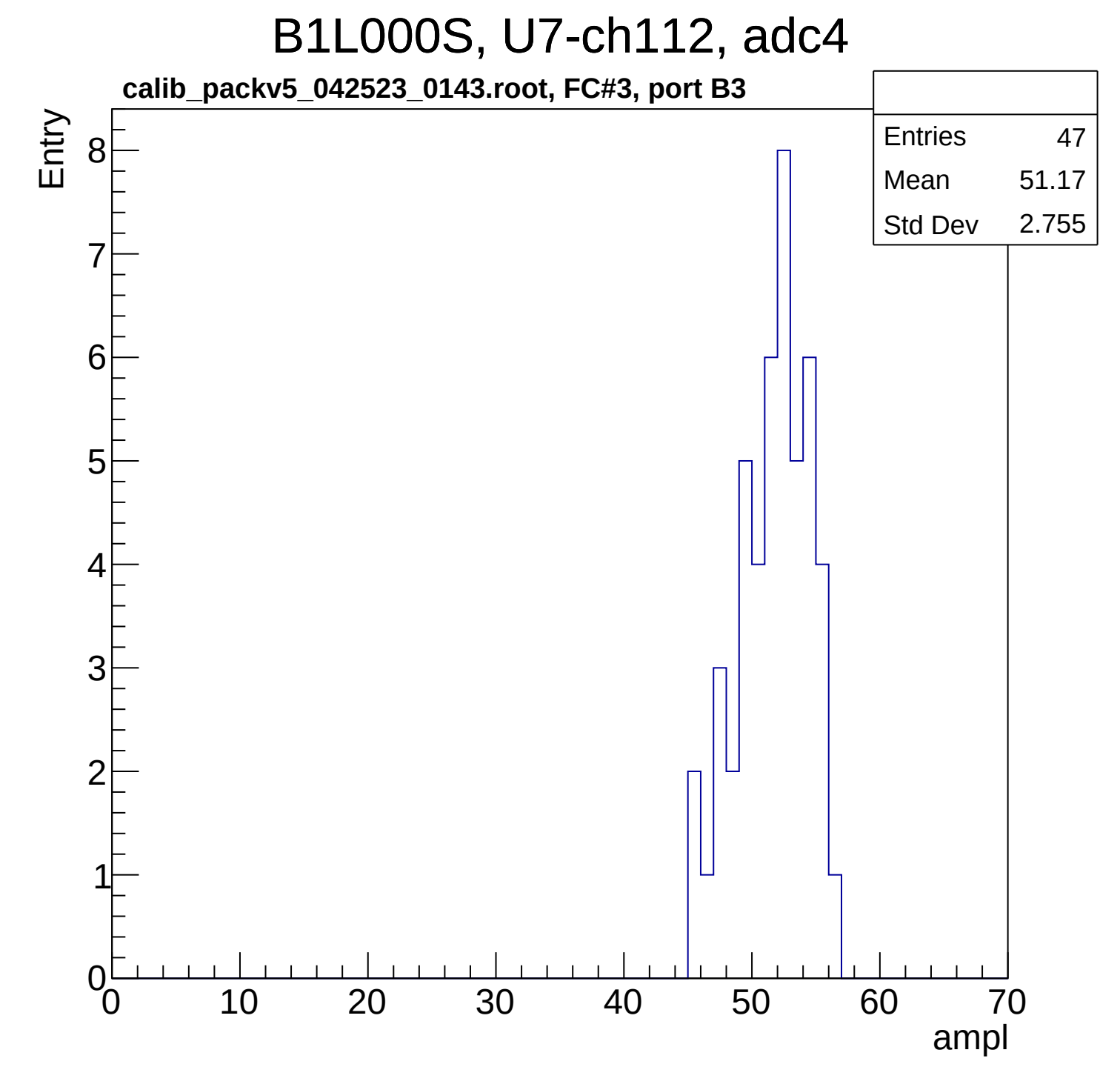
B1L000S, U7-ch112, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 47
Mean 51.17
Std Dev 2.755
Entry
ampl
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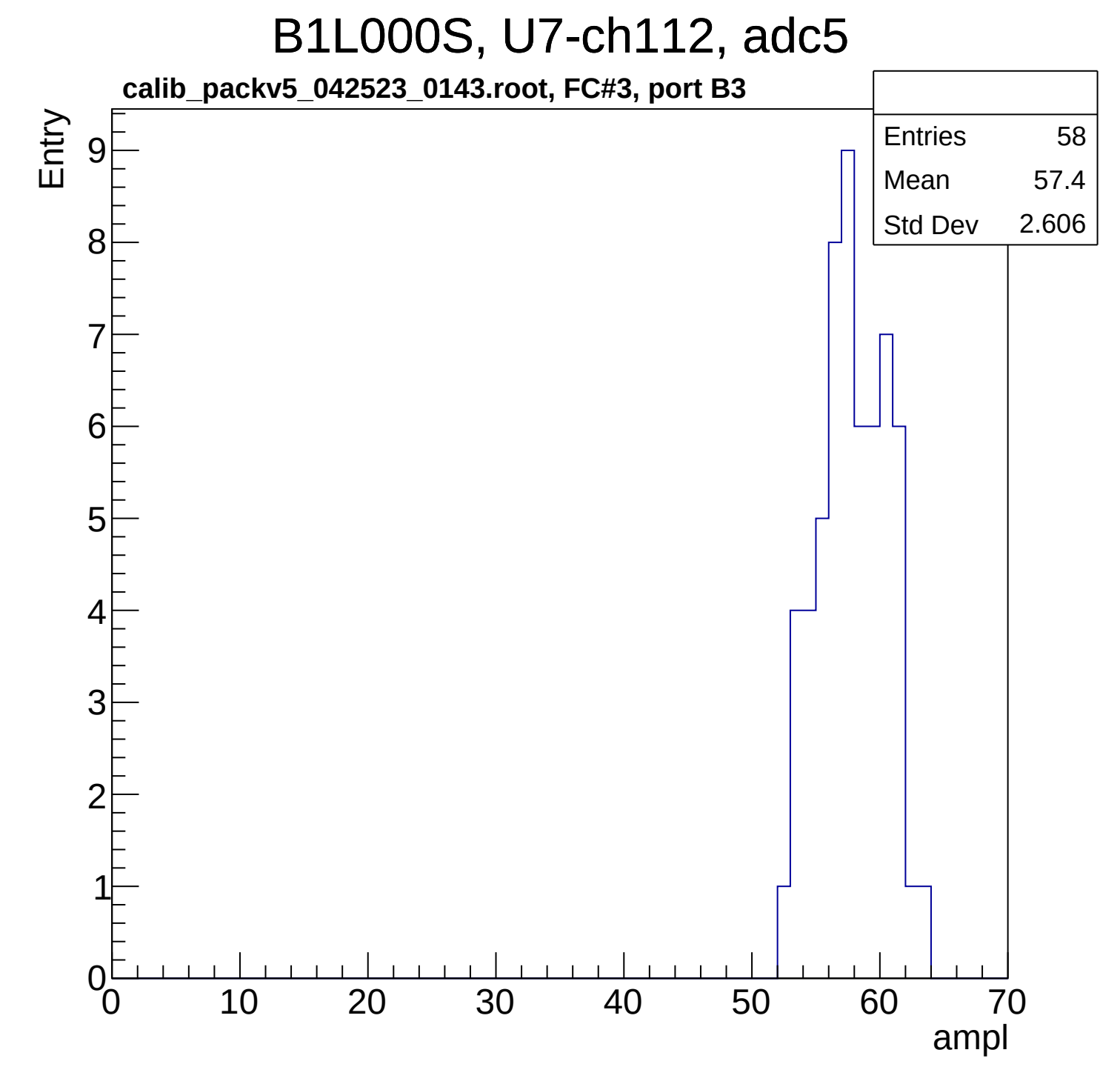
B1L000S, U7-ch112, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 57.4
Std Dev 2.606
Entry
ampl
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6
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0
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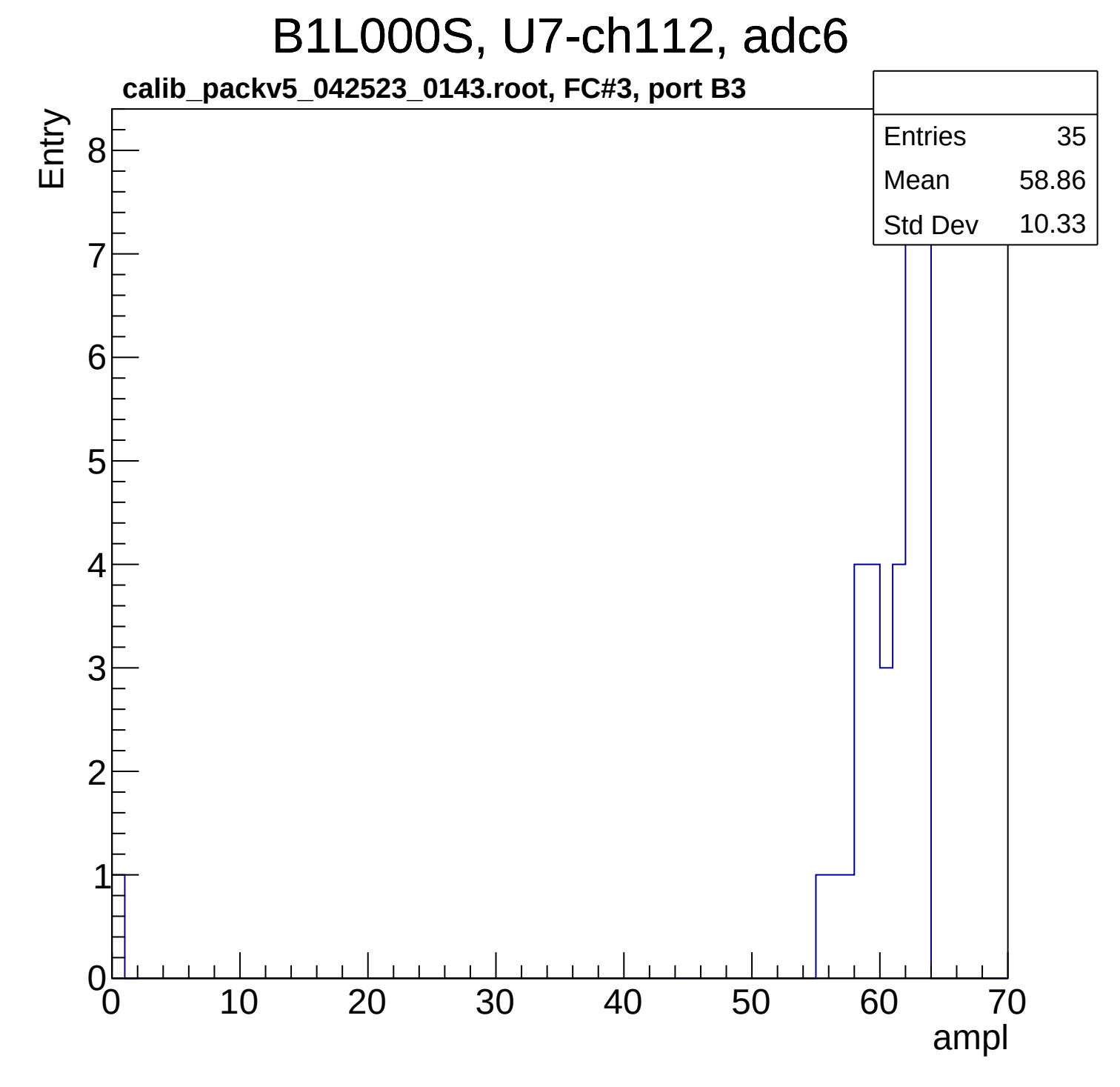
B1L000S, U7-ch112, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 35
Mean 58.86
Std Dev 10.33
Entry
ampl
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1
2
3
4
5
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7
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0
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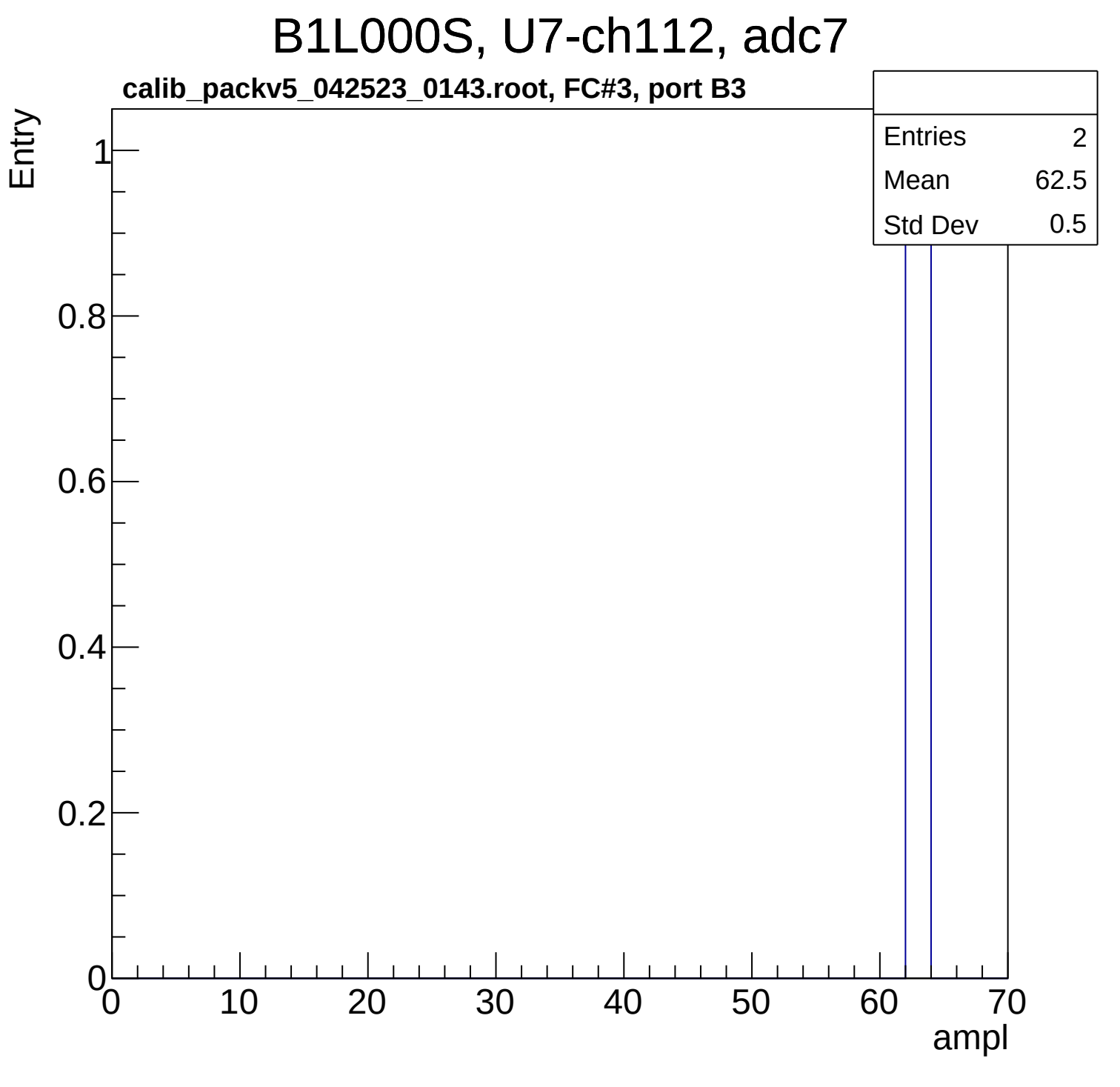
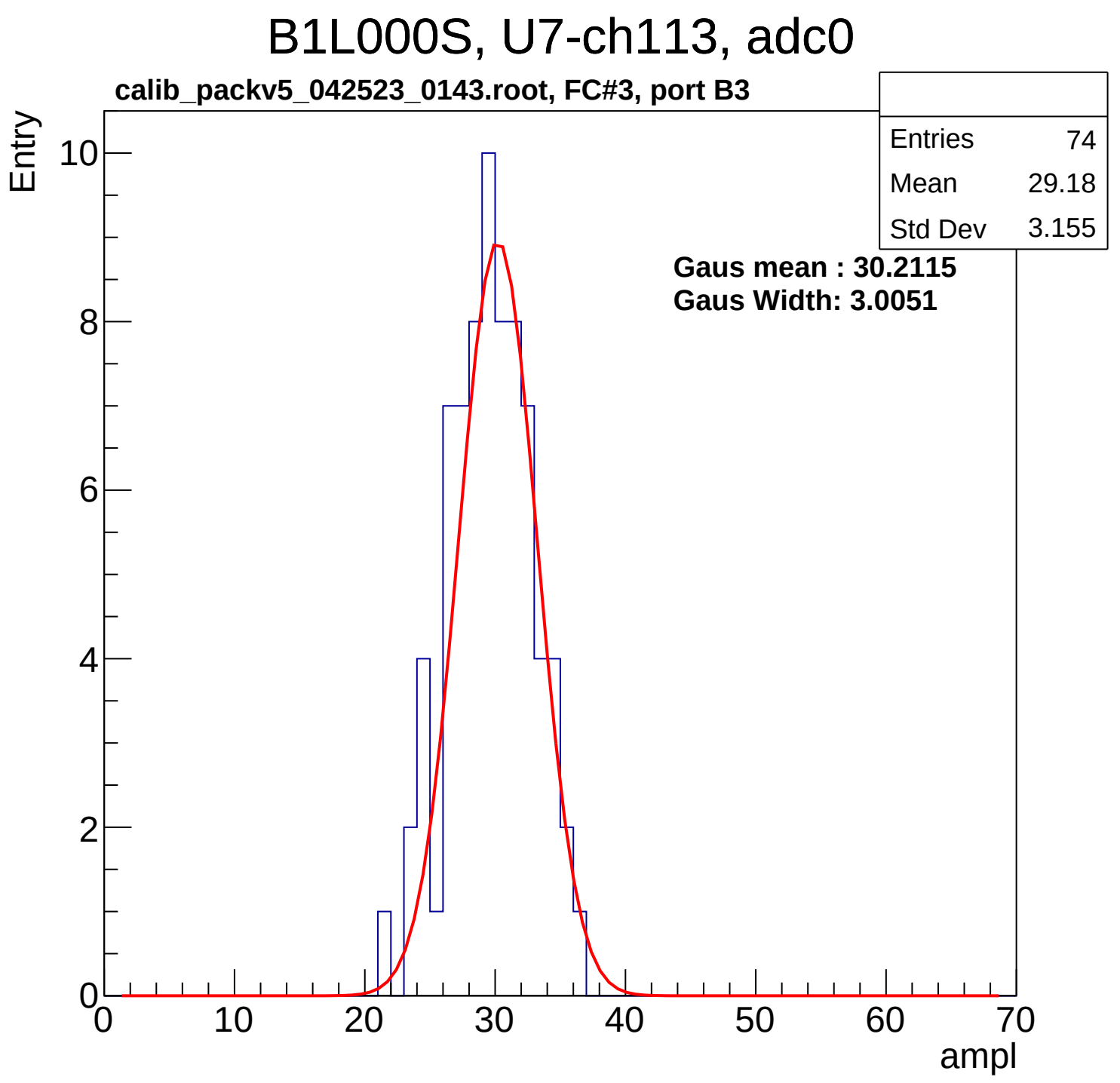

B1L000S, U7-ch112, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch113, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 29.18
Std Dev 3.155
Gaus mean : 30.2115
Gaus Width: 3.0051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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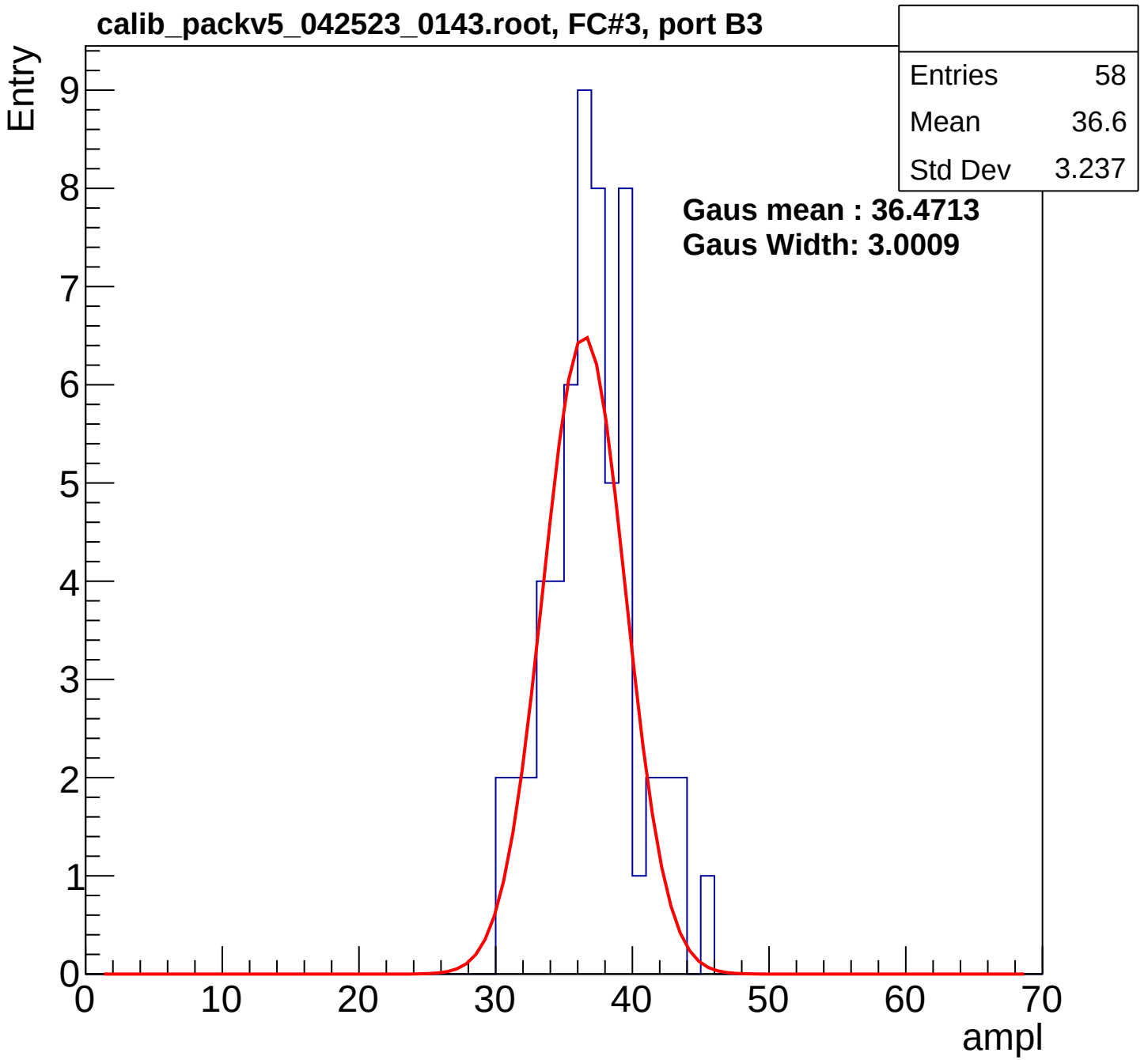
B1L000S, U7-ch113, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 36.6
Std Dev 3.237
Gaus mean : 36.4713
Gaus Width: 3.0009
Entry
ampl
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3
4
5
6
7
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0
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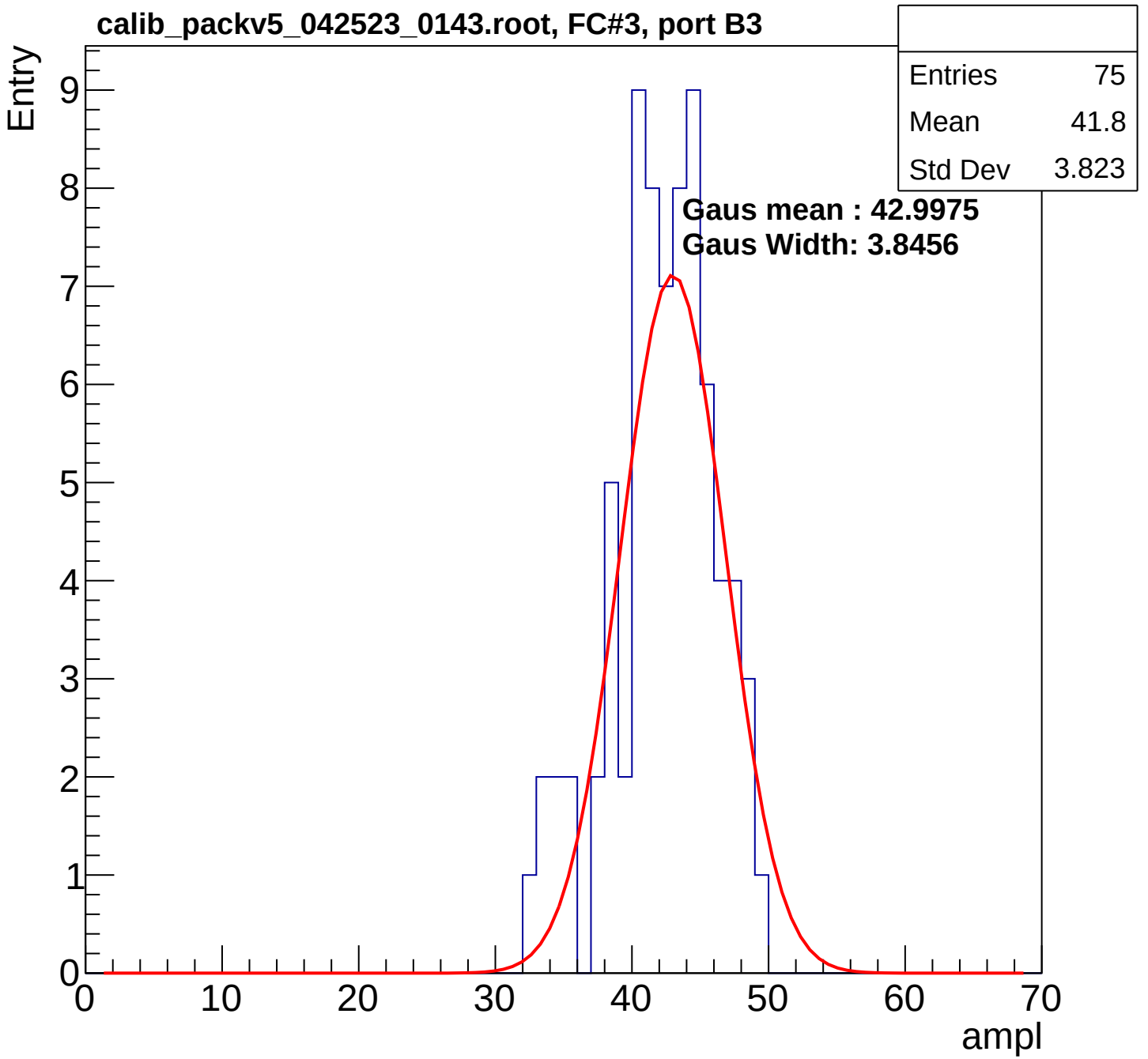
B1L000S, U7-ch113, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 41.8
Std Dev 3.823
Gaus mean : 42.9975
Gaus Width: 3.8456
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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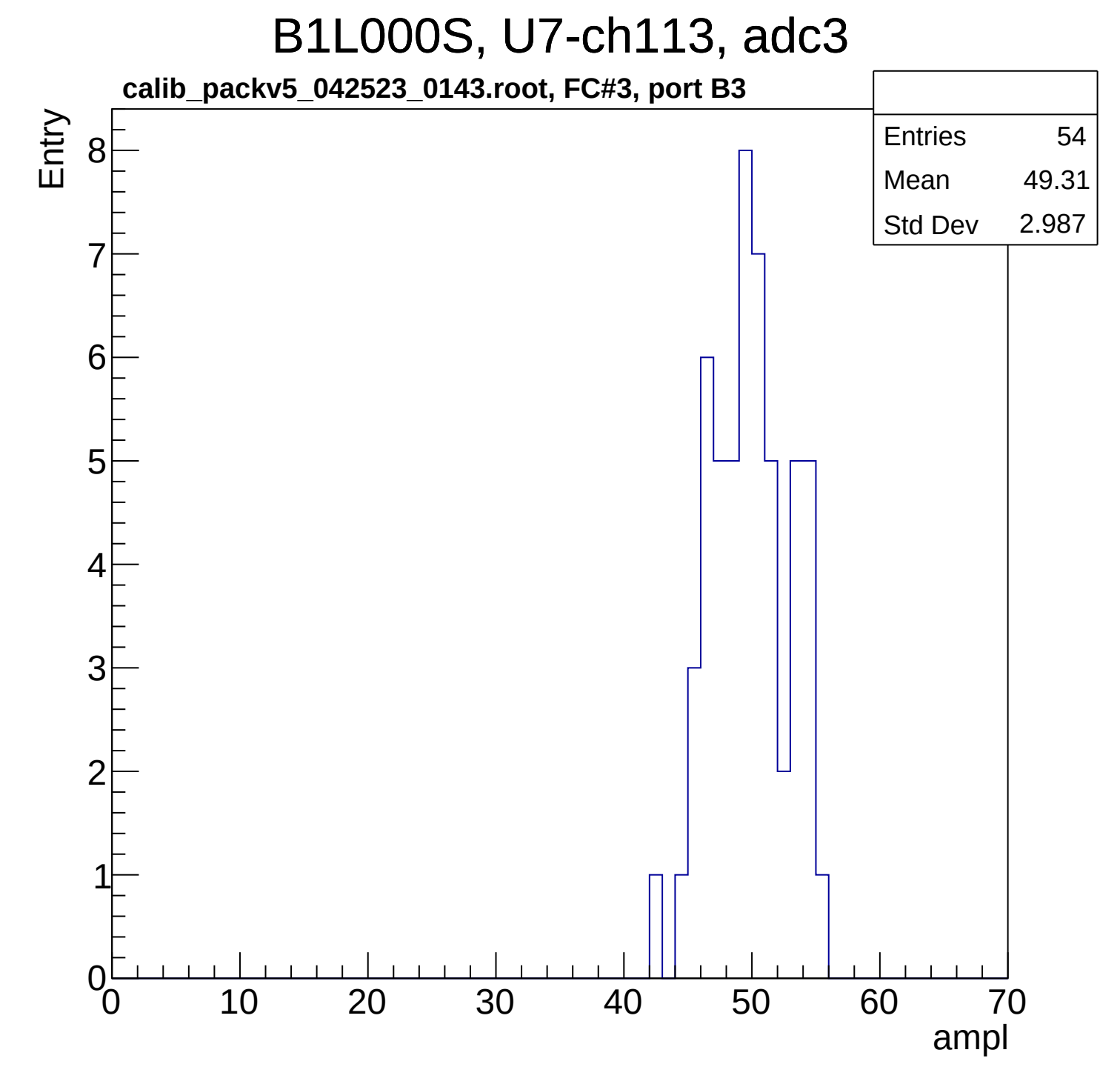
B1L000S, U7-ch113, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 49.31
Std Dev 2.987
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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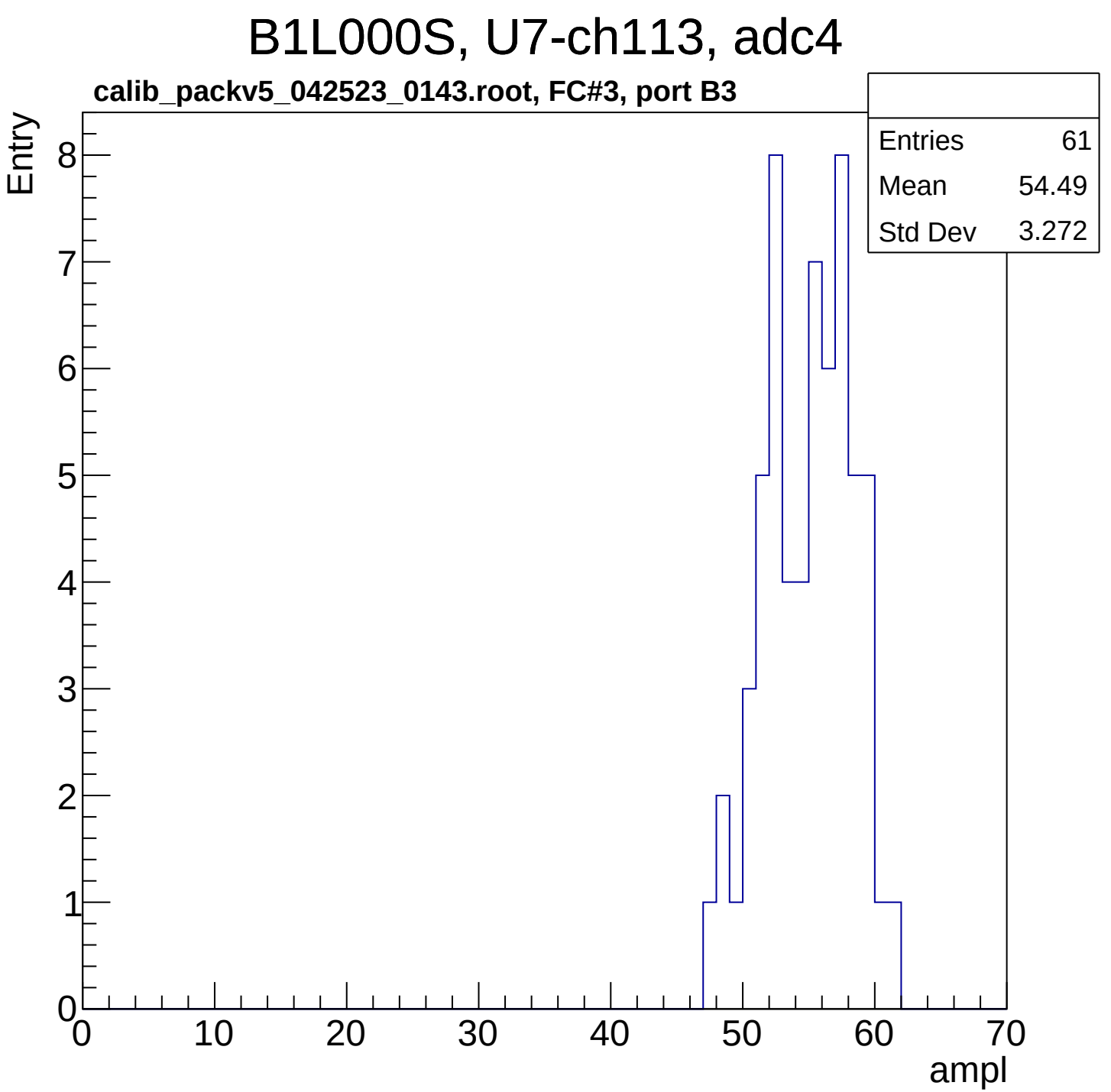
B1L000S, U7-ch113, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 54.49
Std Dev 3.272
Entry
ampl
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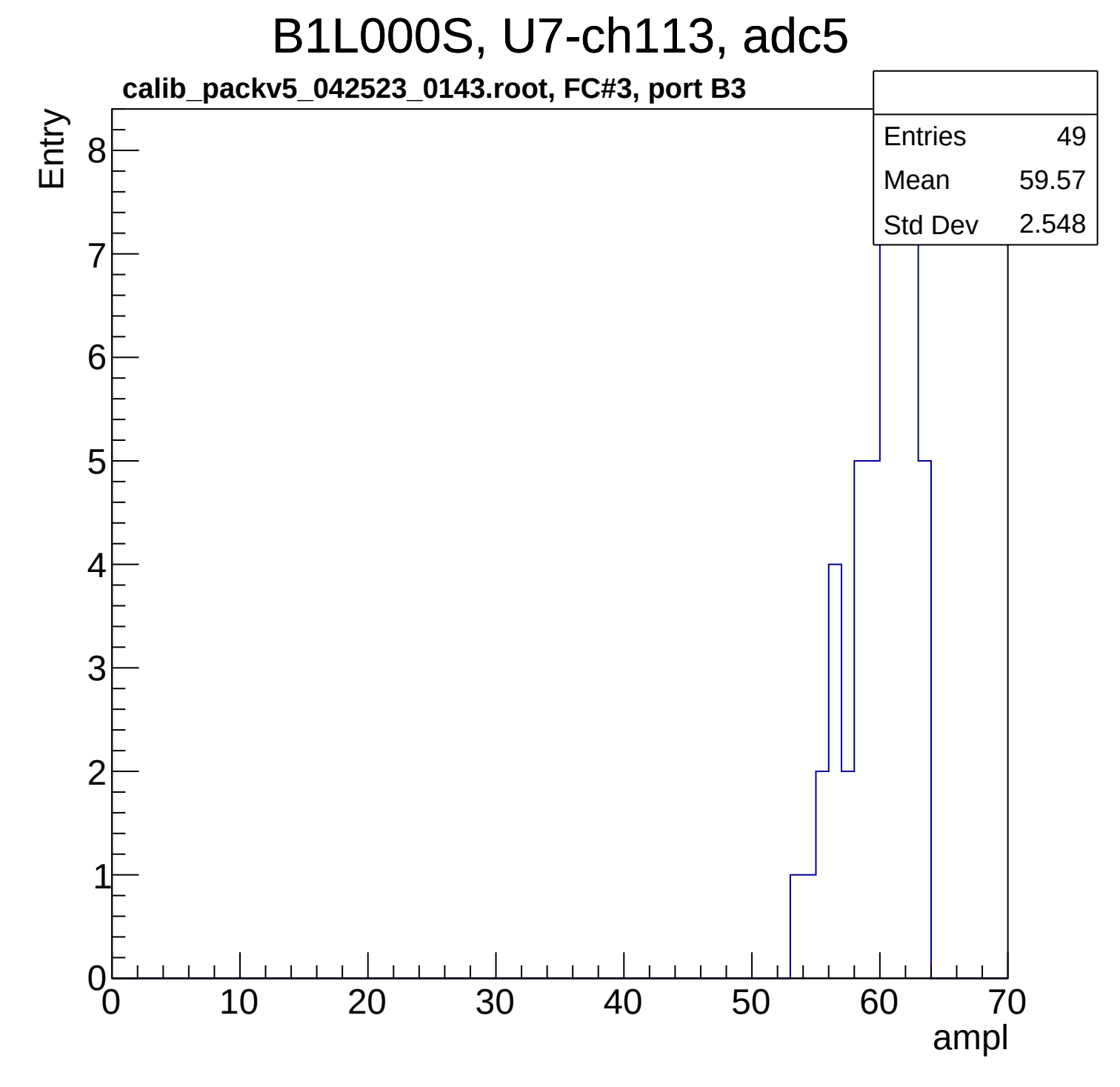
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch113, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 59.57
Std Dev 2.548
Entry
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2
3
4
5
6
7
8
0
10
20
30
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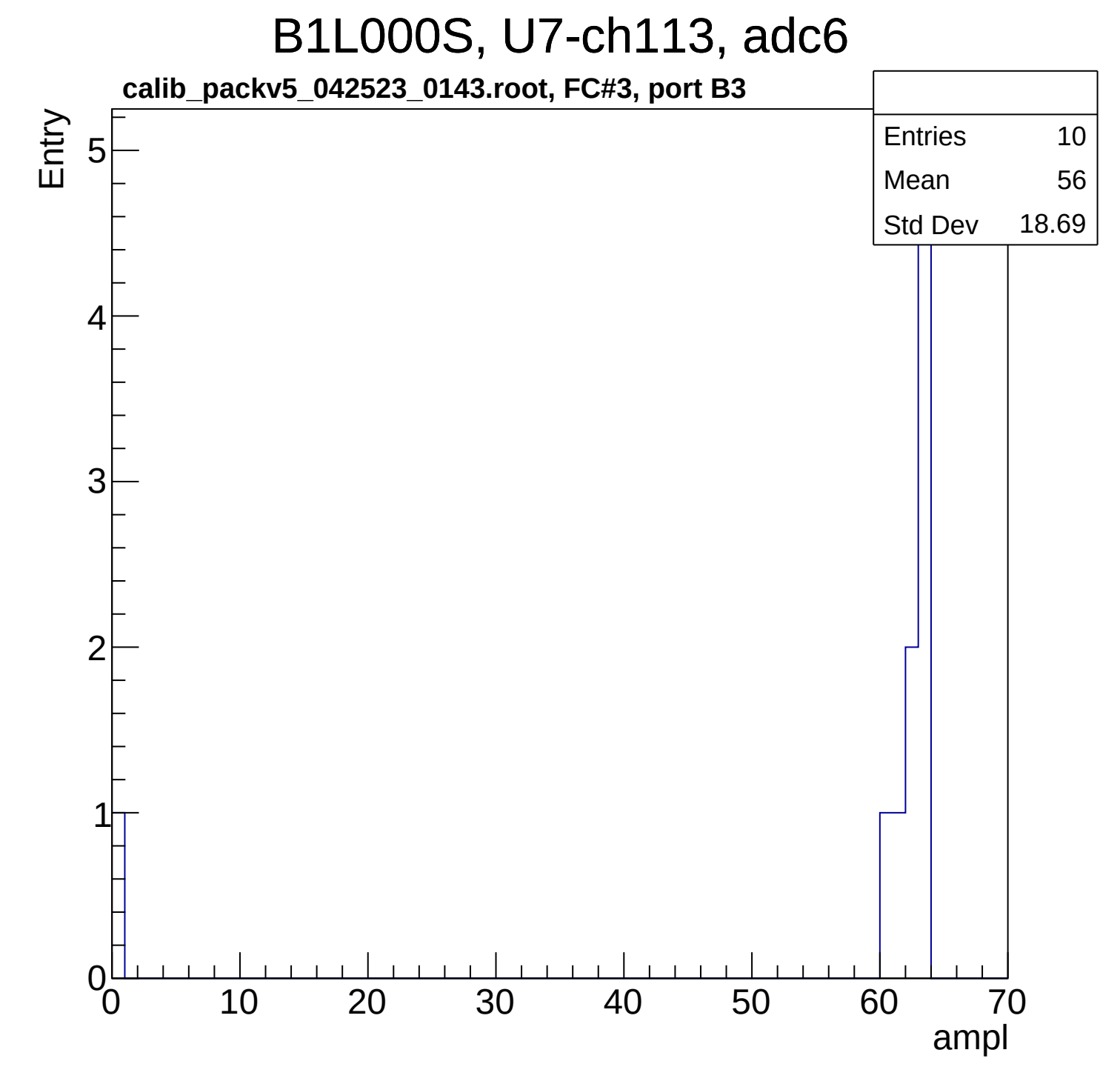
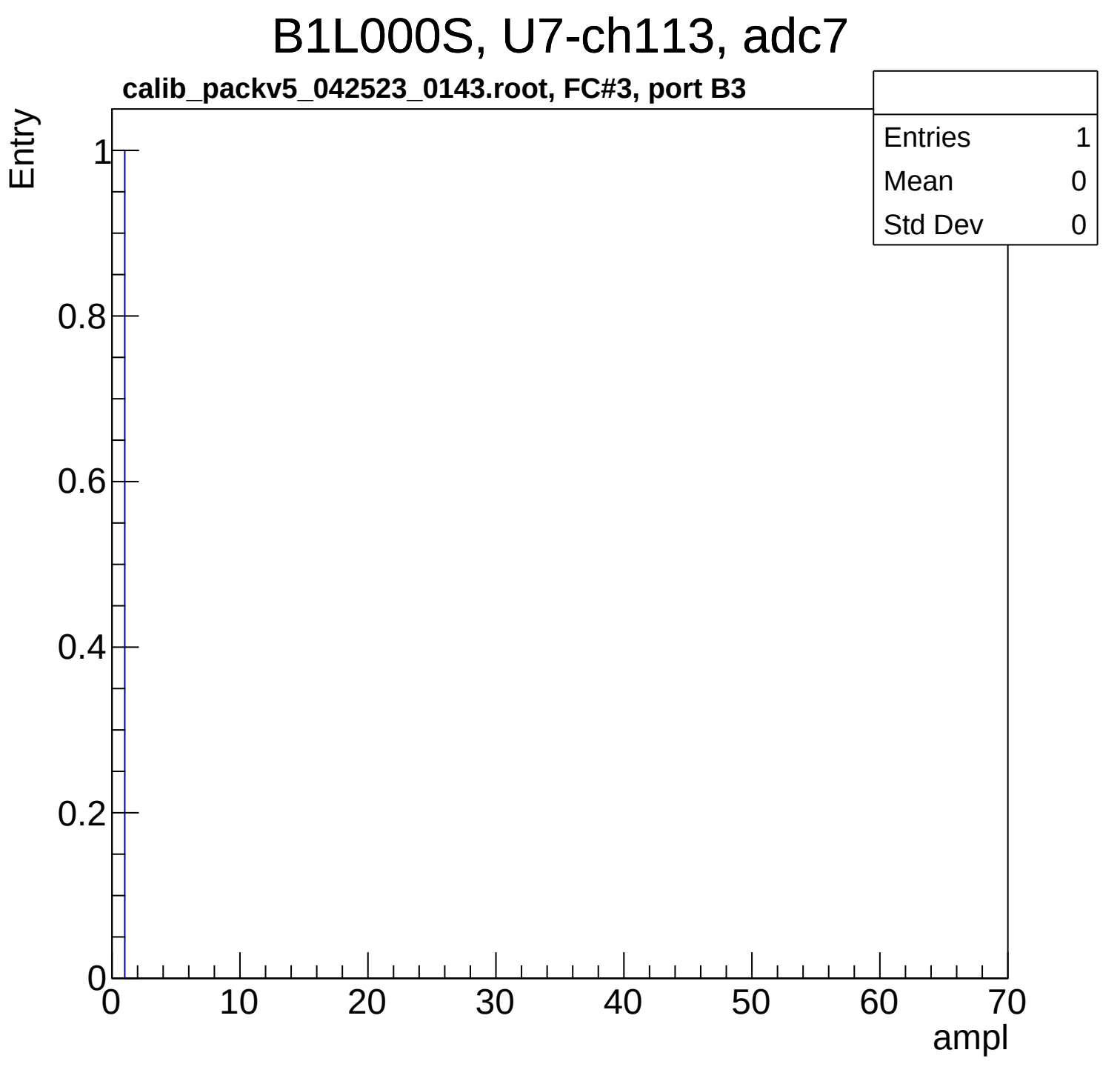
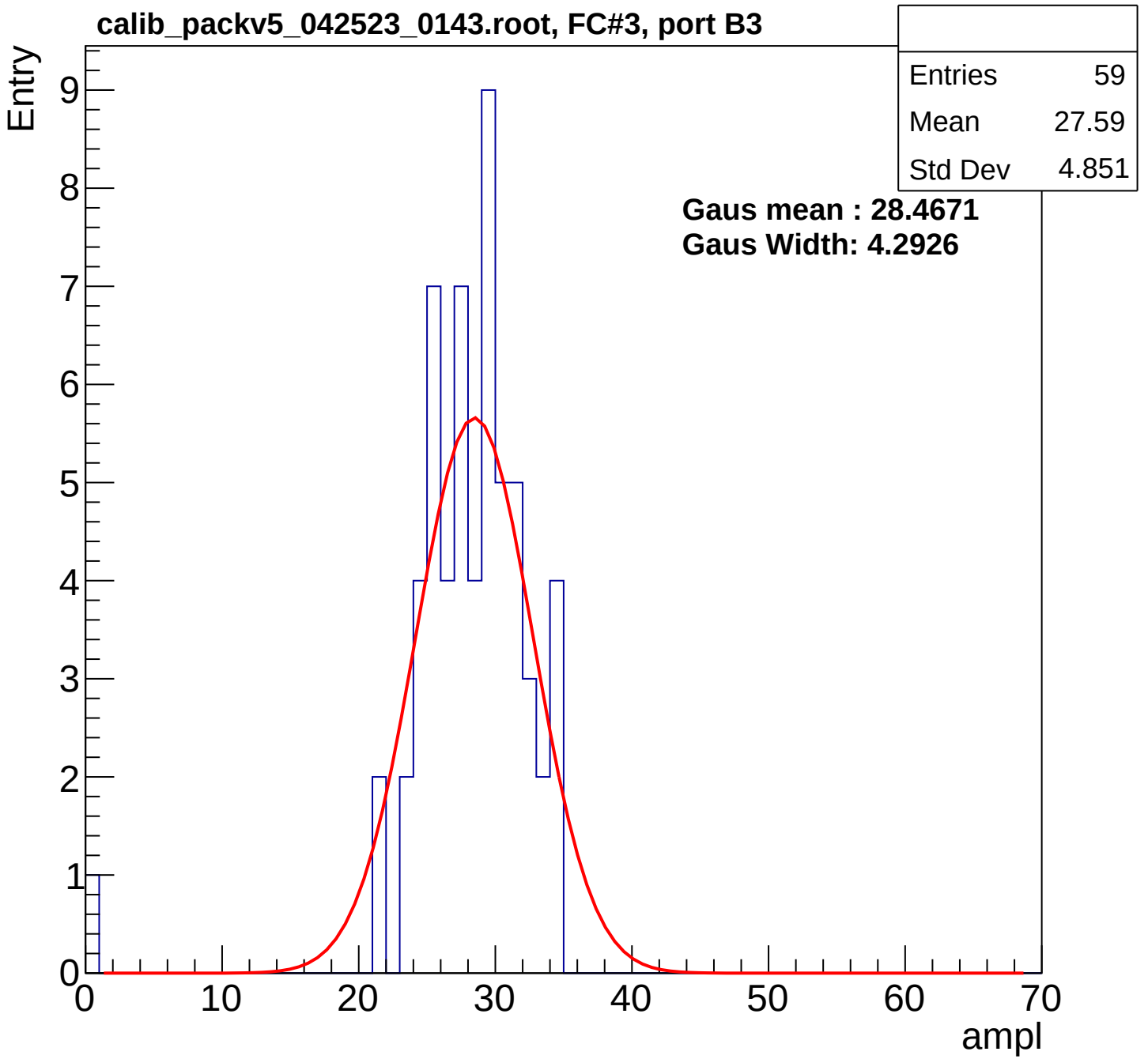
50
60
70
ampl


B1L000S, U7-ch113, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 56
Std Dev 18.69
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch113, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch114, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 27.59
Std Dev 4.851
Gaus mean : 28.4671
Gaus Width: 4.2926
Entry
ampl
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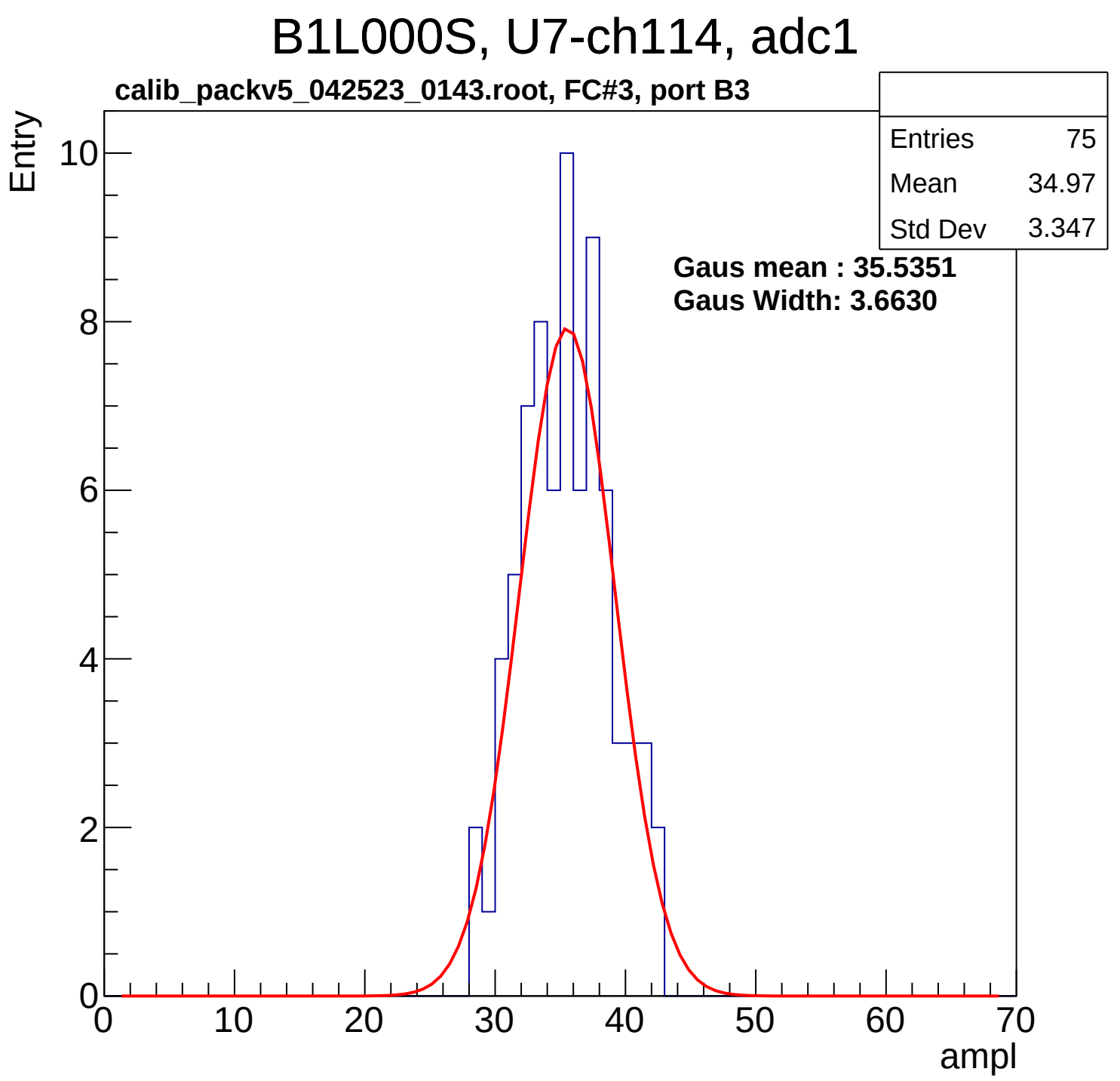
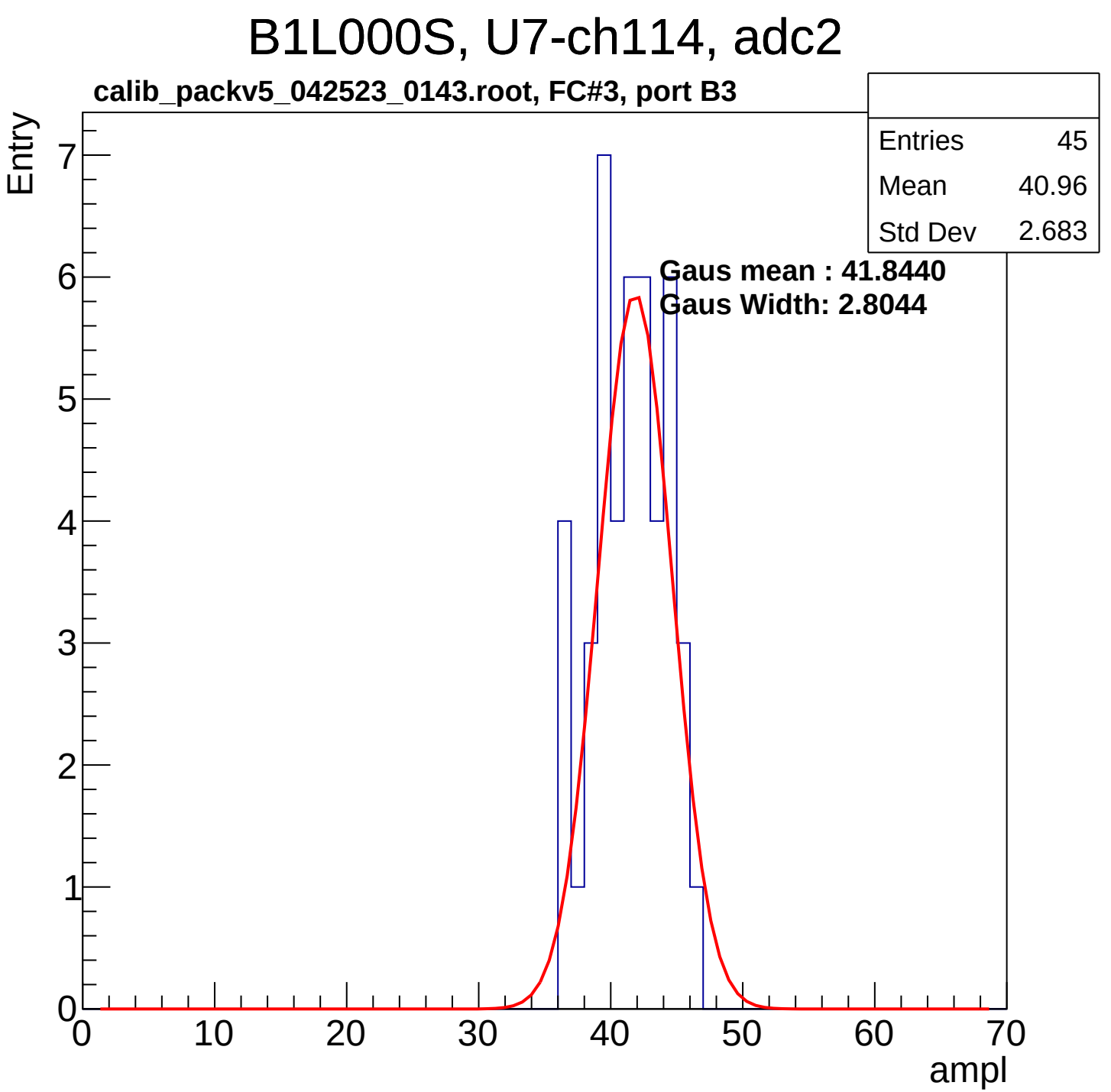
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L000S, U7-ch114, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 34.97
Std Dev 3.347
Gaus mean : 35.5351
Gaus Width: 3.6630
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch114, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 40.96
Std Dev 2.683
Gaus mean : 41.8440
Gaus Width: 2.8044
Entry
ampl
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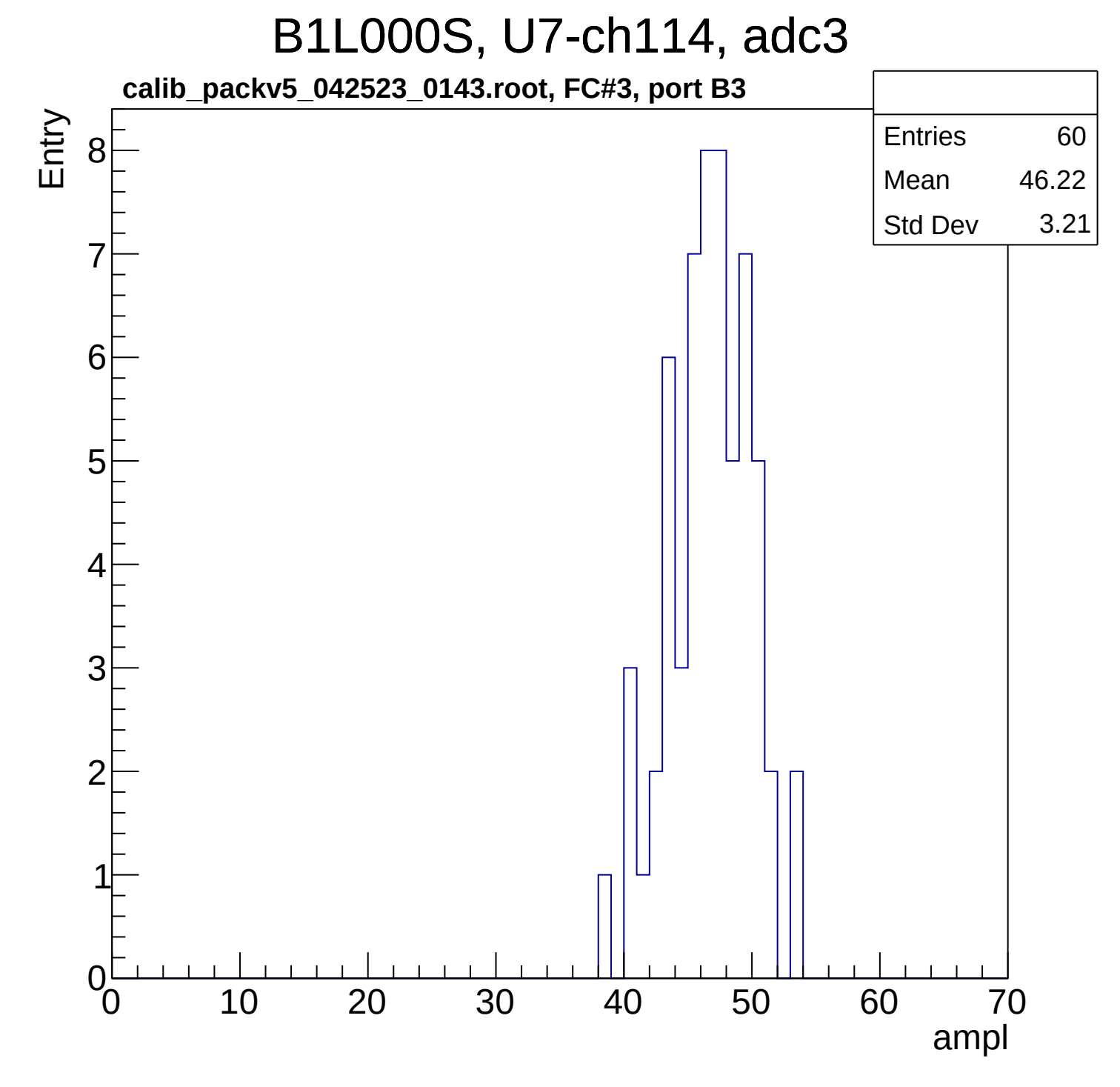
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch114, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 46.22
Std Dev 3.21
Entry
ampl
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0
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60
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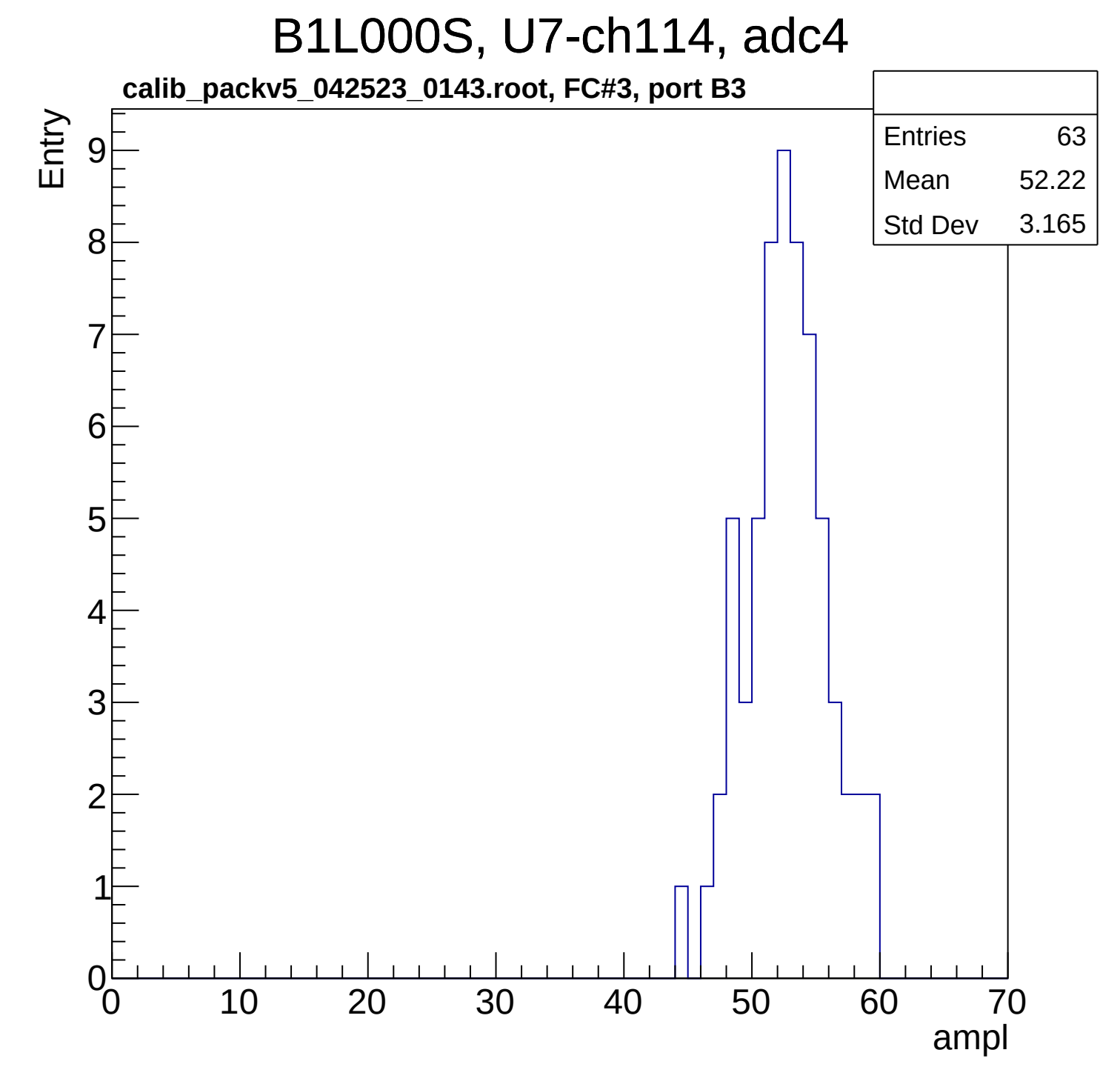
B1L000S, U7-ch114, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 52.22
Std Dev 3.165
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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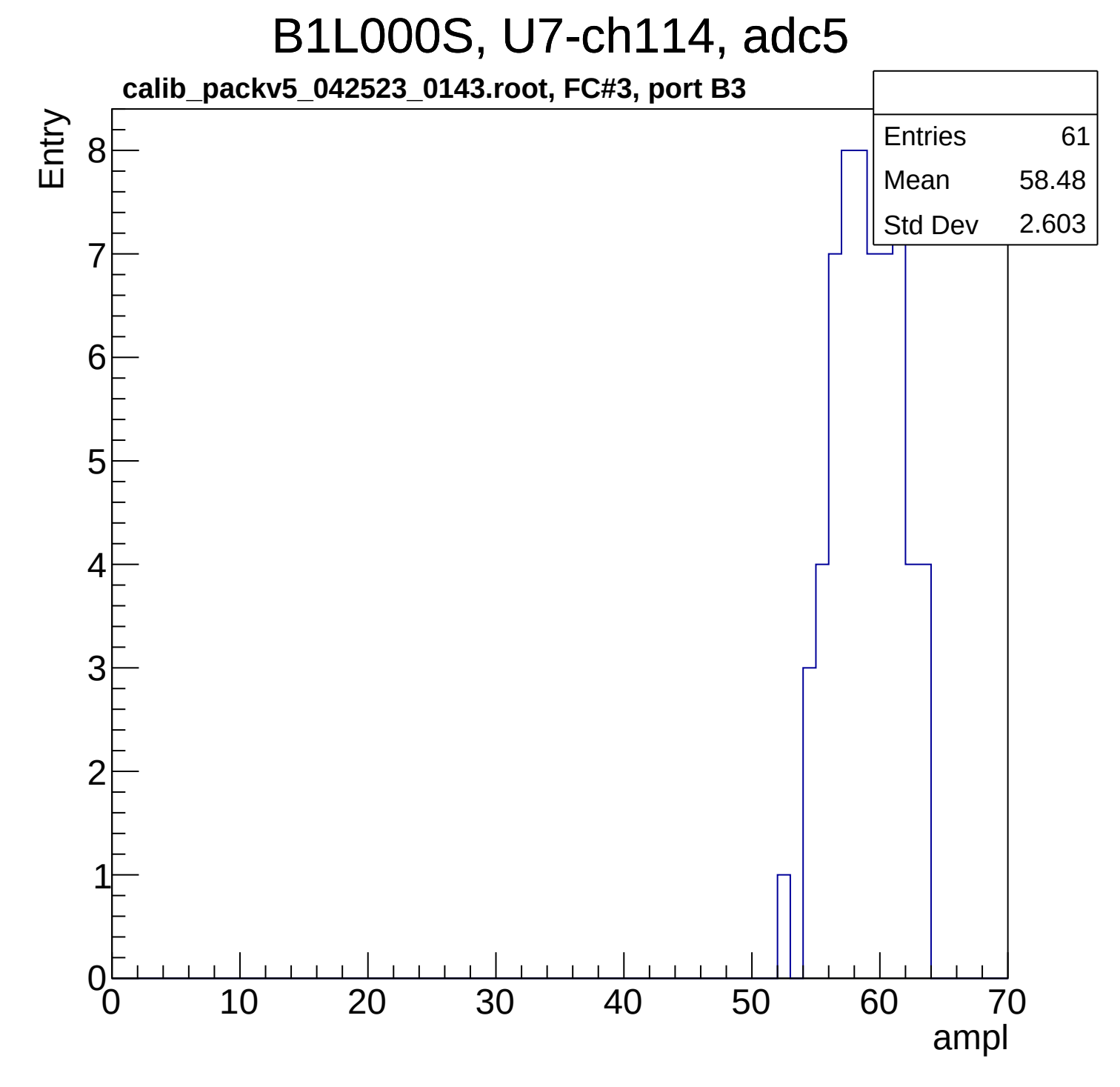
B1L000S, U7-ch114, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.48
Std Dev 2.603
Entry
ampl
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0
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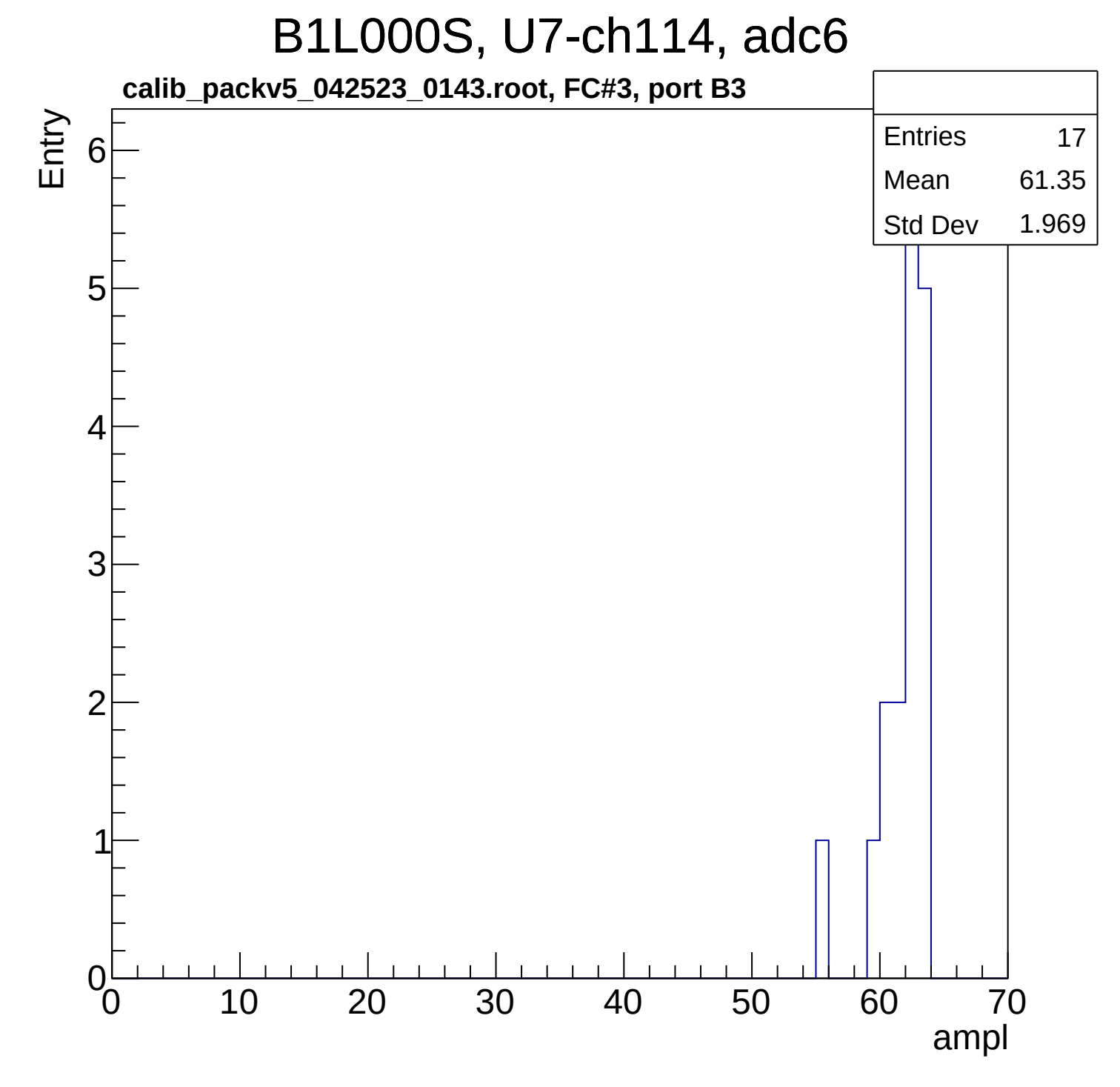
B1L000S, U7-ch114, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 61.35
Std Dev 1.969
Entry
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2
3
4
5
6
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10
20
30
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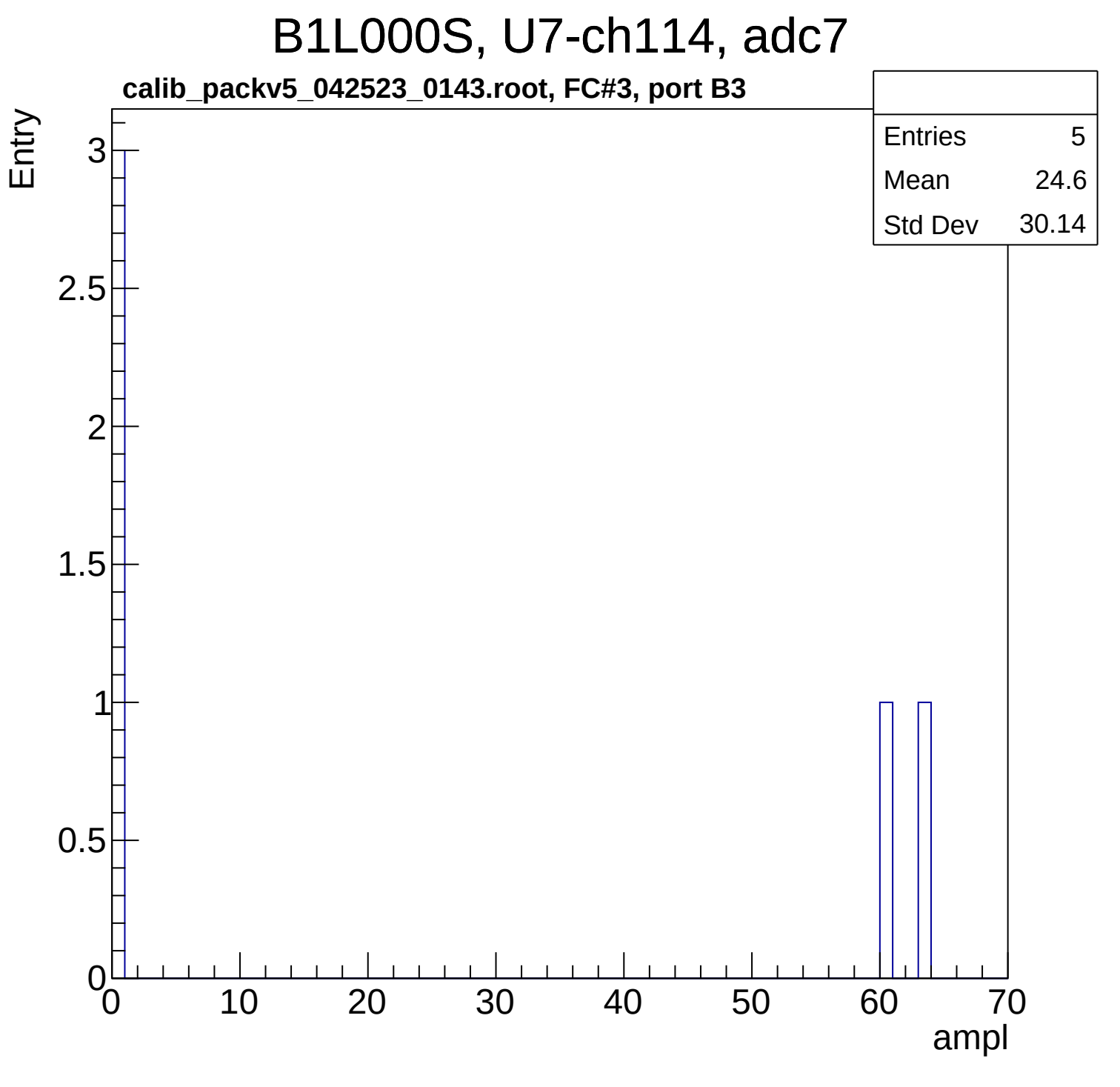
50
60
70
ampl


B1L000S, U7-ch114, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 5
Mean 24.6
Std Dev 30.14
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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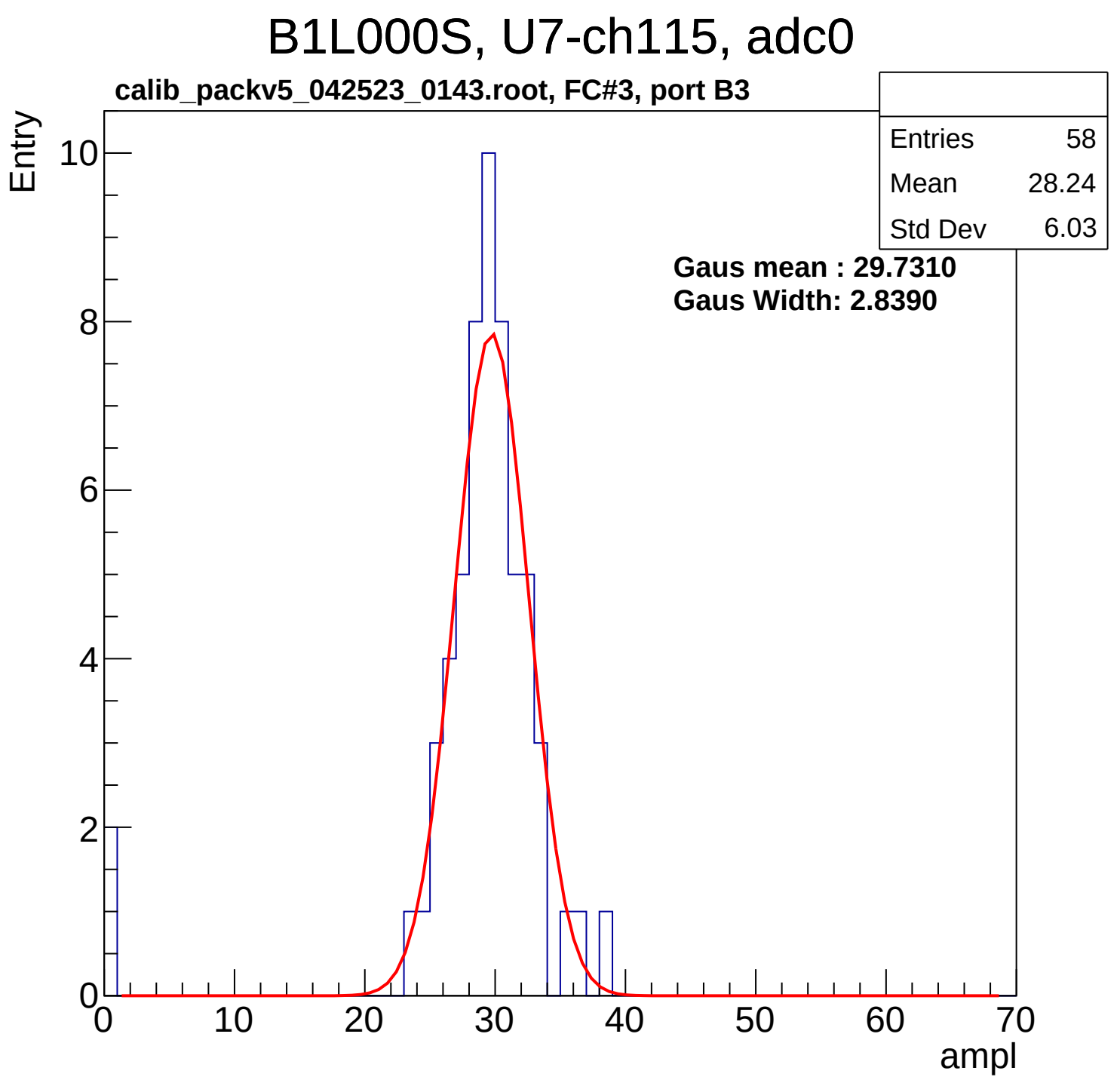
B1L000S, U7-ch115, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 28.24
Std Dev 6.03
Gaus mean : 29.7310
Gaus Width: 2.8390
Entry
ampl
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4
6
8
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0
10
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40
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60
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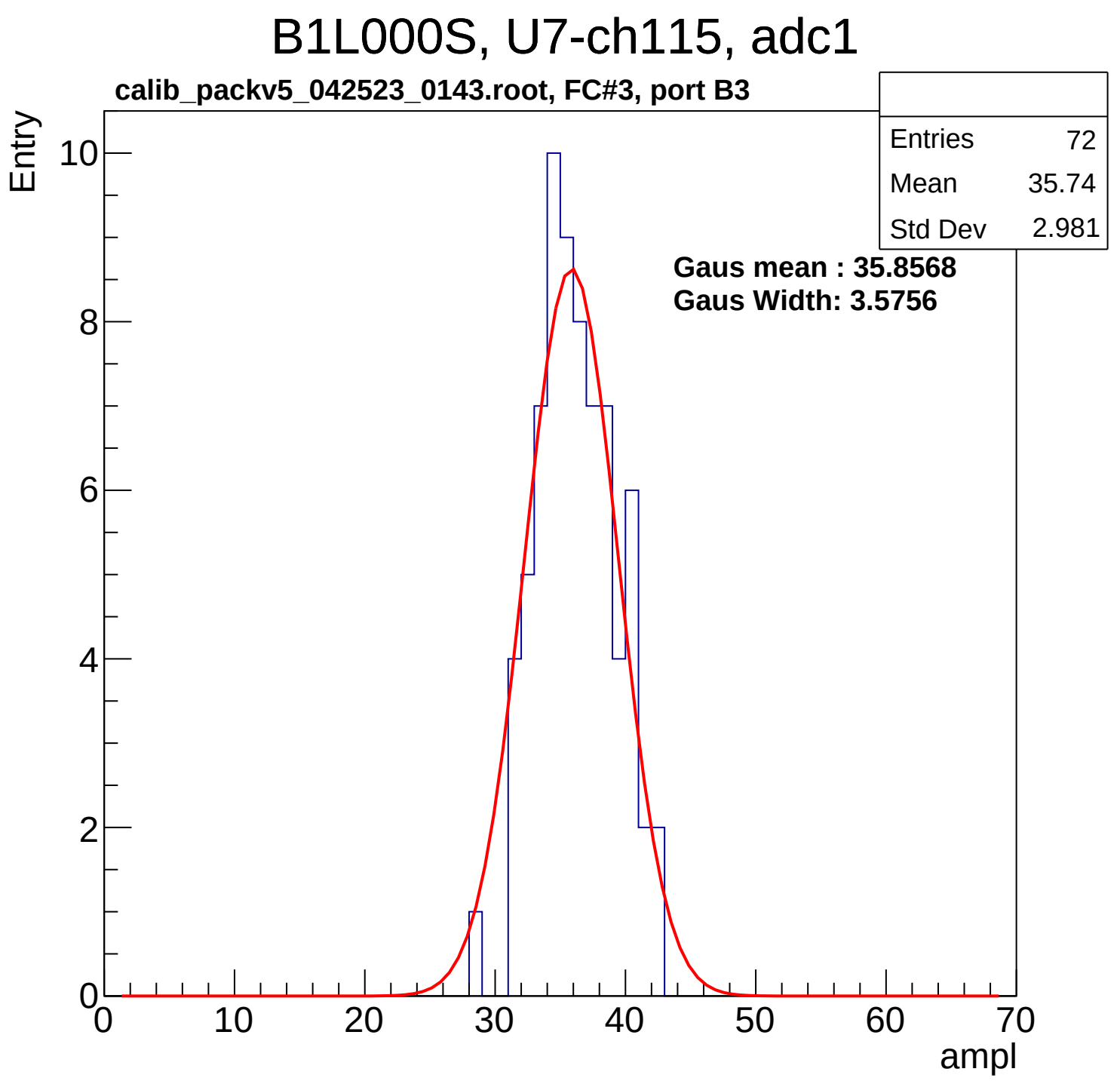
B1L000S, U7-ch115, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 35.74
Std Dev 2.981
Gaus mean : 35.8568
Gaus Width: 3.5756
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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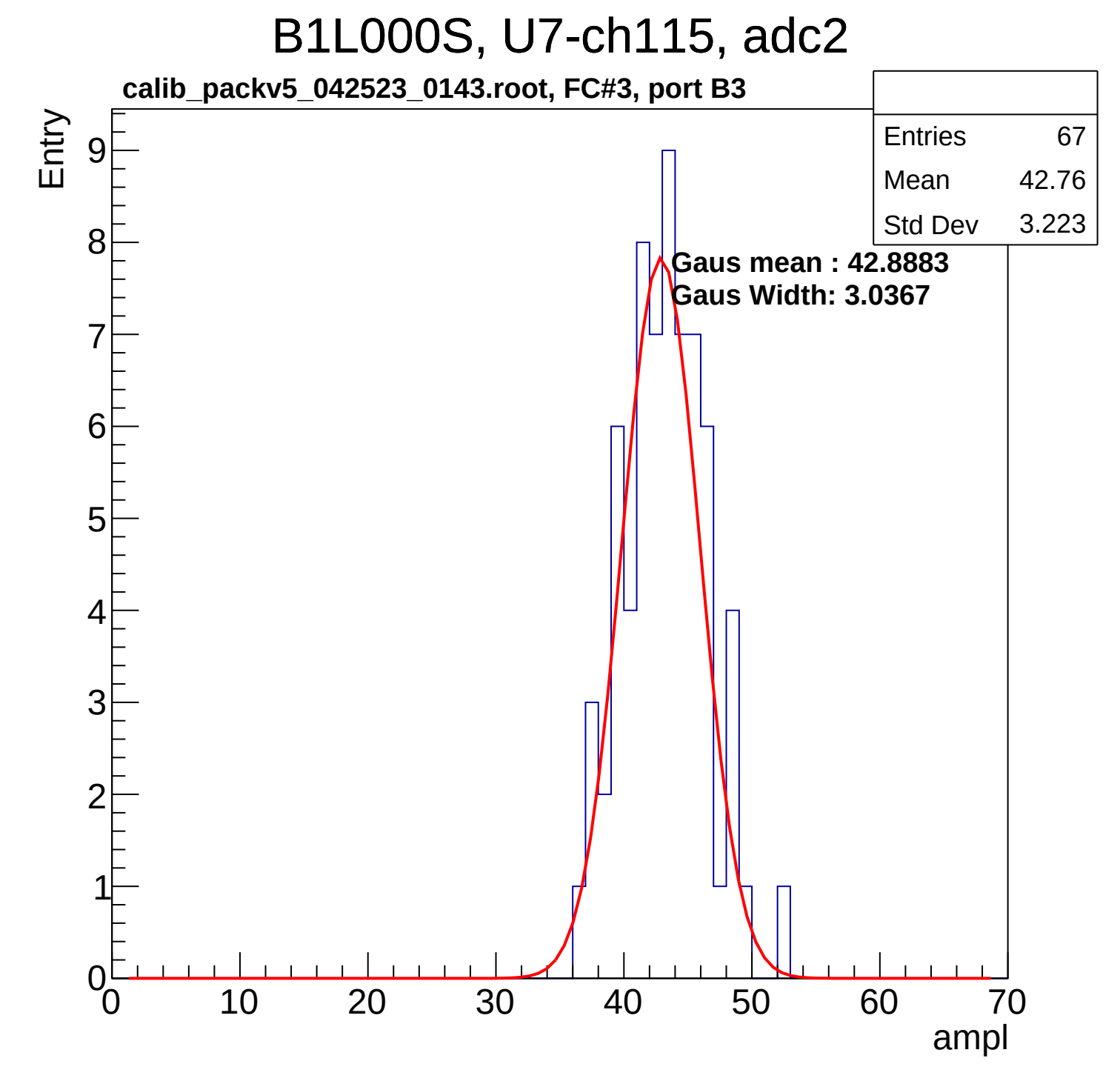
B1L000S, U7-ch115, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 42.76
Std Dev 3.223
Gaus mean : 42.8883
Gaus Width: 3.0367
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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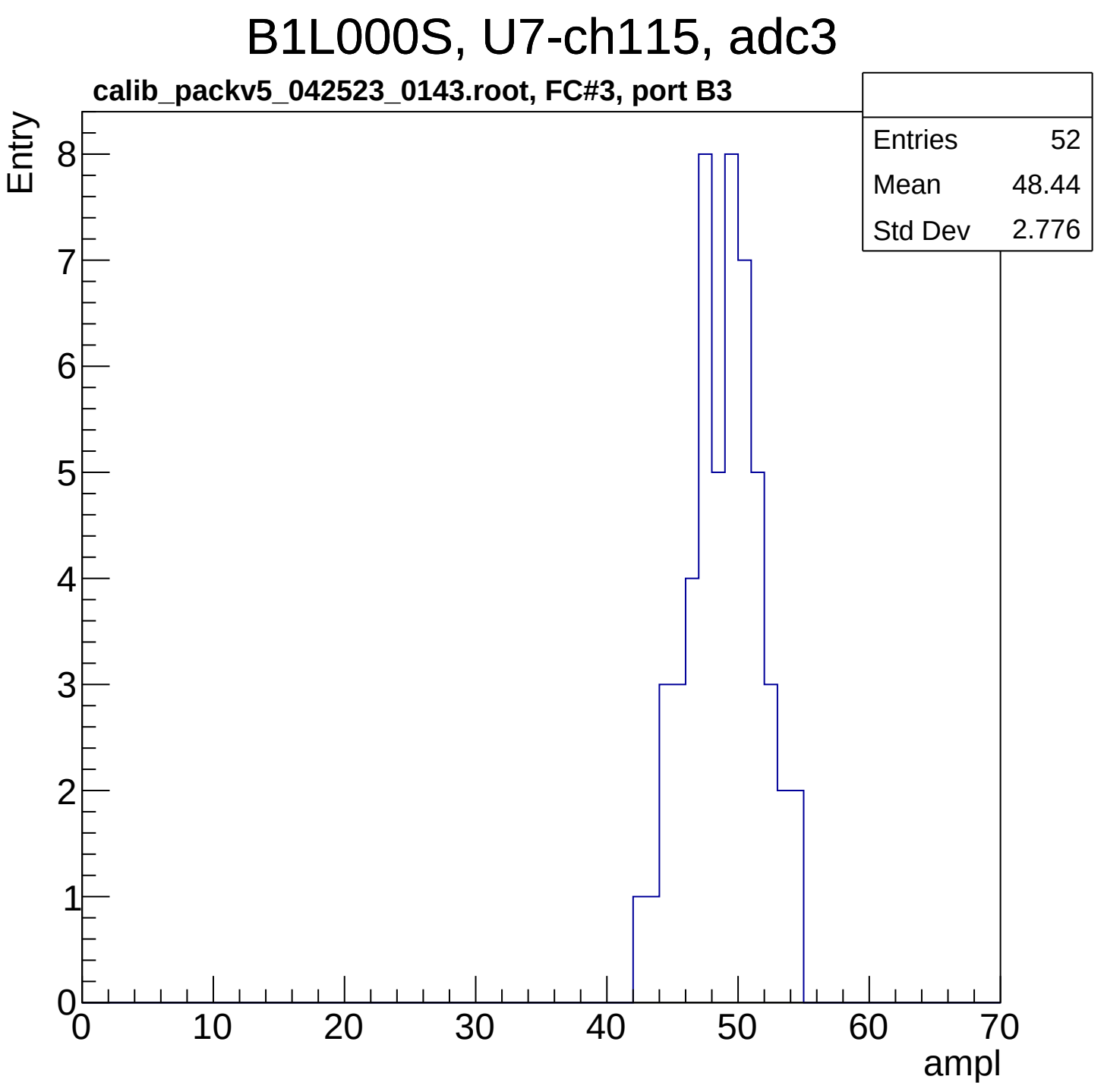
B1L000S, U7-ch115, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 48.44
Std Dev 2.776
Entry
ampl
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4
5
6
7
8
0
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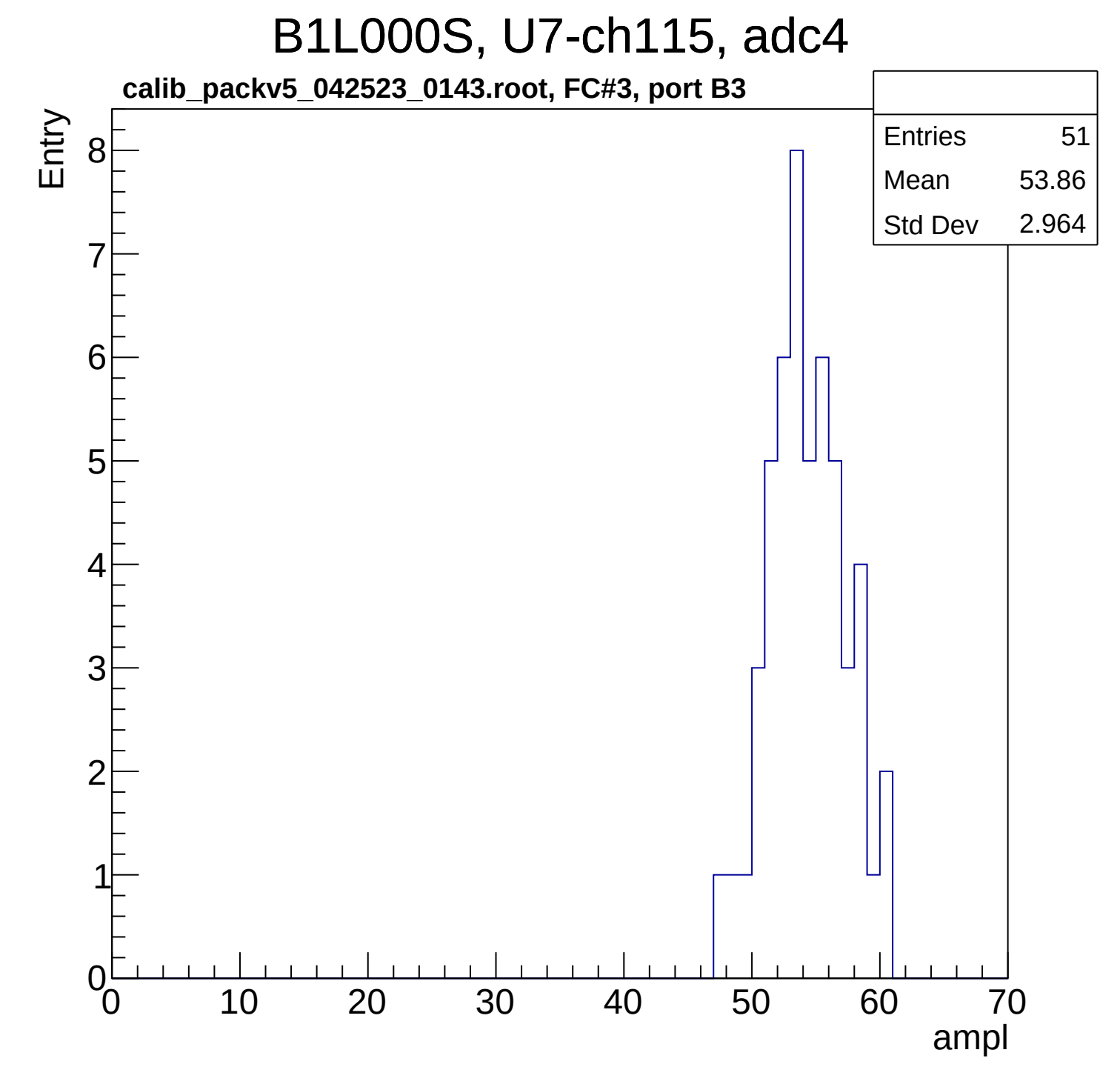
B1L000S, U7-ch115, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 53.86
Std Dev 2.964
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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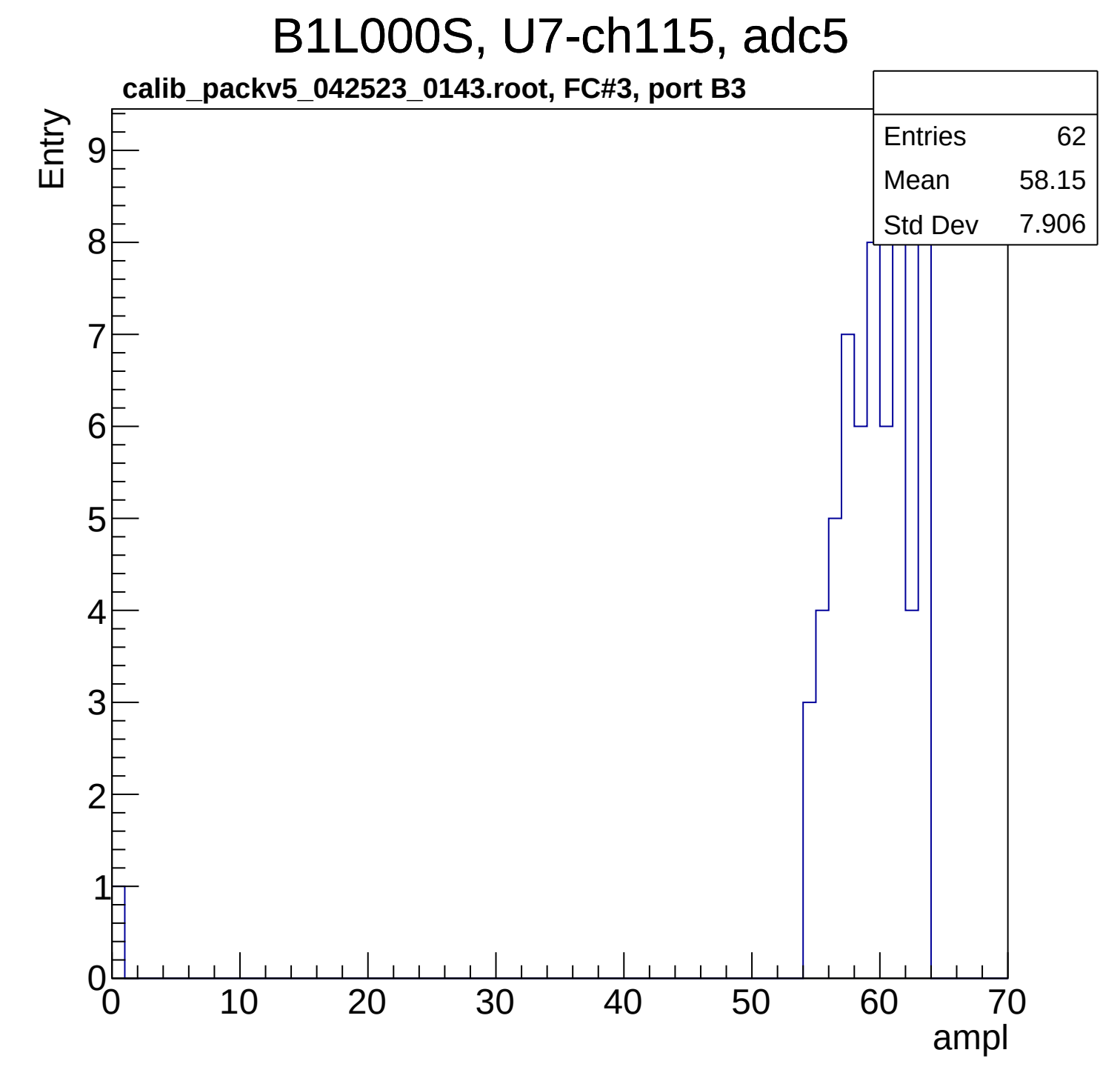
B1L000S, U7-ch115, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 58.15
Std Dev 7.906
Entry
ampl
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2
3
4
5
6
7
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0
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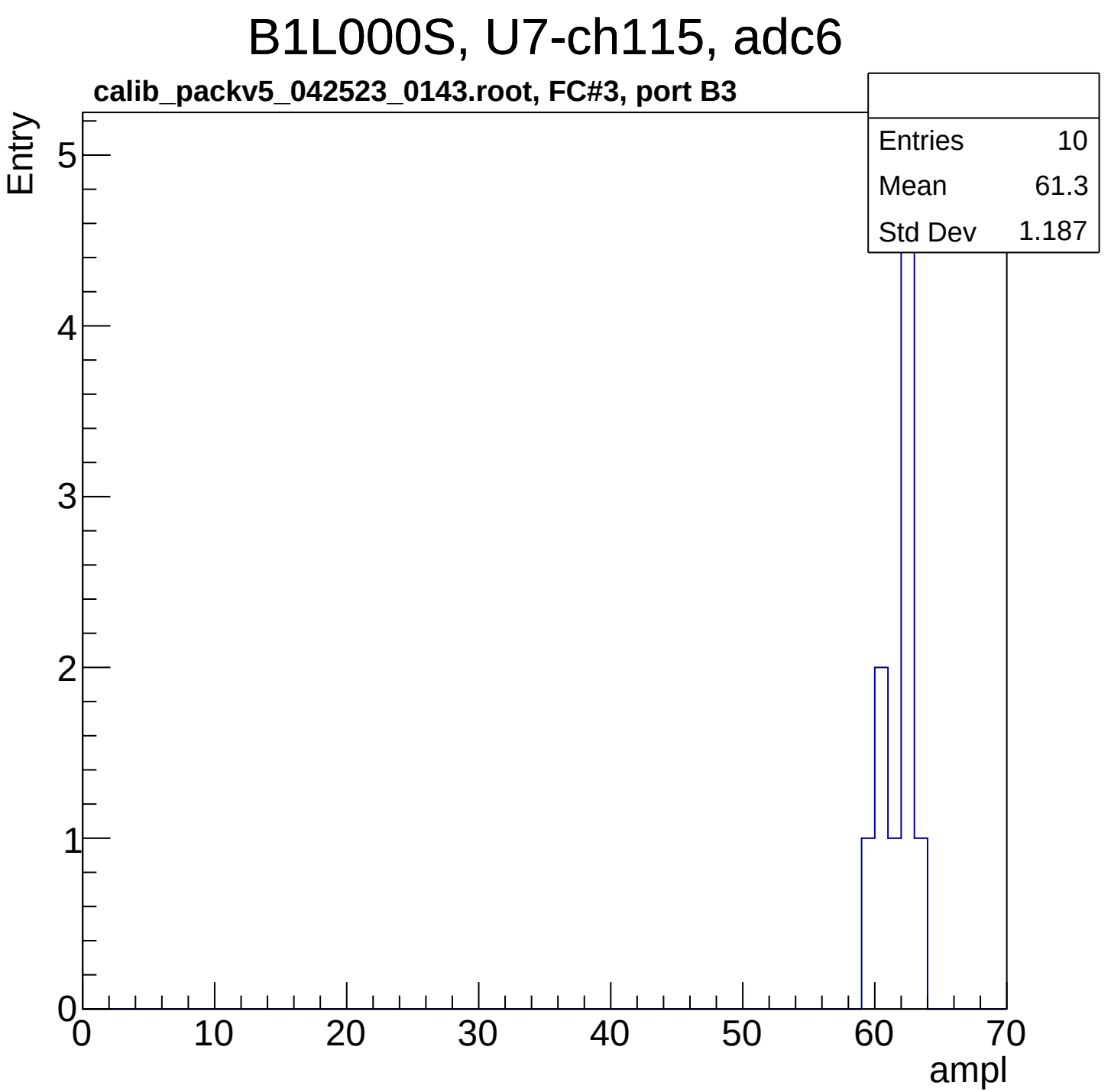
B1L000S, U7-ch115, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.3
Std Dev 1.187
Entry
0
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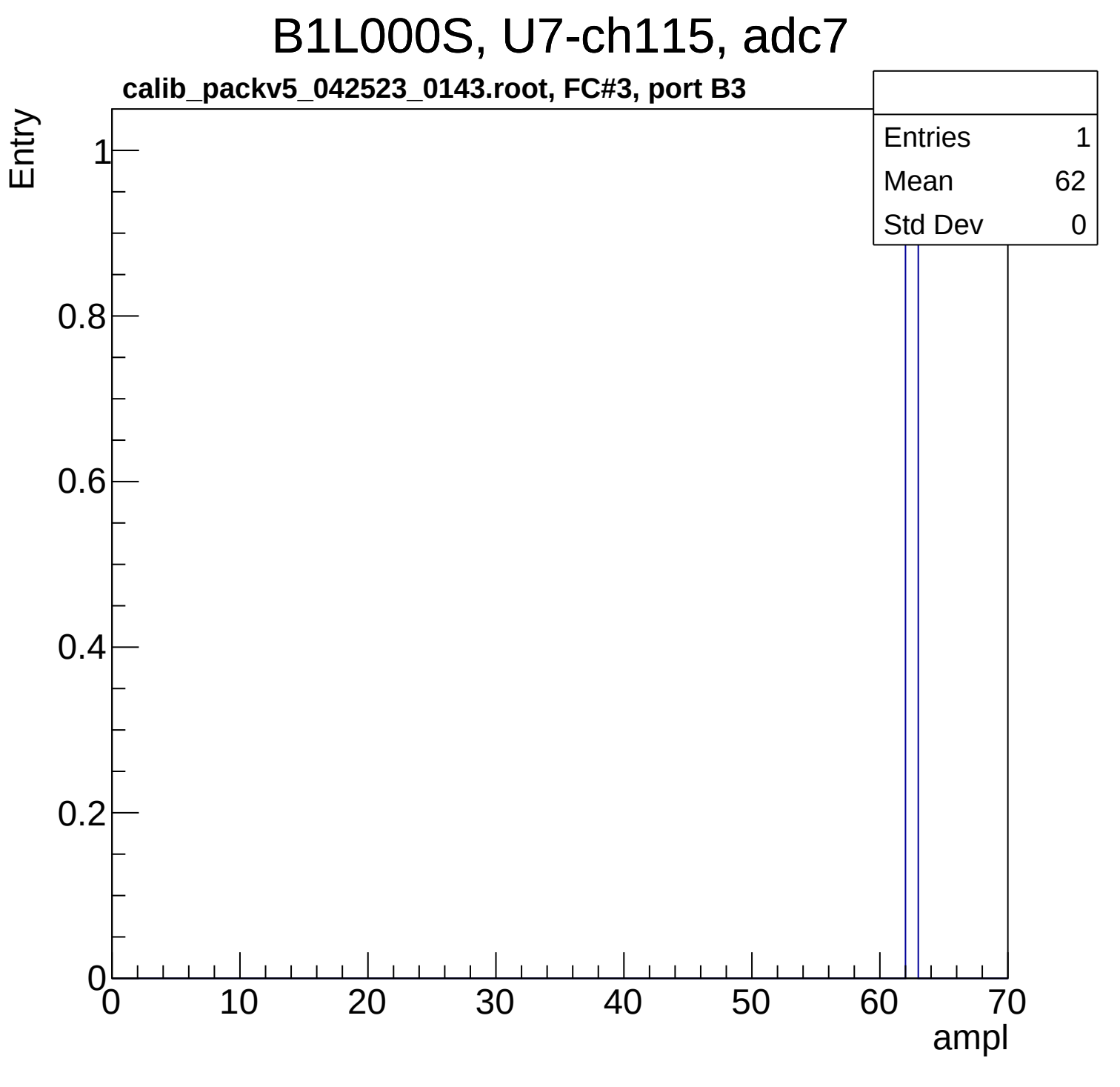
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch115, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 62
Std Dev 0
Entry
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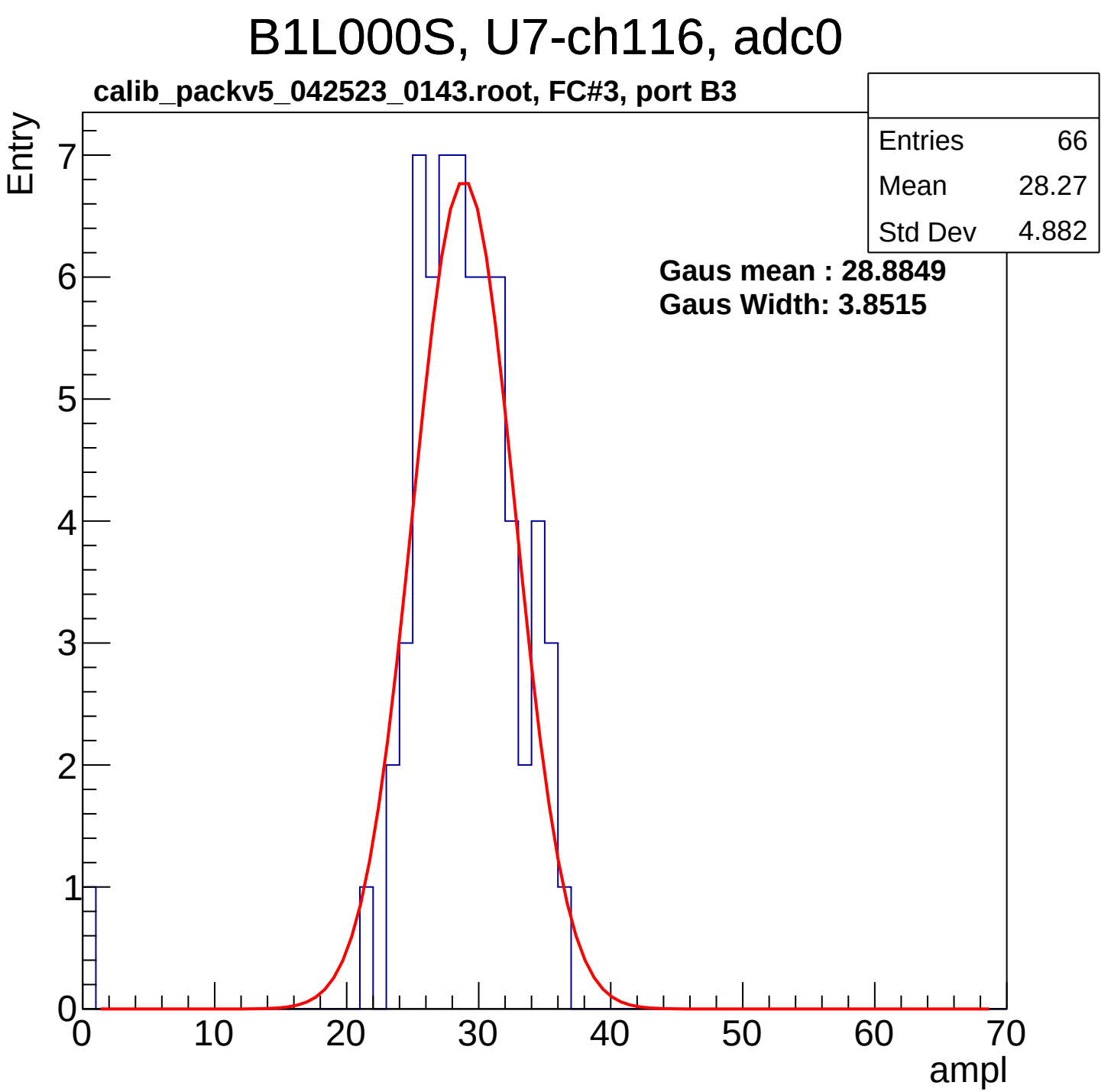
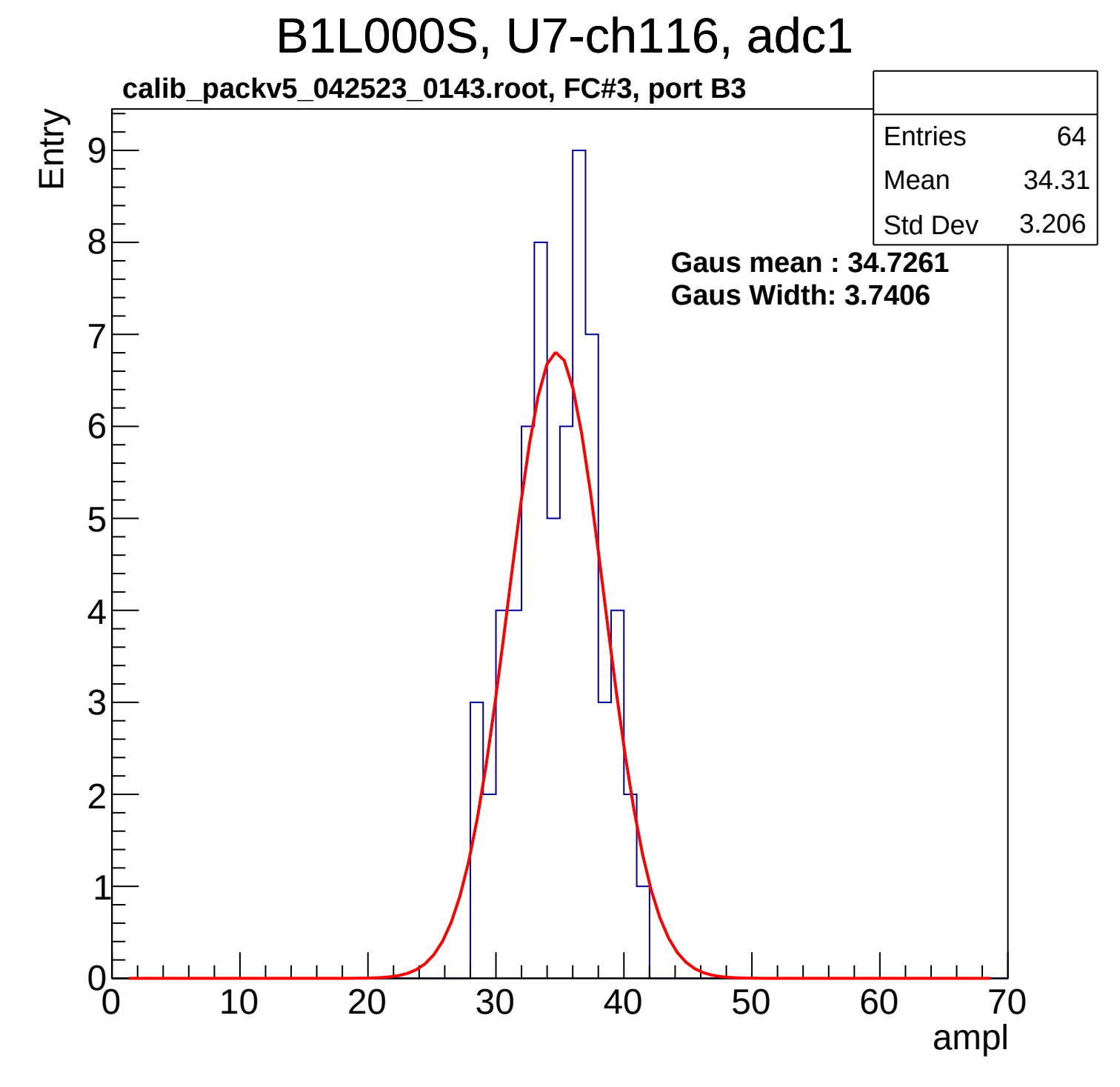
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch116, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 66
Mean 28.27
Std Dev 4.882
Gaus mean : 28.8849
Gaus Width: 3.8515
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch116, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 34.31
Std Dev 3.206
Gaus mean : 34.7261
Gaus Width: 3.7406
Entry
ampl
0
10
20
30
40
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1
2
3
4
5
6
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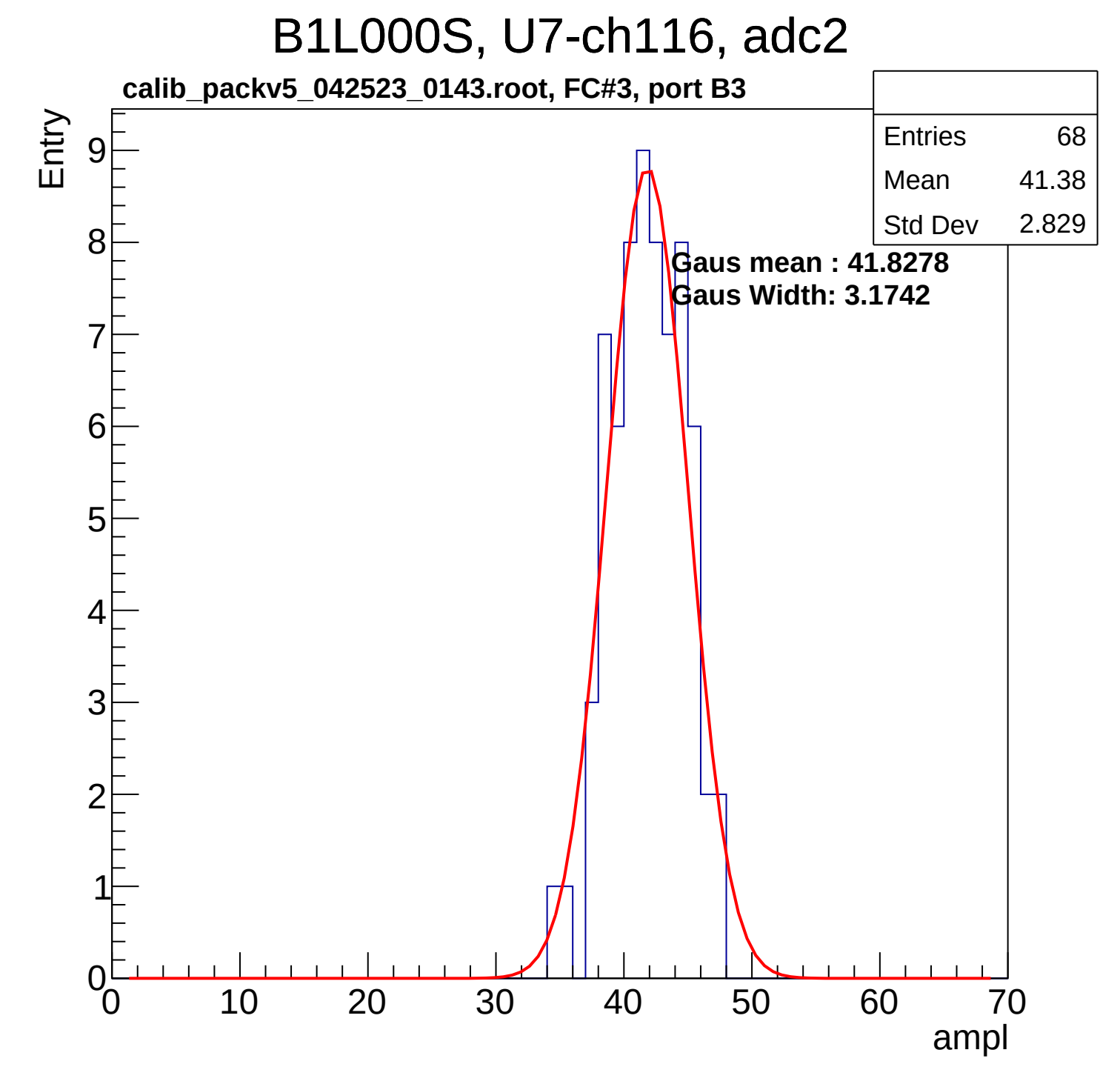
8
9


B1L000S, U7-ch116, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 41.38
Std Dev 2.829
Gaus mean : 41.8278
Gaus Width: 3.1742
Entry
ampl
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30
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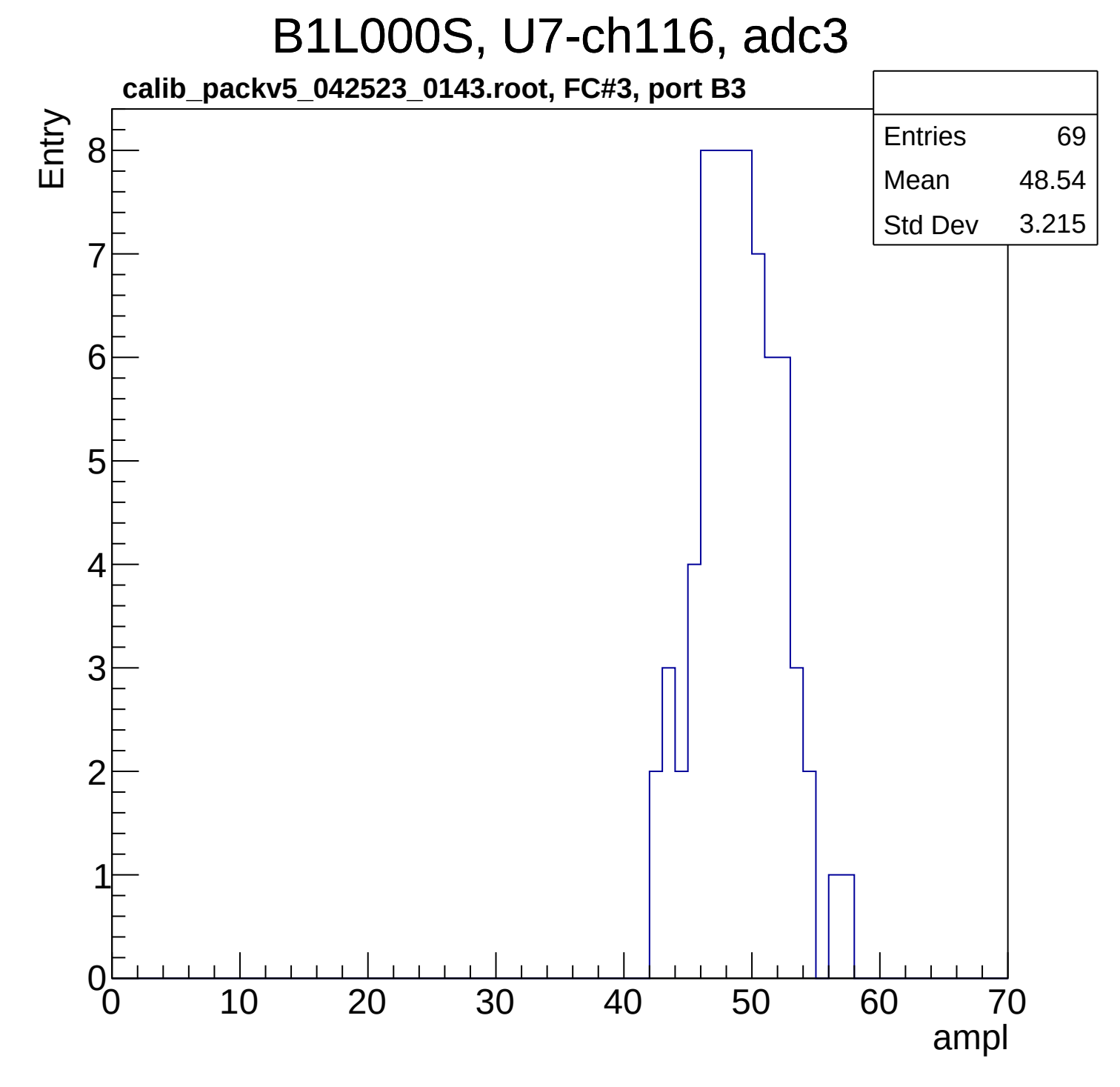
B1L000S, U7-ch116, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 48.54
Std Dev 3.215
Entry
ampl
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2
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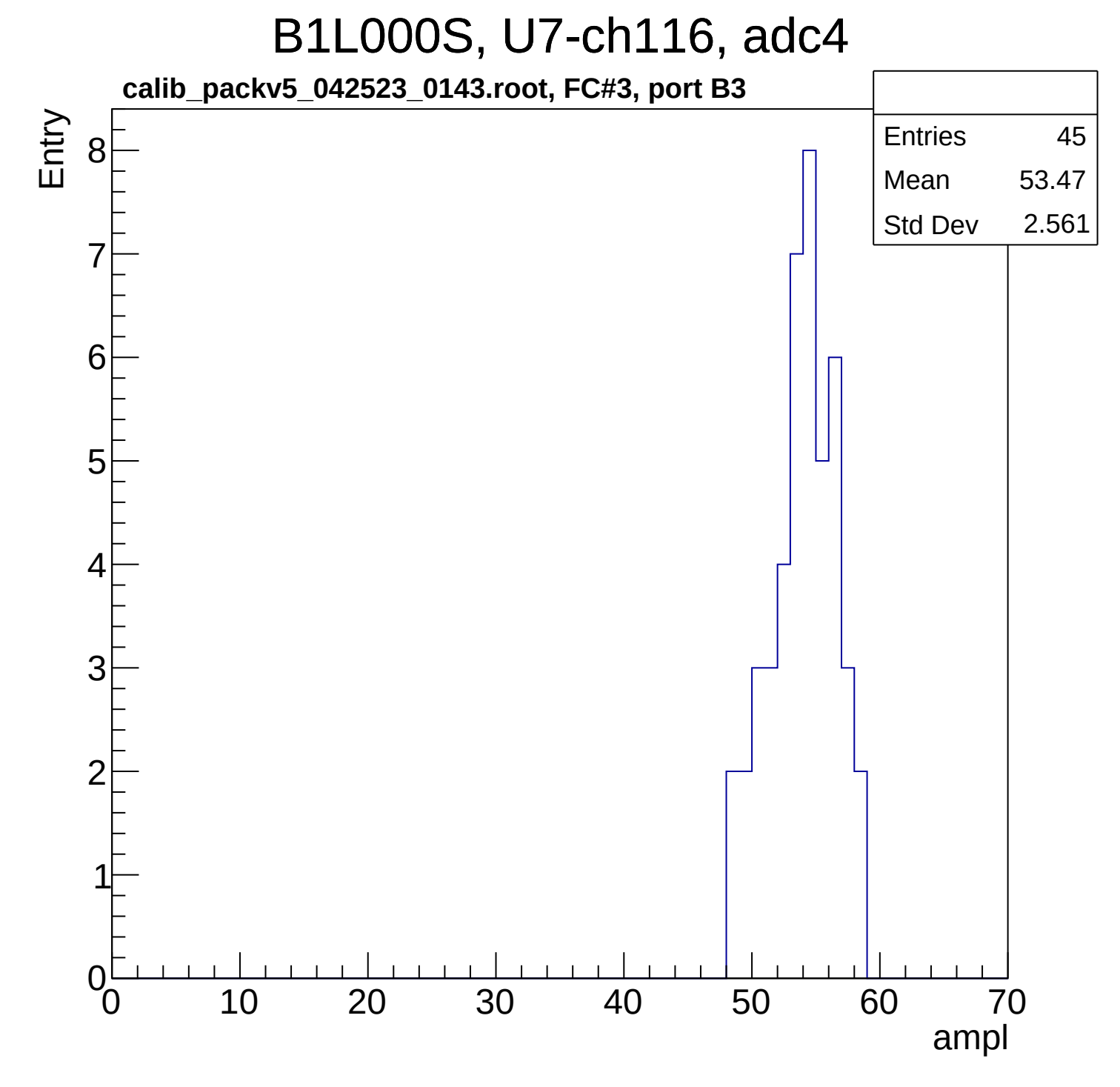
B1L000S, U7-ch116, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 45
Mean 53.47
Std Dev 2.561
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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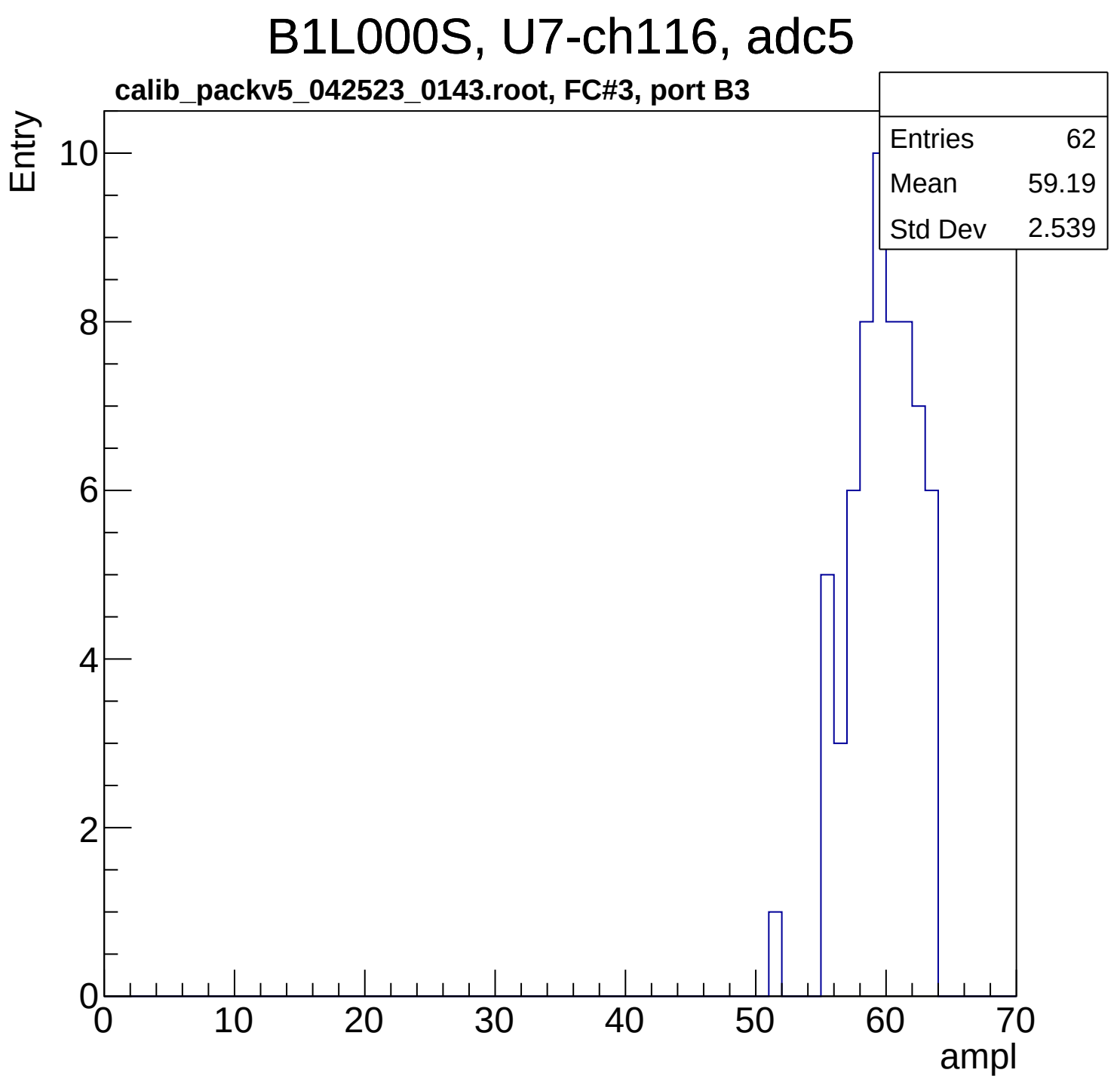
B1L000S, U7-ch116, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 59.19
Std Dev 2.539
Entry
ampl
0
2
4
6
8
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0
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20
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40
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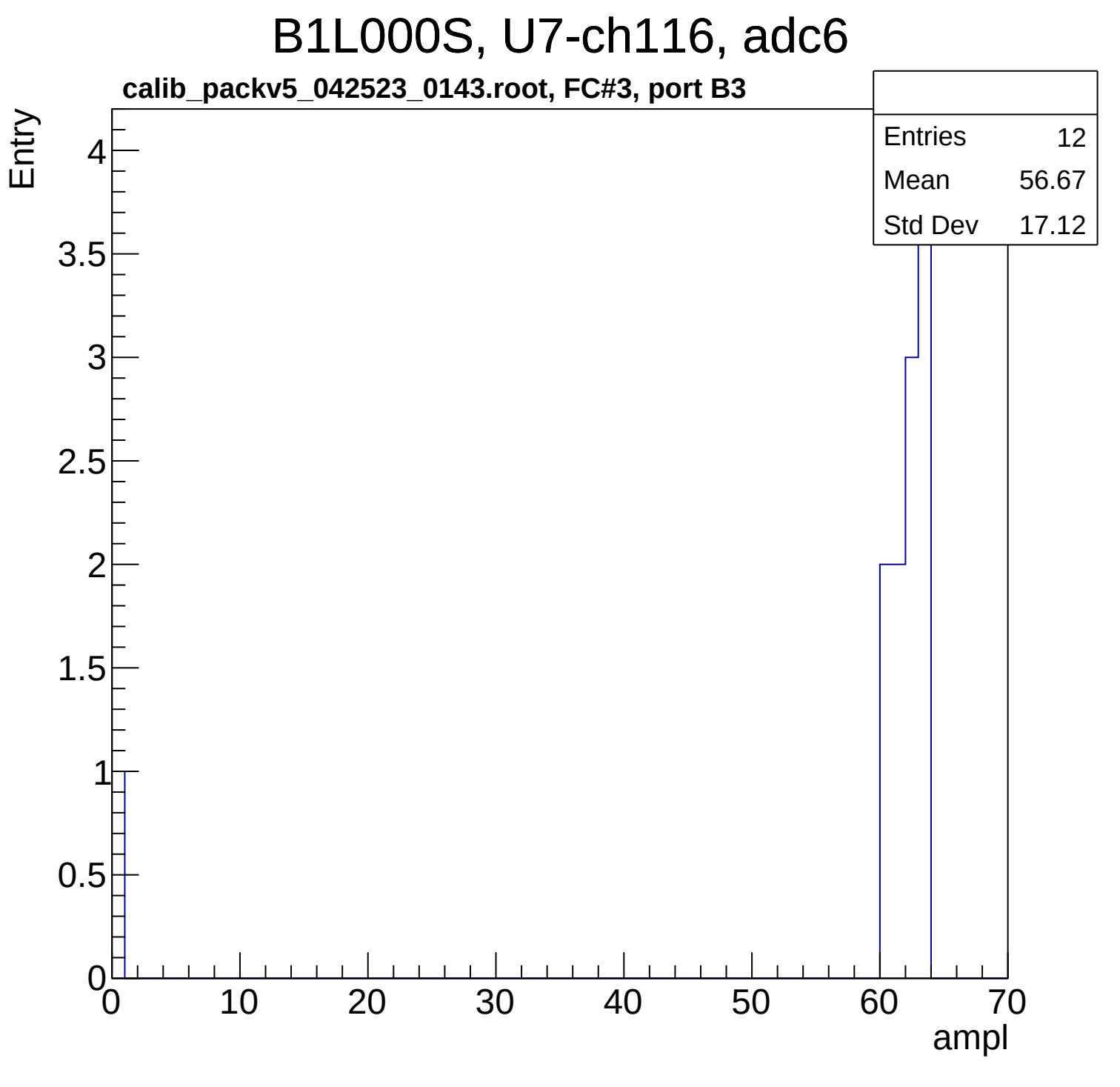
B1L000S, U7-ch116, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 56.67
Std Dev 17.12
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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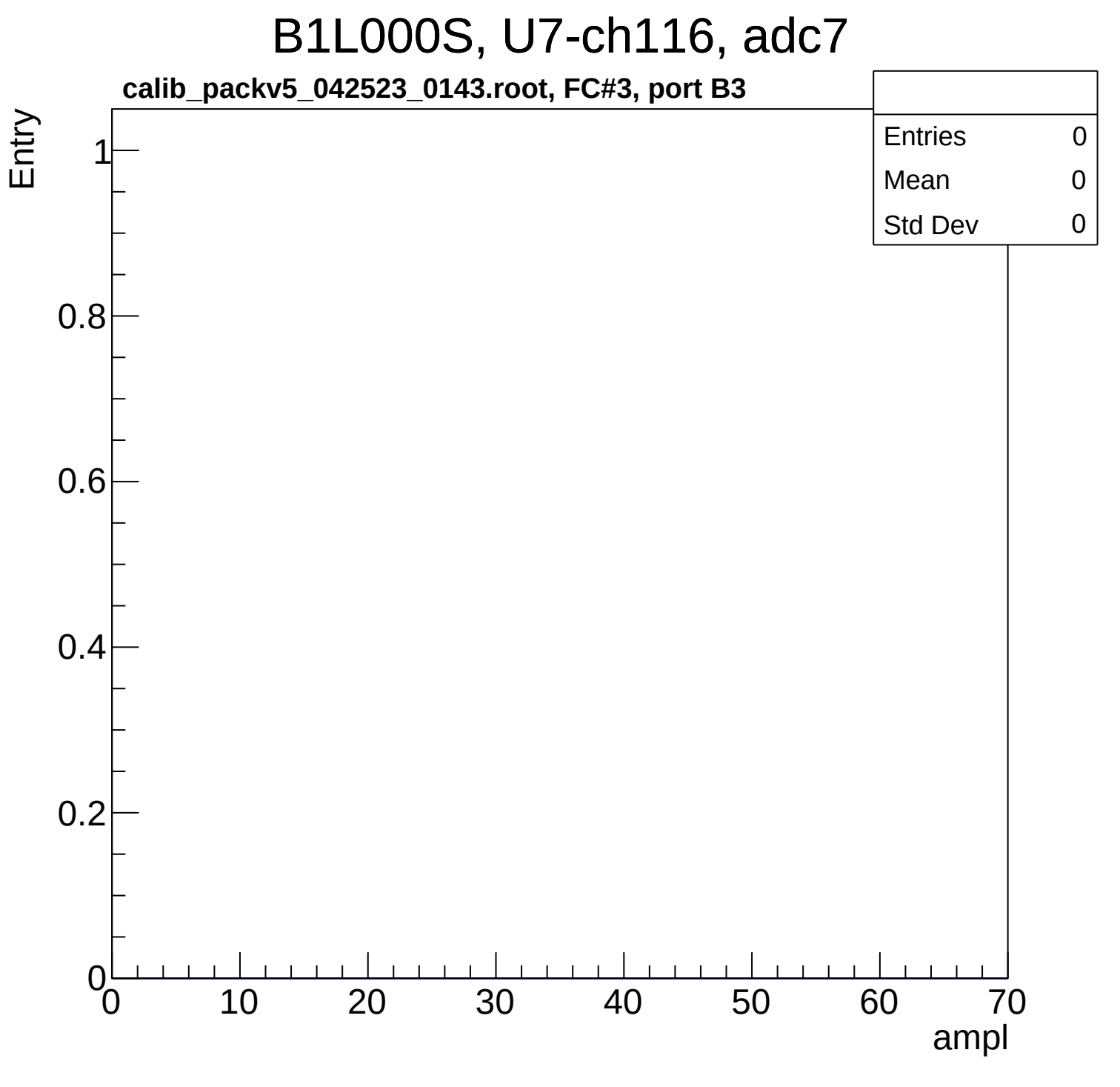
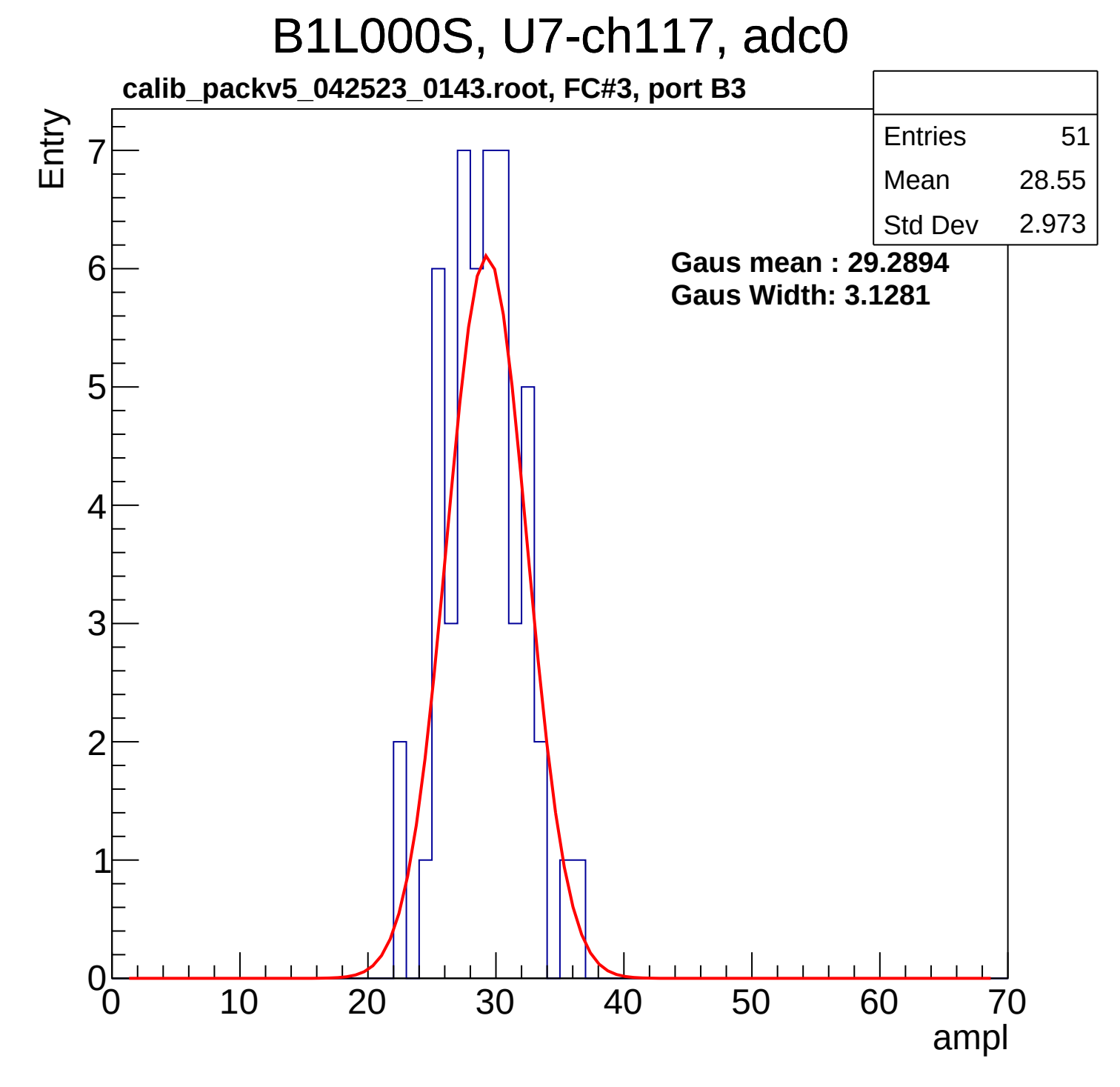

B1L000S, U7-ch116, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch117, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 28.55
Std Dev 2.973
Gaus mean : 29.2894
Gaus Width: 3.1281
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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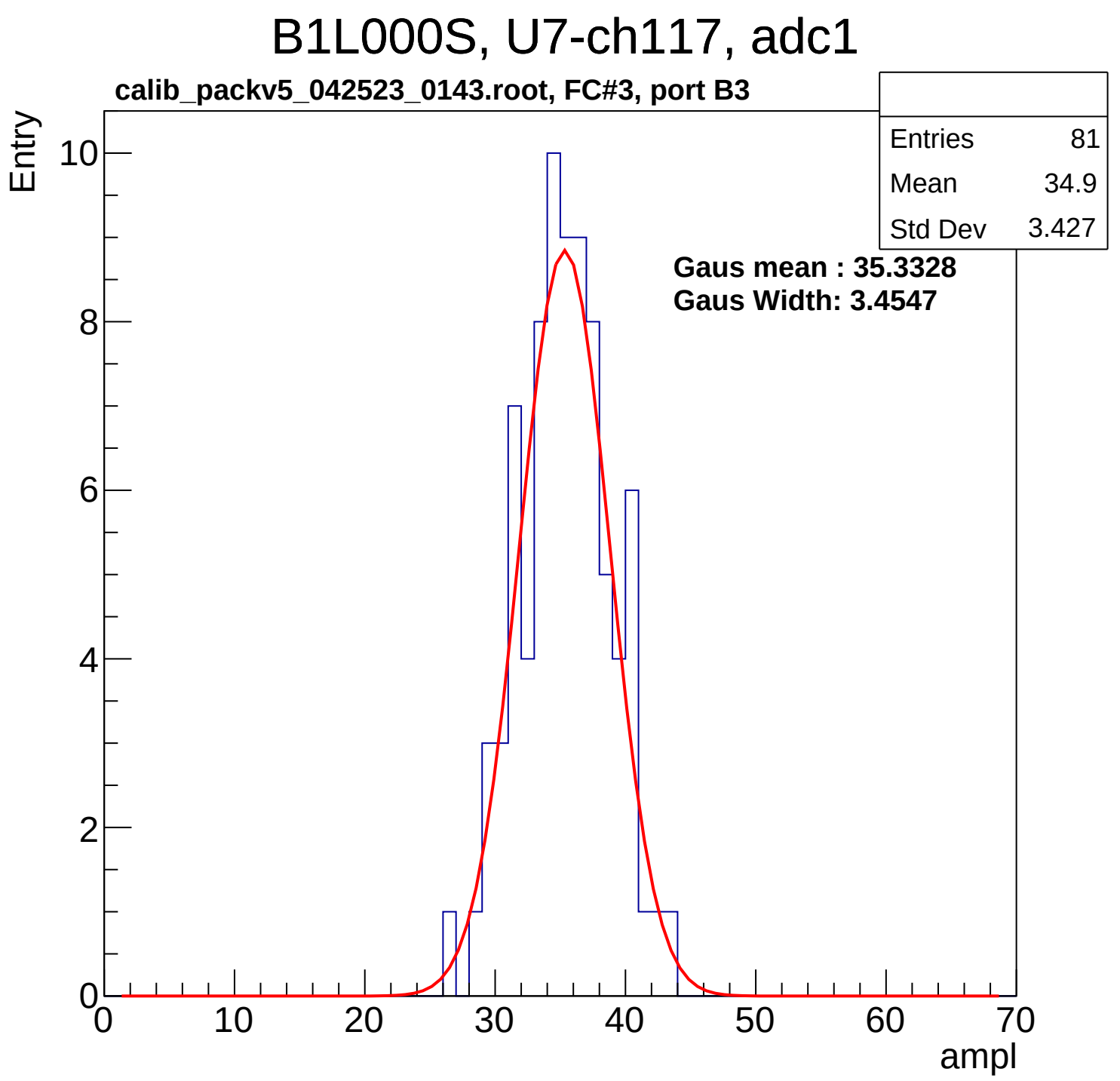
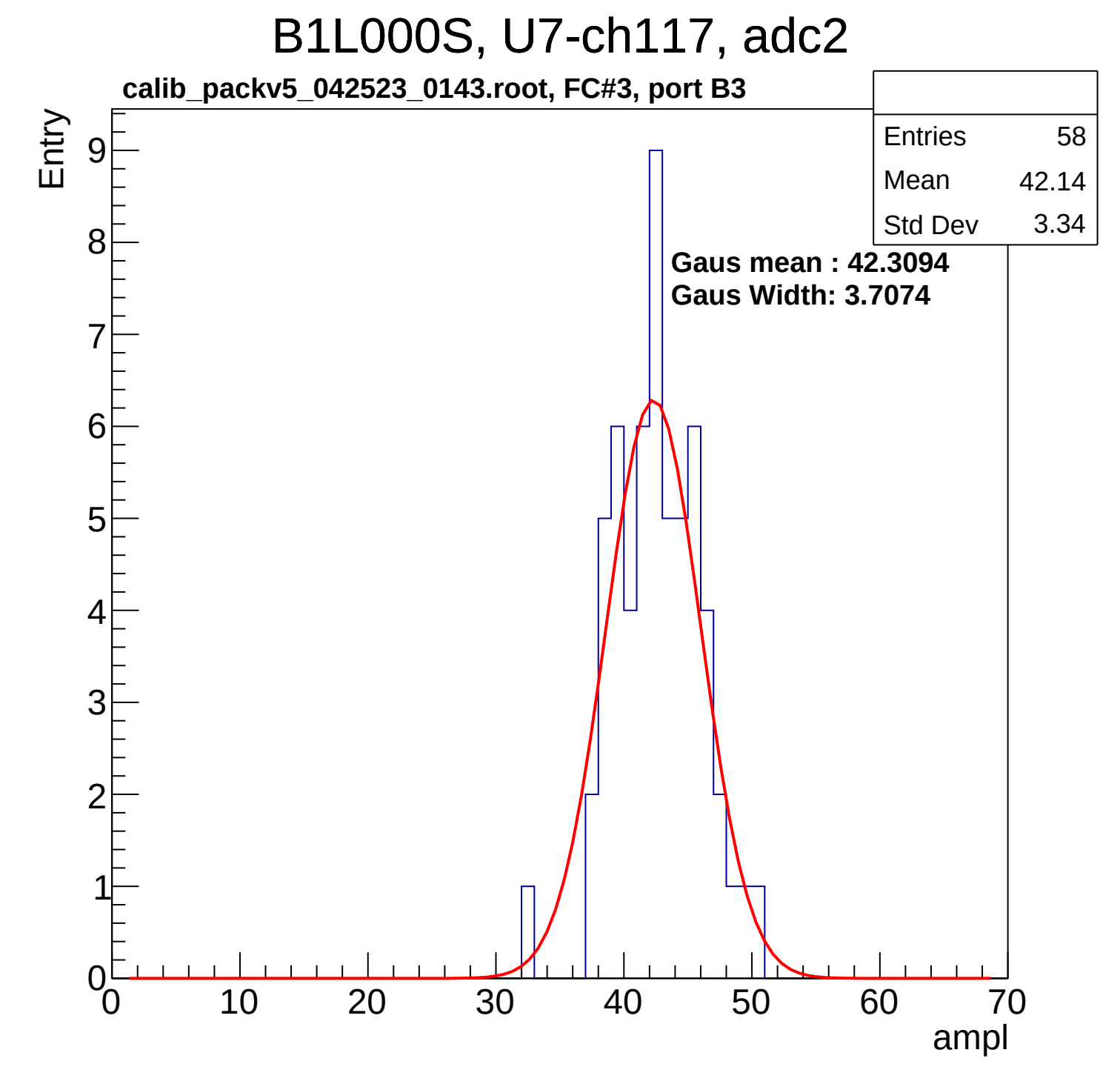

B1L000S, U7-ch117, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 81
Mean 34.9
Std Dev 3.427
Gaus mean : 35.3328
Gaus Width: 3.4547
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch117, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 42.14
Std Dev 3.34
Gaus mean : 42.3094
Gaus Width: 3.7074
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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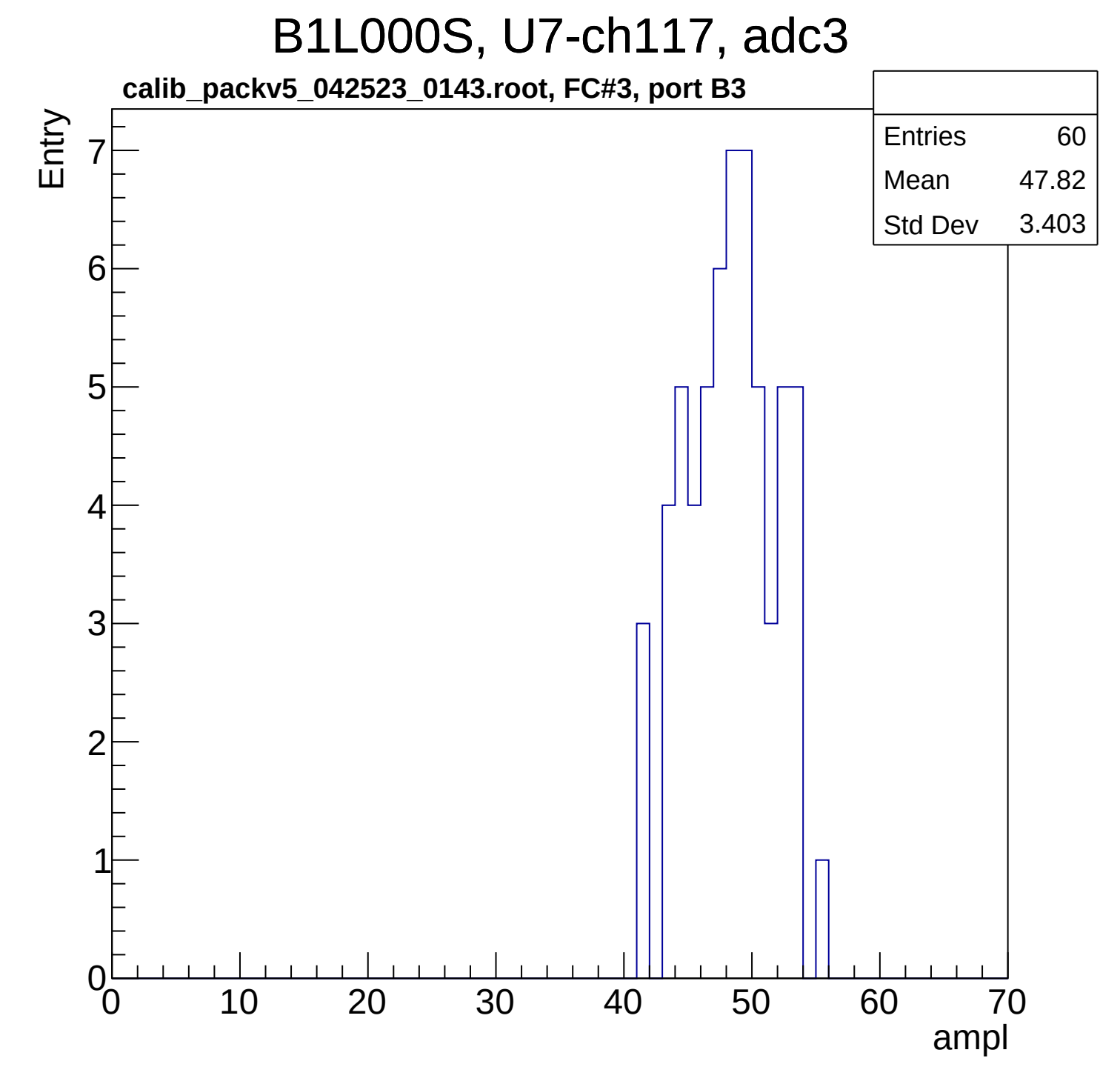
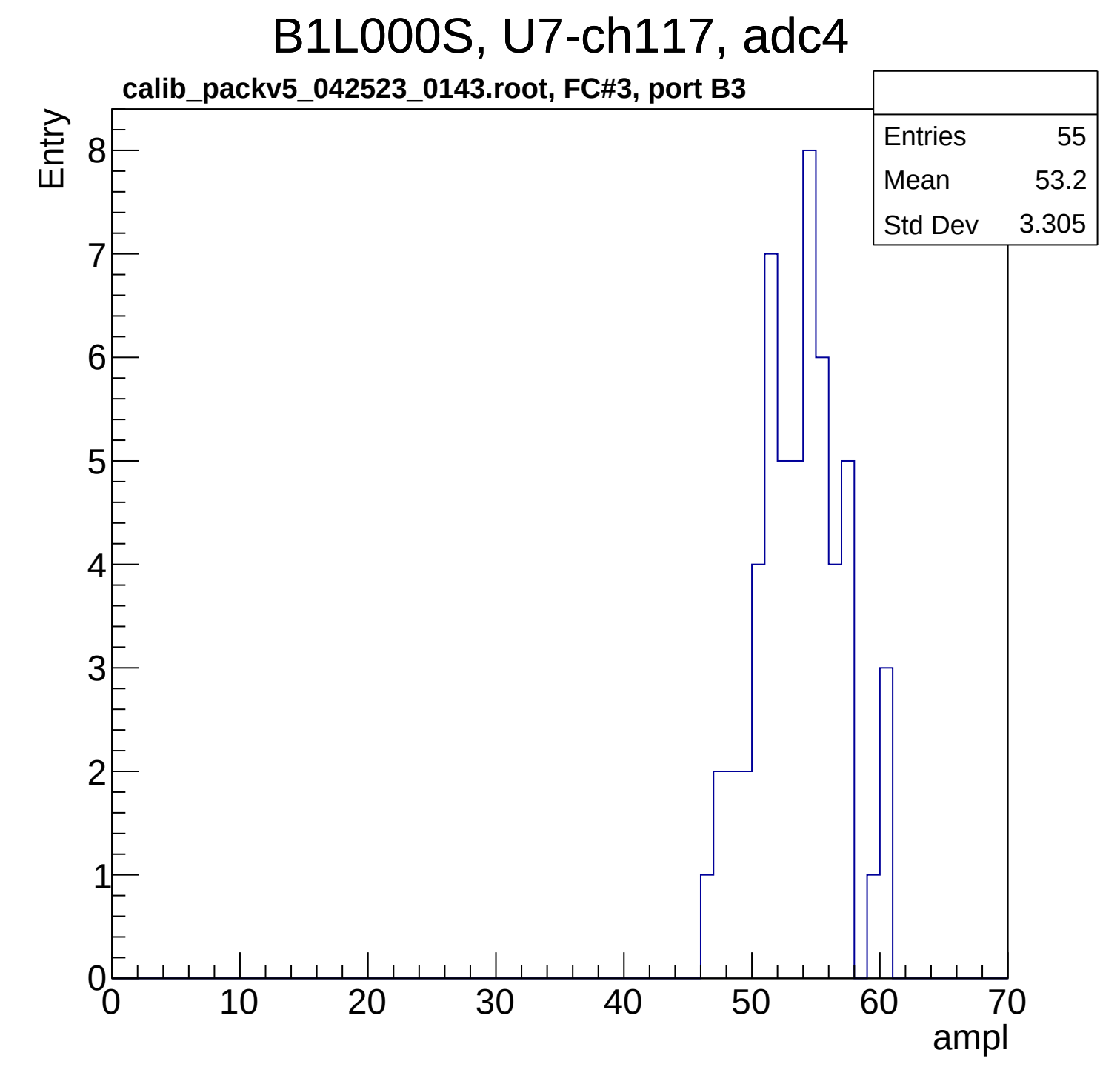

B1L000S, U7-ch117, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 47.82
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch117, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 55
Mean 53.2
Std Dev 3.305
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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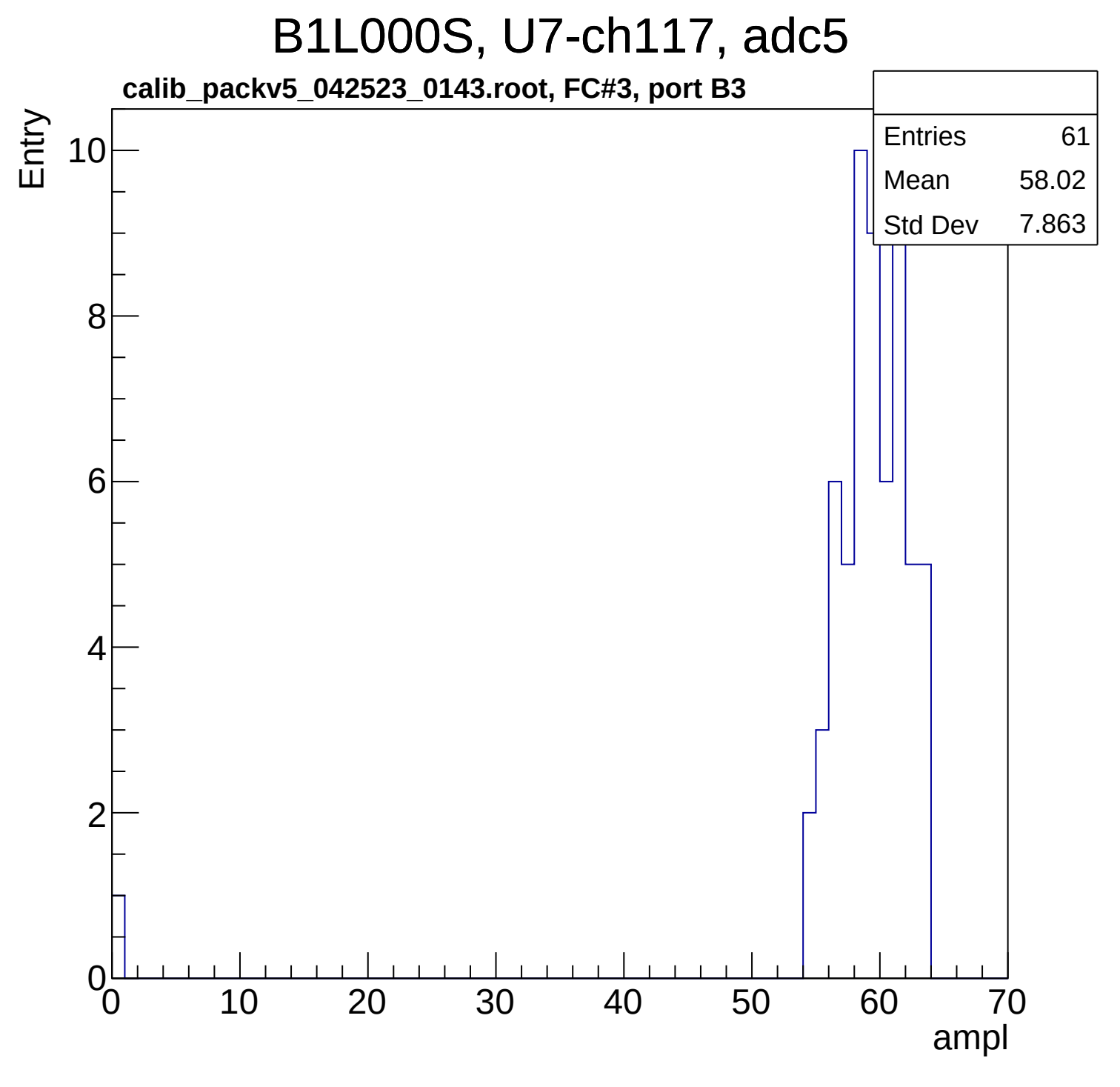
B1L000S, U7-ch117, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 58.02
Std Dev 7.863
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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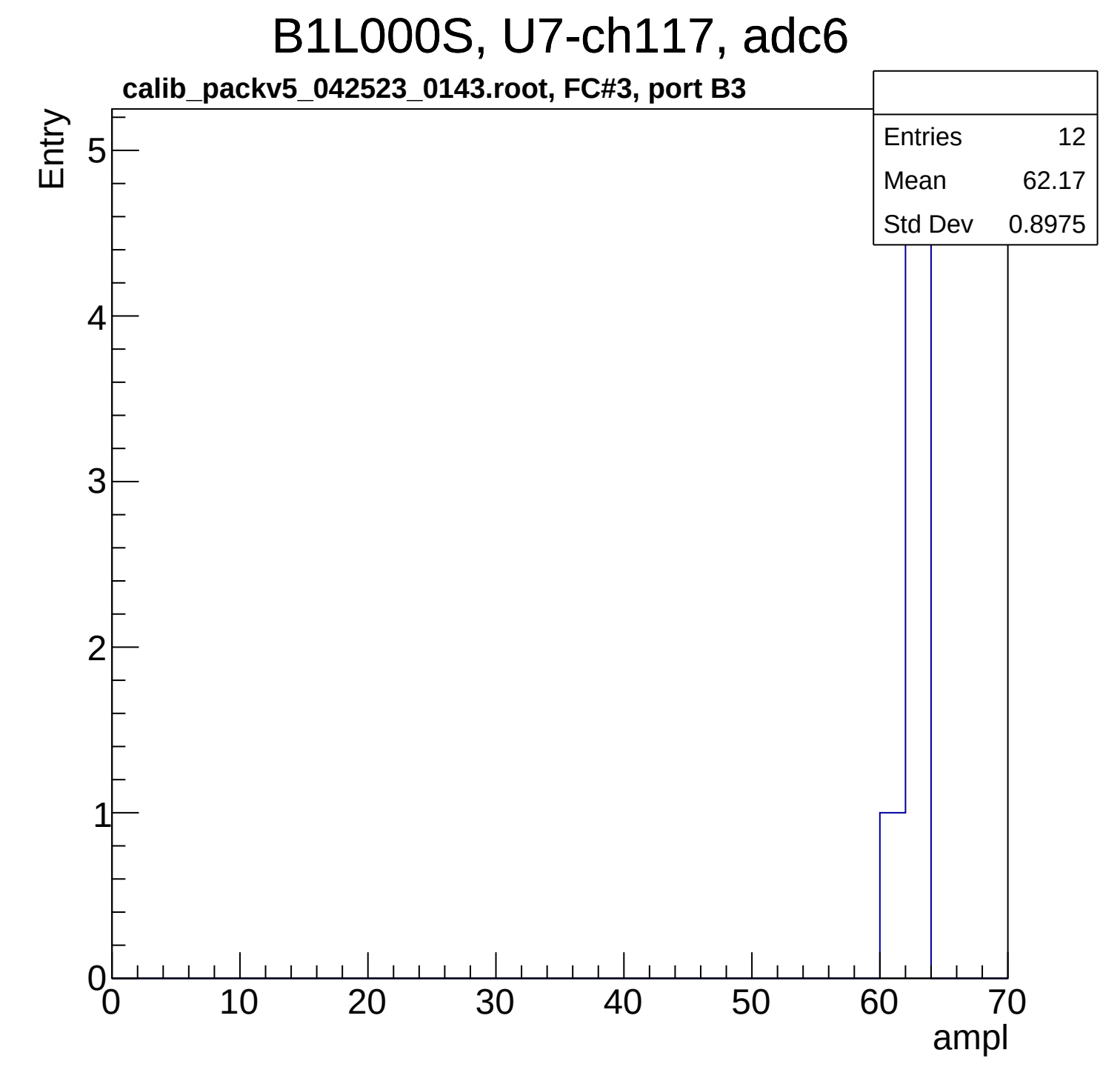
B1L000S, U7-ch117, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 12
Mean 62.17
Std Dev 0.8975
Entry
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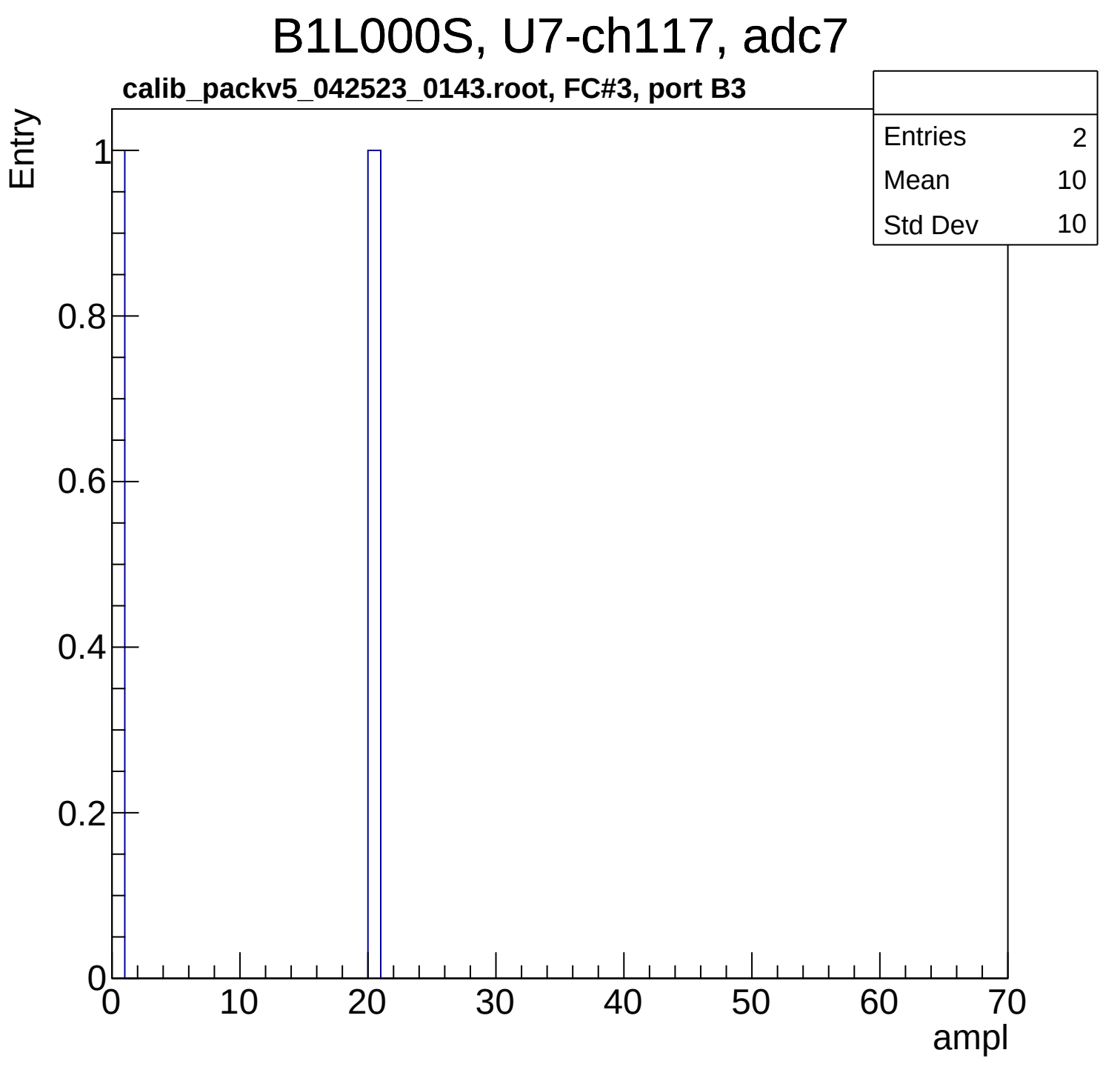
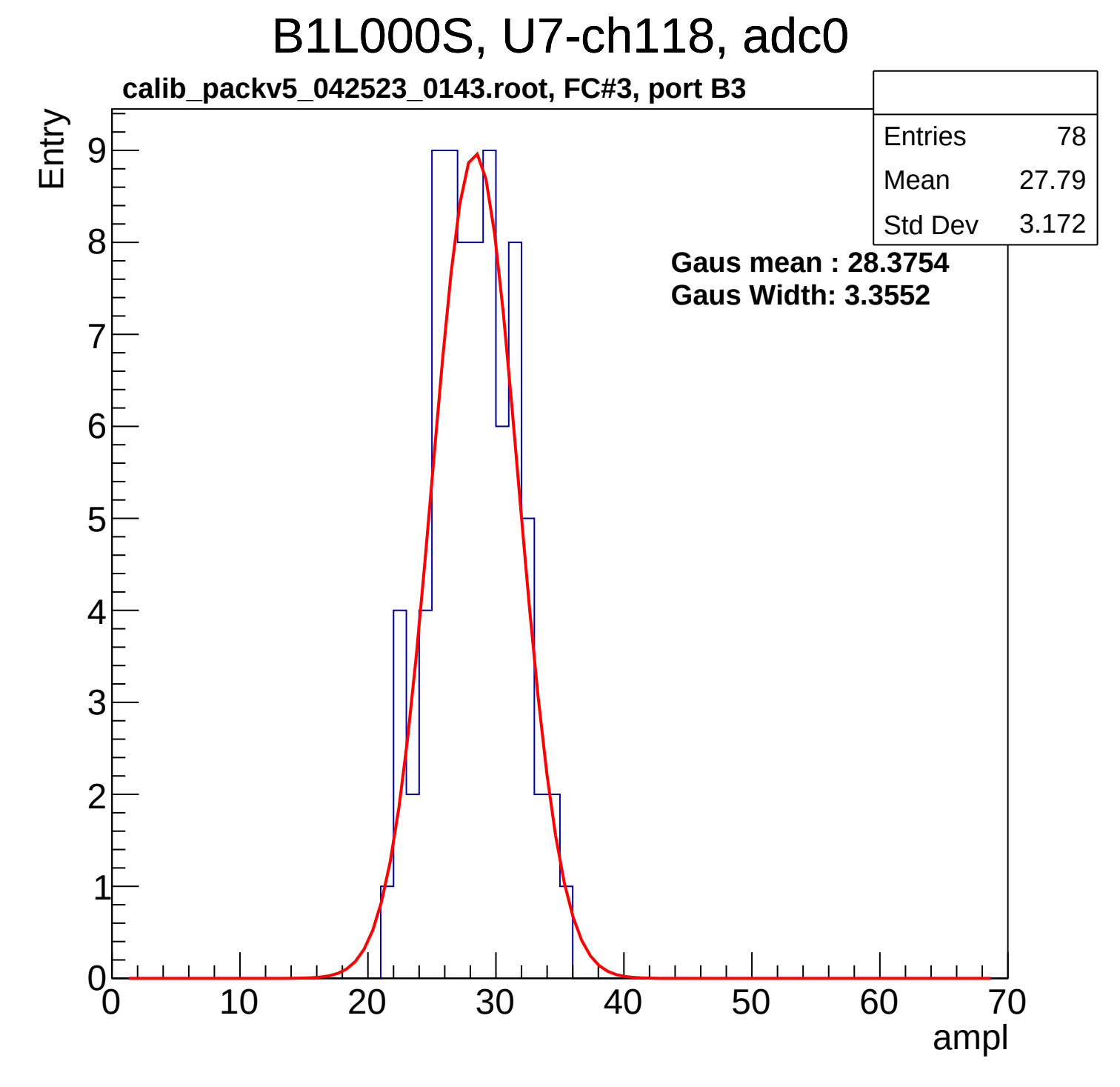
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch117, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch118, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 78
Mean 27.79
Std Dev 3.172
Gaus mean : 28.3754
Gaus Width: 3.3552
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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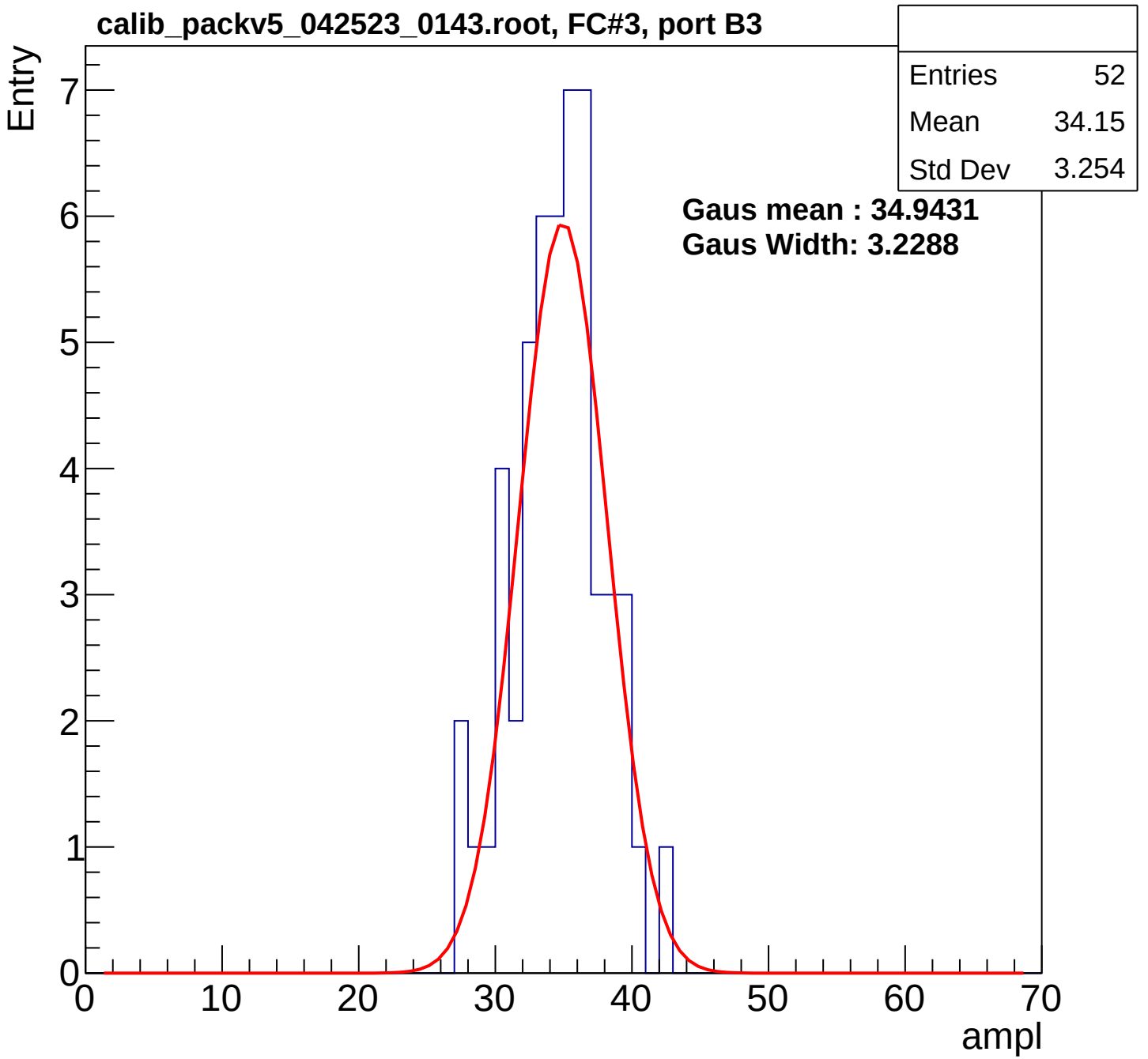
B1L000S, U7-ch118, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 34.15
Std Dev 3.254
Gaus mean : 34.9431
Gaus Width: 3.2288
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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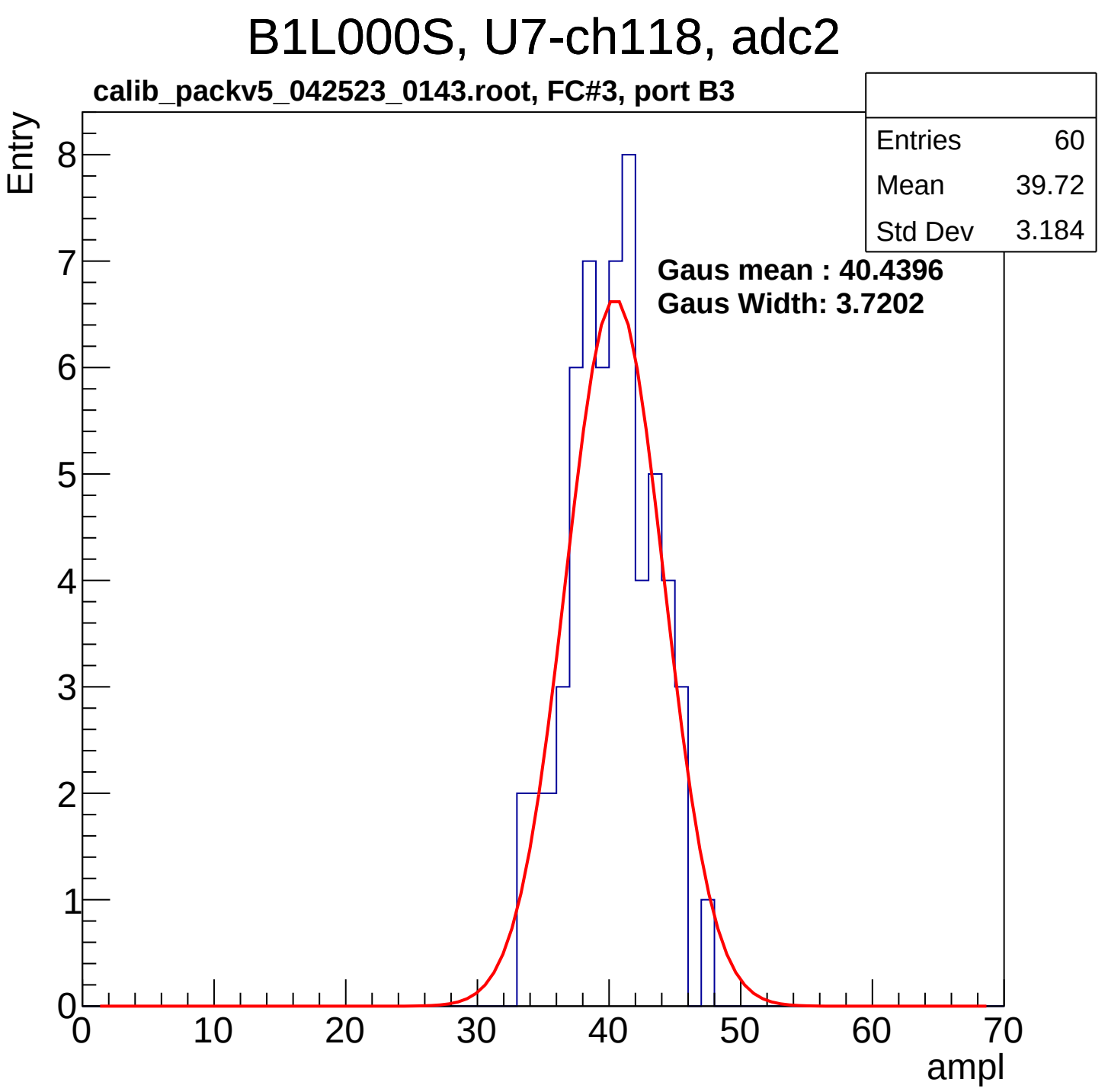
B1L000S, U7-ch118, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 39.72
Std Dev 3.184
Gaus mean : 40.4396
Gaus Width: 3.7202
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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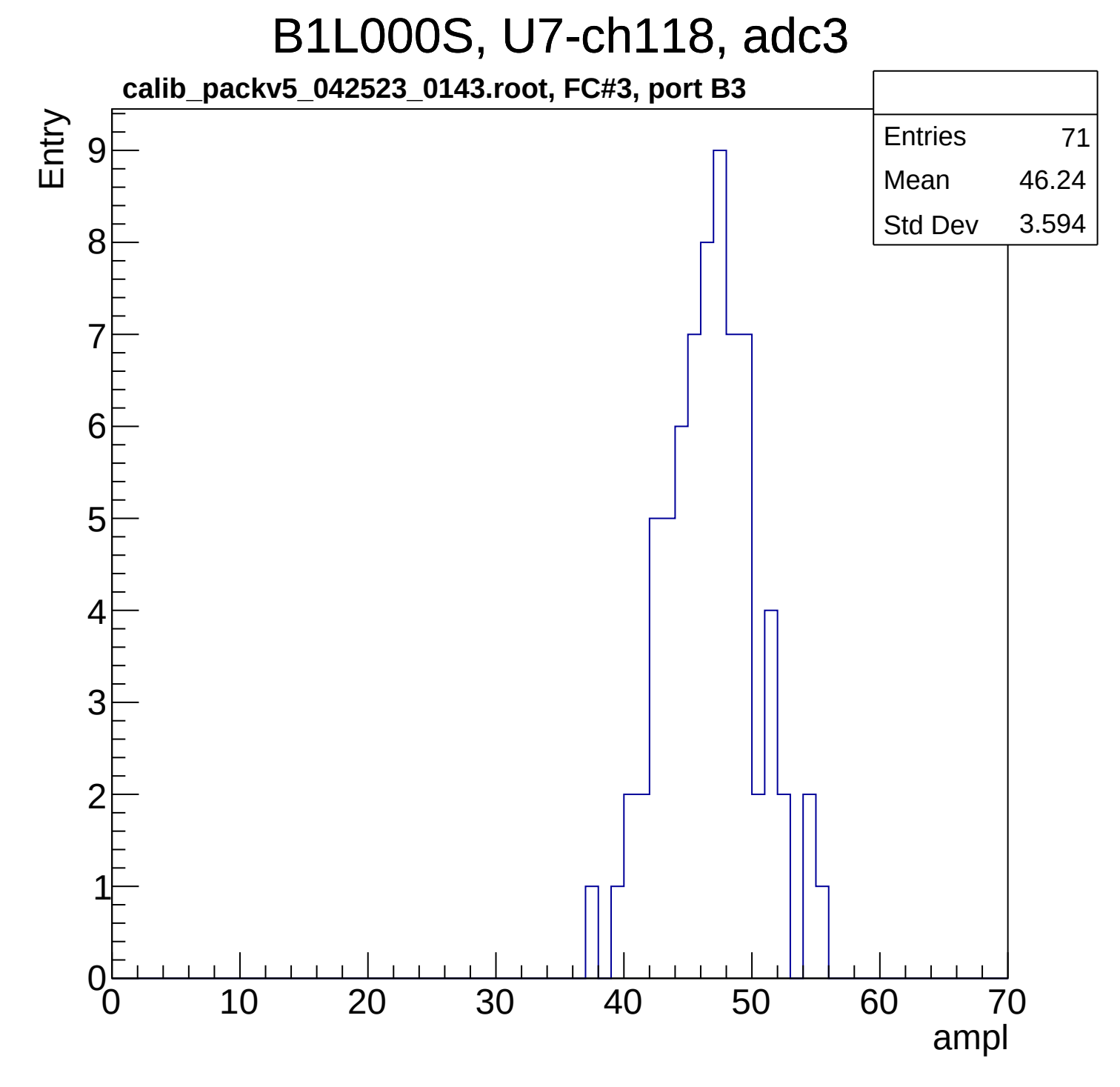
B1L000S, U7-ch118, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 71
Mean 46.24
Std Dev 3.594
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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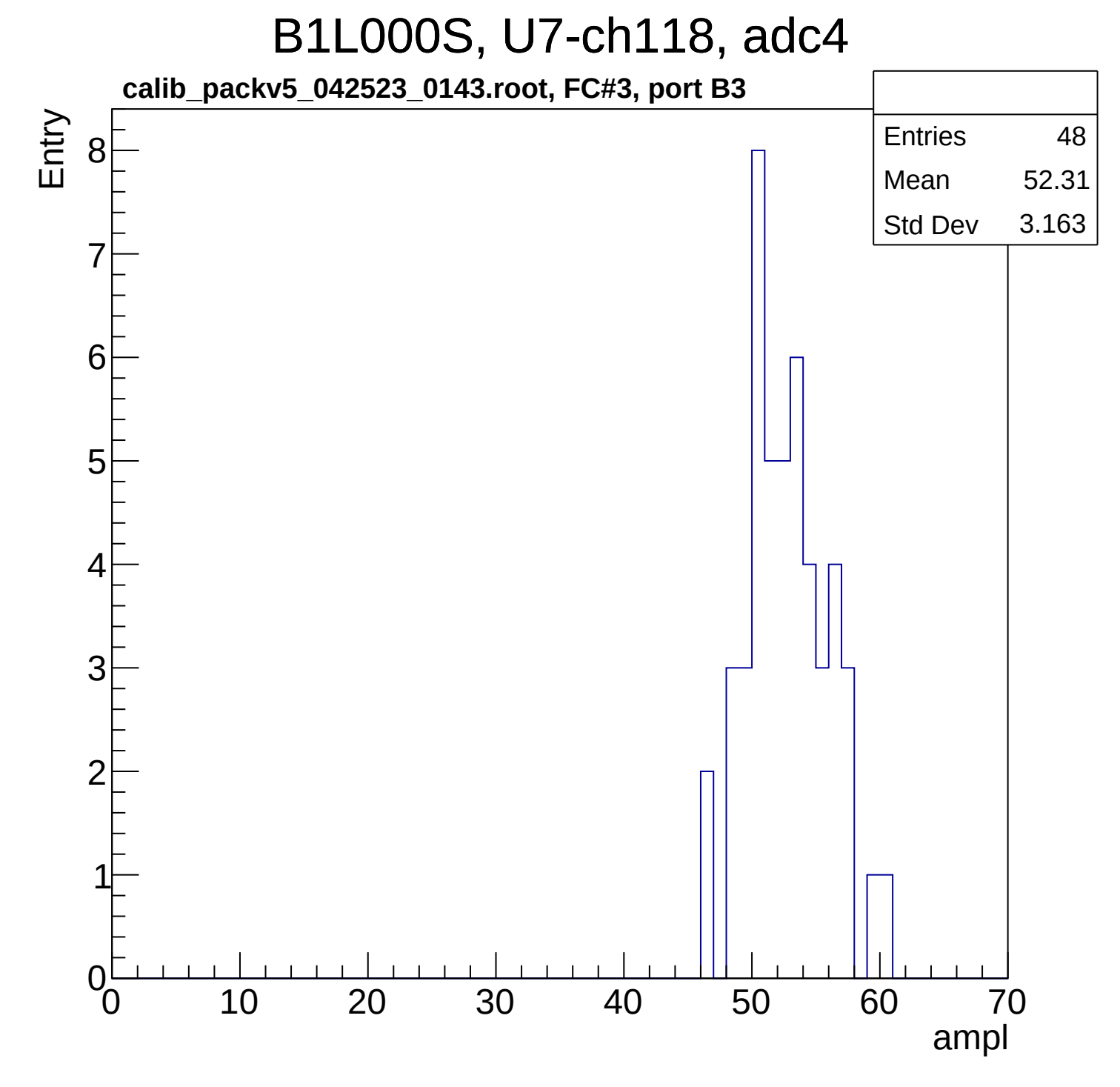
B1L000S, U7-ch118, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 48
Mean 52.31
Std Dev 3.163
Entry
ampl
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2
3
4
5
6
7
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0
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20
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60
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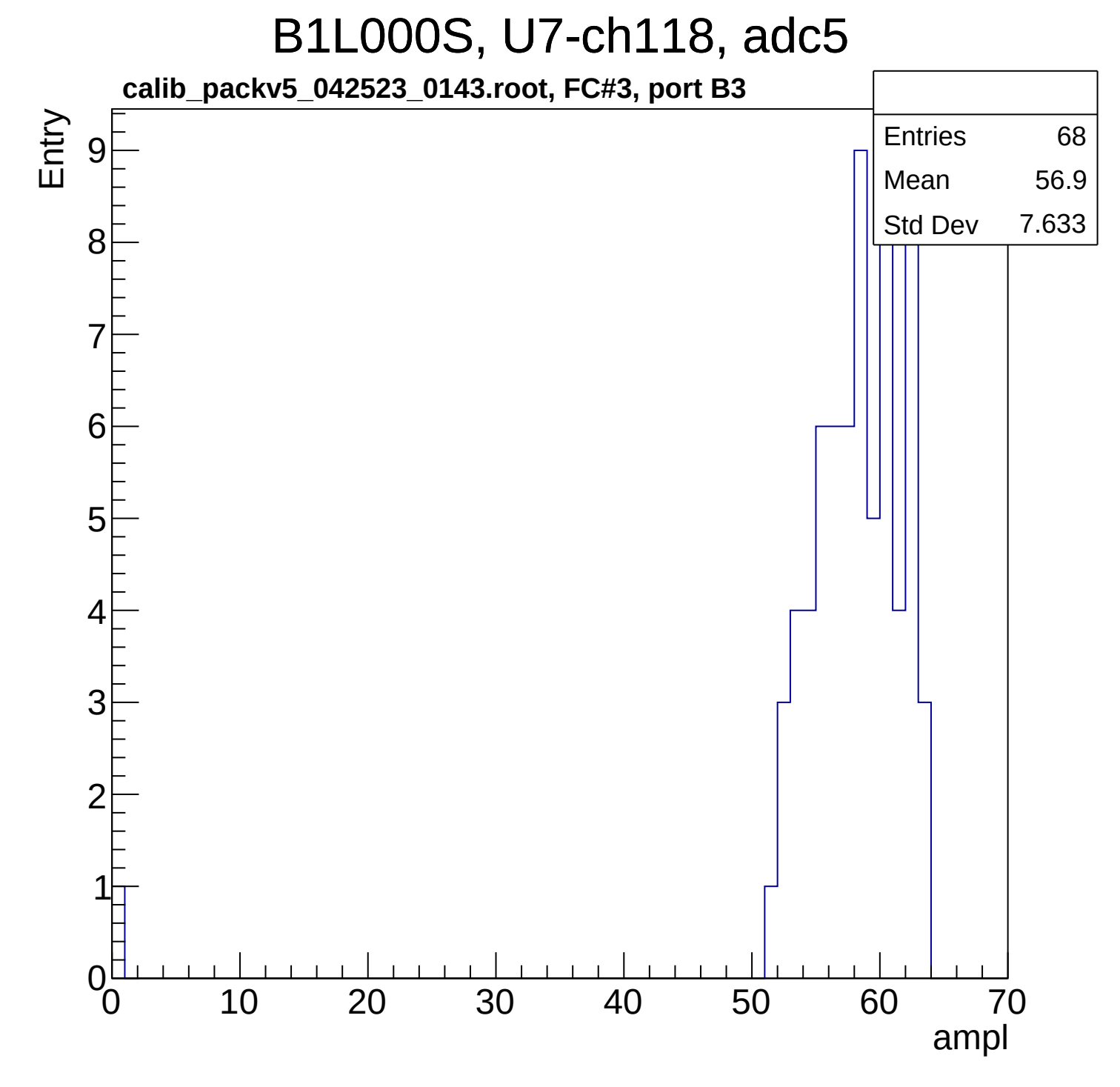
B1L000S, U7-ch118, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 56.9
Std Dev 7.633
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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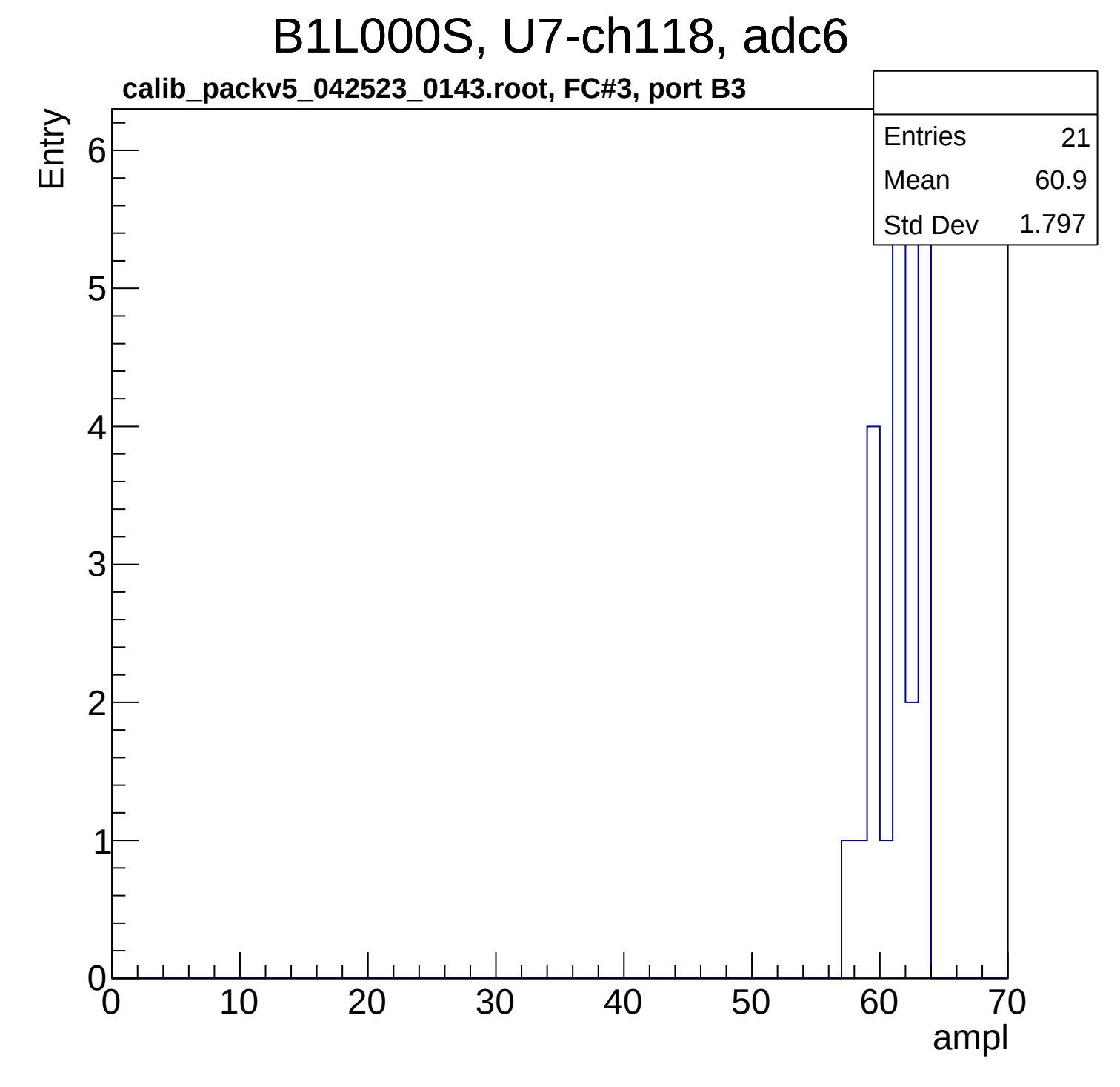
B1L000S, U7-ch118, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 60.9
Std Dev 1.797
Entry
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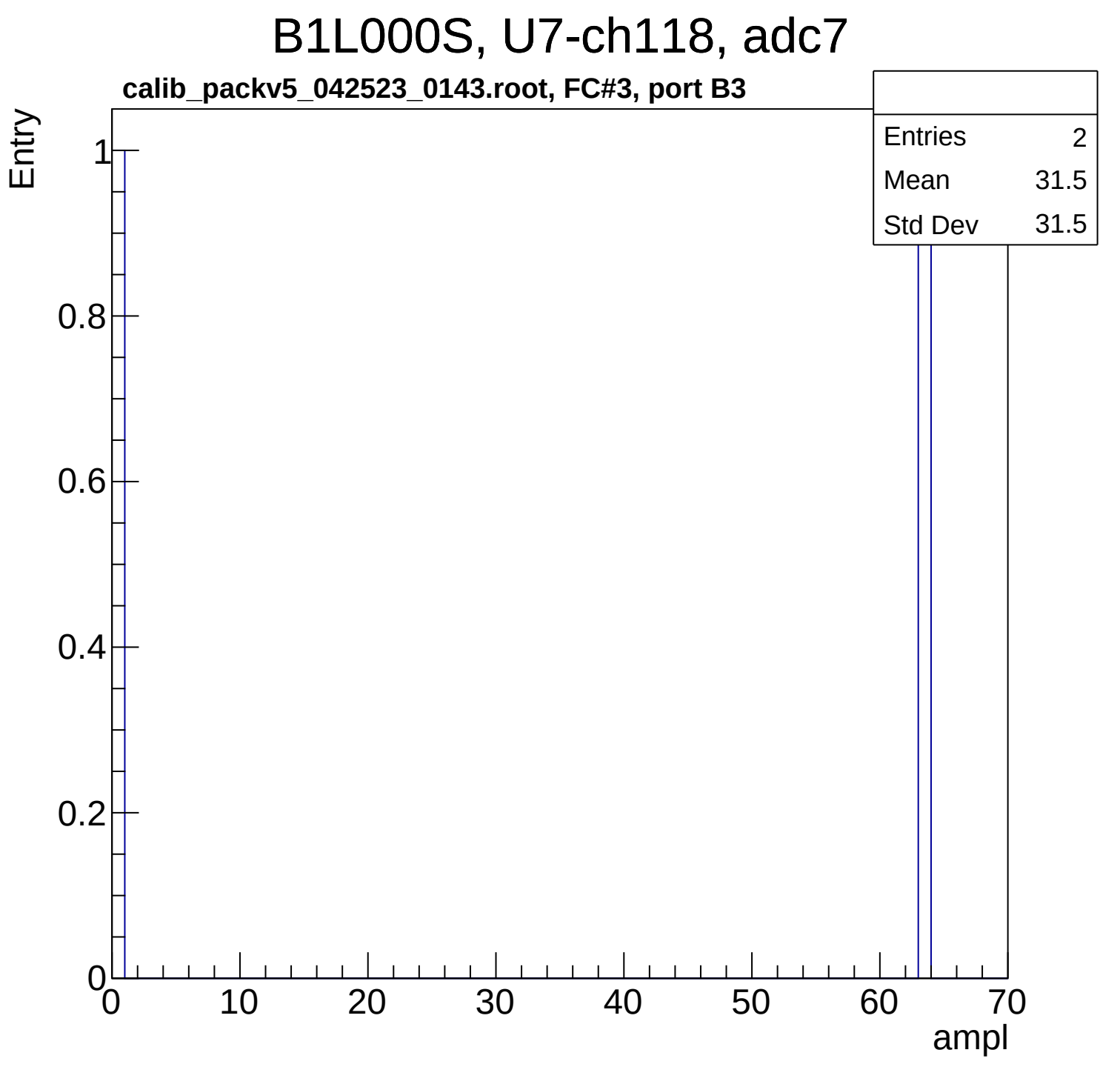
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch118, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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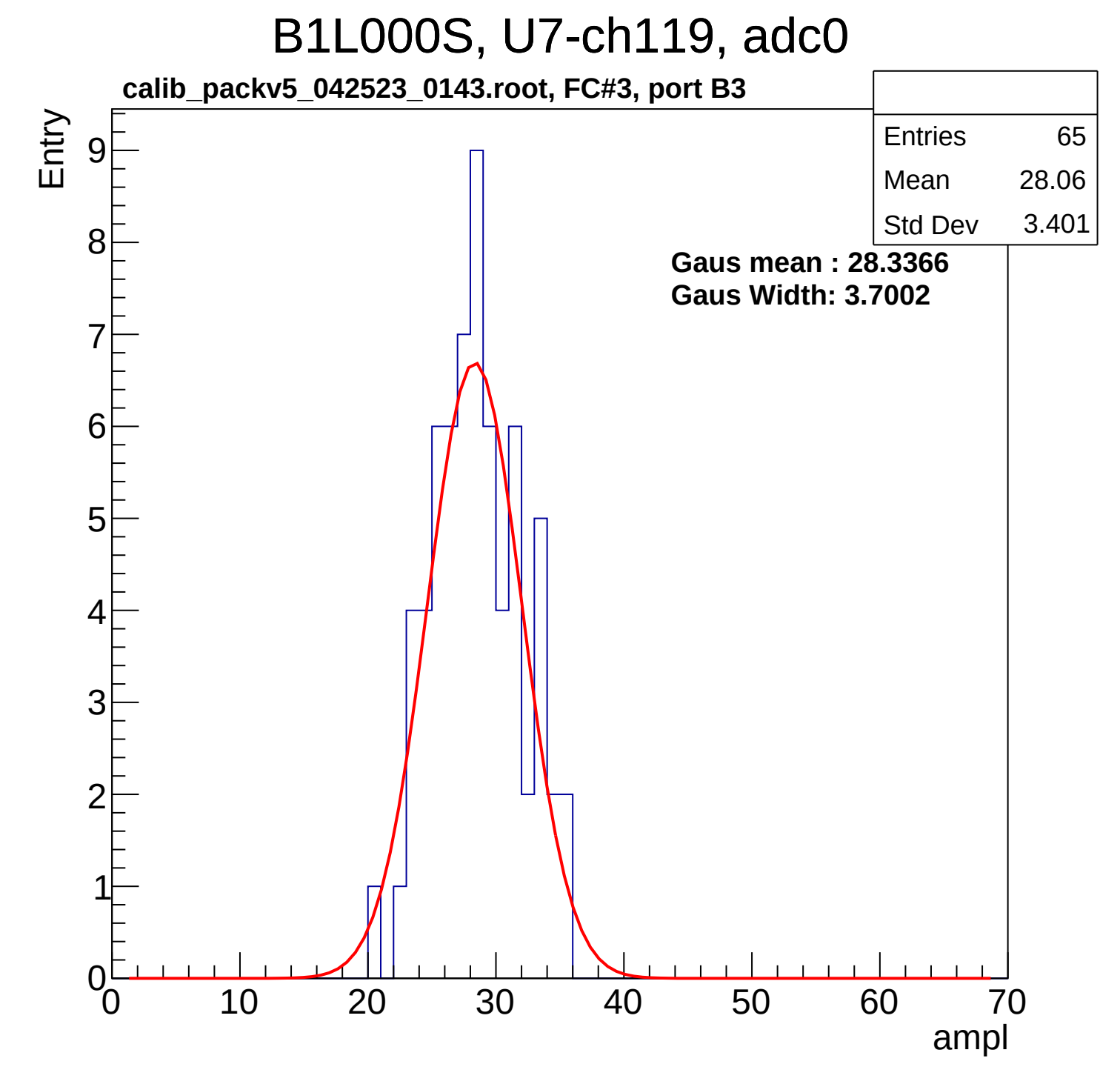
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch119, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.06
Std Dev 3.401
Gaus mean : 28.3366
Gaus Width: 3.7002
Entry
ampl
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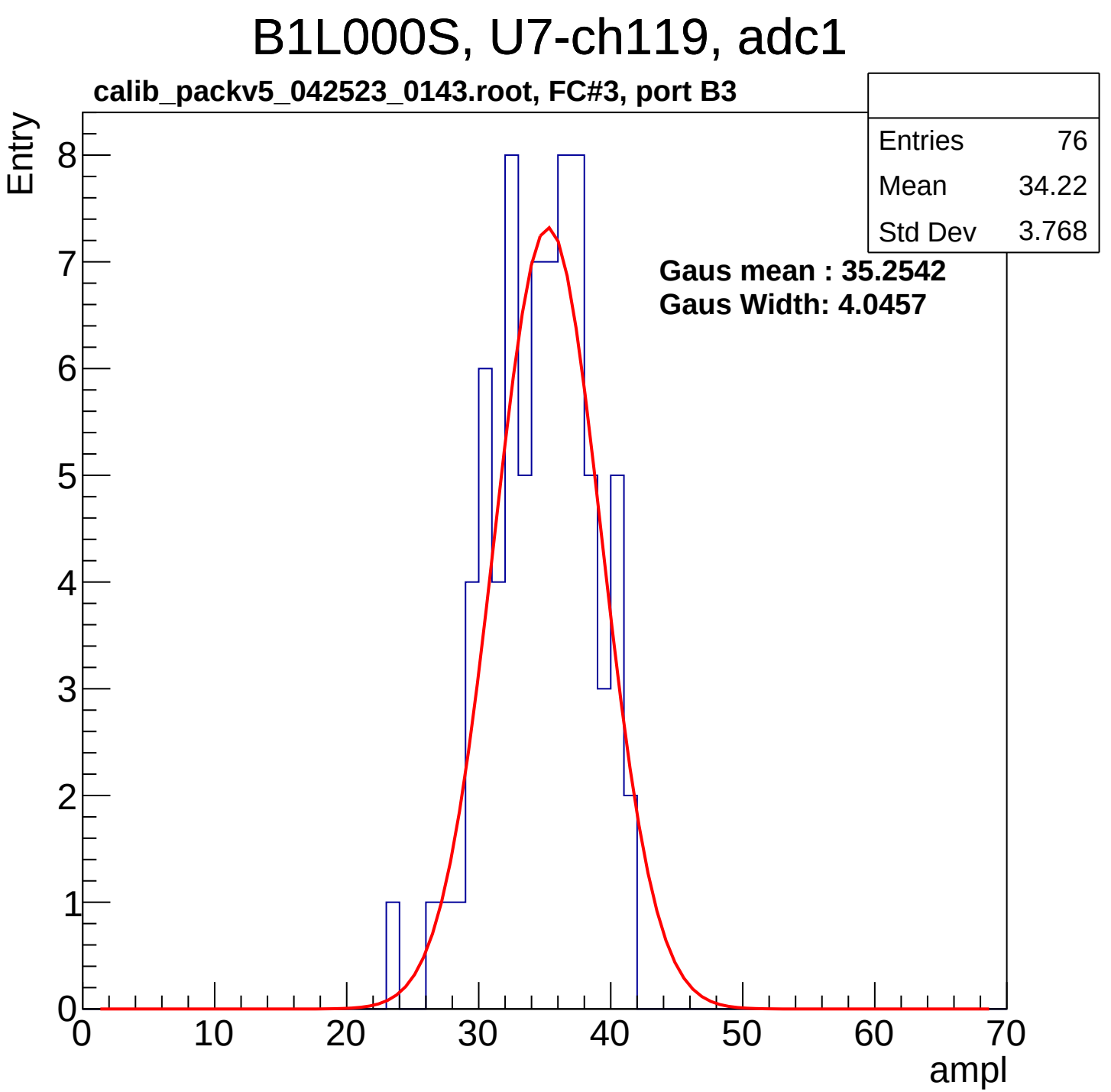
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch119, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 76
Mean 34.22
Std Dev 3.768
Gaus mean : 35.2542
Gaus Width: 4.0457
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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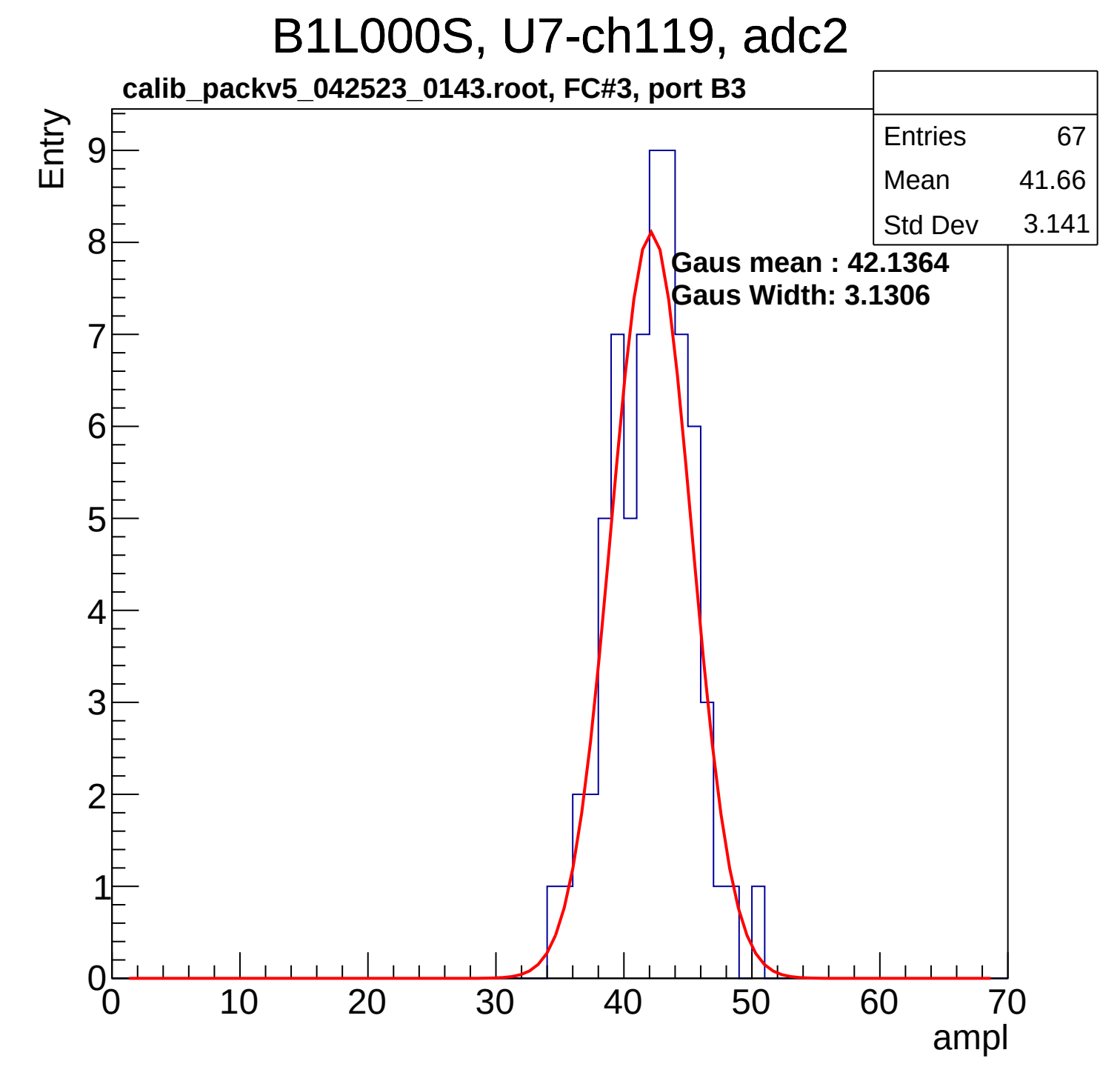
B1L000S, U7-ch119, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 67
Mean 41.66
Std Dev 3.141
Gaus mean : 42.1364
Gaus Width: 3.1306
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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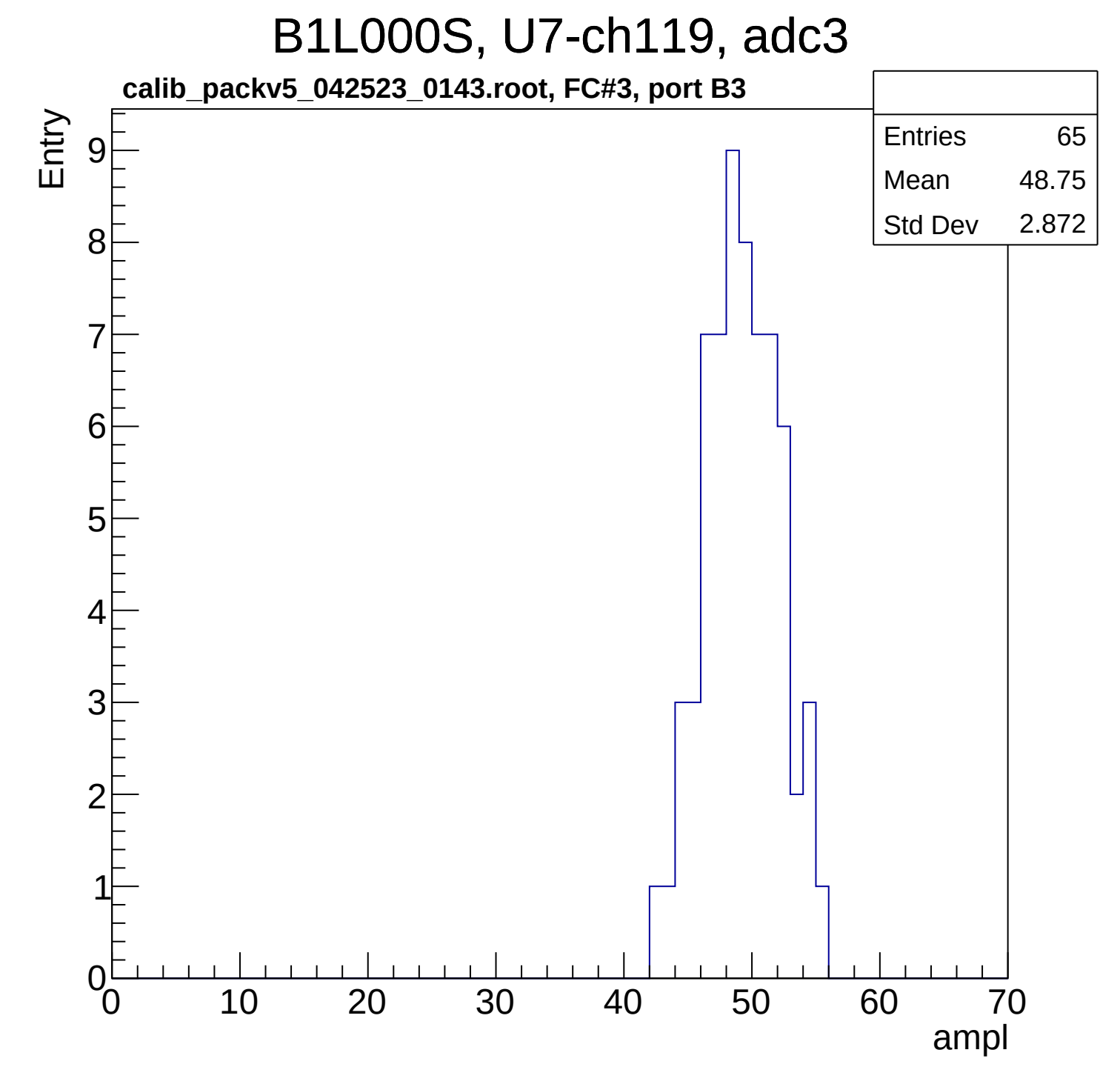
B1L000S, U7-ch119, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 48.75
Std Dev 2.872
Entry
ampl
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3
4
5
6
7
8
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0
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60
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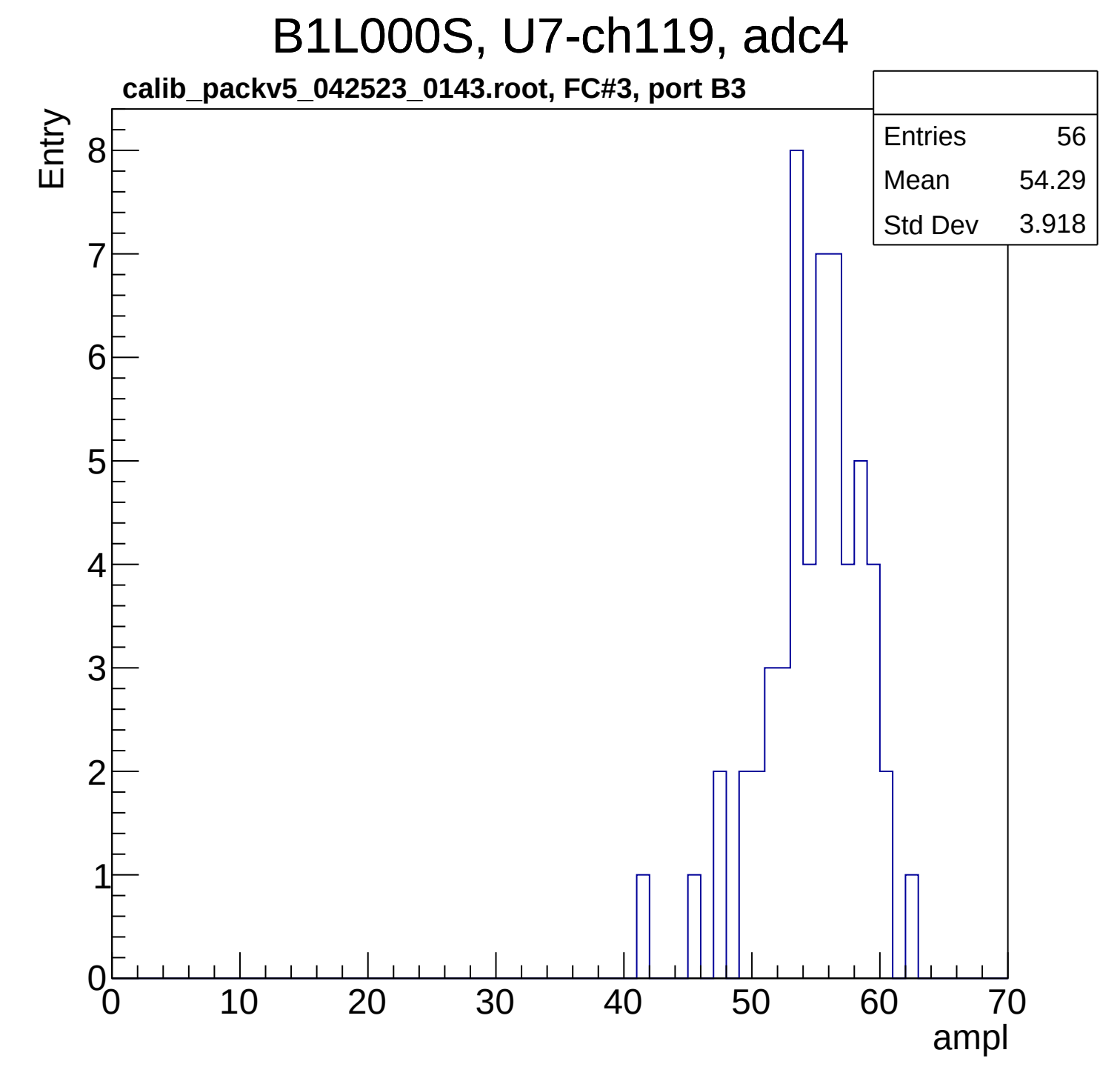
B1L000S, U7-ch119, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 54.29
Std Dev 3.918
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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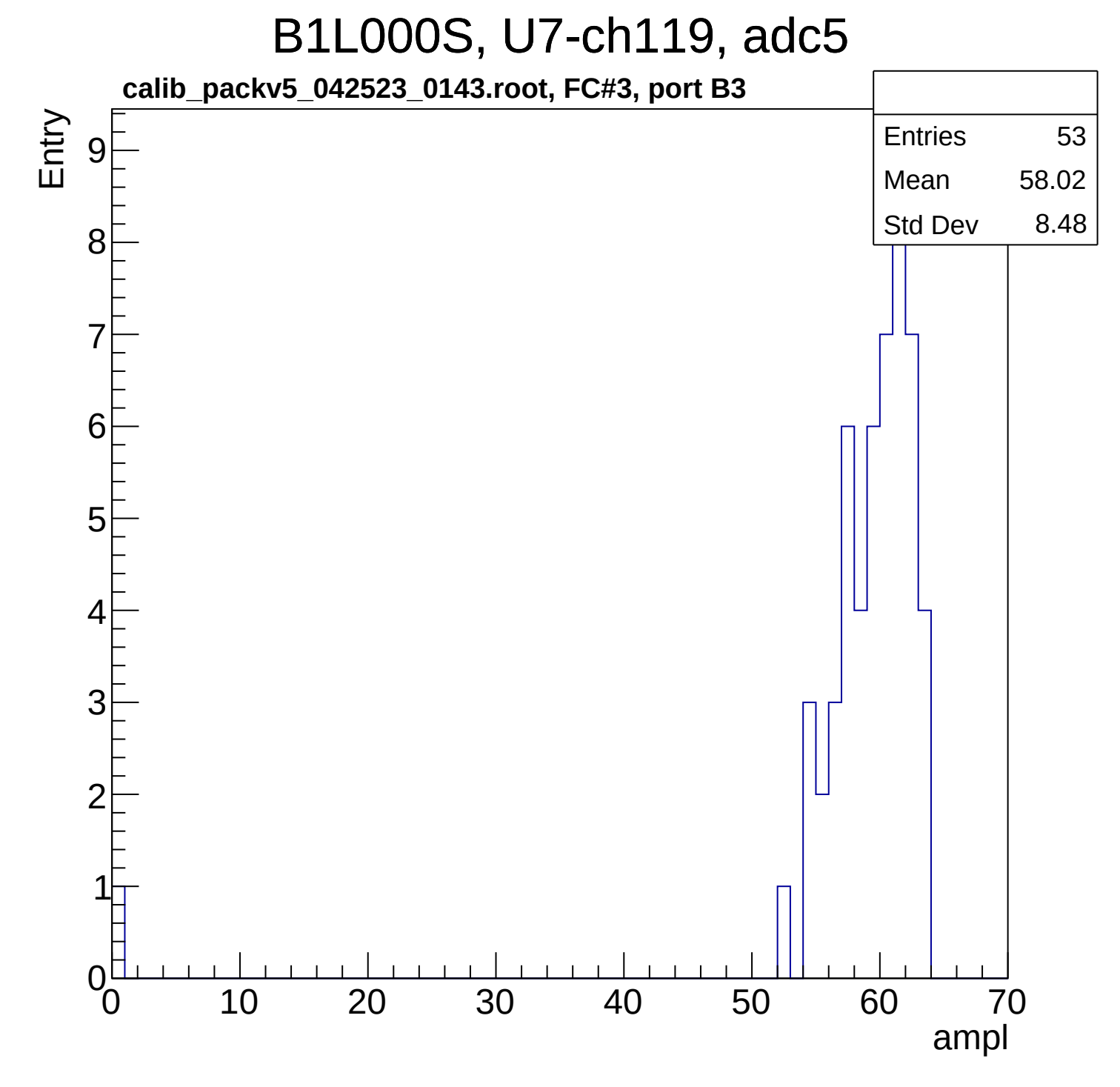
B1L000S, U7-ch119, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.02
Std Dev 8.48
Entry
ampl
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2
3
4
5
6
7
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0
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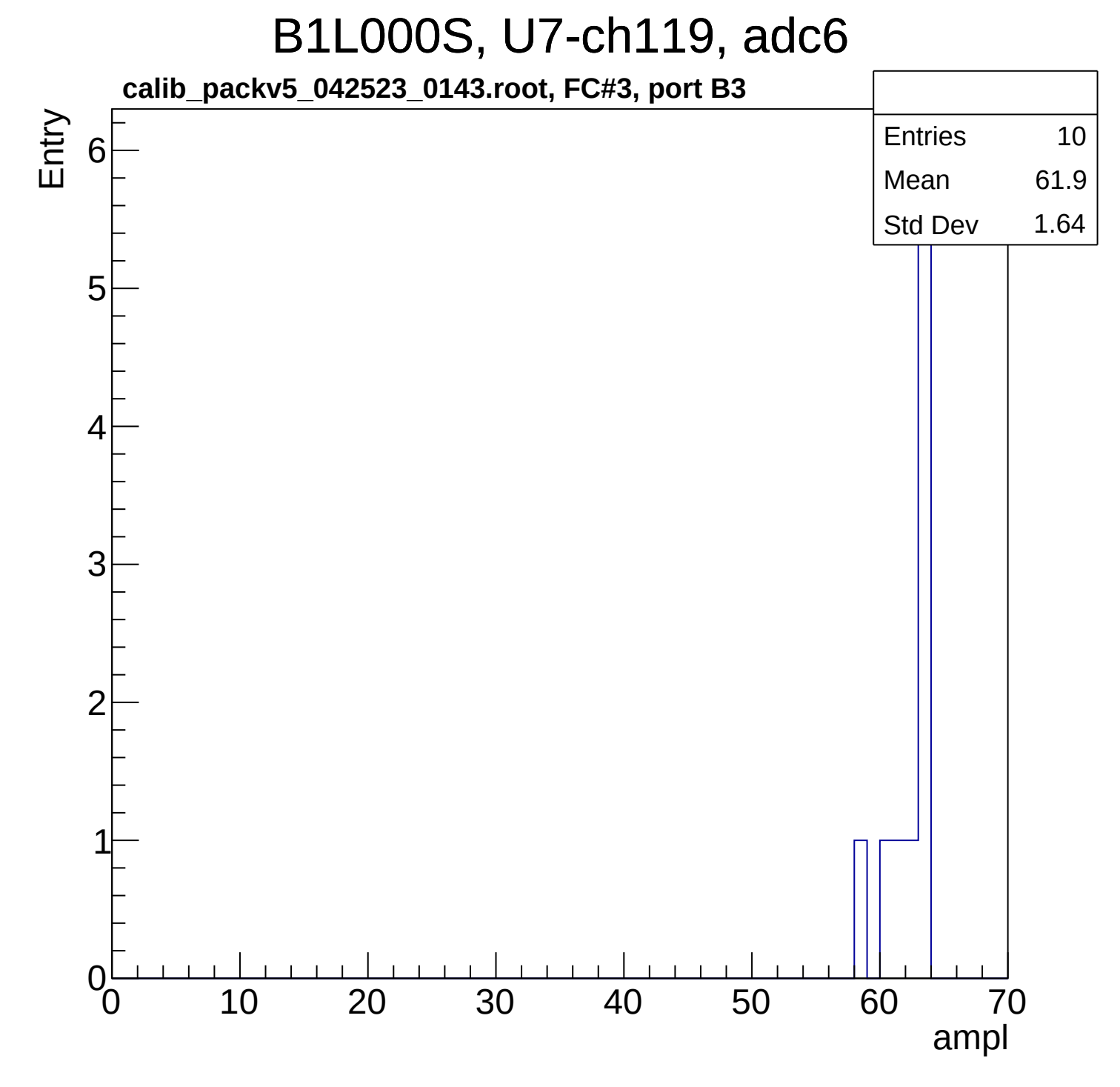
B1L000S, U7-ch119, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 10
Mean 61.9
Std Dev 1.64
Entry
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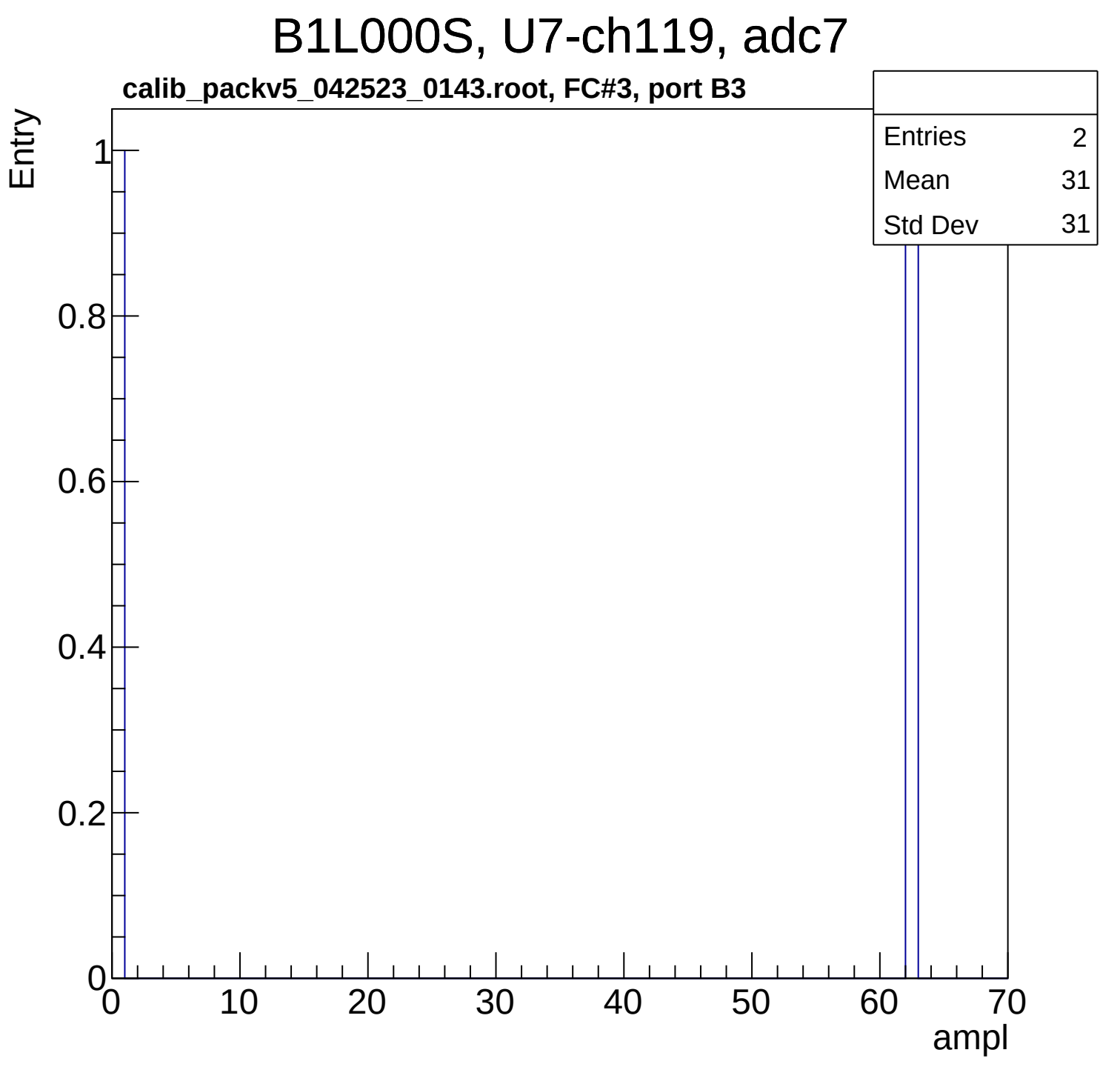
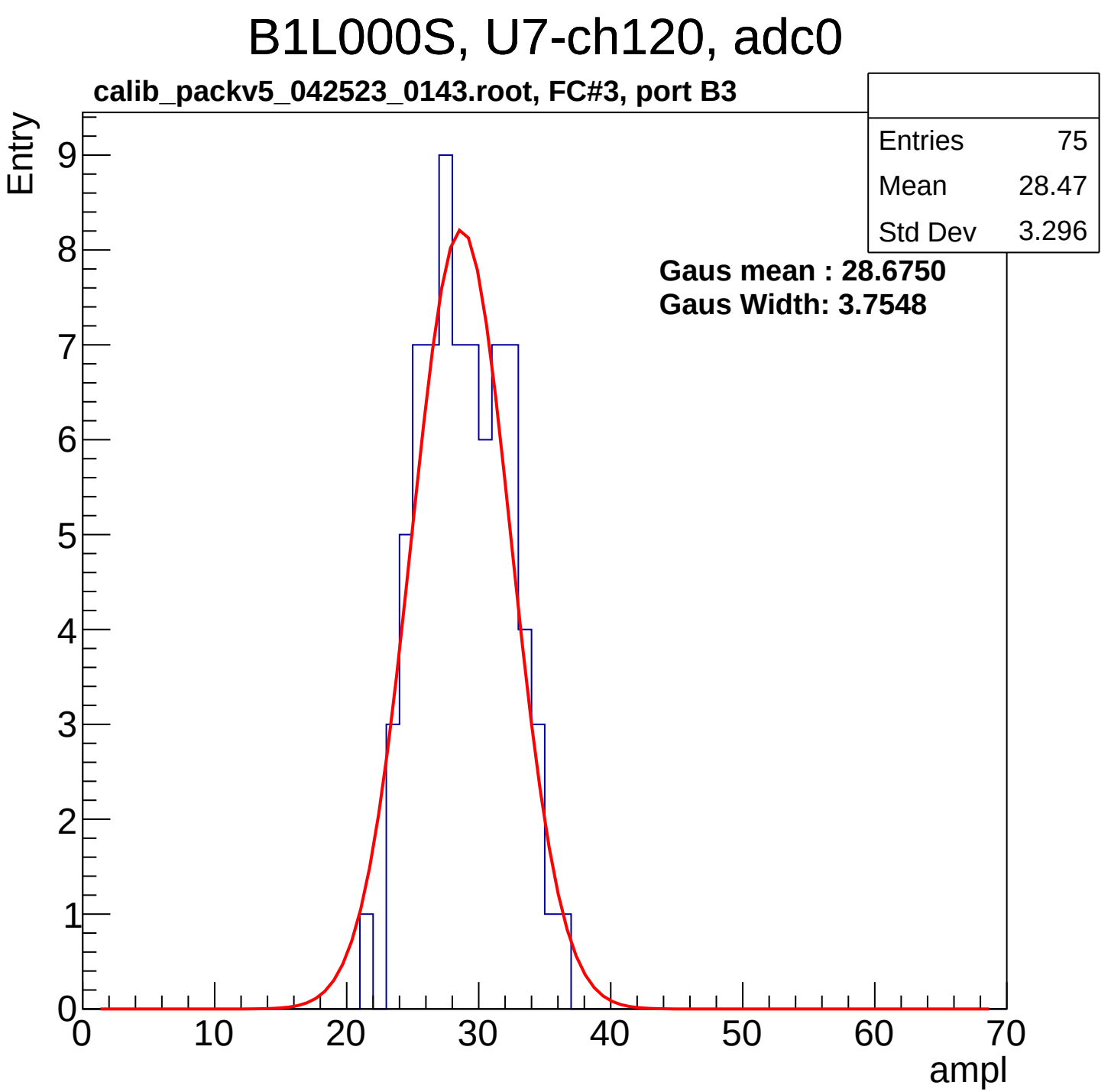
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch119, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch120, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 28.47
Std Dev 3.296
Gaus mean : 28.6750
Gaus Width: 3.7548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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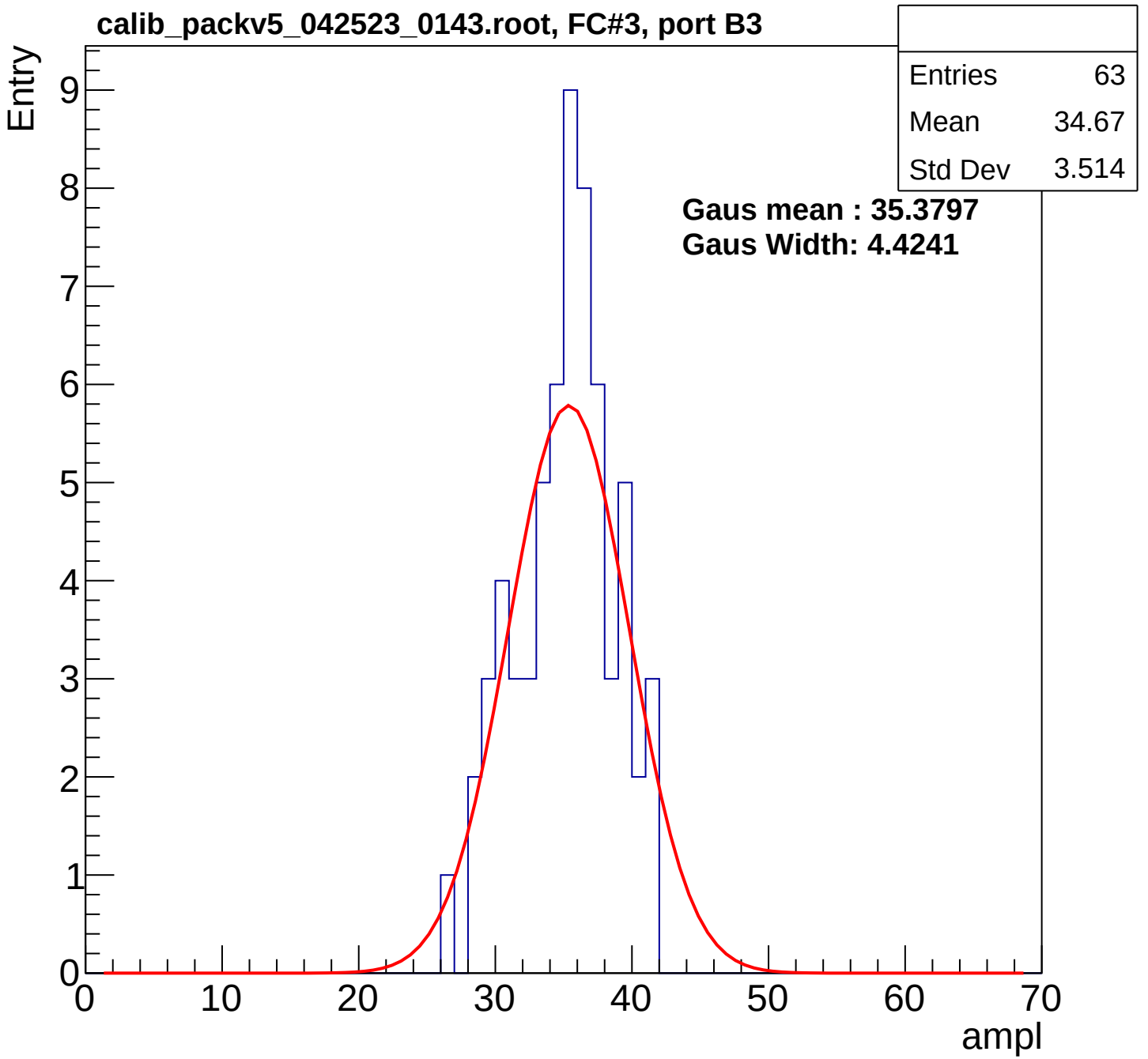
B1L000S, U7-ch120, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 34.67
Std Dev 3.514
Gaus mean : 35.3797
Gaus Width: 4.4241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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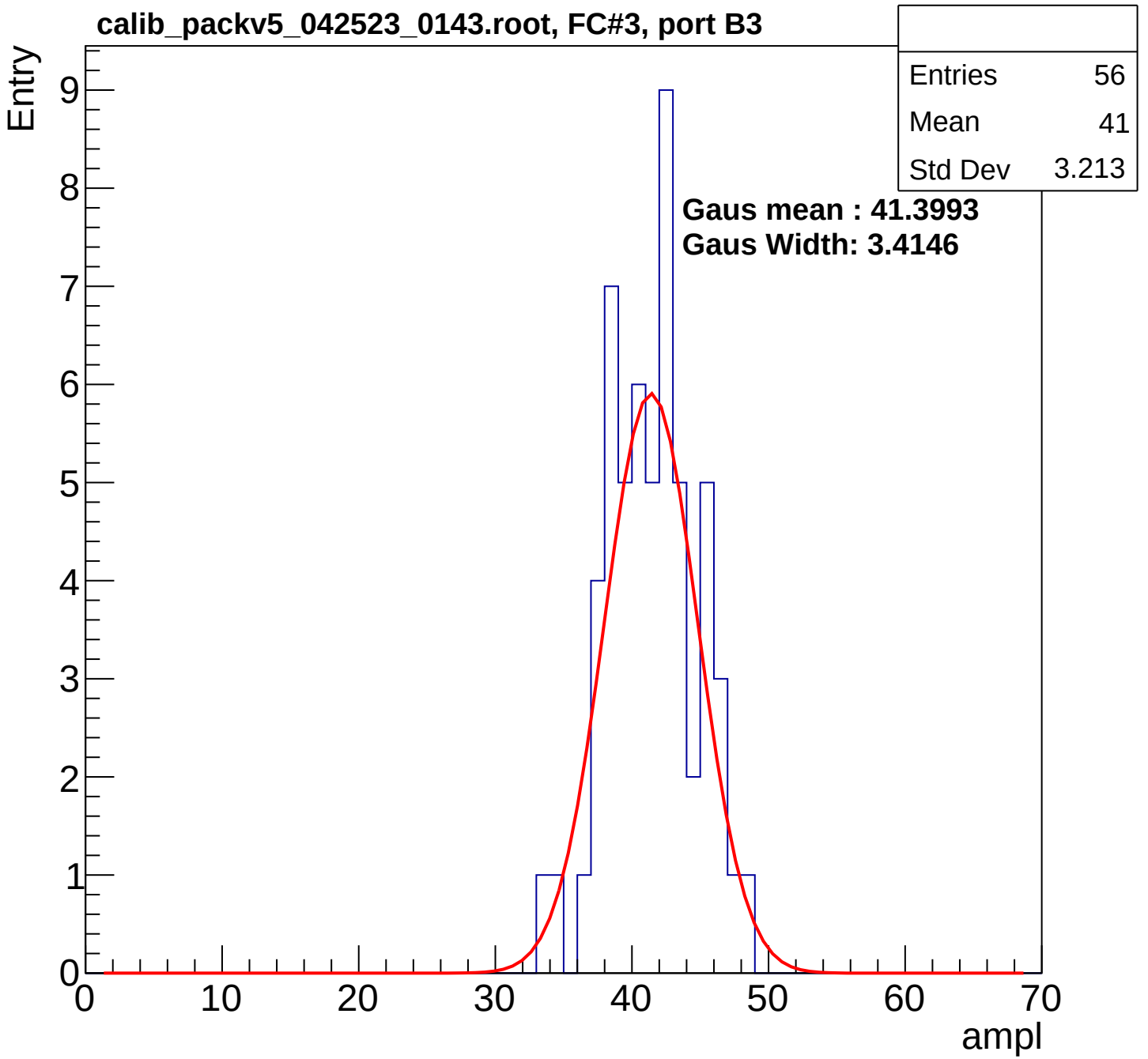
B1L000S, U7-ch120, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 41
Std Dev 3.213
Gaus mean : 41.3993
Gaus Width: 3.4146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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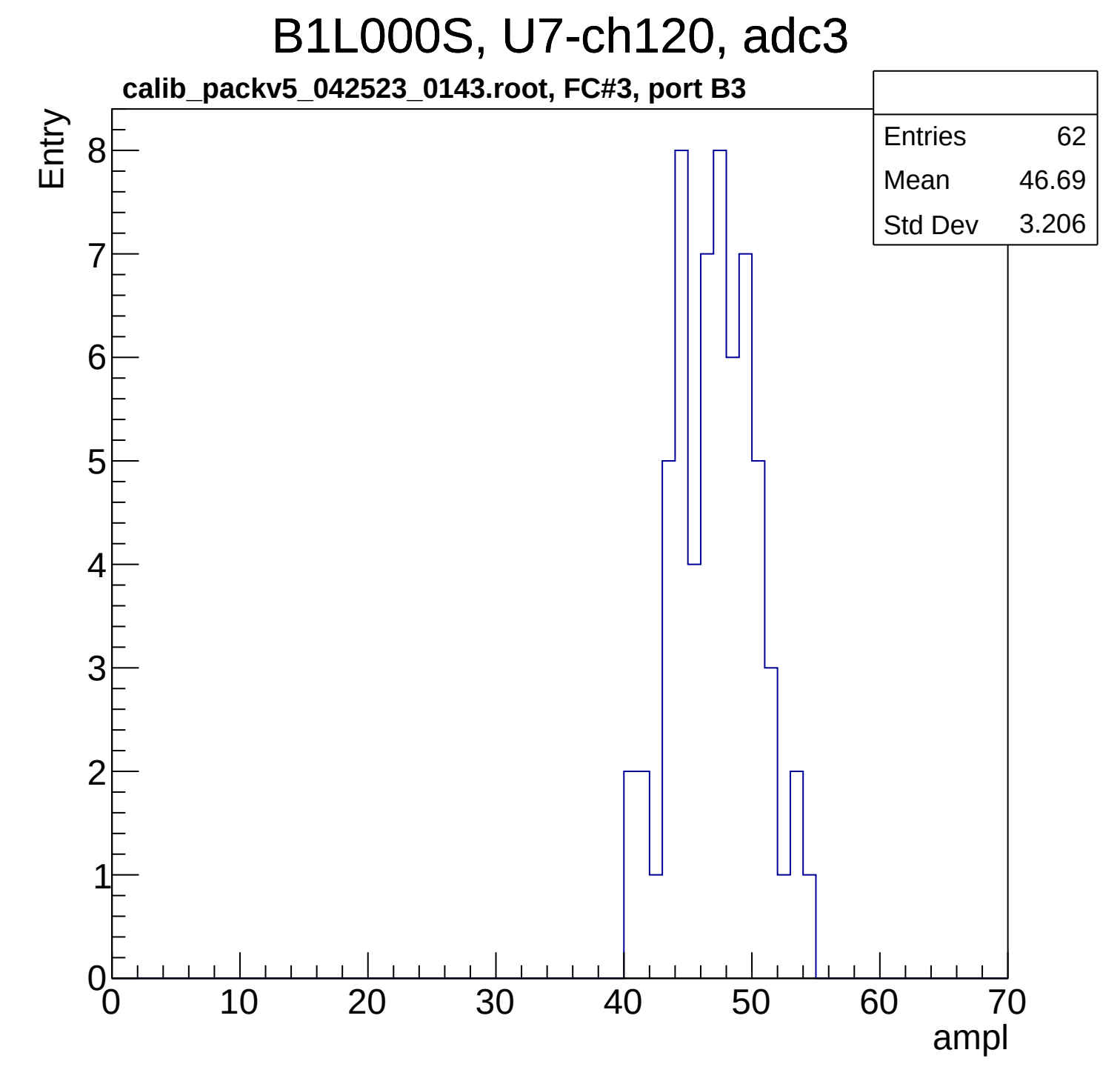
B1L000S, U7-ch120, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 46.69
Std Dev 3.206
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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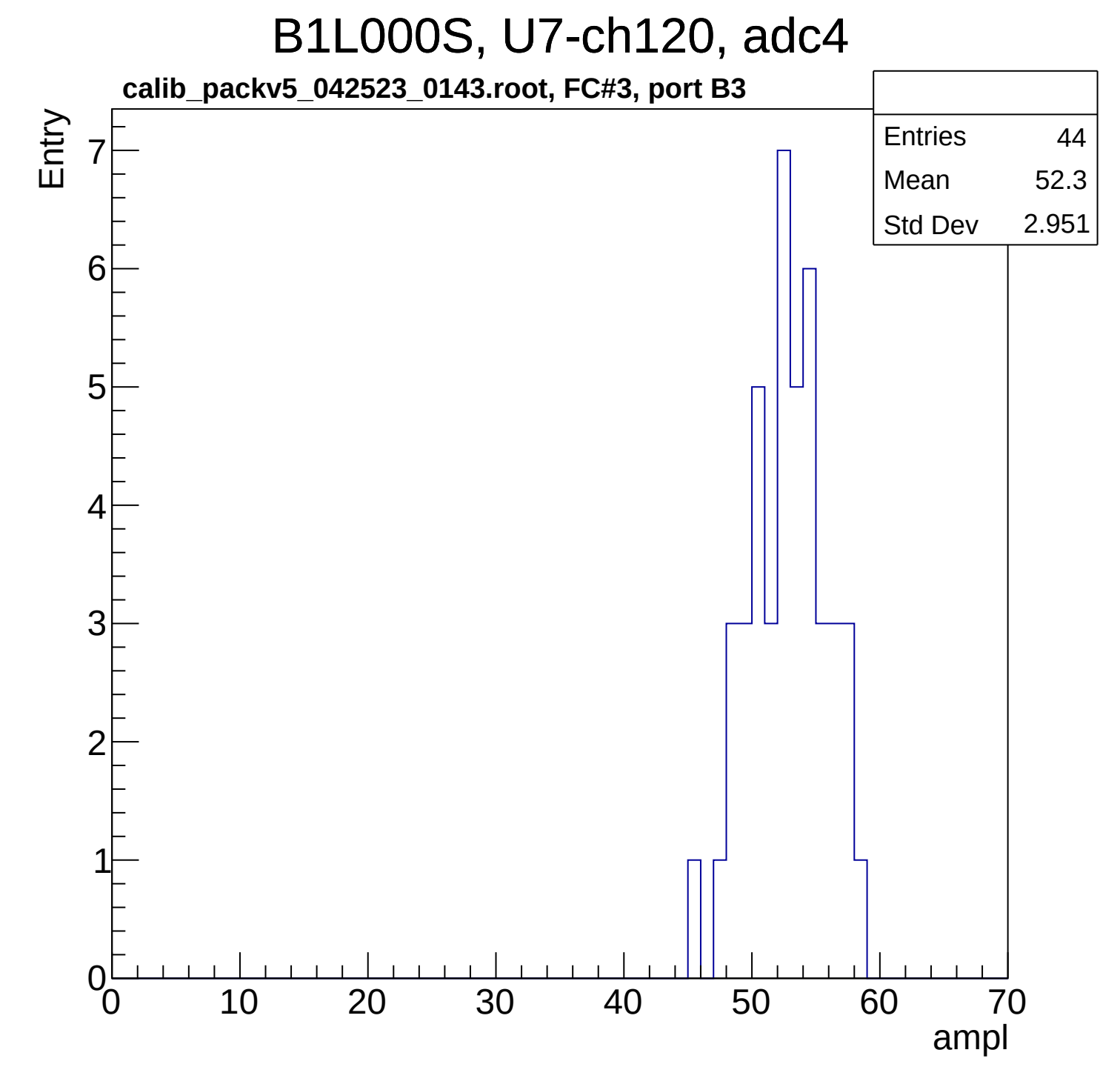
B1L000S, U7-ch120, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 44
Mean 52.3
Std Dev 2.951
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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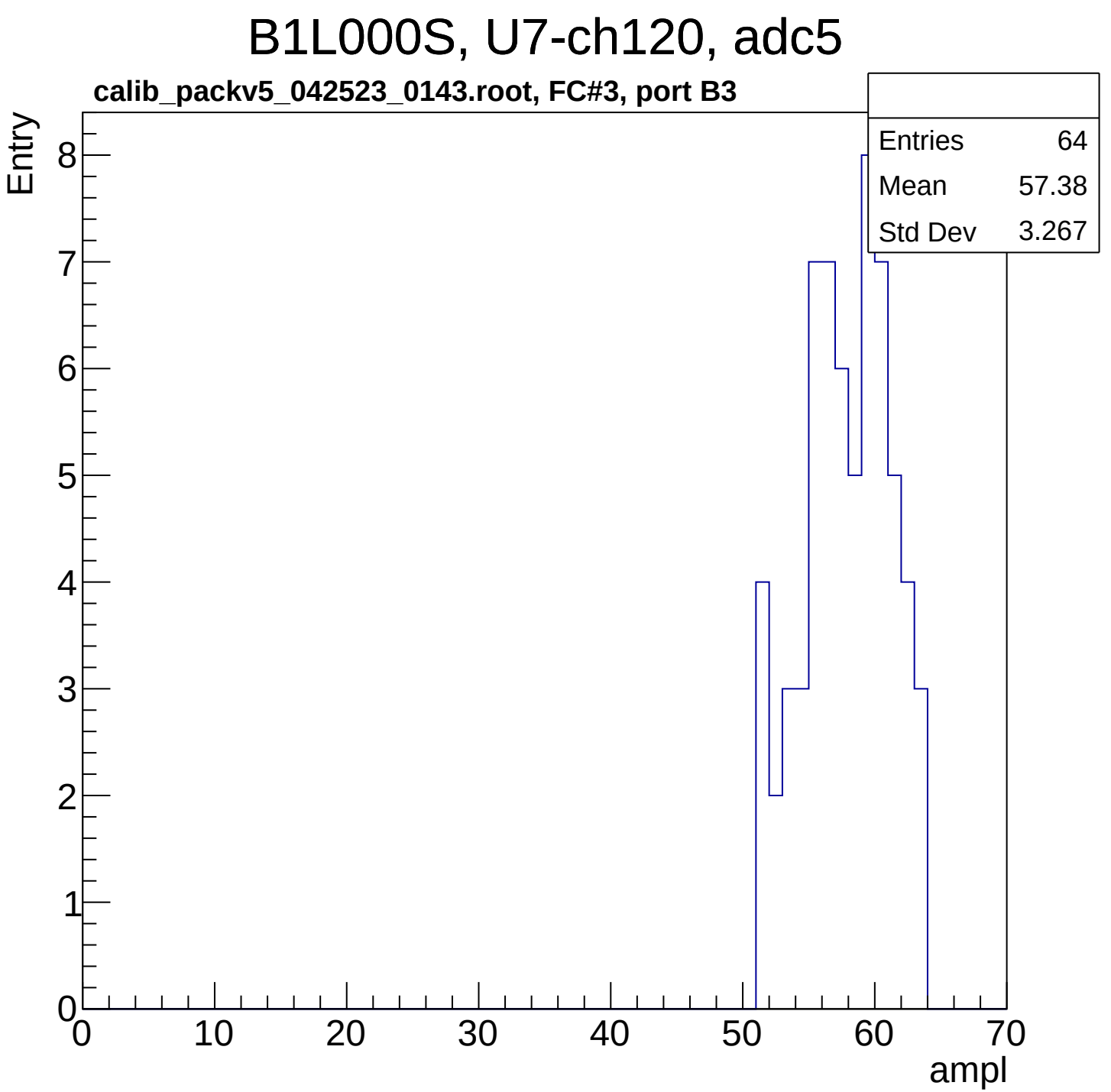
B1L000S, U7-ch120, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 64
Mean 57.38
Std Dev 3.267
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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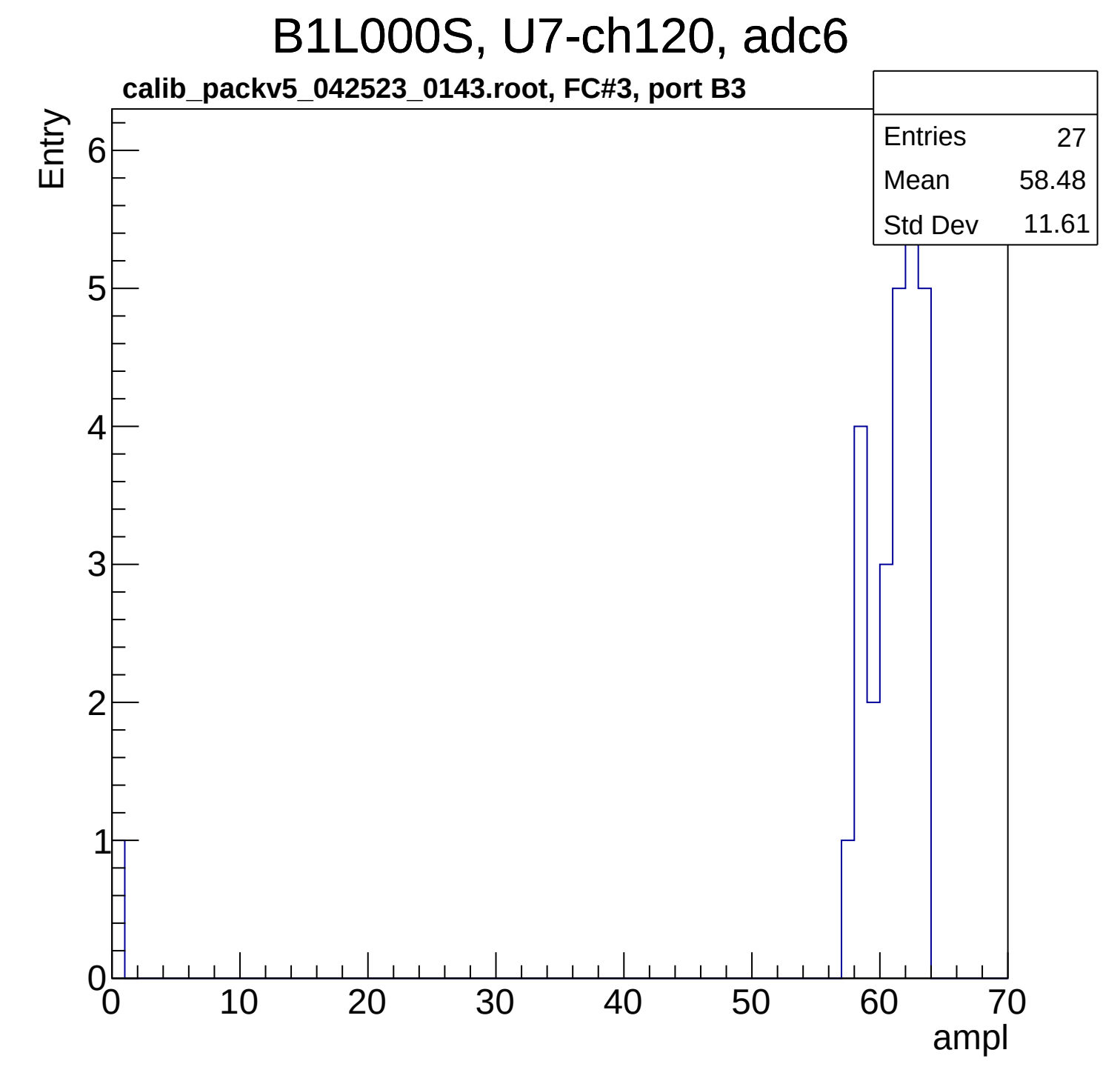
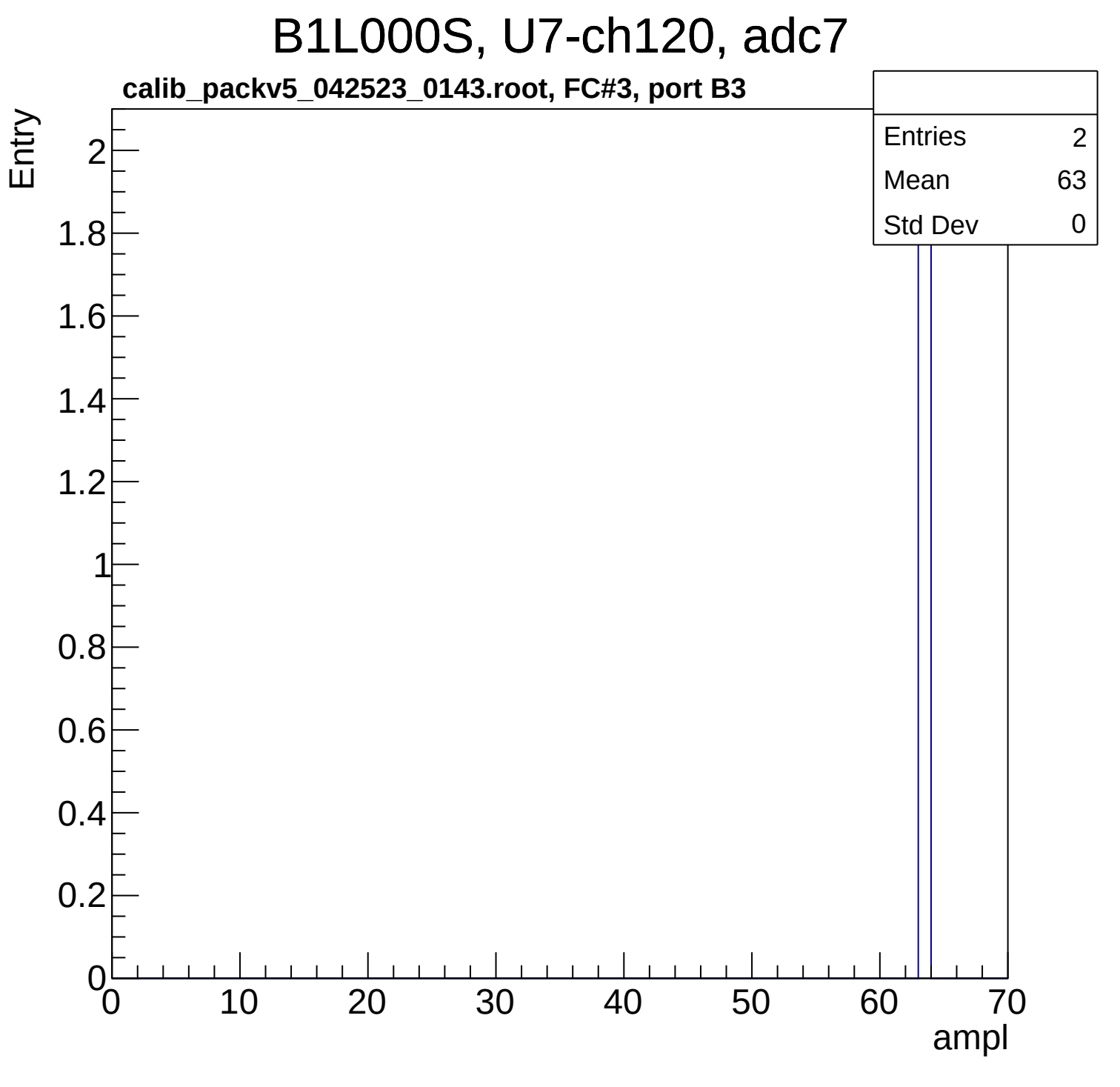
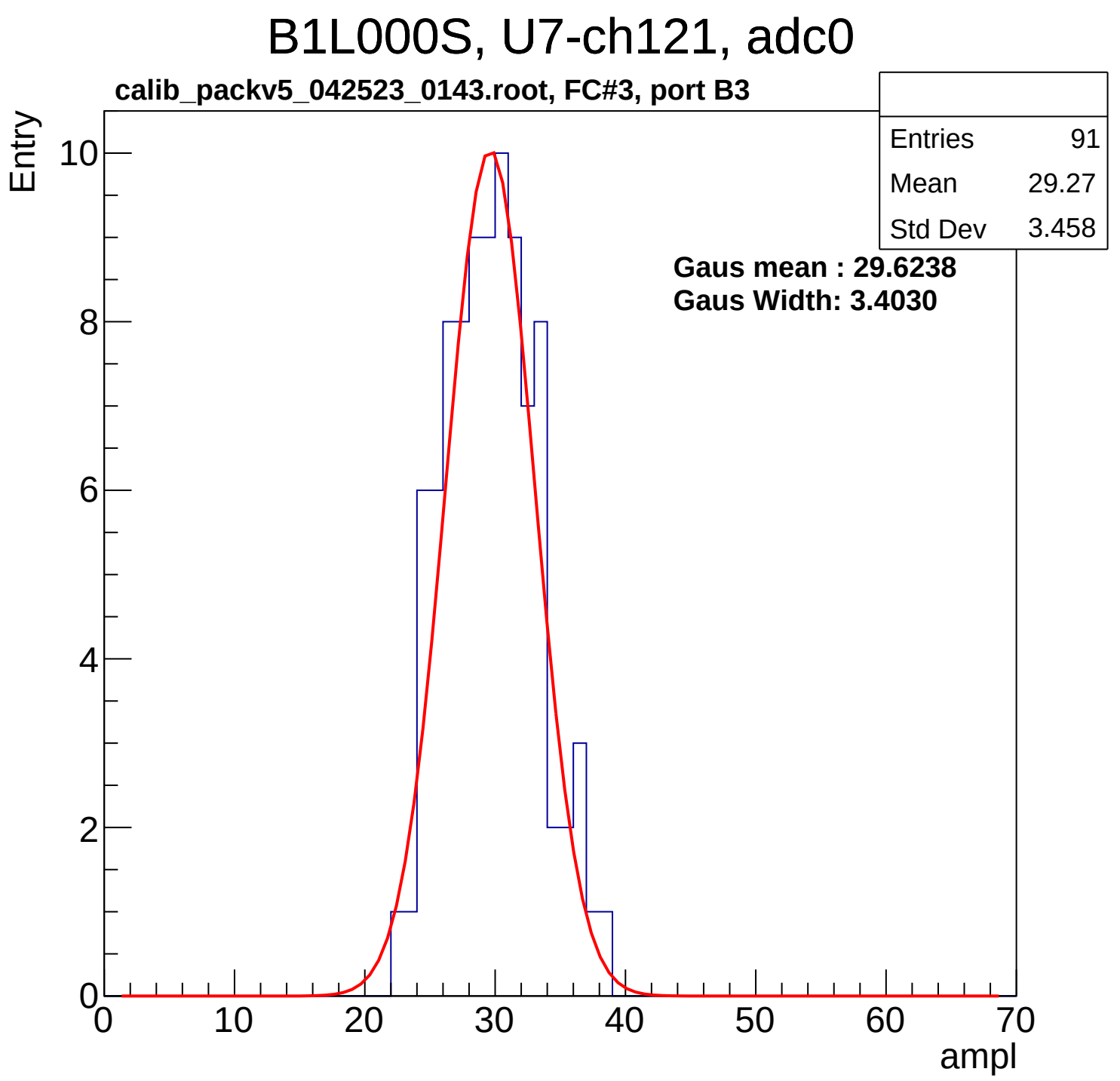
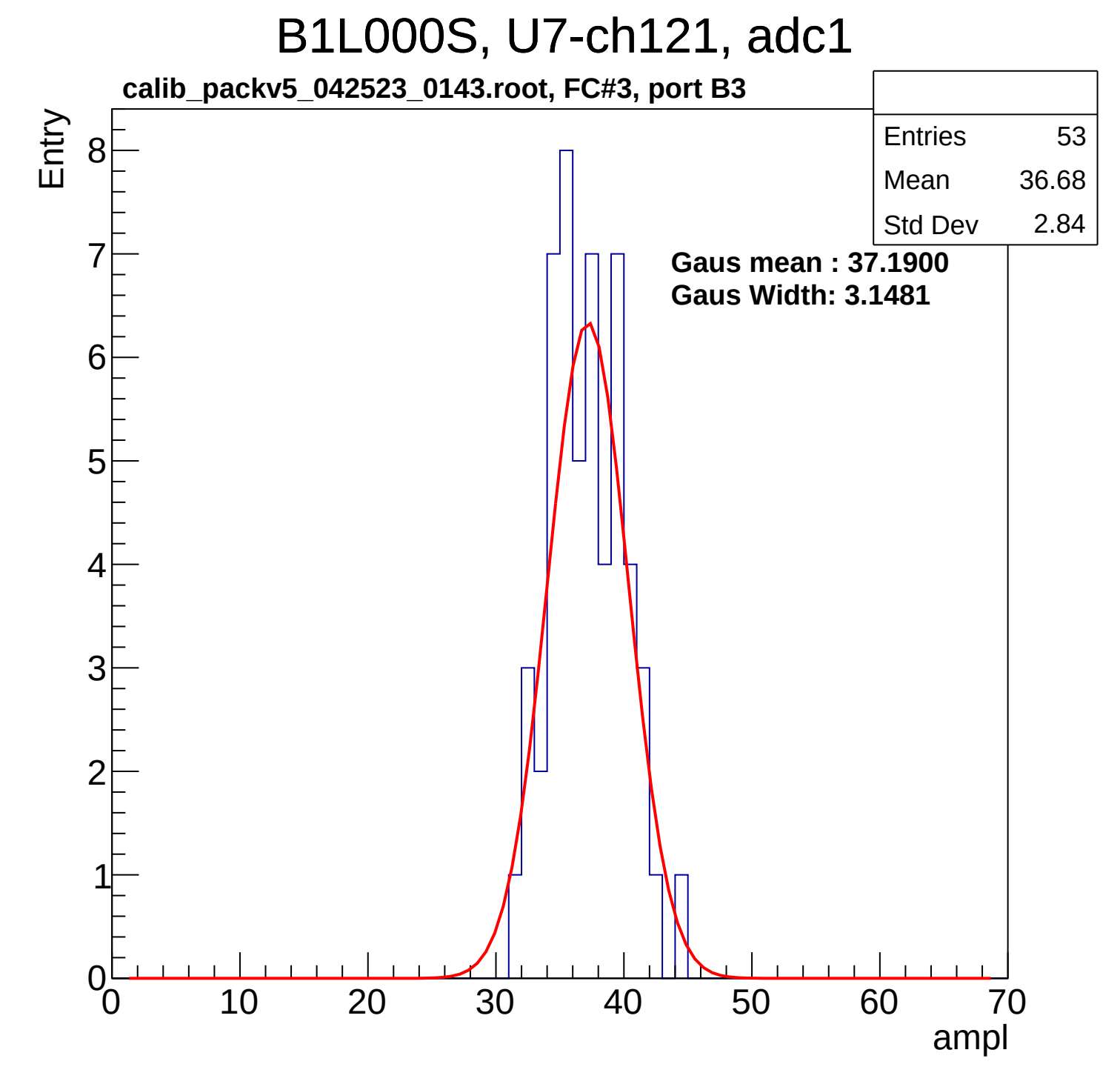
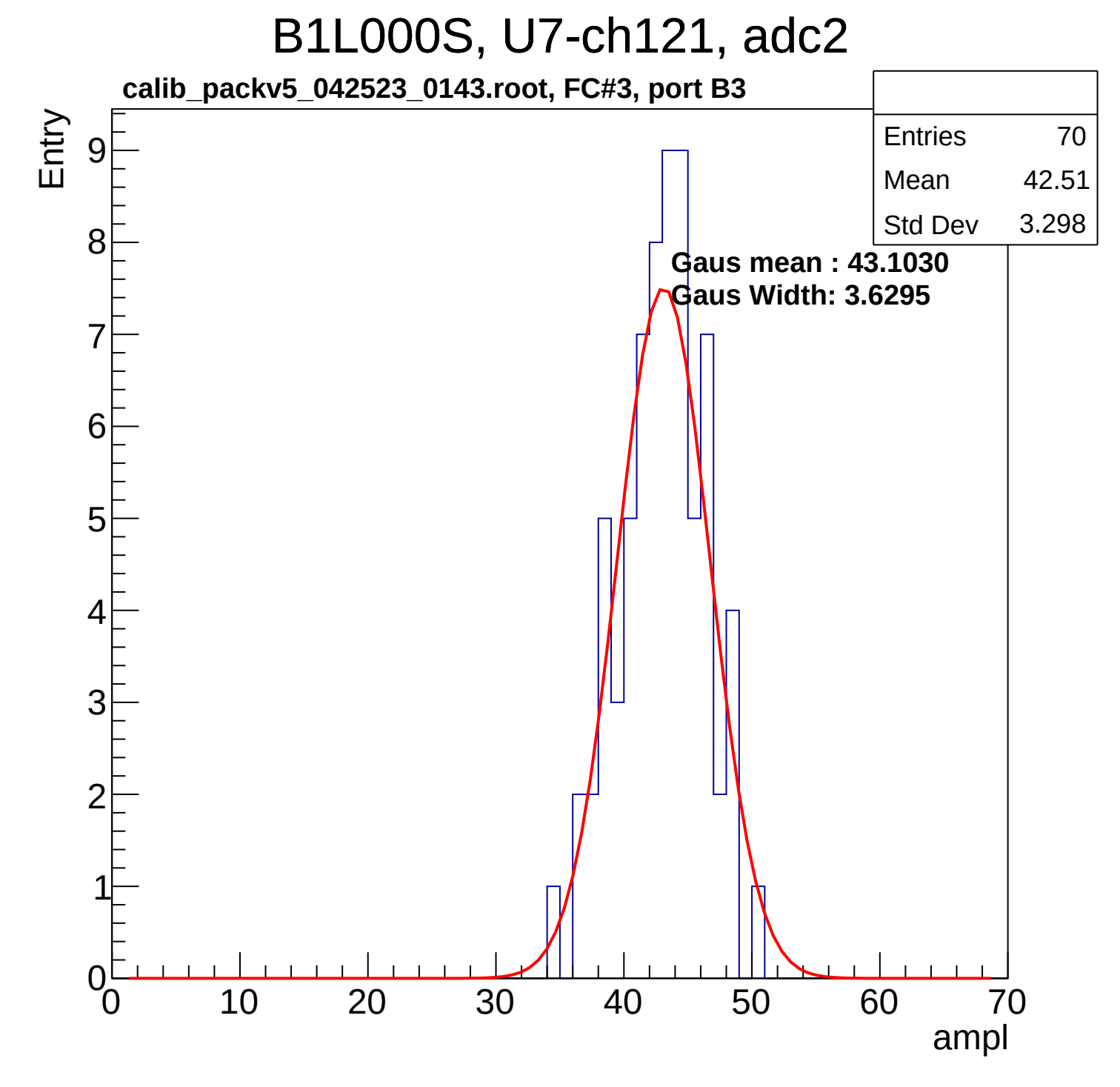
60
70


B1L000S, U7-ch120, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 27
Mean 58.48
Std Dev 11.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch120, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch121, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 91
Mean 29.27
Std Dev 3.458
Gaus mean : 29.6238
Gaus Width: 3.4030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch121, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 36.68
Std Dev 2.84
Gaus mean : 37.1900
Gaus Width: 3.1481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch121, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 70
Mean 42.51
Std Dev 3.298
Gaus mean : 43.1030
Gaus Width: 3.6295
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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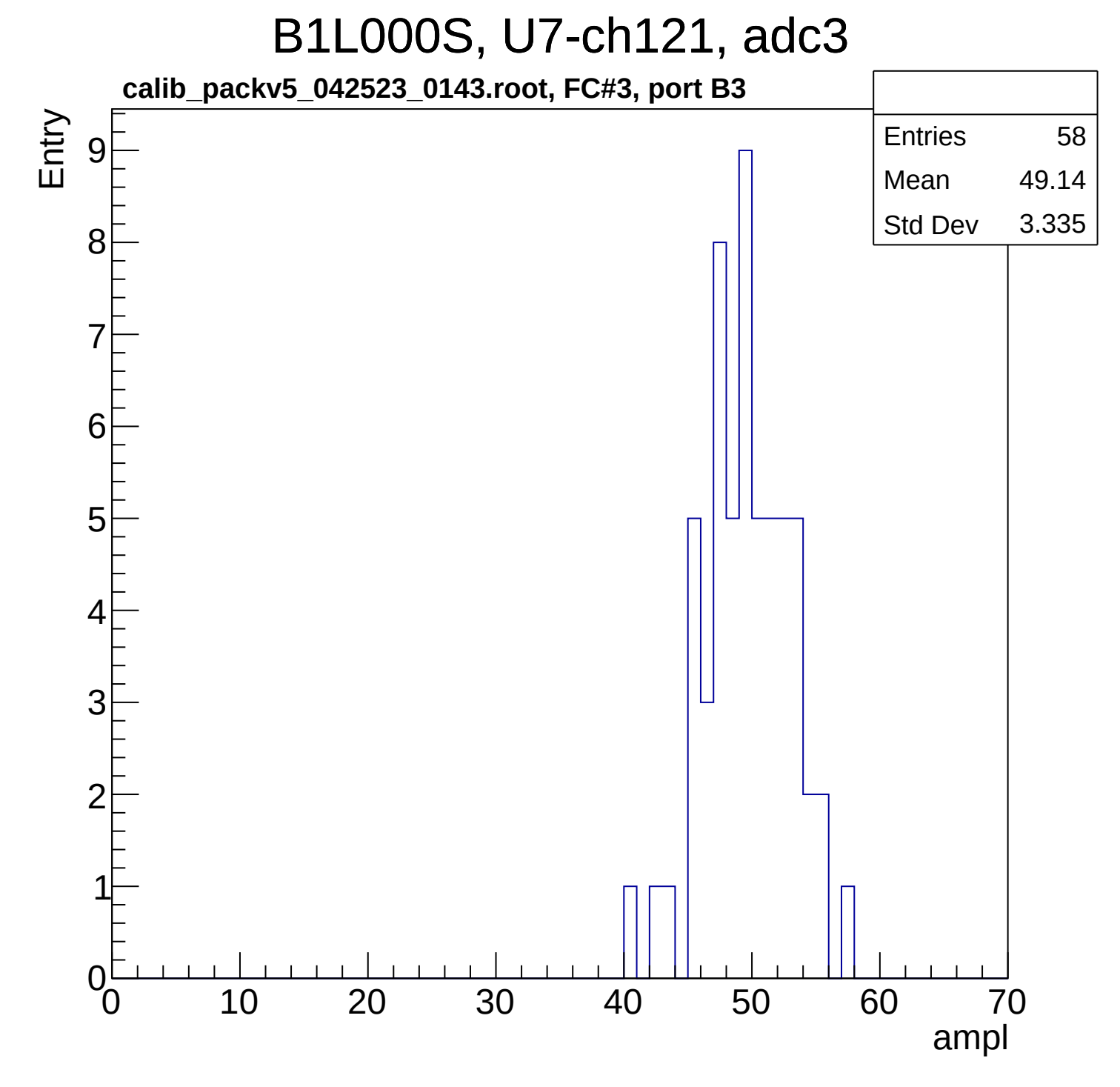
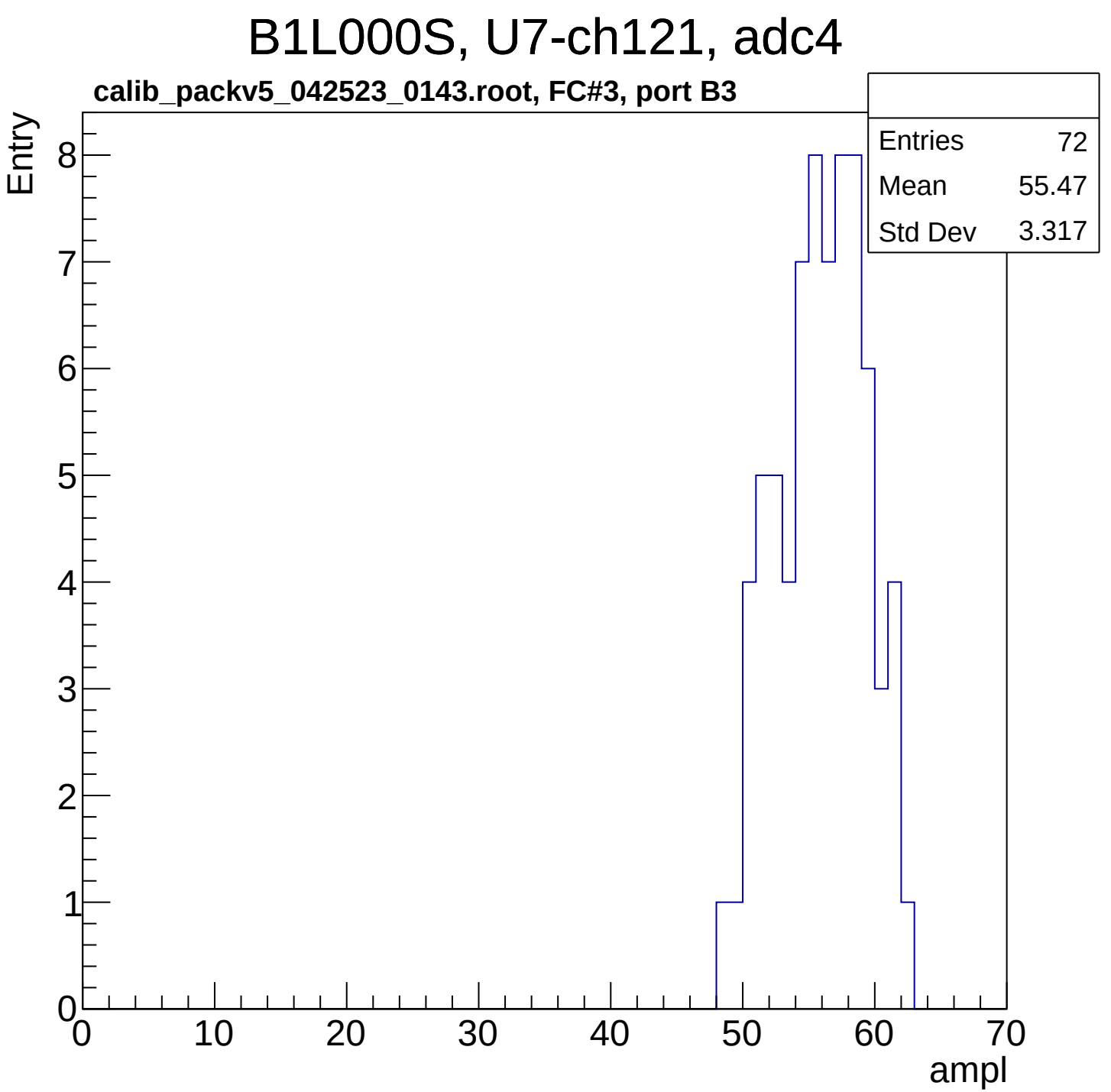

B1L000S, U7-ch121, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 49.14
Std Dev 3.335
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch121, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 55.47
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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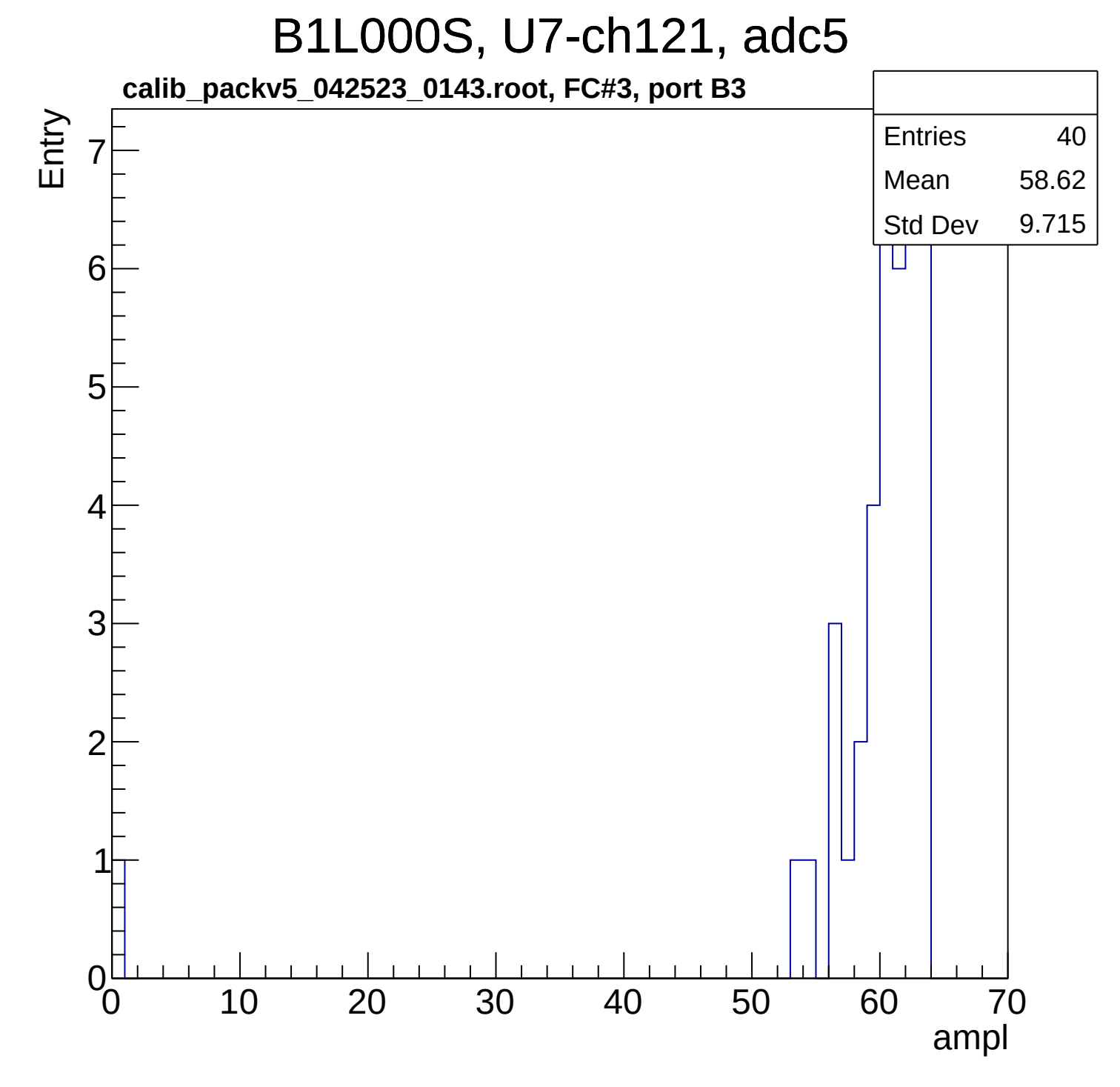
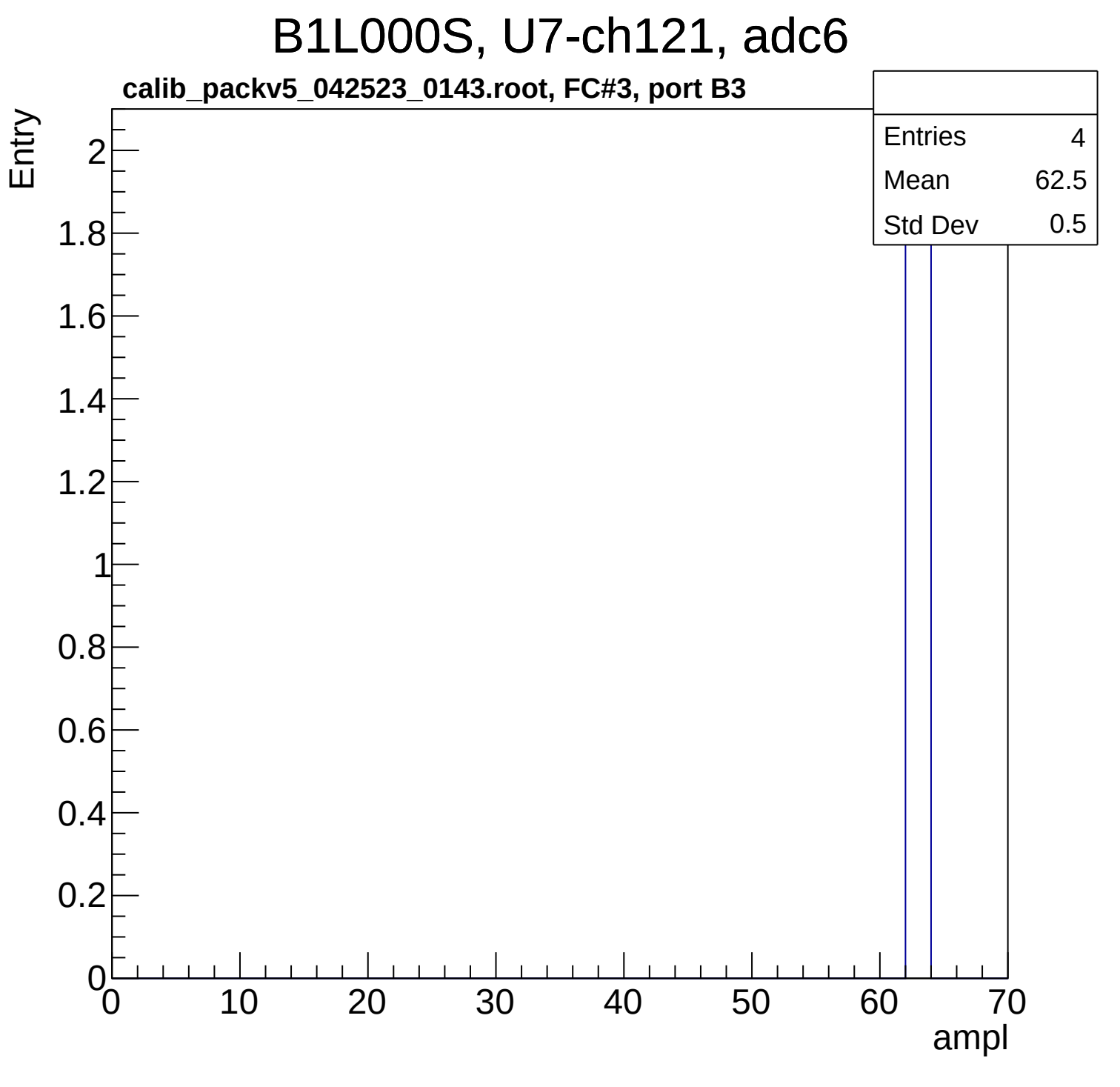
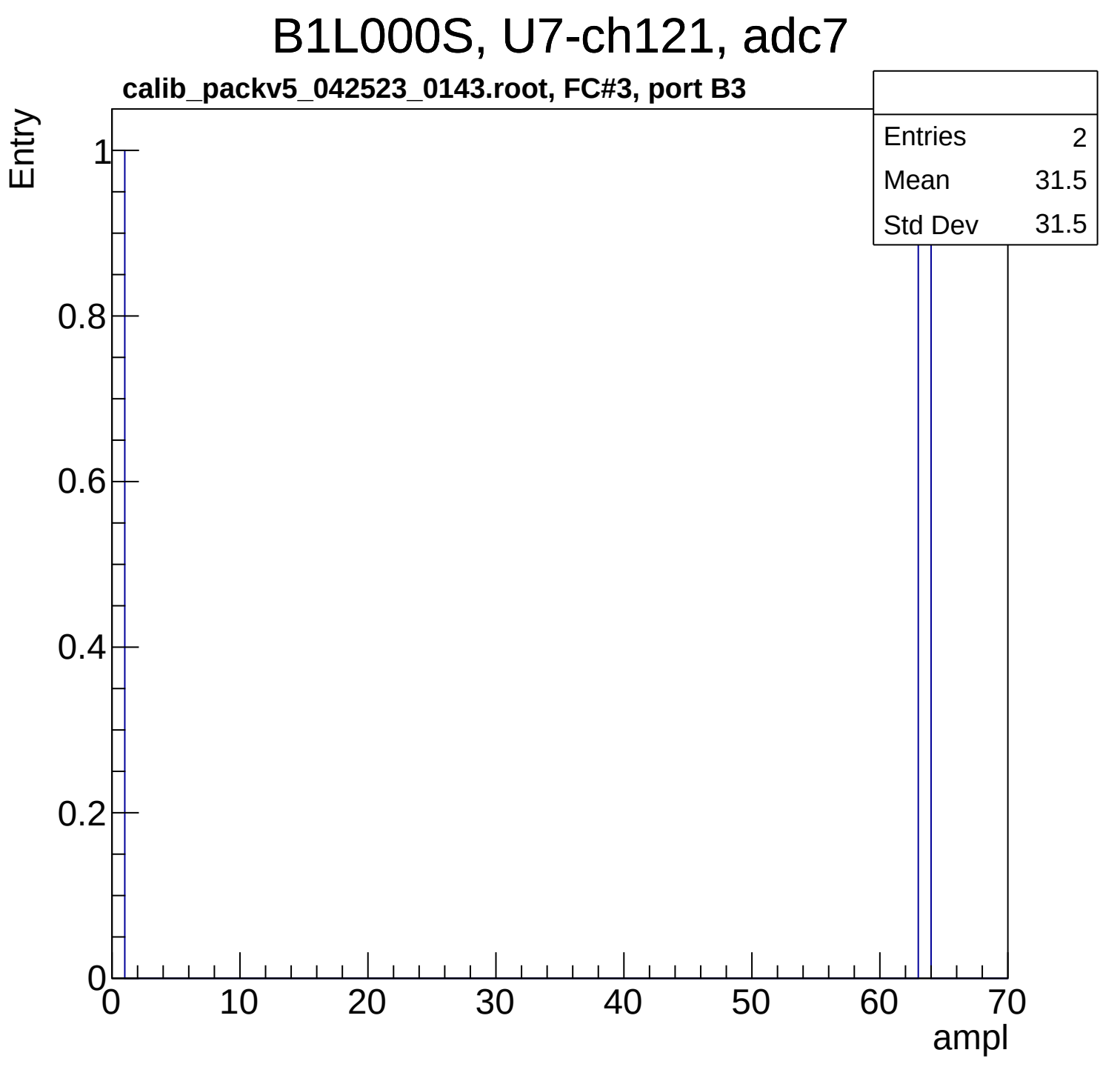
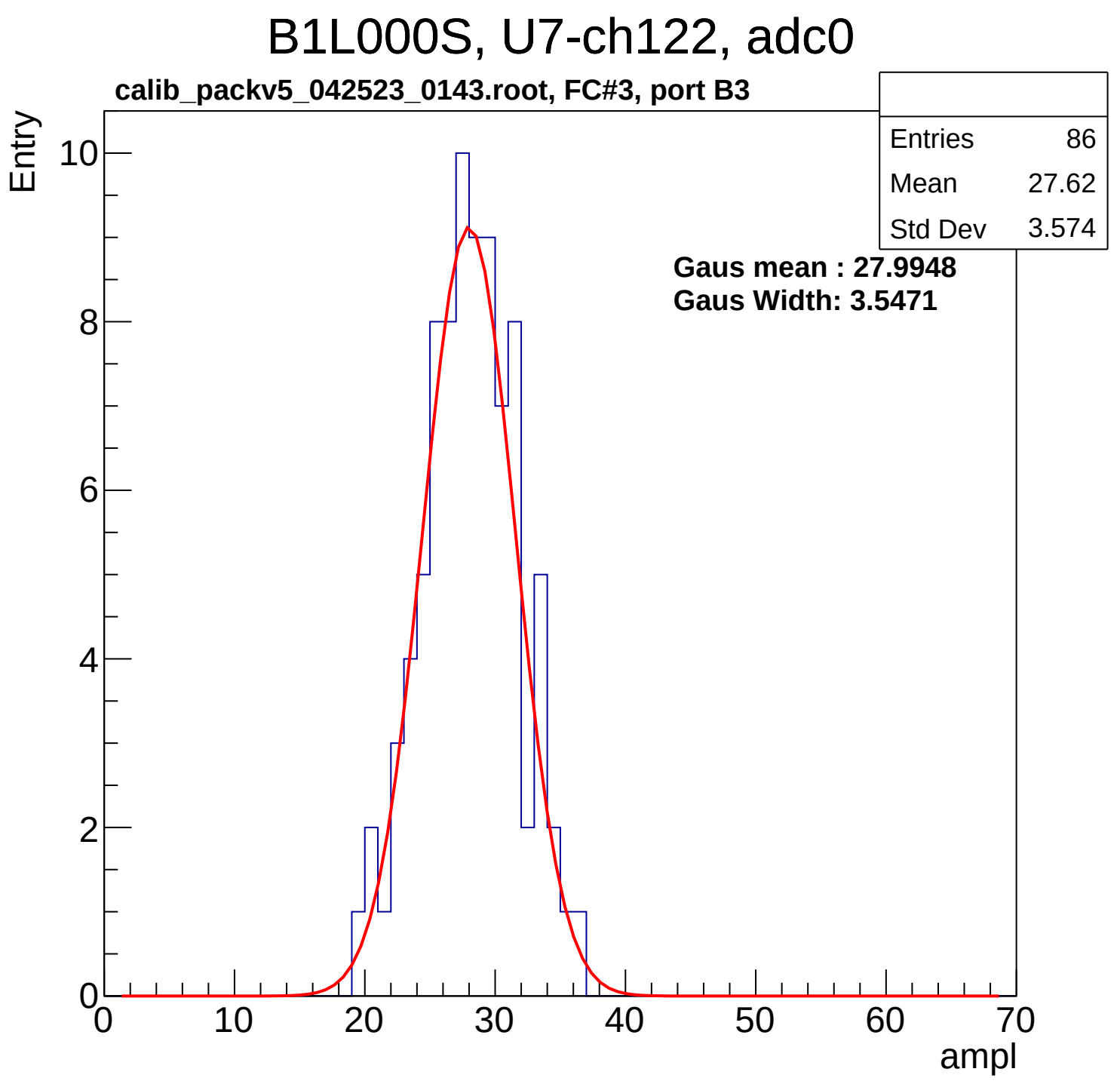
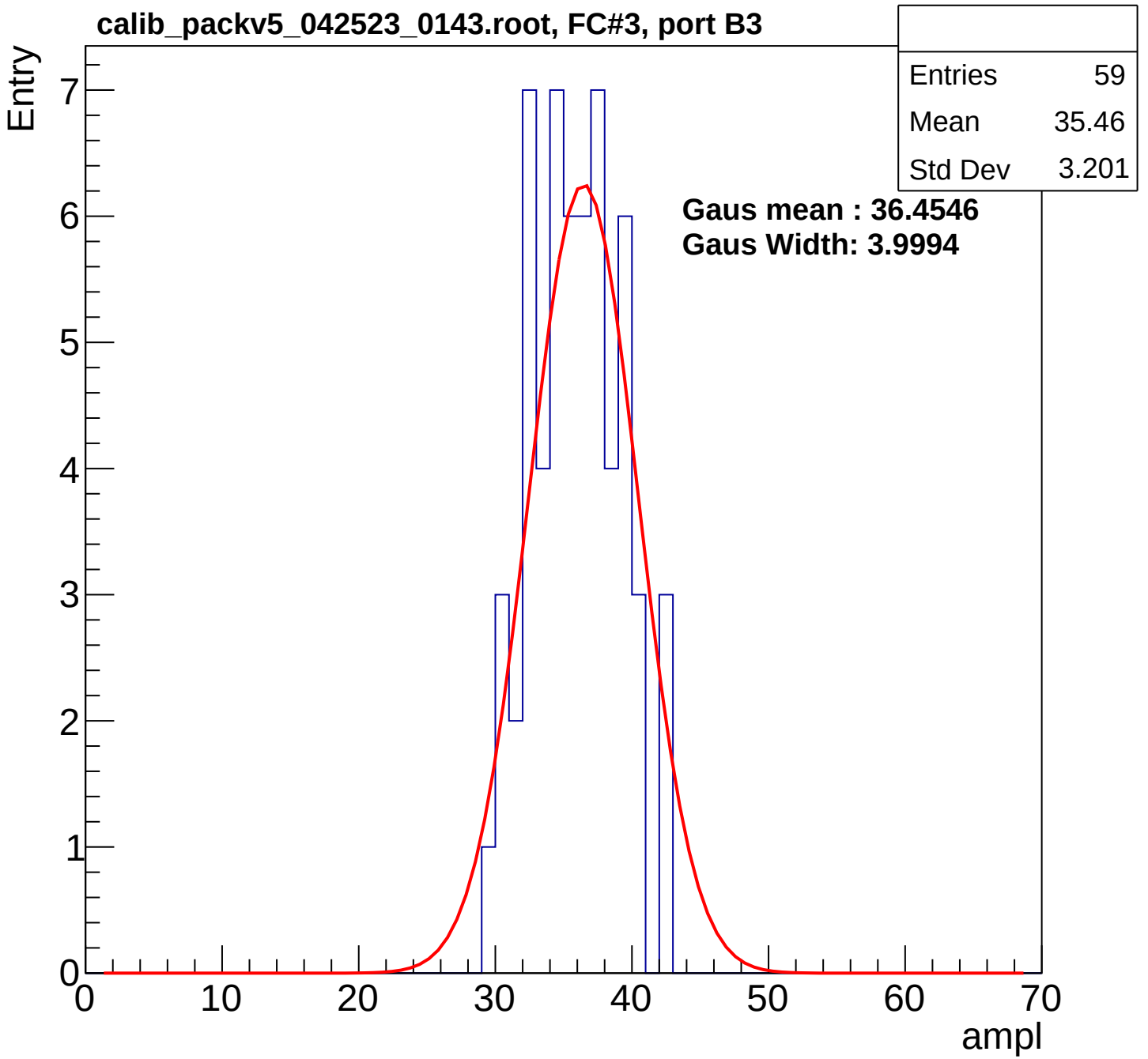
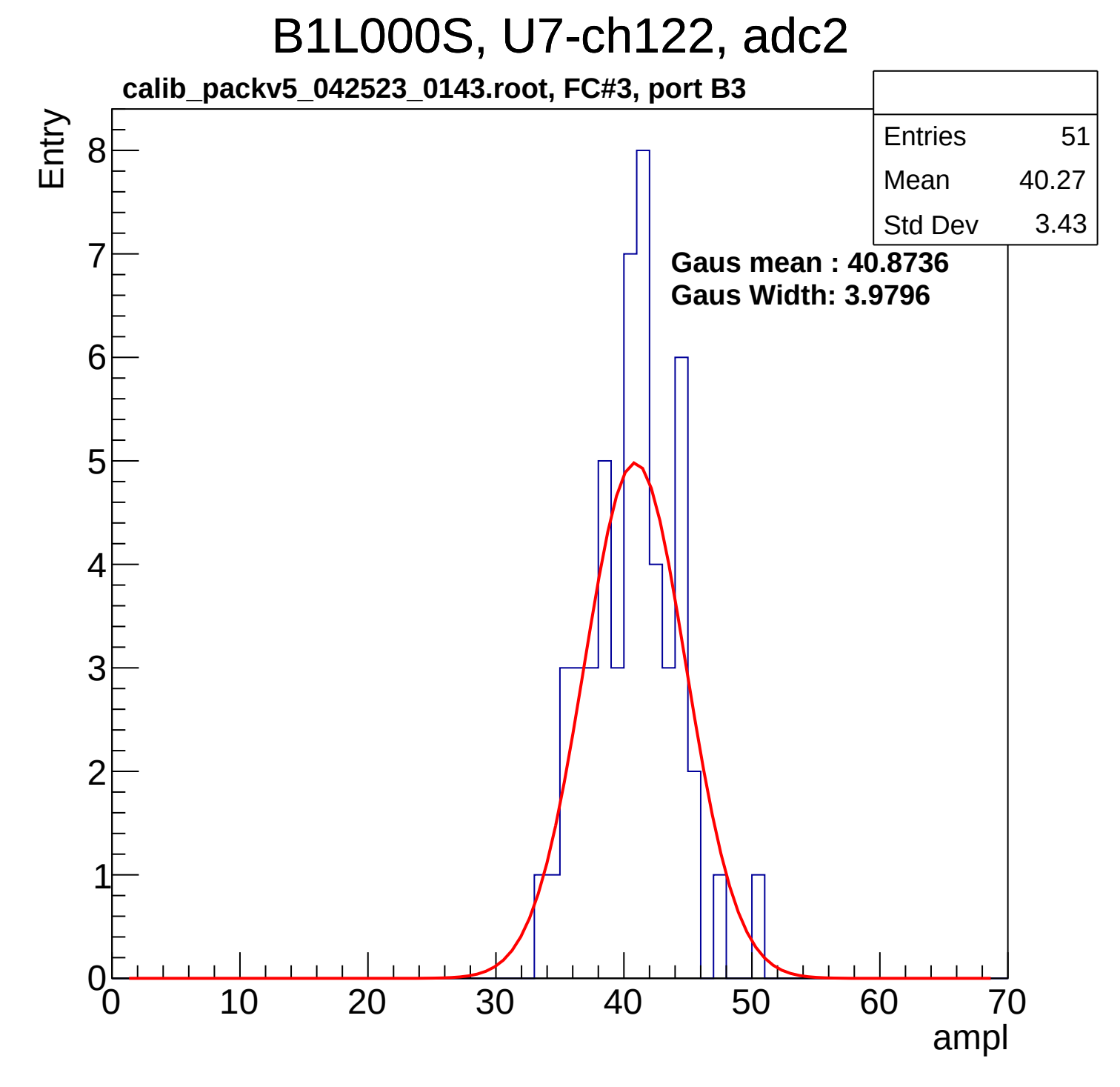

B1L000S, U7-ch121, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 58.62
Std Dev 9.715
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

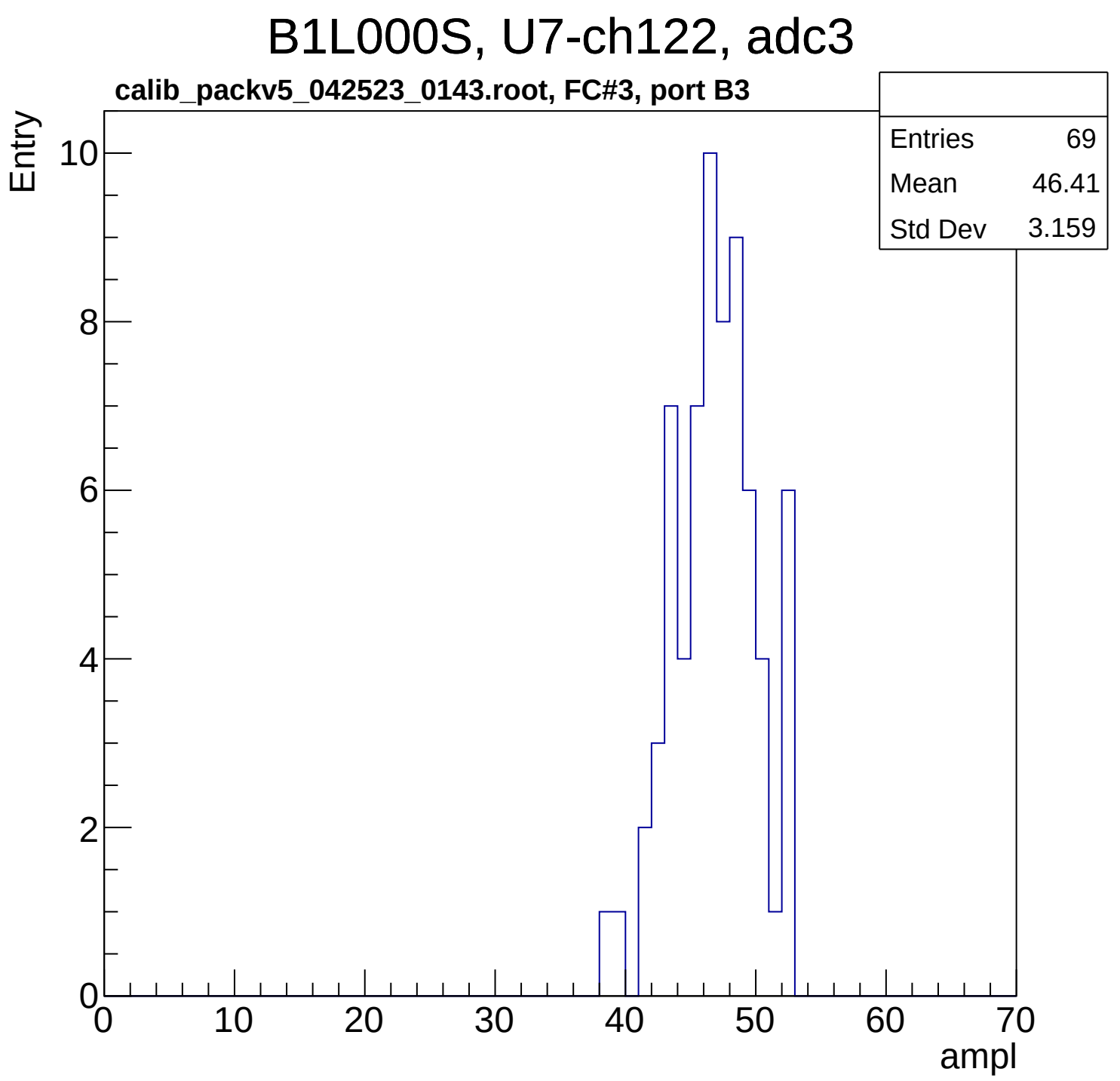

B1L000S, U7-ch121, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch121, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch122, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 86
Mean 27.62
Std Dev 3.574
Gaus mean : 27.9948
Gaus Width: 3.5471
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L000S, U7-ch122, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 59
Mean 35.46
Std Dev 3.201
Gaus mean : 36.4546
Gaus Width: 3.9994
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch122, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 51
Mean 40.27
Std Dev 3.43
Gaus mean : 40.8736
Gaus Width: 3.9796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch122, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 69
Mean 46.41
Std Dev 3.159
Entry
0
2
4
6
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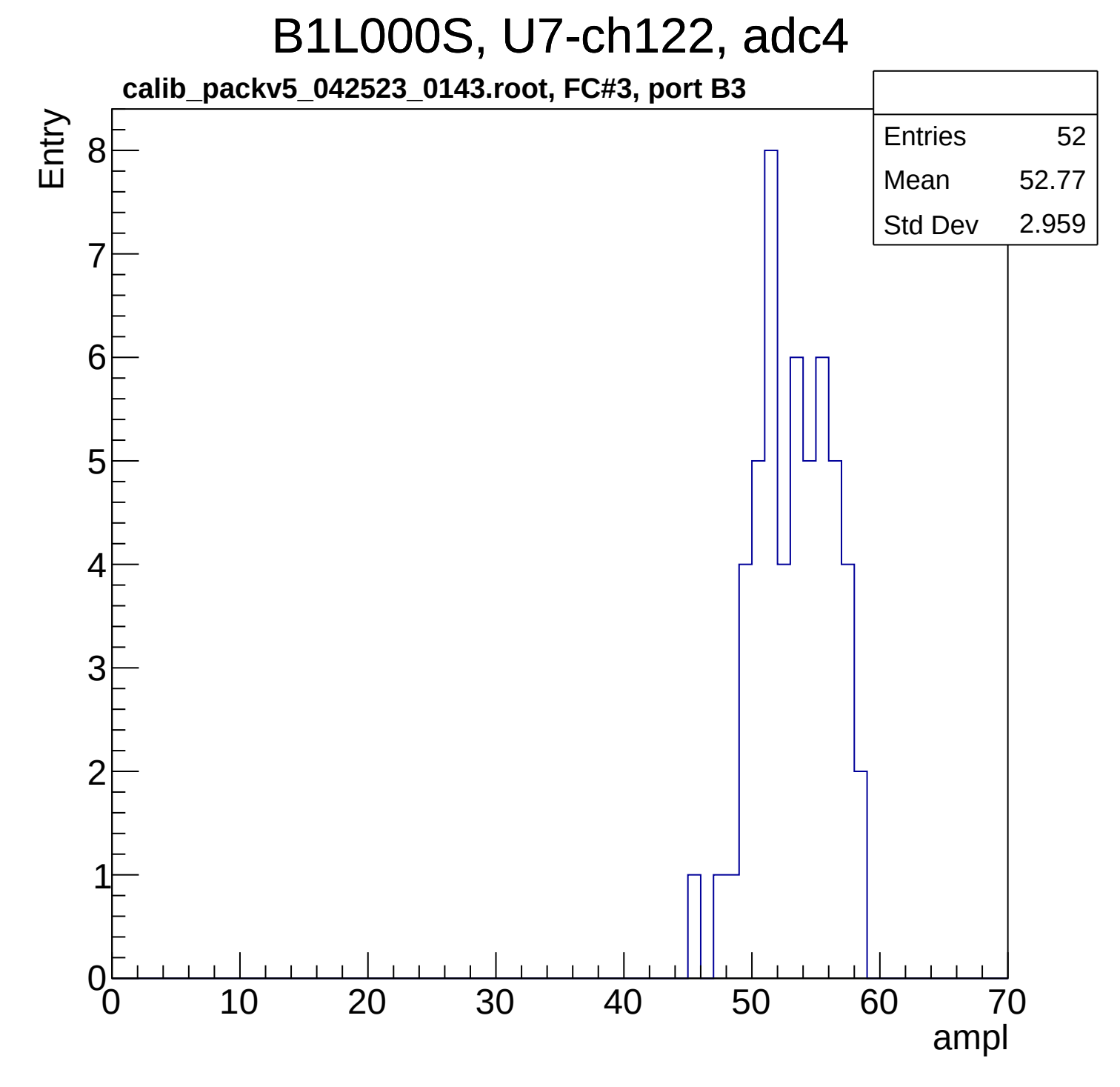
10
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch122, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 52
Mean 52.77
Std Dev 2.959
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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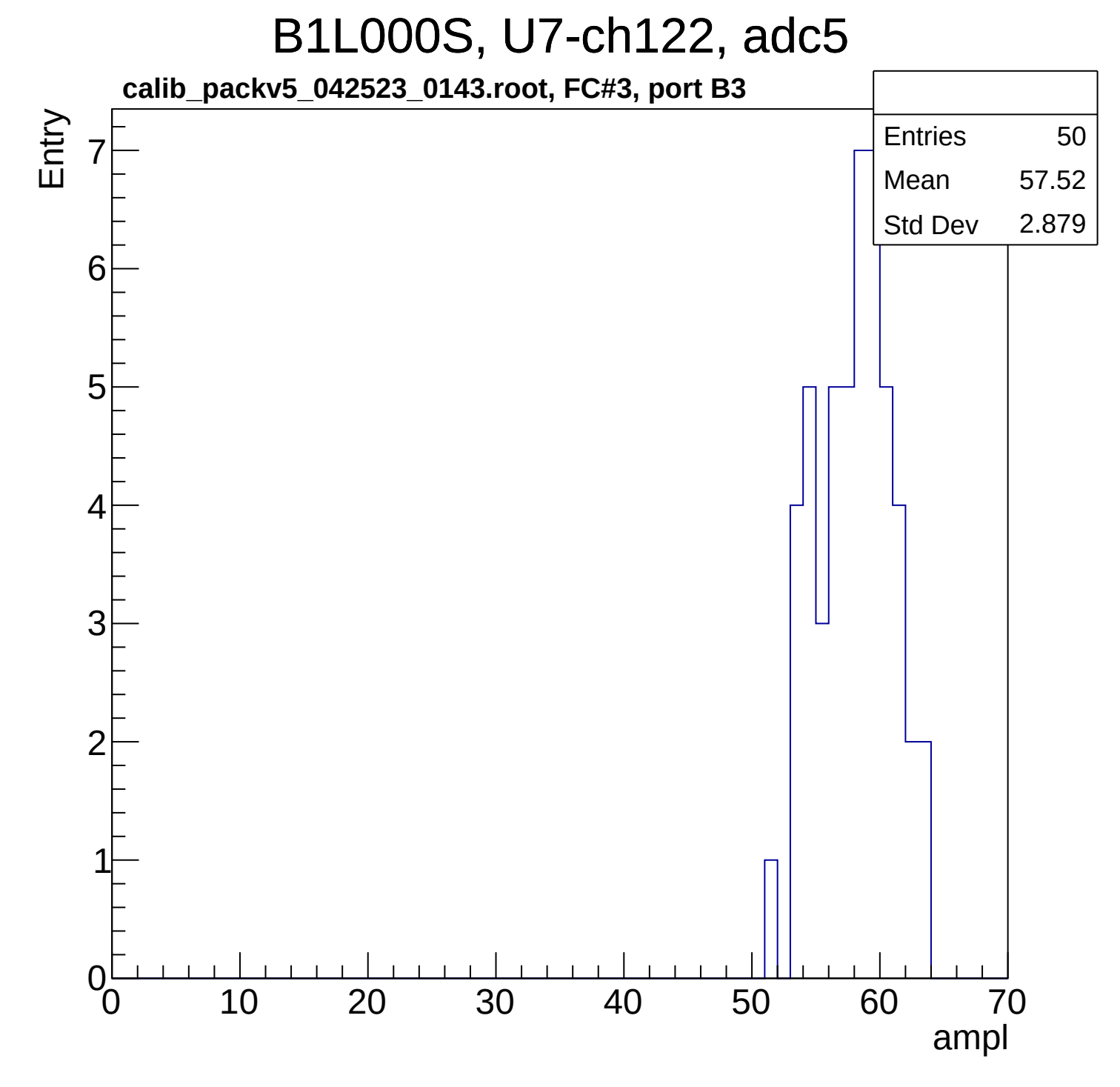
B1L000S, U7-ch122, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 50
Mean 57.52
Std Dev 2.879
Entry
ampl
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3
4
5
6
7
0
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20
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40
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60
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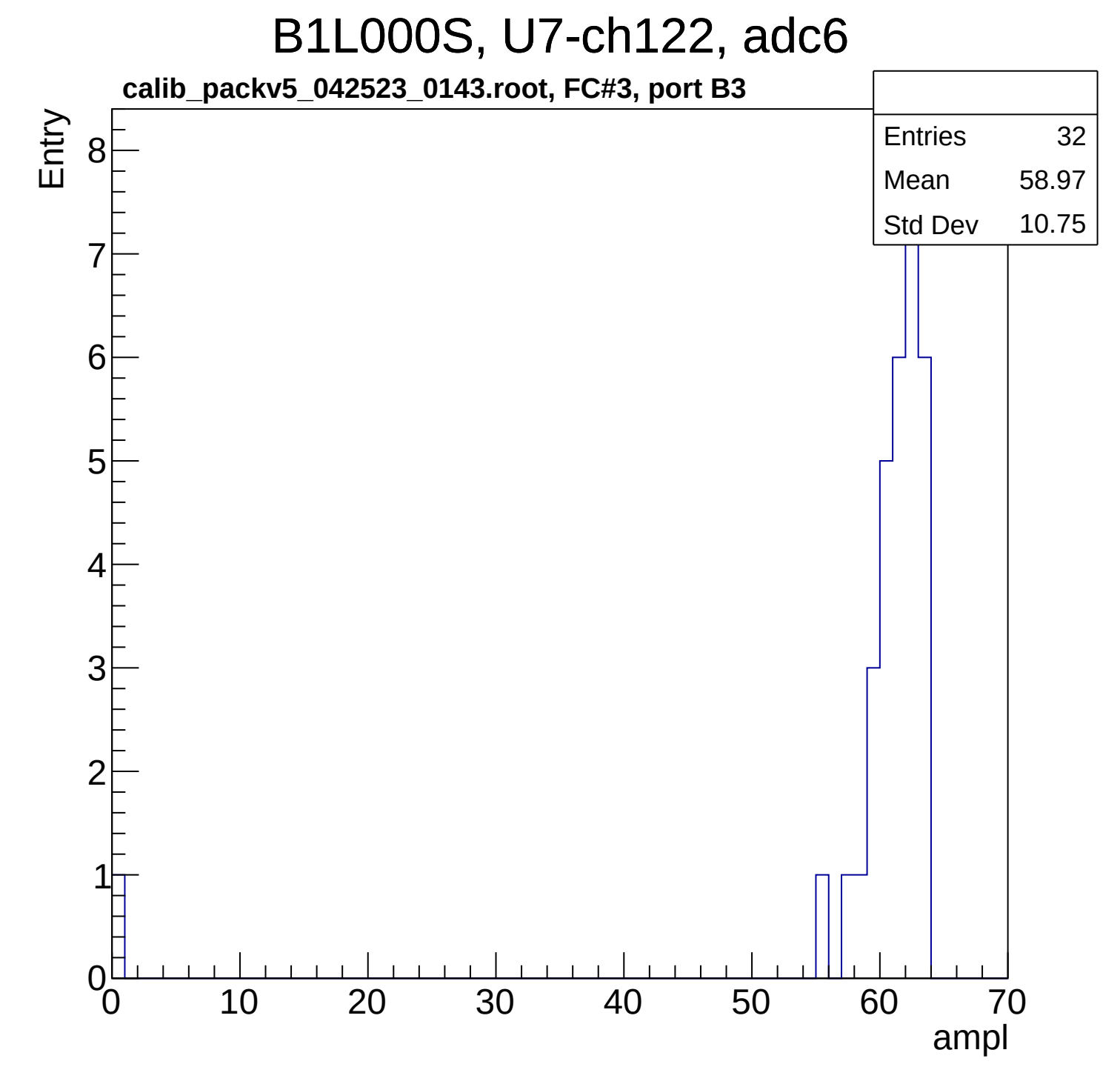
B1L000S, U7-ch122, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 32
Mean 58.97
Std Dev 10.75
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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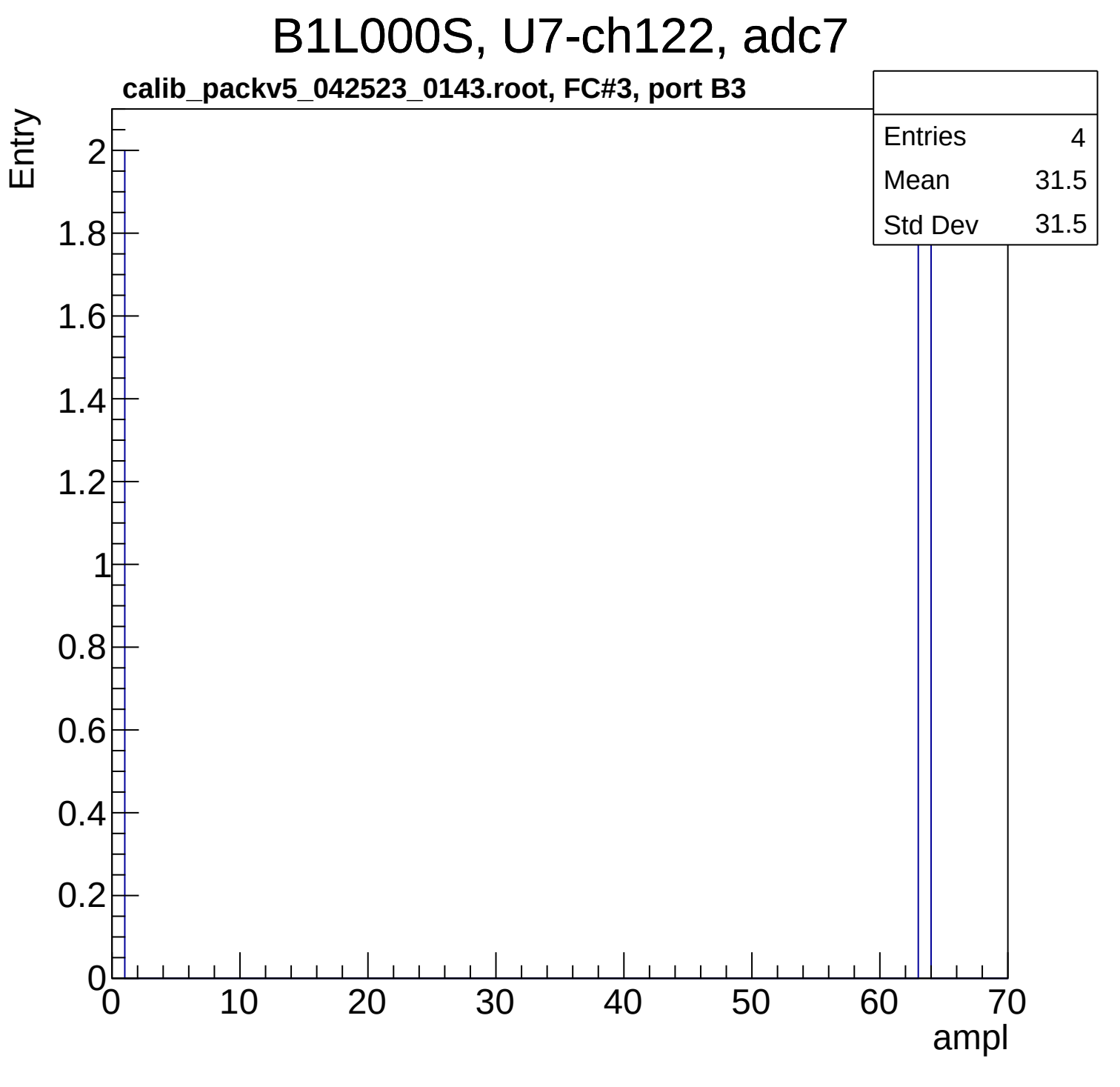
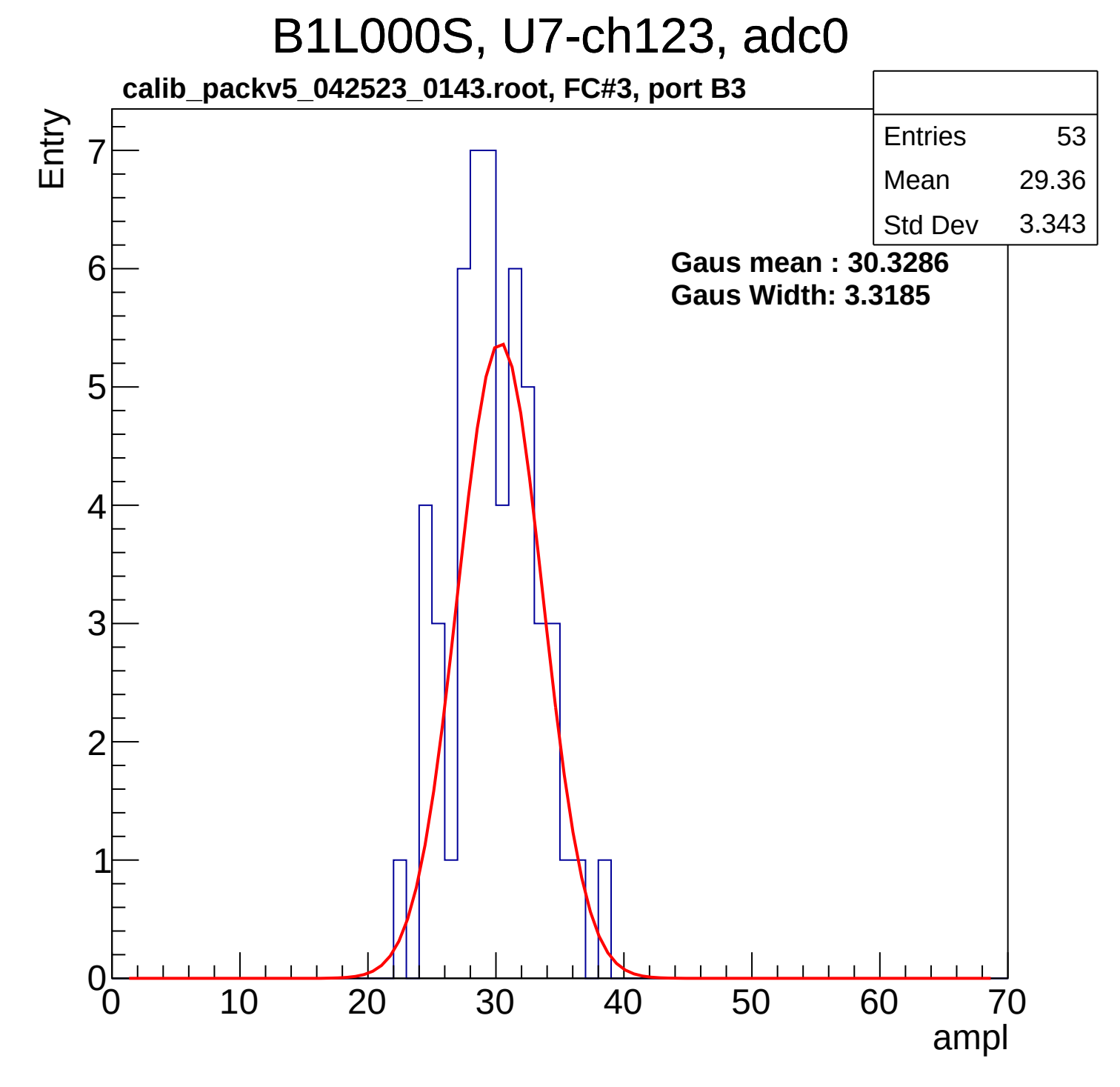
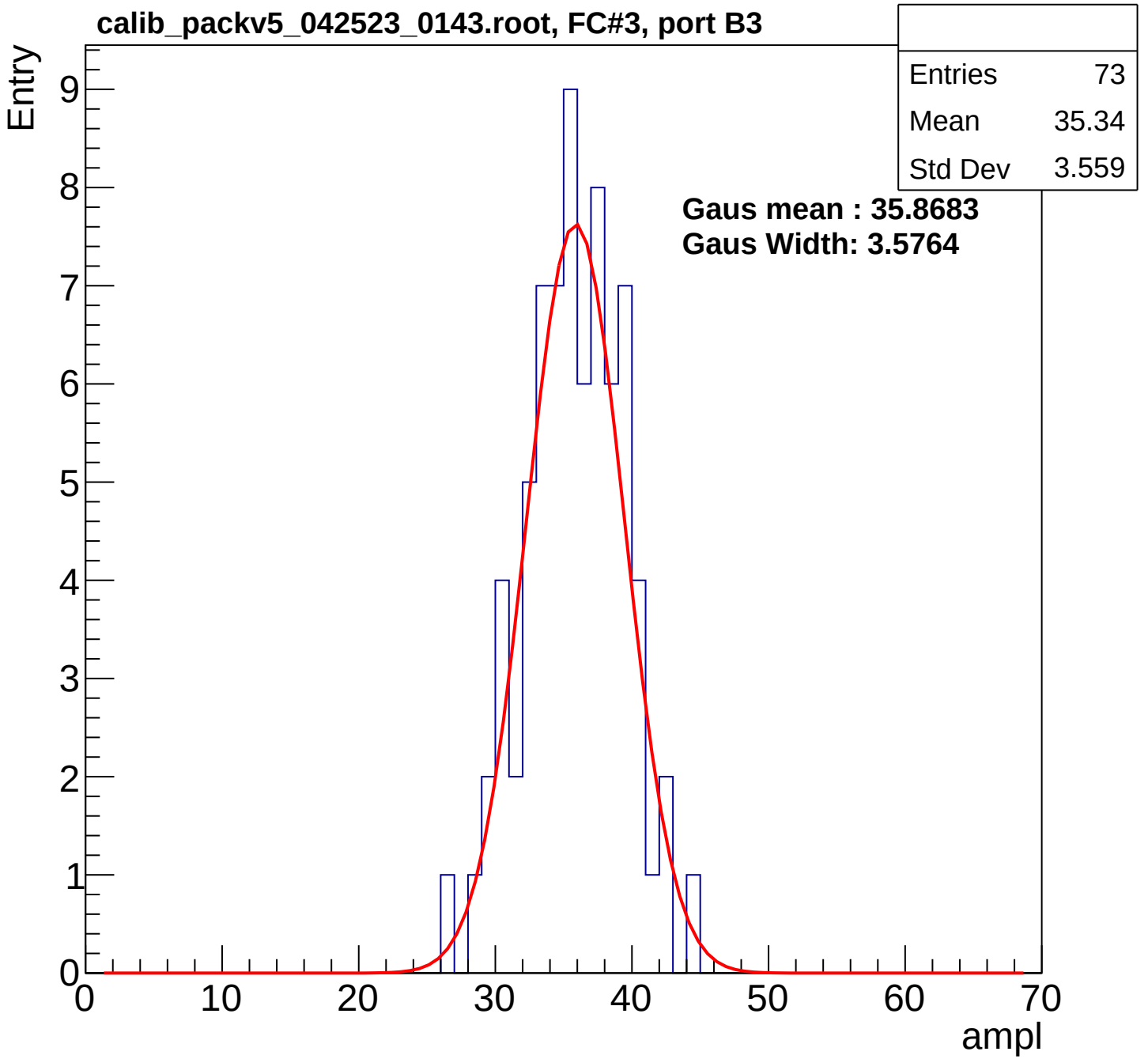
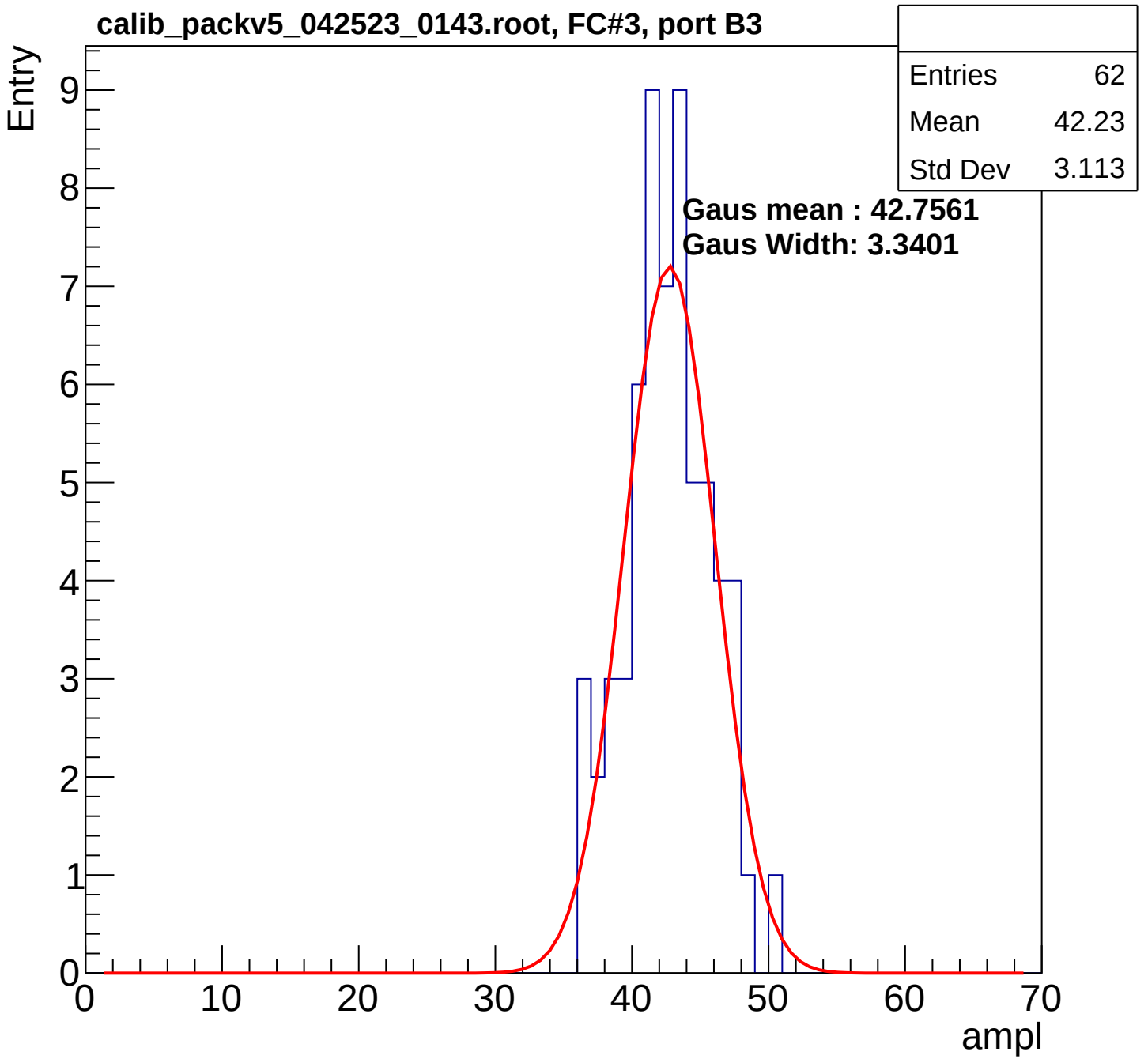

B1L000S, U7-ch122, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L000S, U7-ch123, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 29.36
Std Dev 3.343
Gaus mean : 30.3286
Gaus Width: 3.3185
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L000S, U7-ch123, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 73
Mean 35.34
Std Dev 3.559
Gaus mean : 35.8683
Gaus Width: 3.5764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch123, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 42.23
Std Dev 3.113
Gaus mean : 42.7561
Gaus Width: 3.3401
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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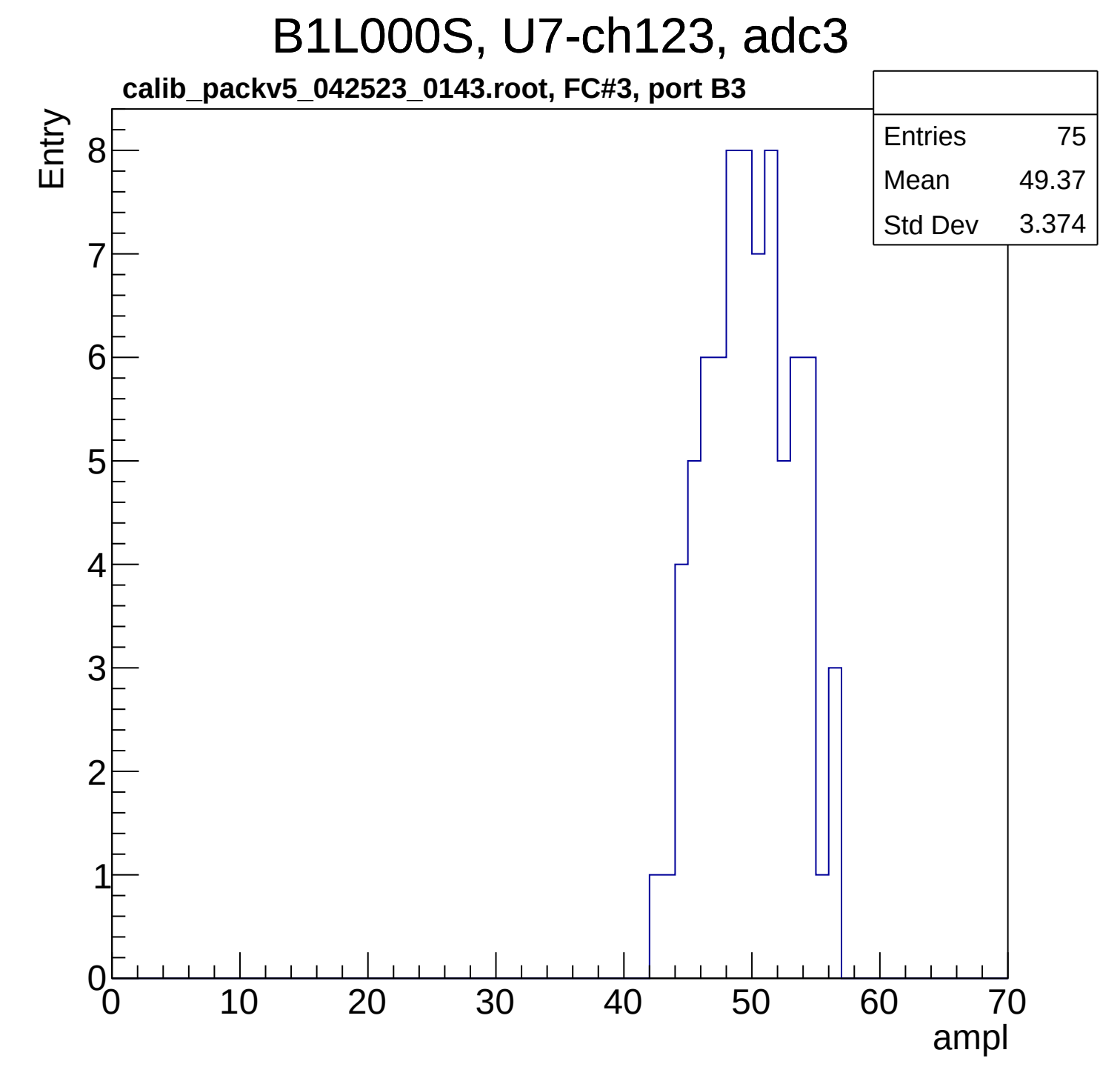
B1L000S, U7-ch123, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 75
Mean 49.37
Std Dev 3.374
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
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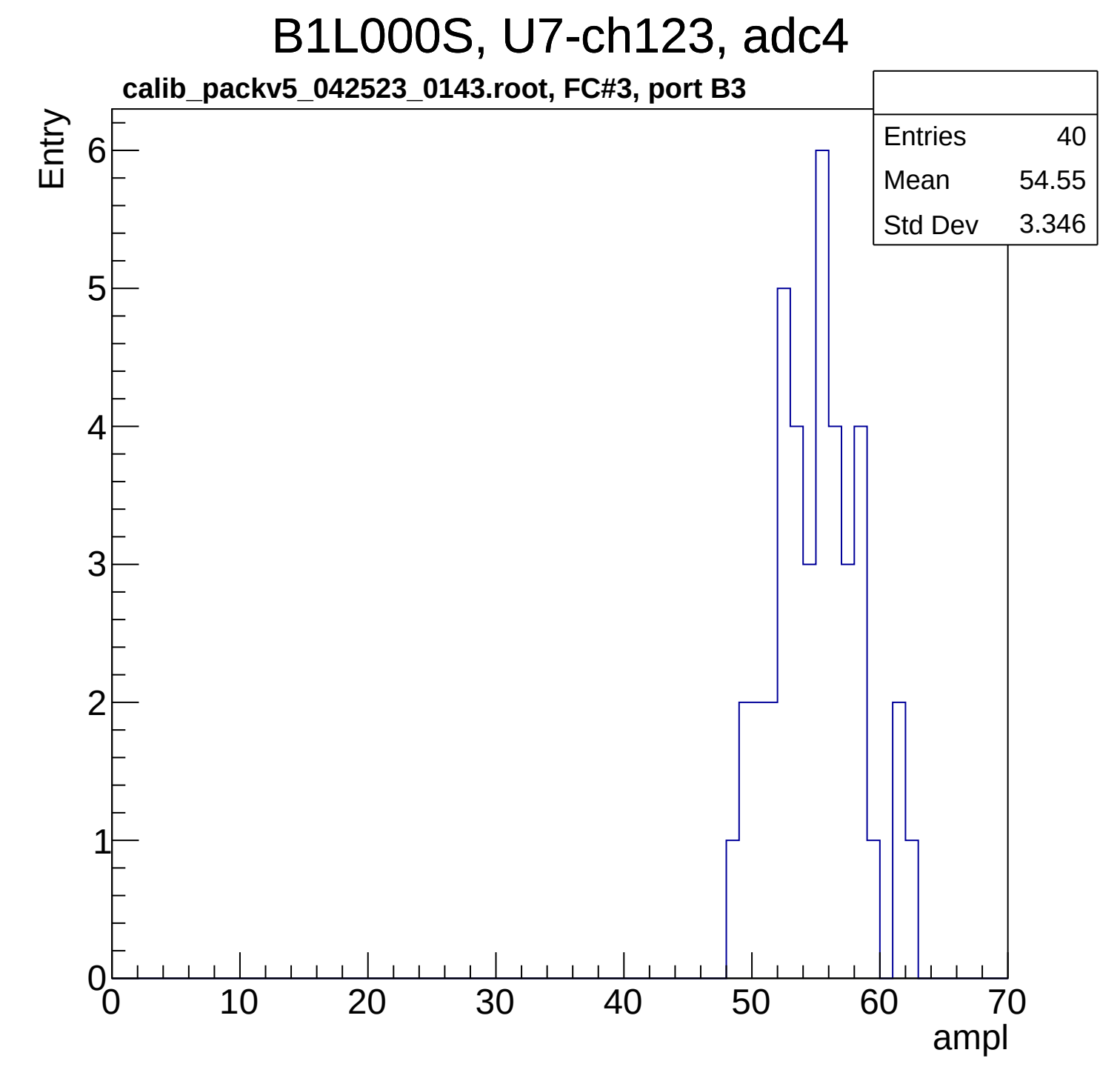
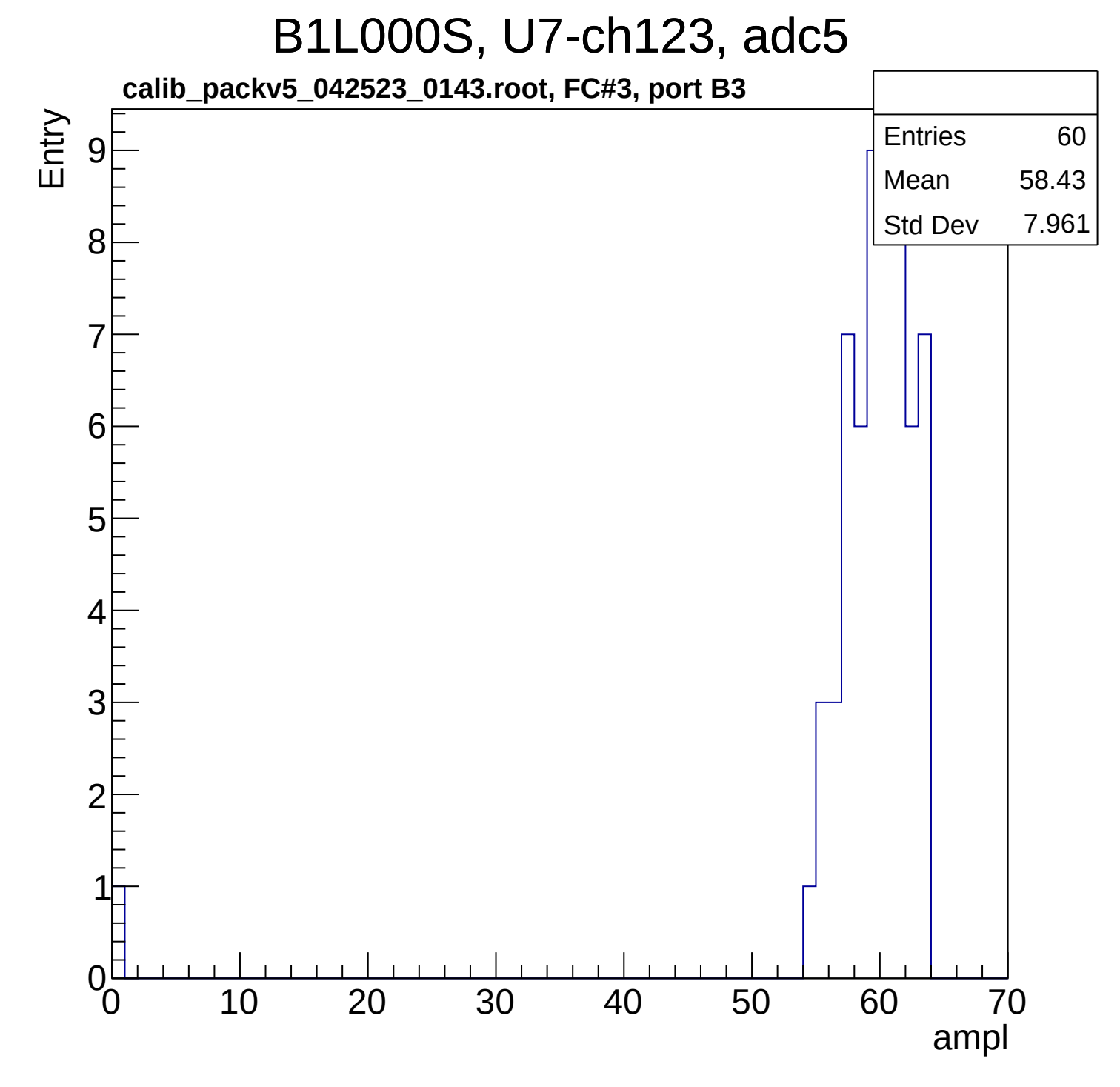
60
70


B1L000S, U7-ch123, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 40
Mean 54.55
Std Dev 3.346
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch123, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 58.43
Std Dev 7.961
Entry
ampl
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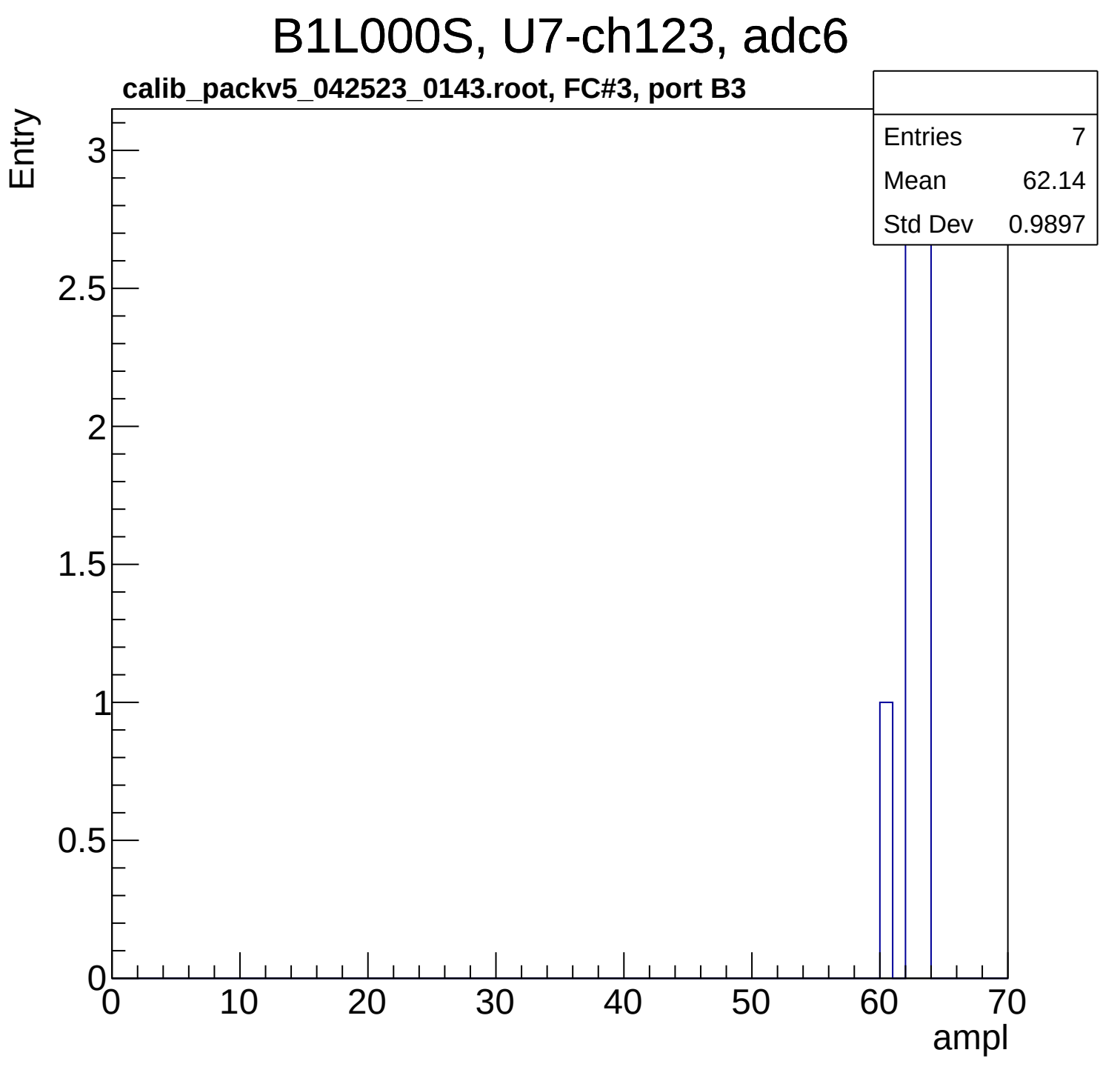
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L000S, U7-ch123, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
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40
50
60
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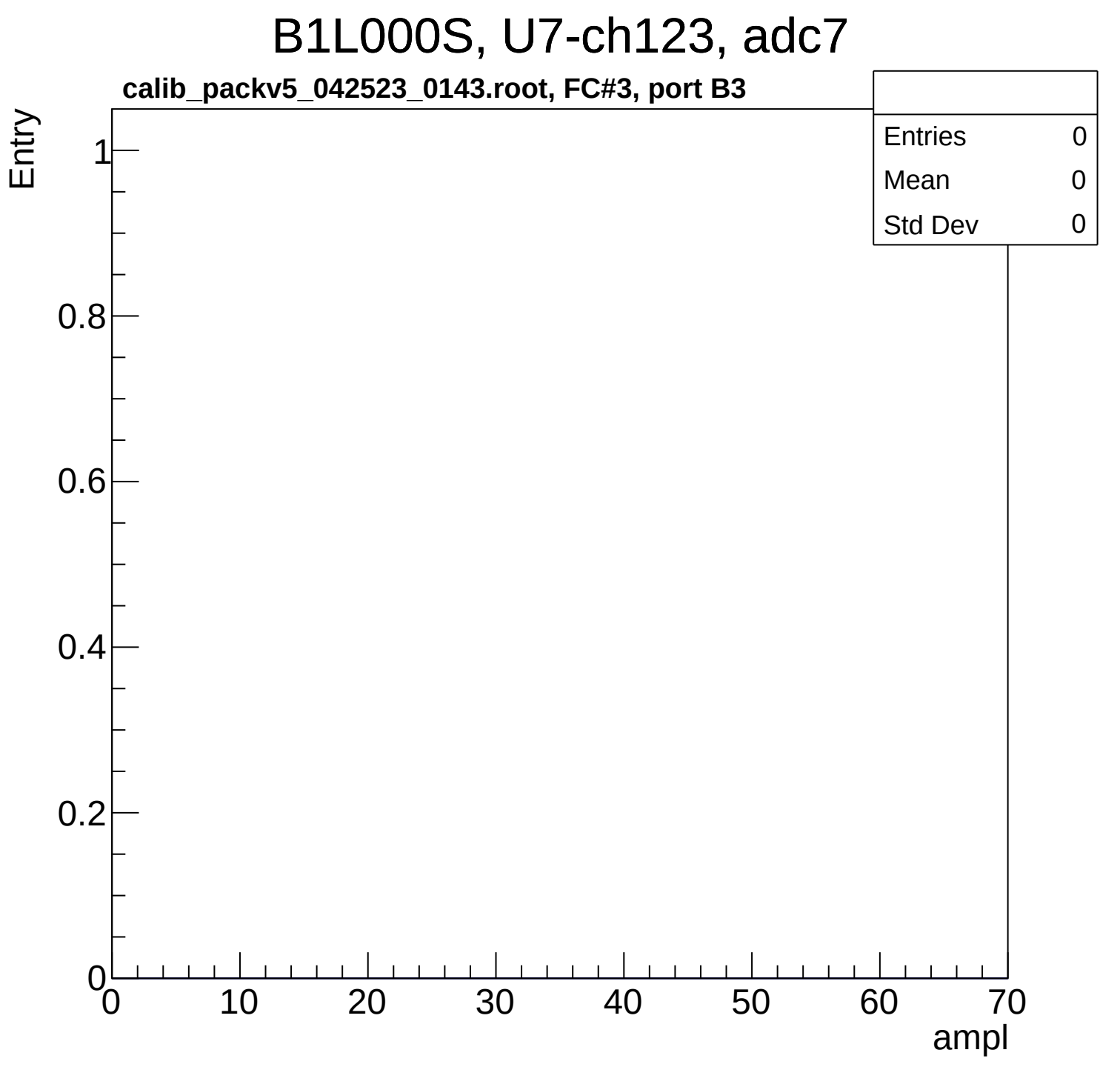
B1L000S, U7-ch123, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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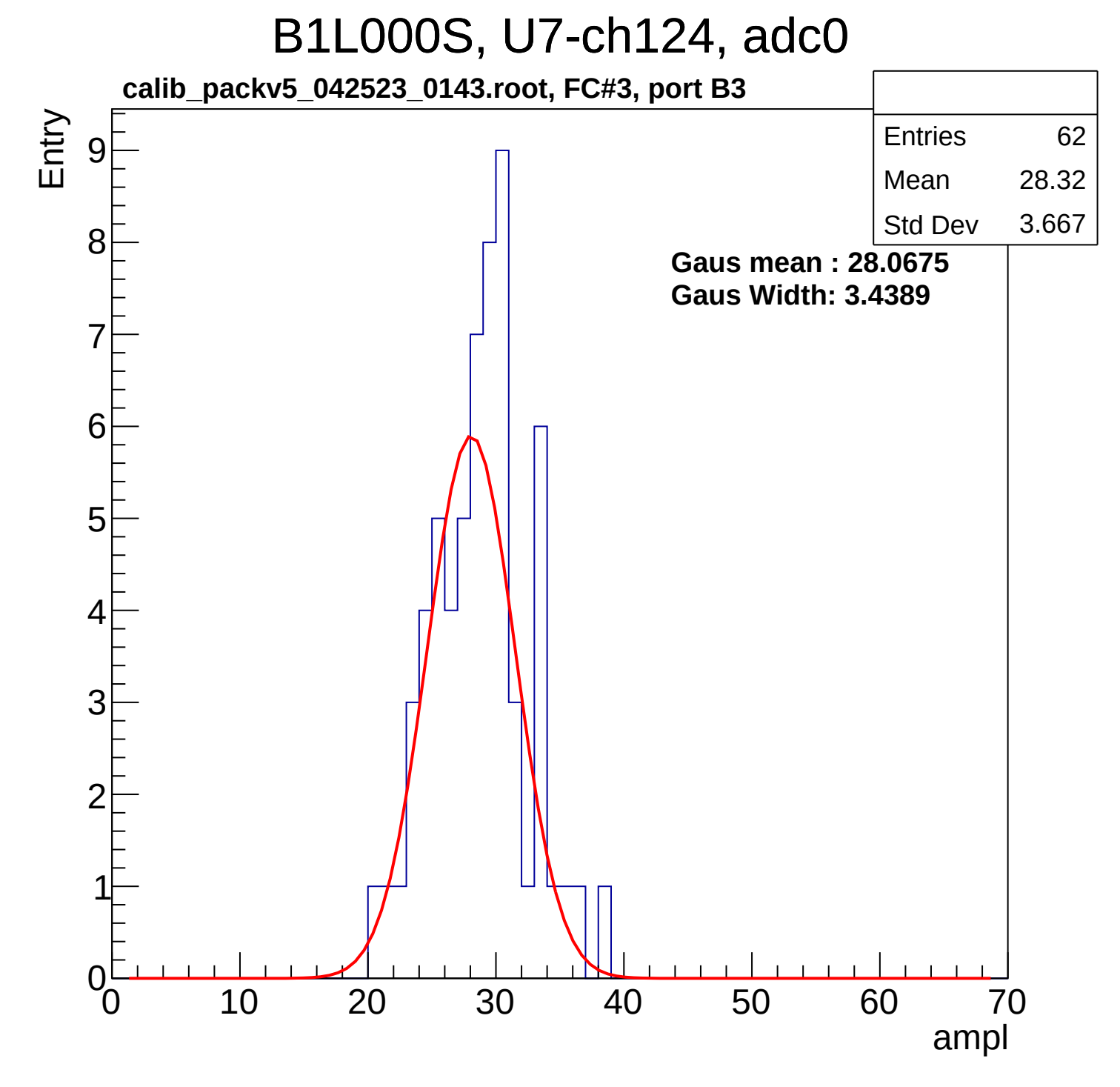
40
50
60
70
ampl


B1L000S, U7-ch124, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 62
Mean 28.32
Std Dev 3.667
Gaus mean : 28.0675
Gaus Width: 3.4389
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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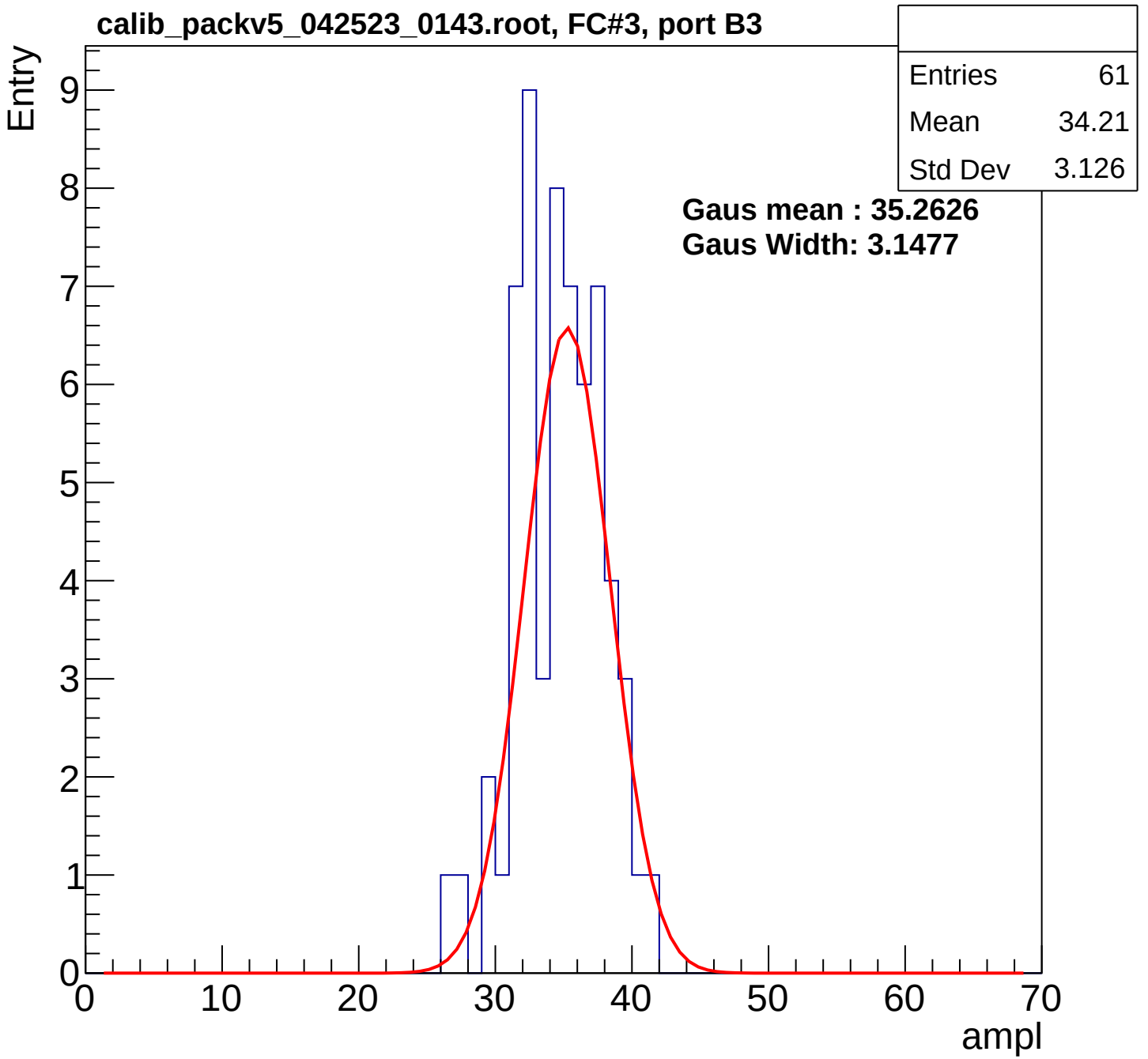
B1L000S, U7-ch124, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 61
Mean 34.21
Std Dev 3.126
Gaus mean : 35.2626
Gaus Width: 3.1477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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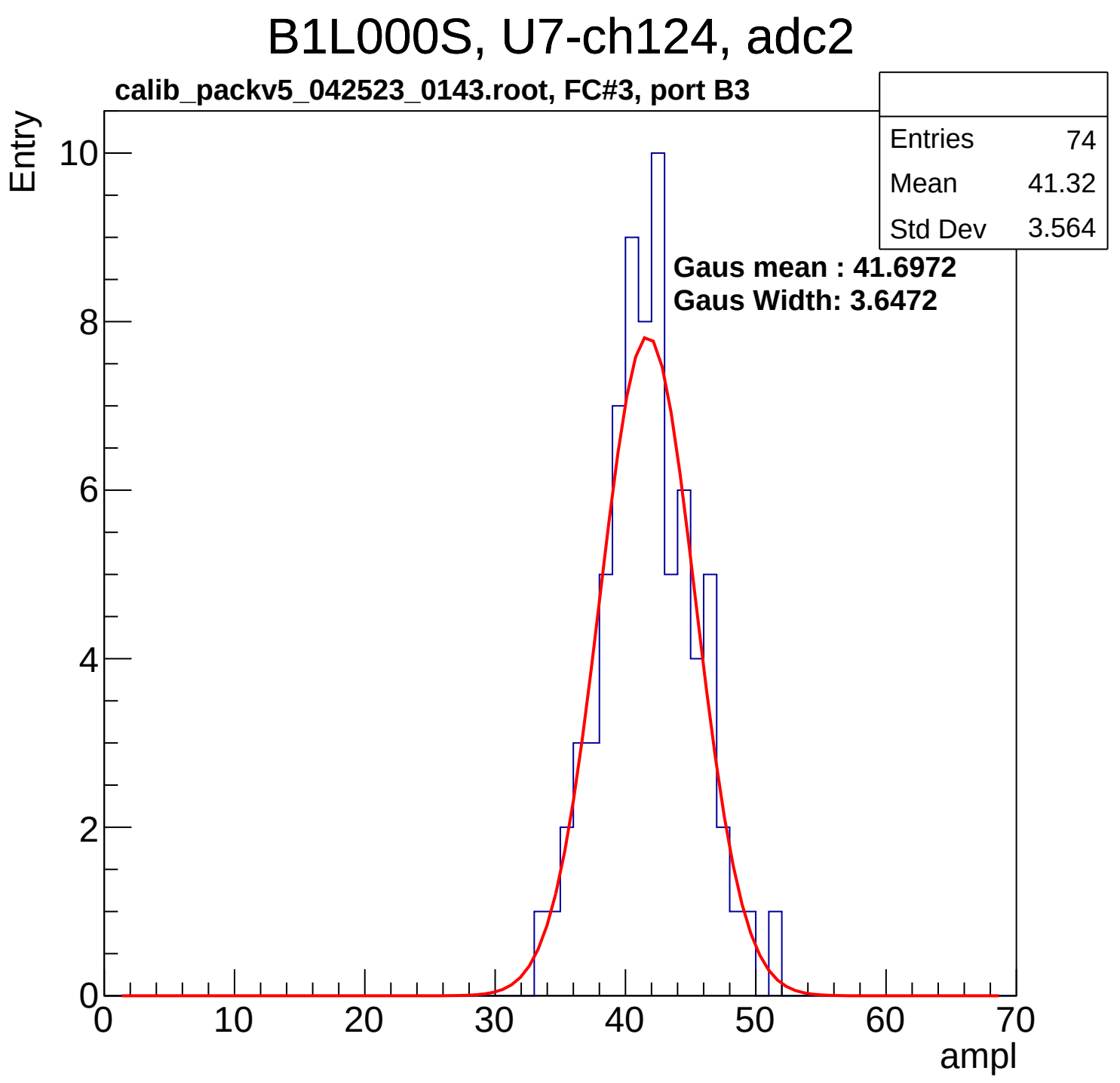
B1L000S, U7-ch124, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 74
Mean 41.32
Std Dev 3.564
Gaus mean : 41.6972
Gaus Width: 3.6472
Entry
ampl
0
2
4
6
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0
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40
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60
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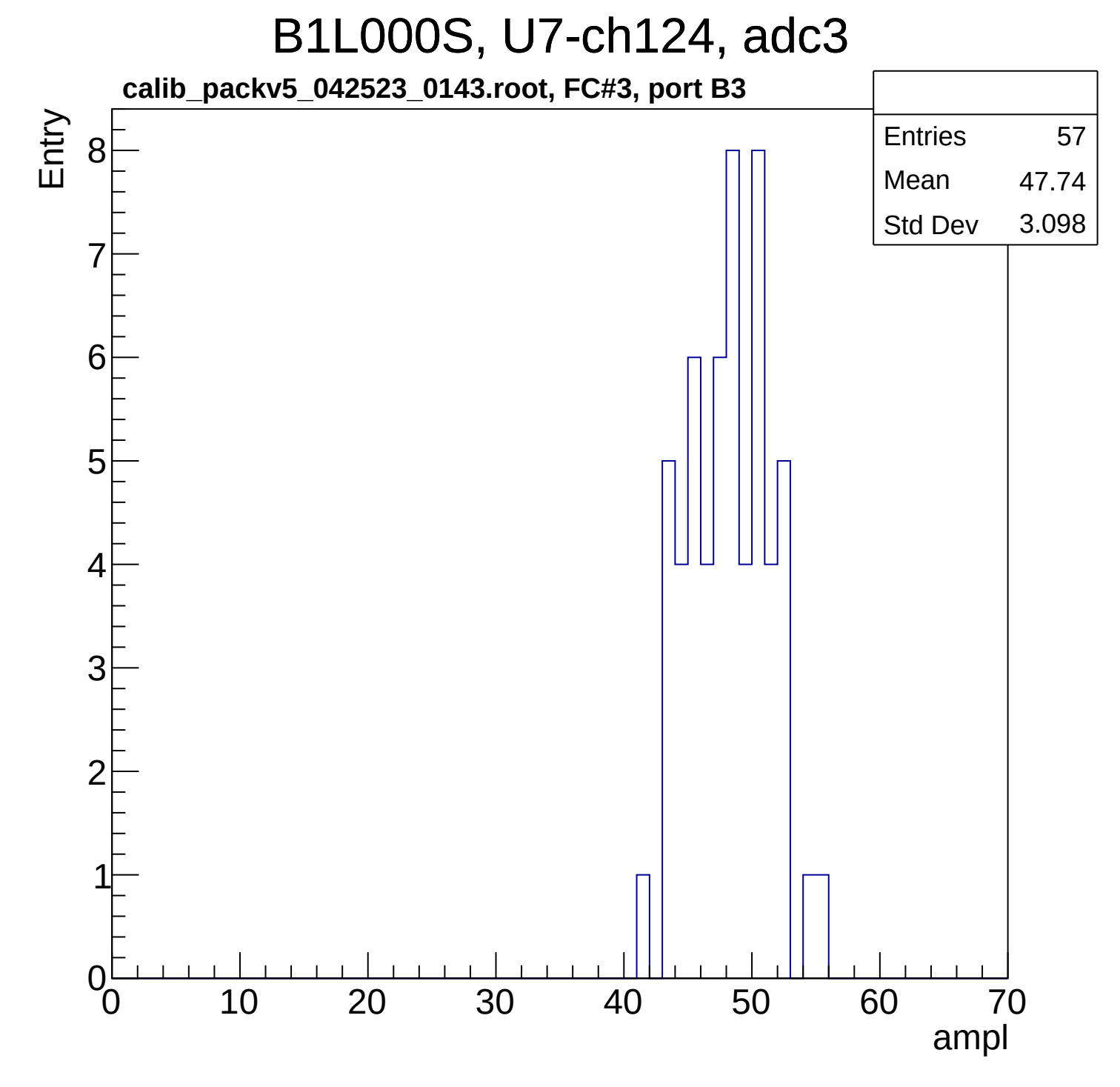
B1L000S, U7-ch124, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 57
Mean 47.74
Std Dev 3.098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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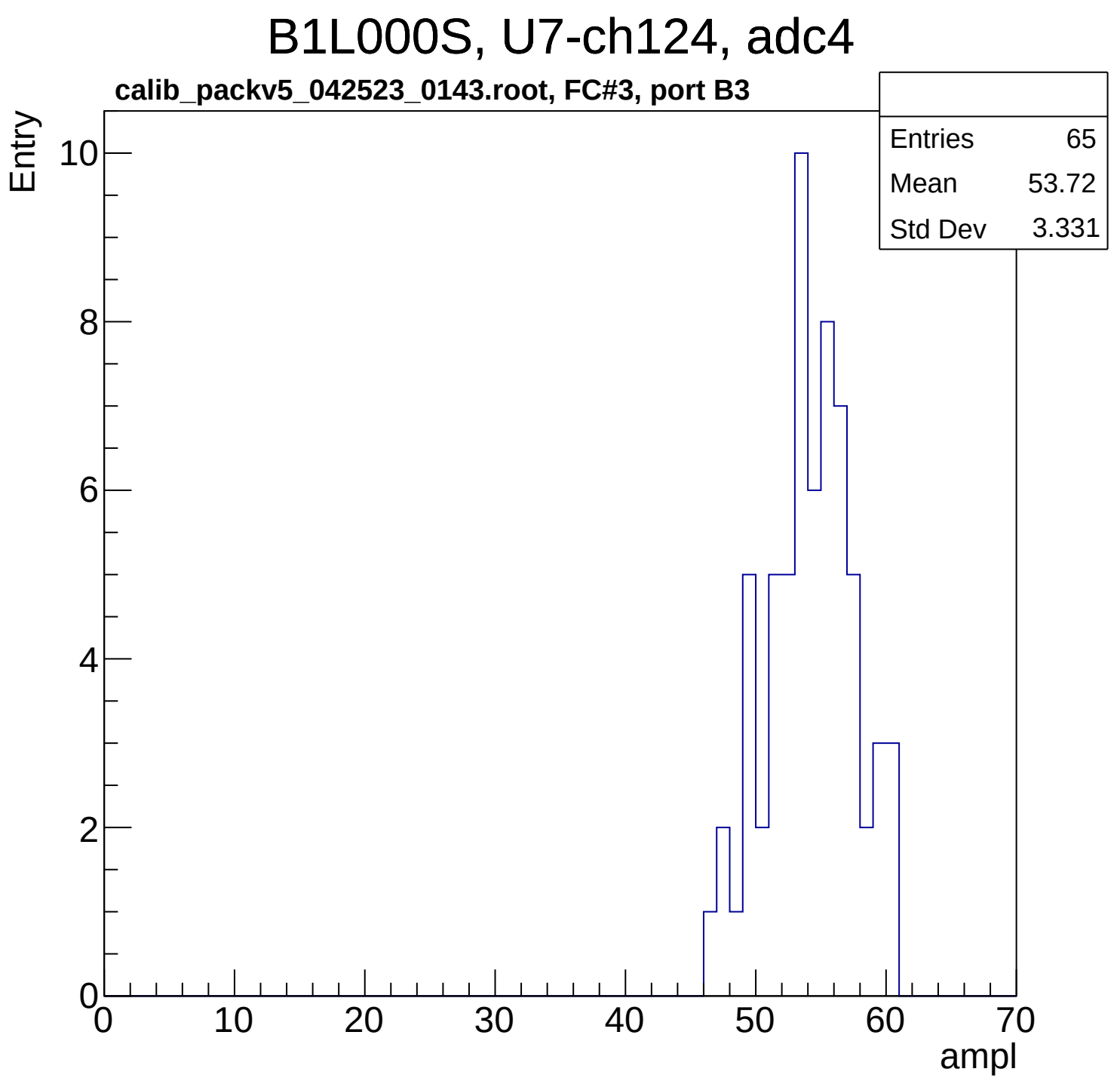
B1L000S, U7-ch124, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 53.72
Std Dev 3.331
Entry
ampl
0
2
4
6
8
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0
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40
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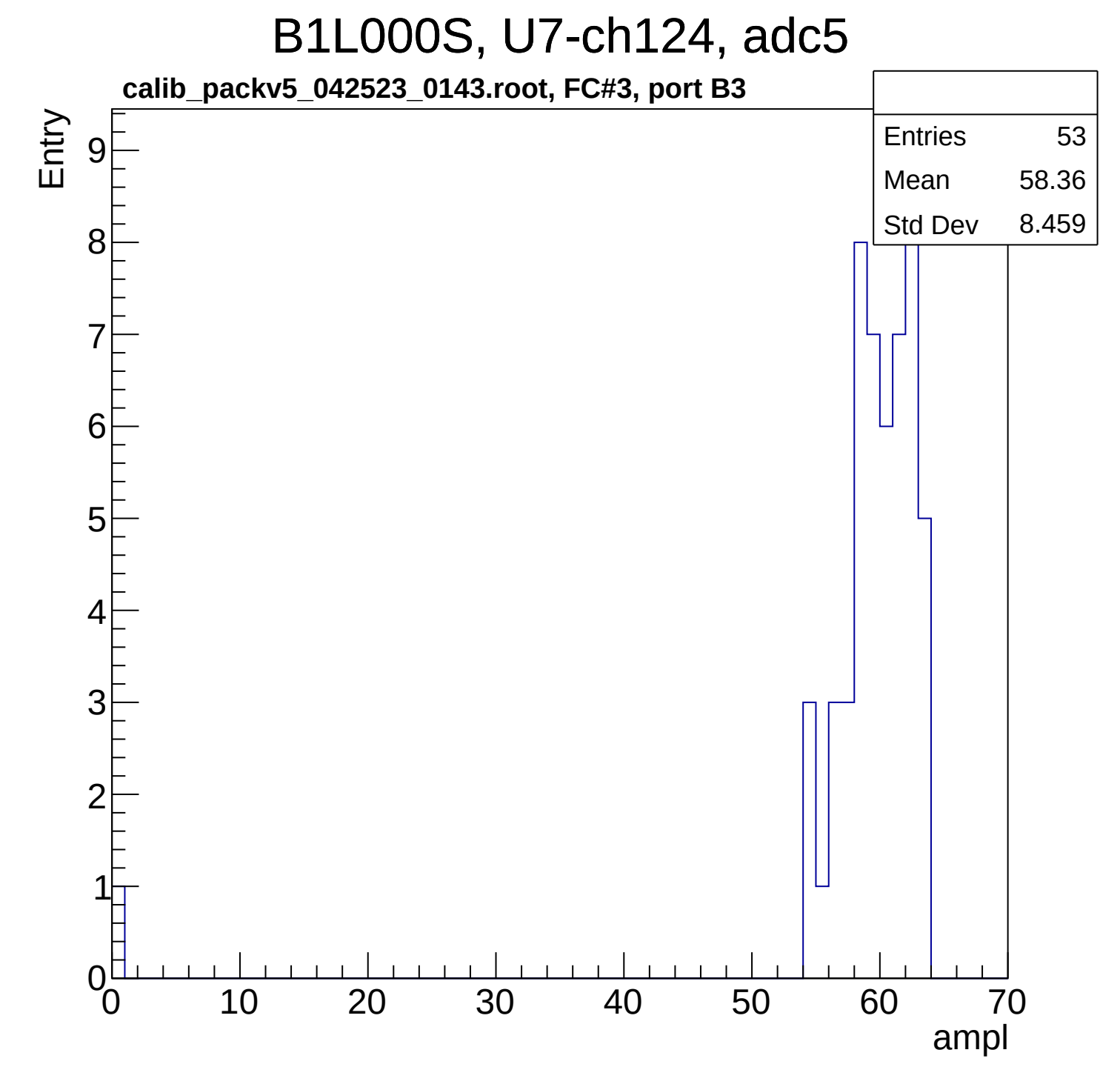
B1L000S, U7-ch124, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 58.36
Std Dev 8.459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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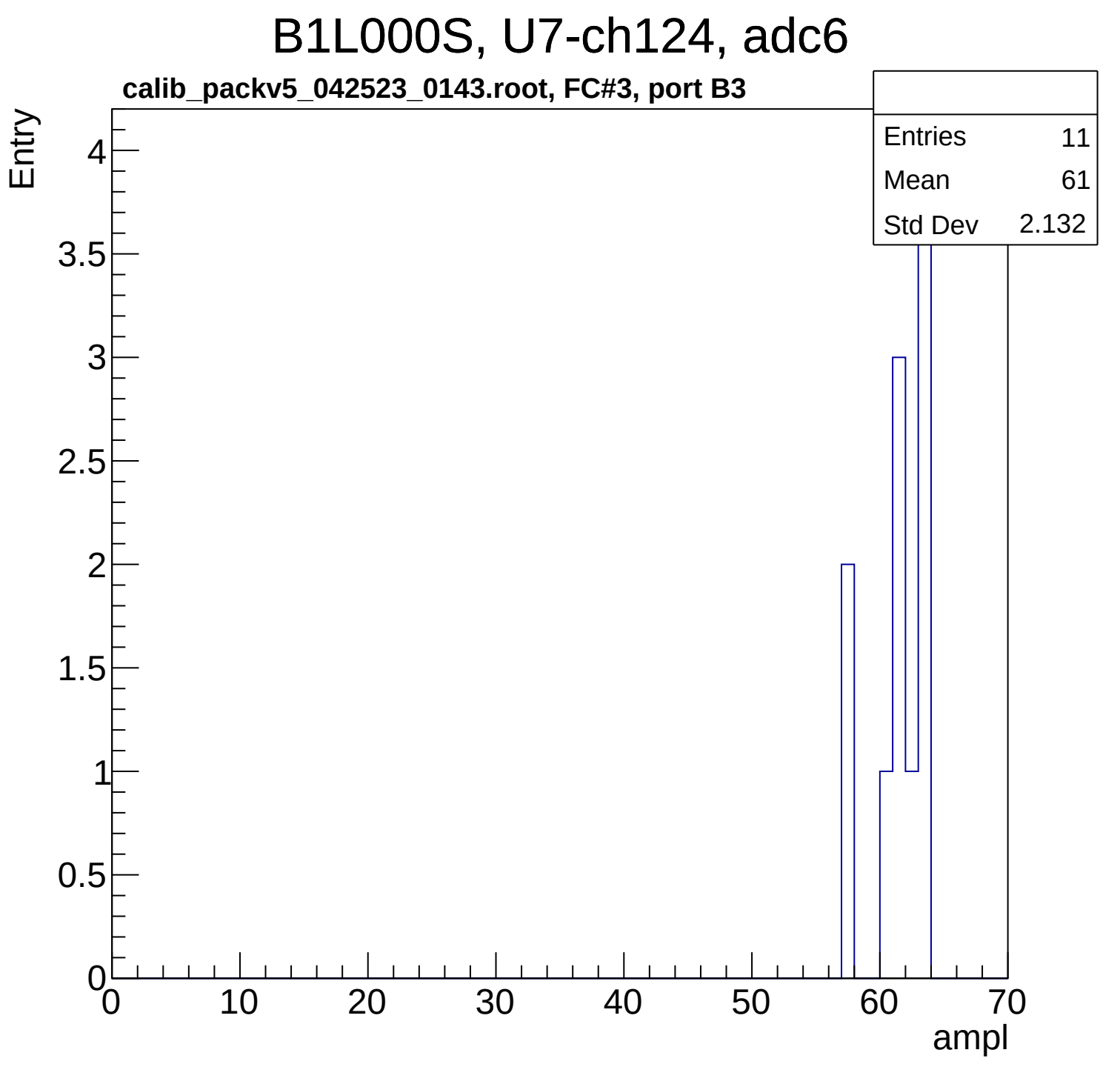
B1L000S, U7-ch124, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 11
Mean 61
Std Dev 2.132
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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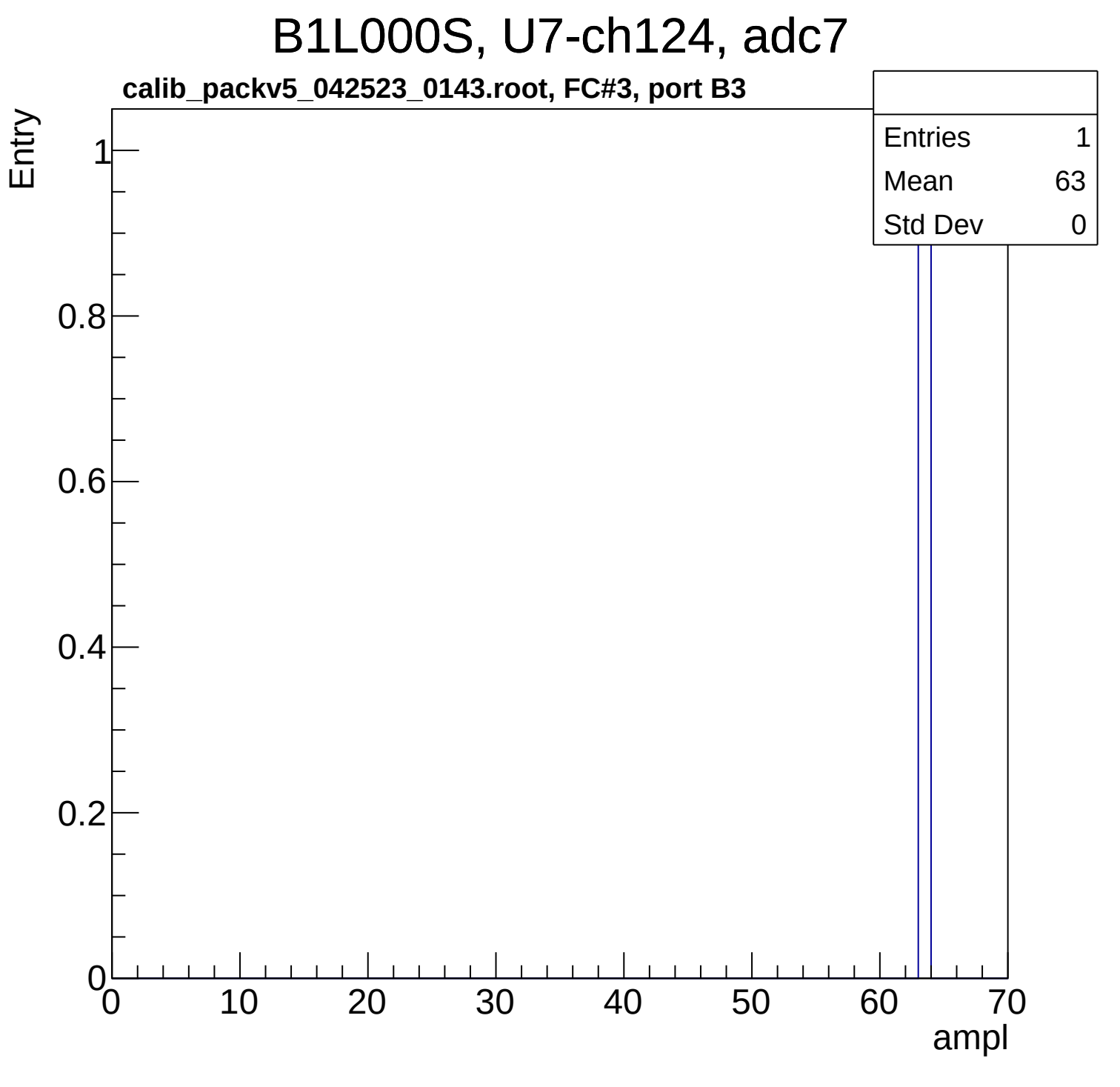
B1L000S, U7-ch124, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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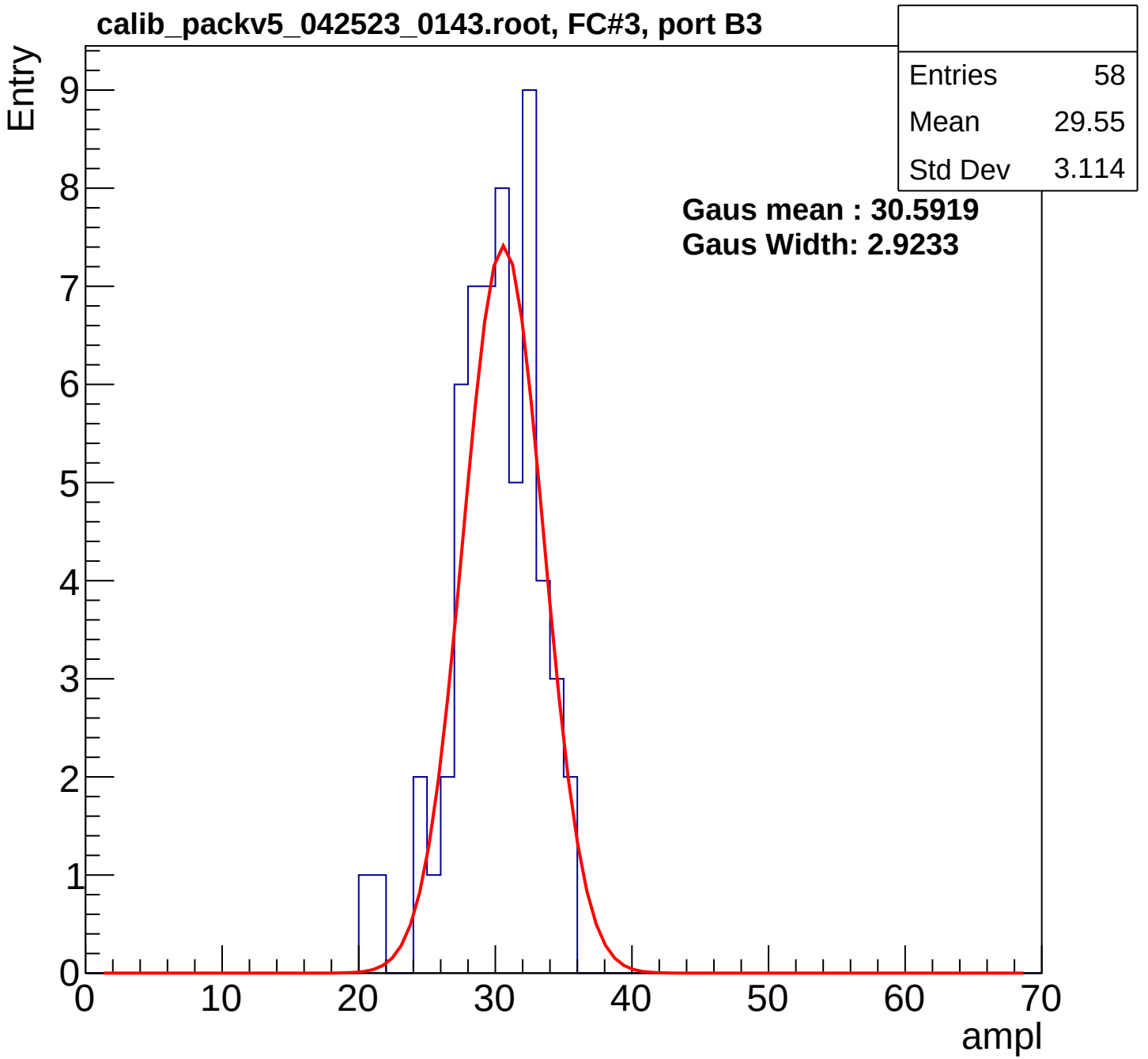
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch125, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 29.55
Std Dev 3.114
Gaus mean : 30.5919
Gaus Width: 2.9233
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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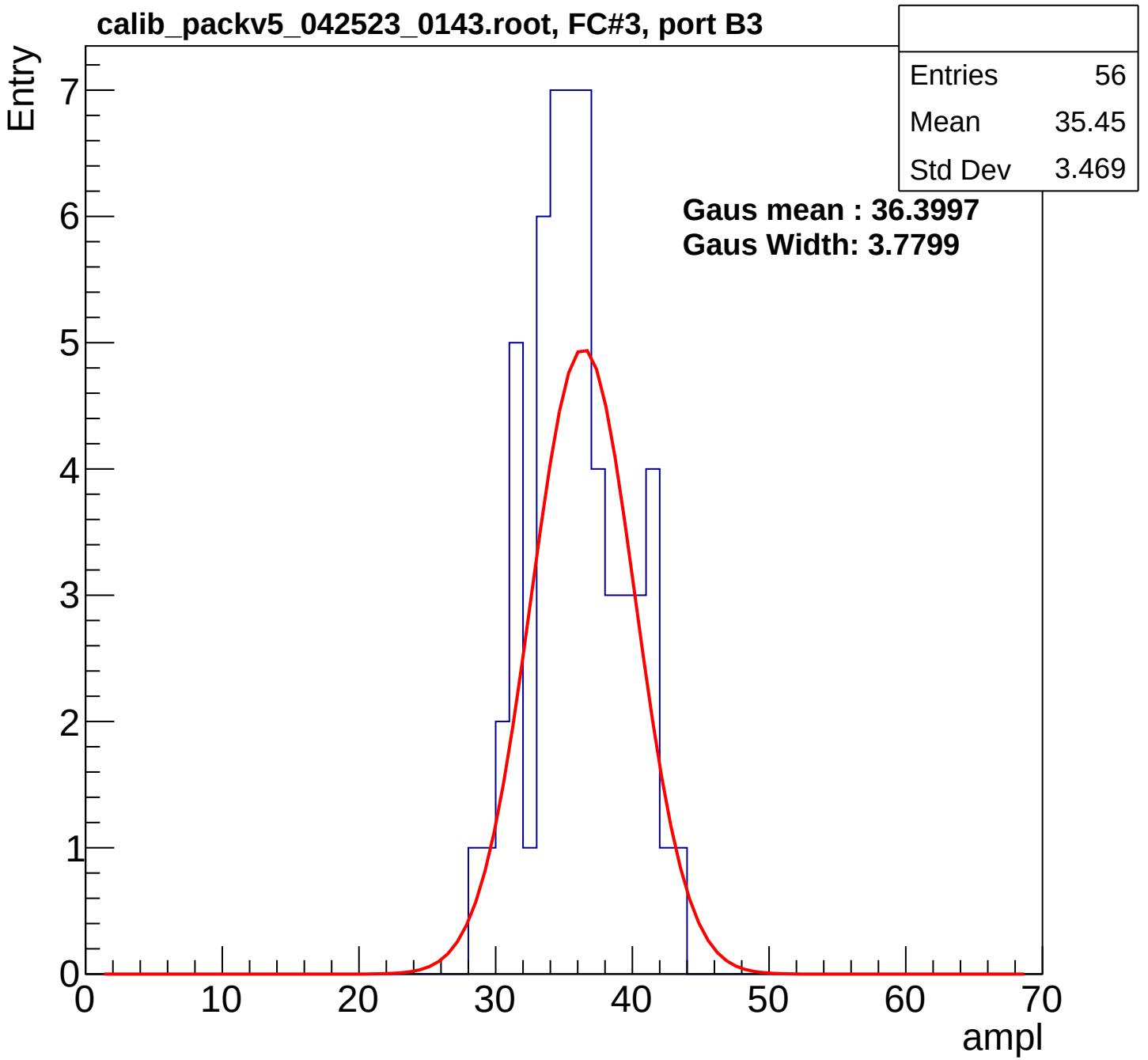
B1L000S, U7-ch125, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 56
Mean 35.45
Std Dev 3.469
Gaus mean : 36.3997
Gaus Width: 3.7799
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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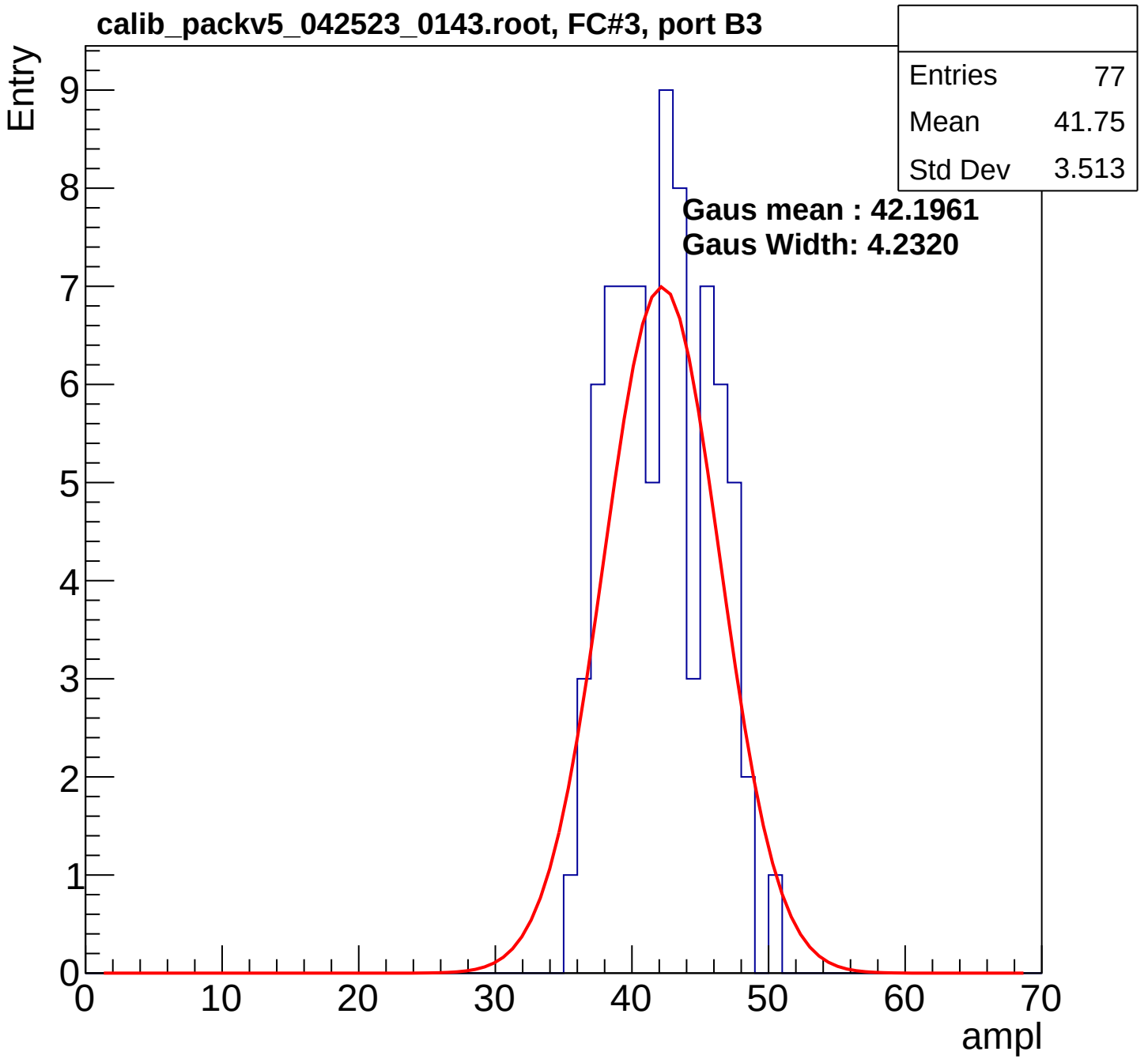
B1L000S, U7-ch125, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 41.75
Std Dev 3.513
Gaus mean : 42.1961
Gaus Width: 4.2320
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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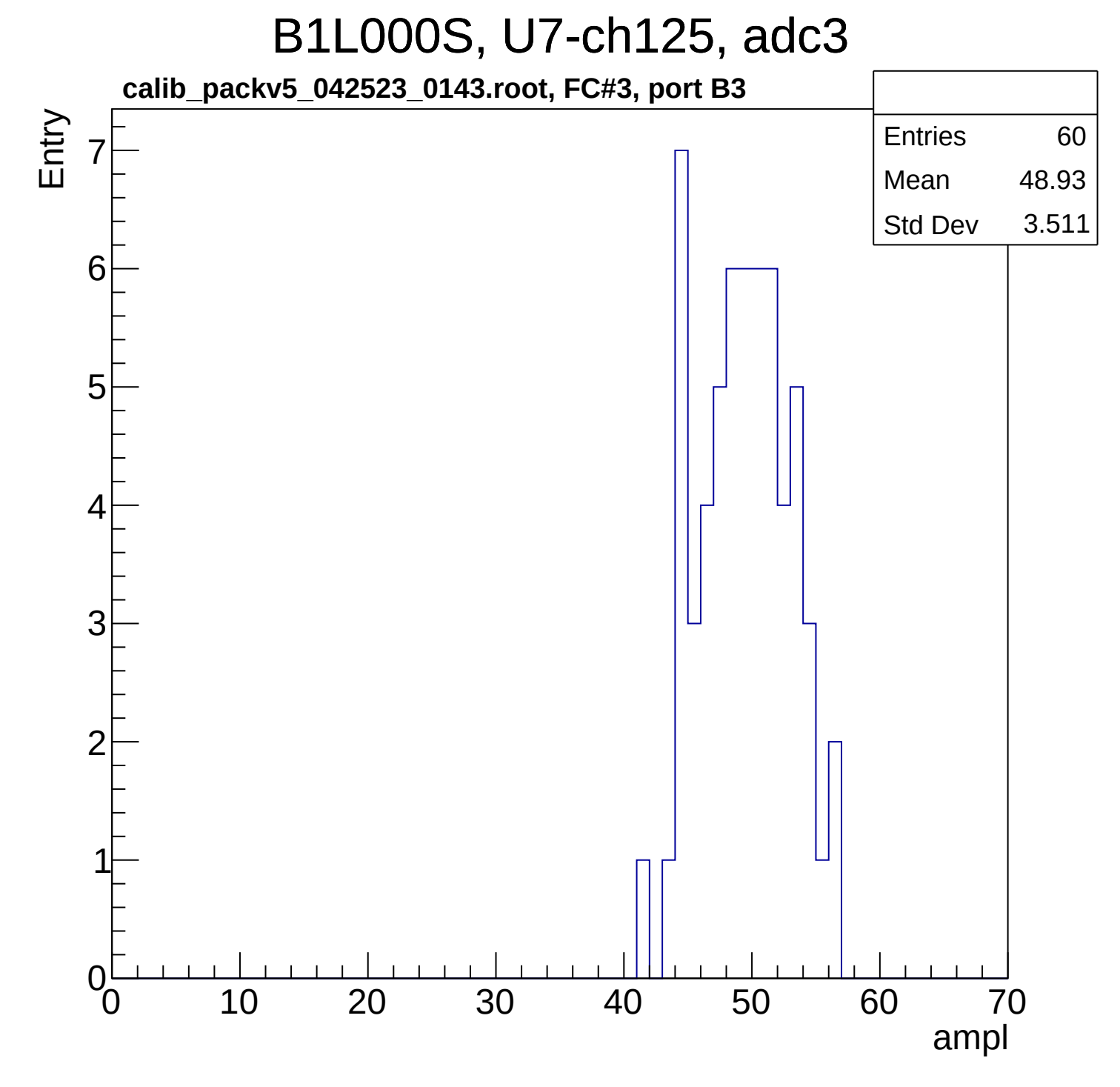
B1L000S, U7-ch125, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 60
Mean 48.93
Std Dev 3.511
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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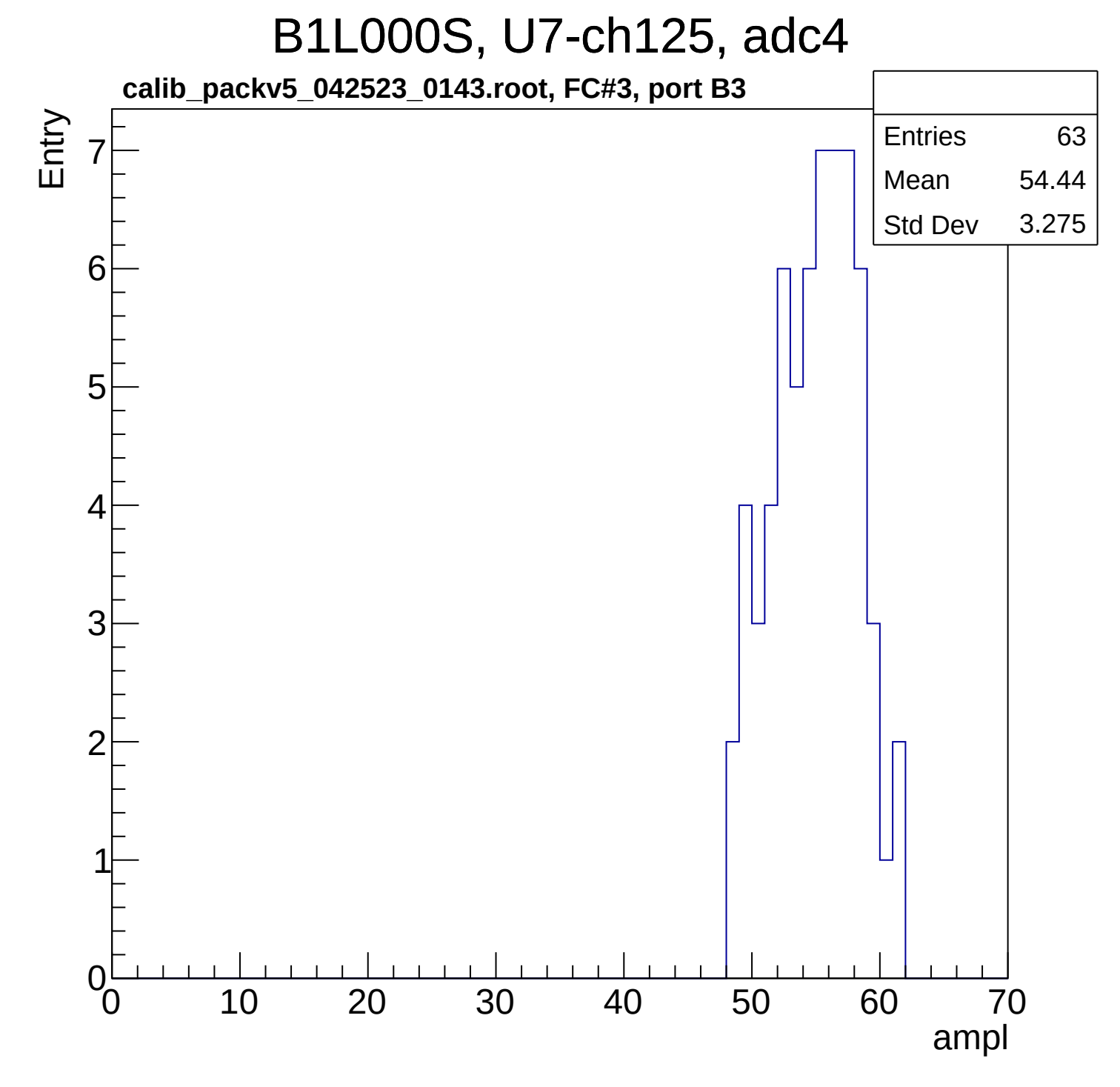
B1L000S, U7-ch125, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 63
Mean 54.44
Std Dev 3.275
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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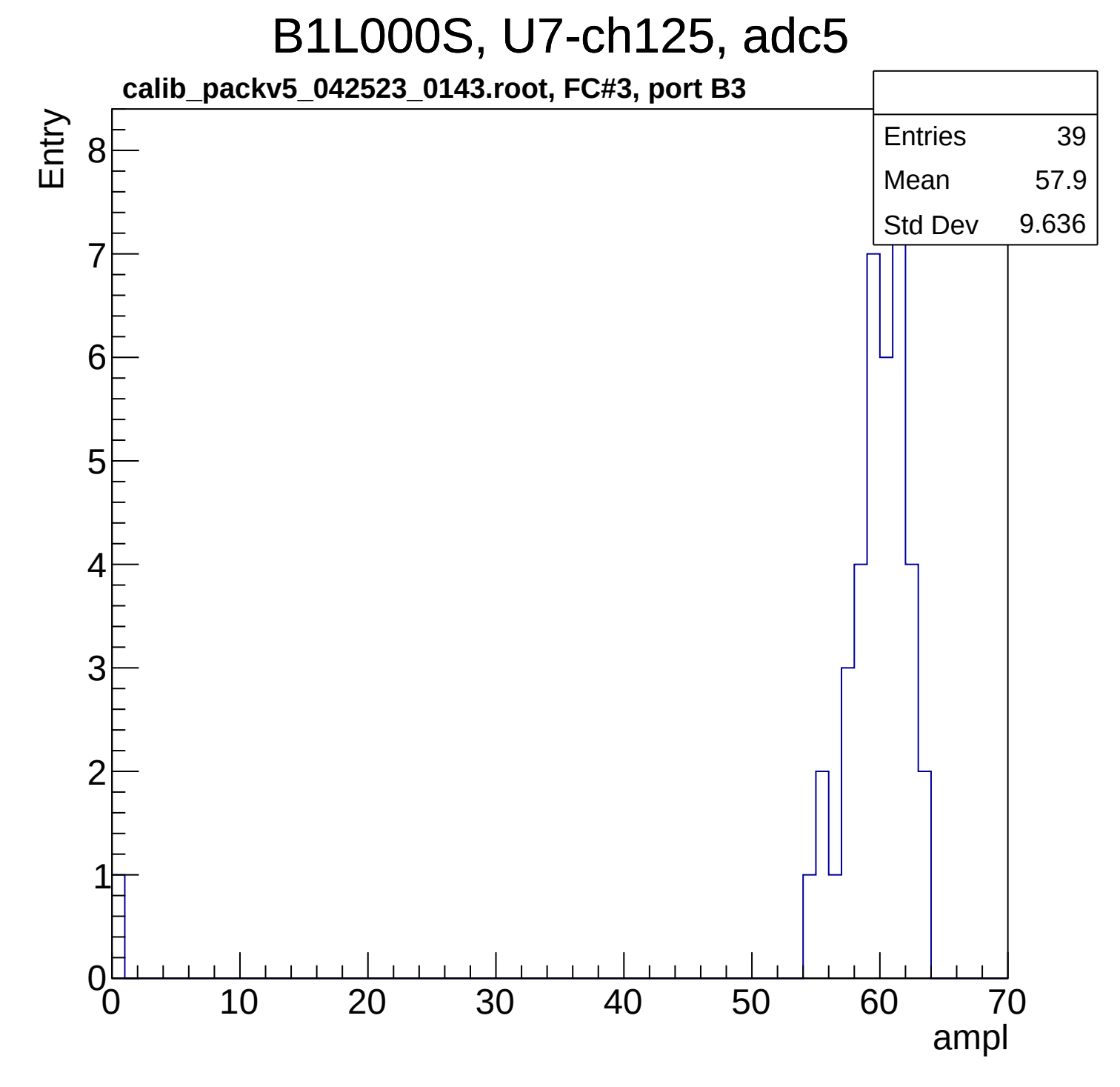
B1L000S, U7-ch125, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 39
Mean 57.9
Std Dev 9.636
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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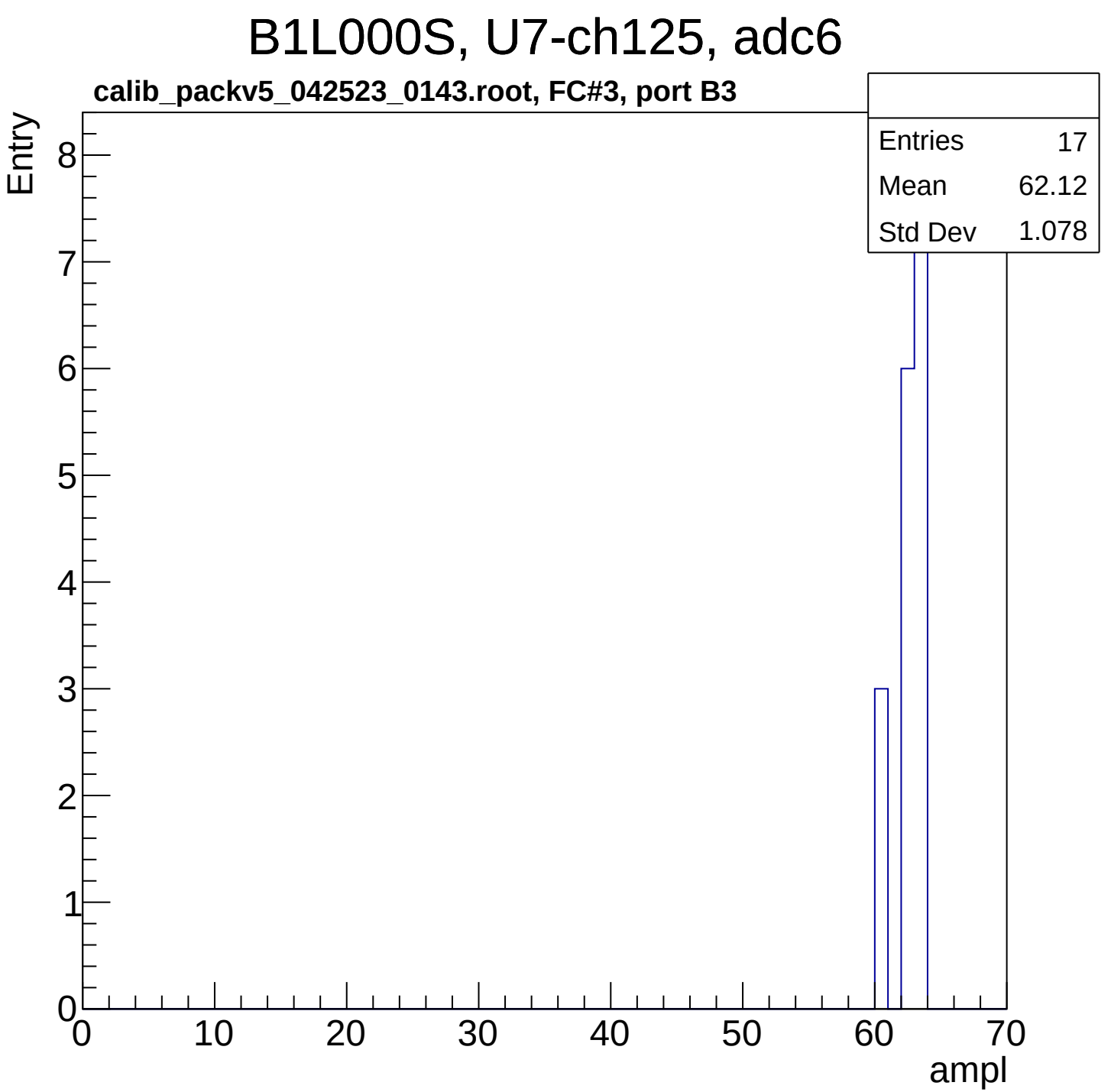
B1L000S, U7-ch125, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 17
Mean 62.12
Std Dev 1.078
Entry
ampl
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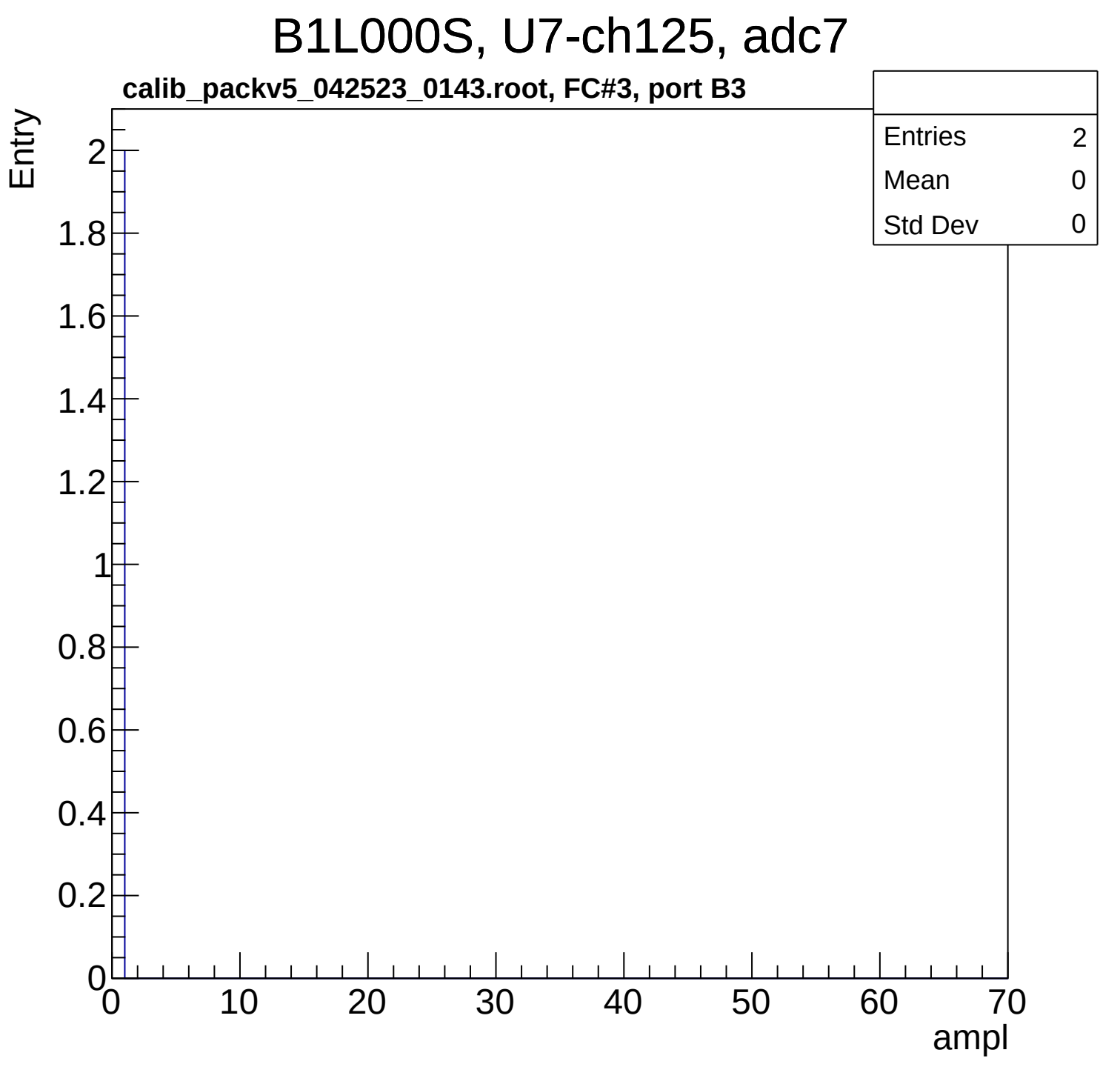
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch125, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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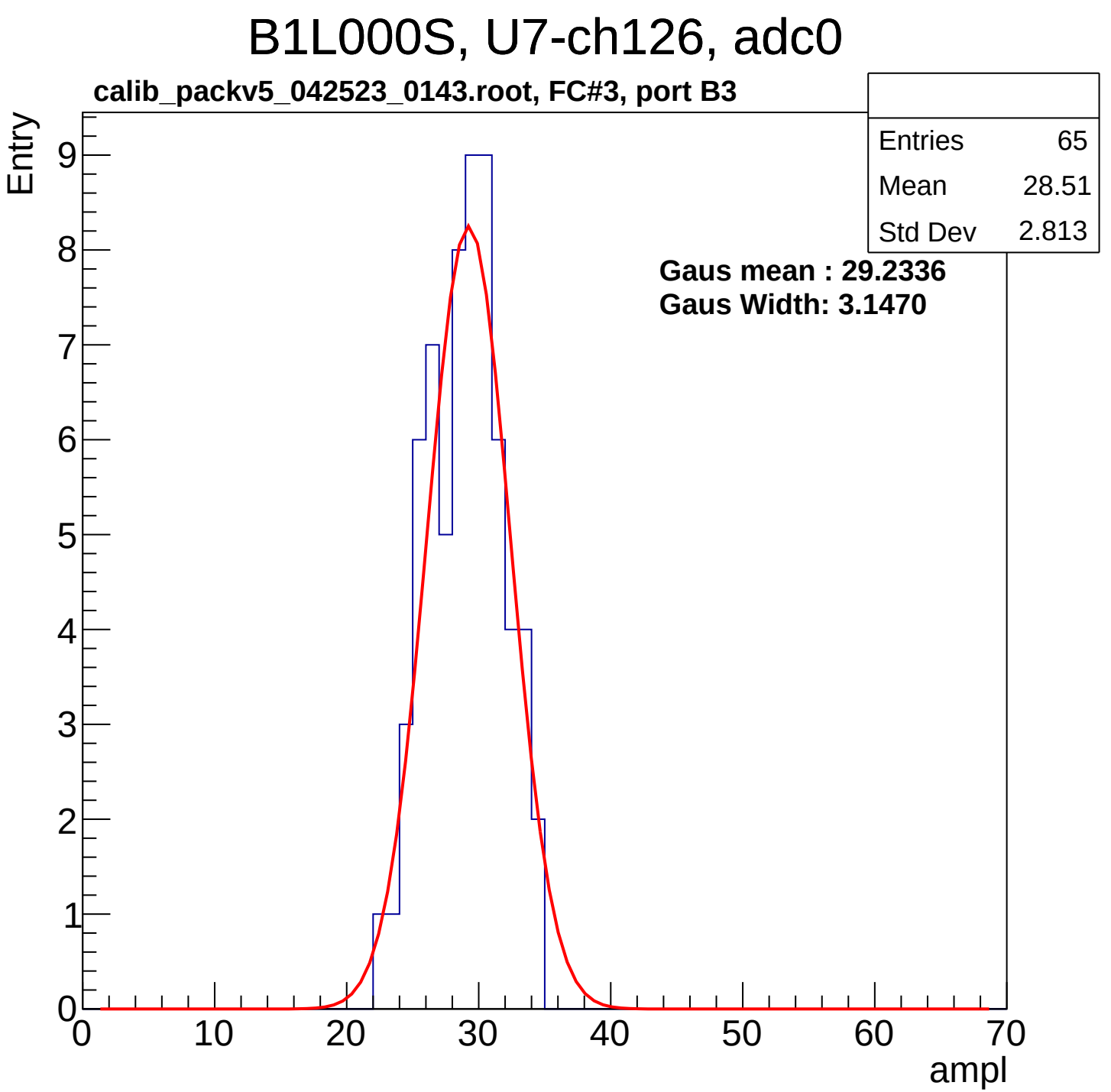
20
30
40
50
60
70


B1L000S, U7-ch126, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 28.51
Std Dev 2.813
Gaus mean : 29.2336
Gaus Width: 3.1470
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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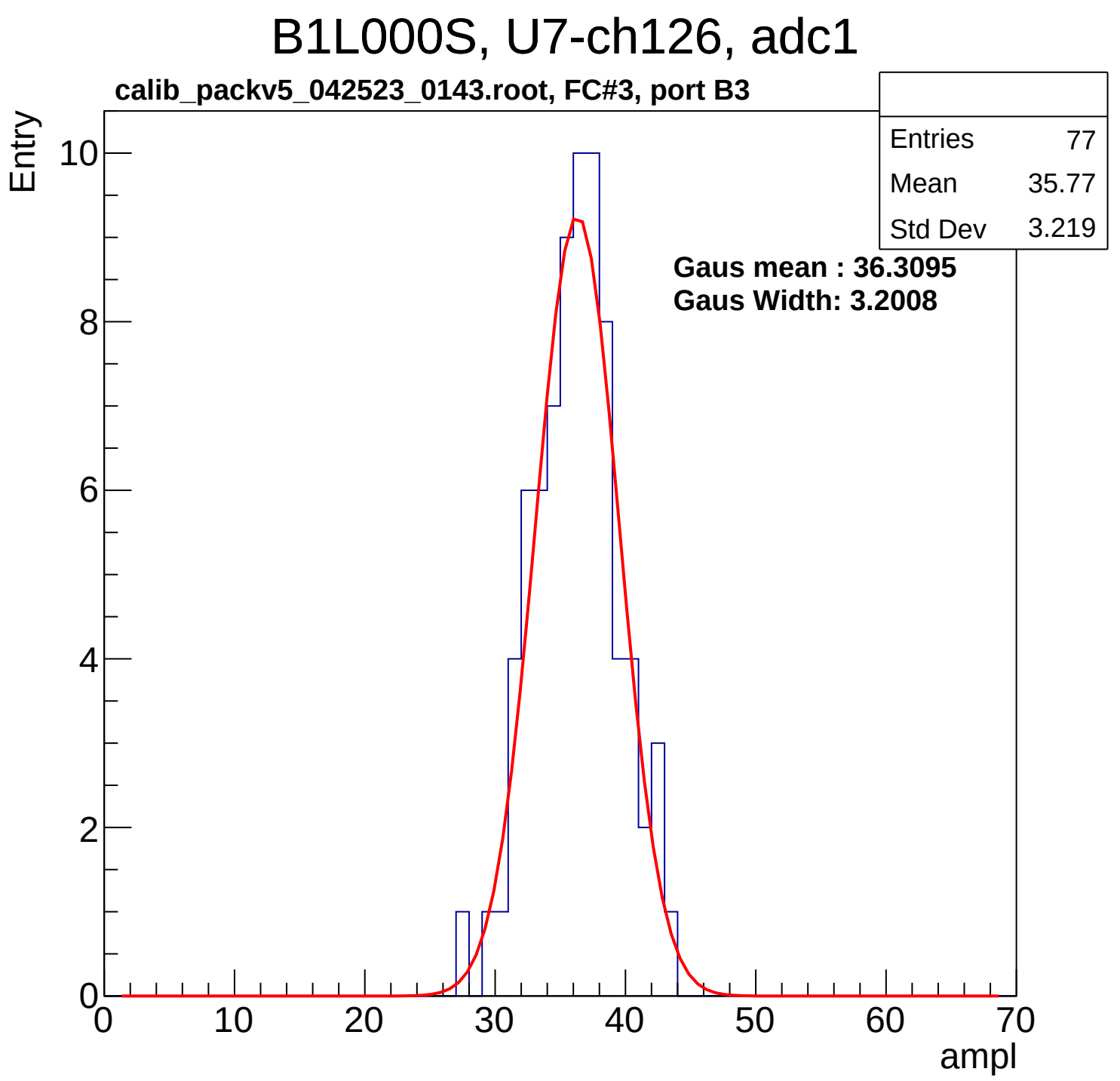
B1L000S, U7-ch126, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 77
Mean 35.77
Std Dev 3.219
Gaus mean : 36.3095
Gaus Width: 3.2008
Entry
ampl
0
2
4
6
8
10
0
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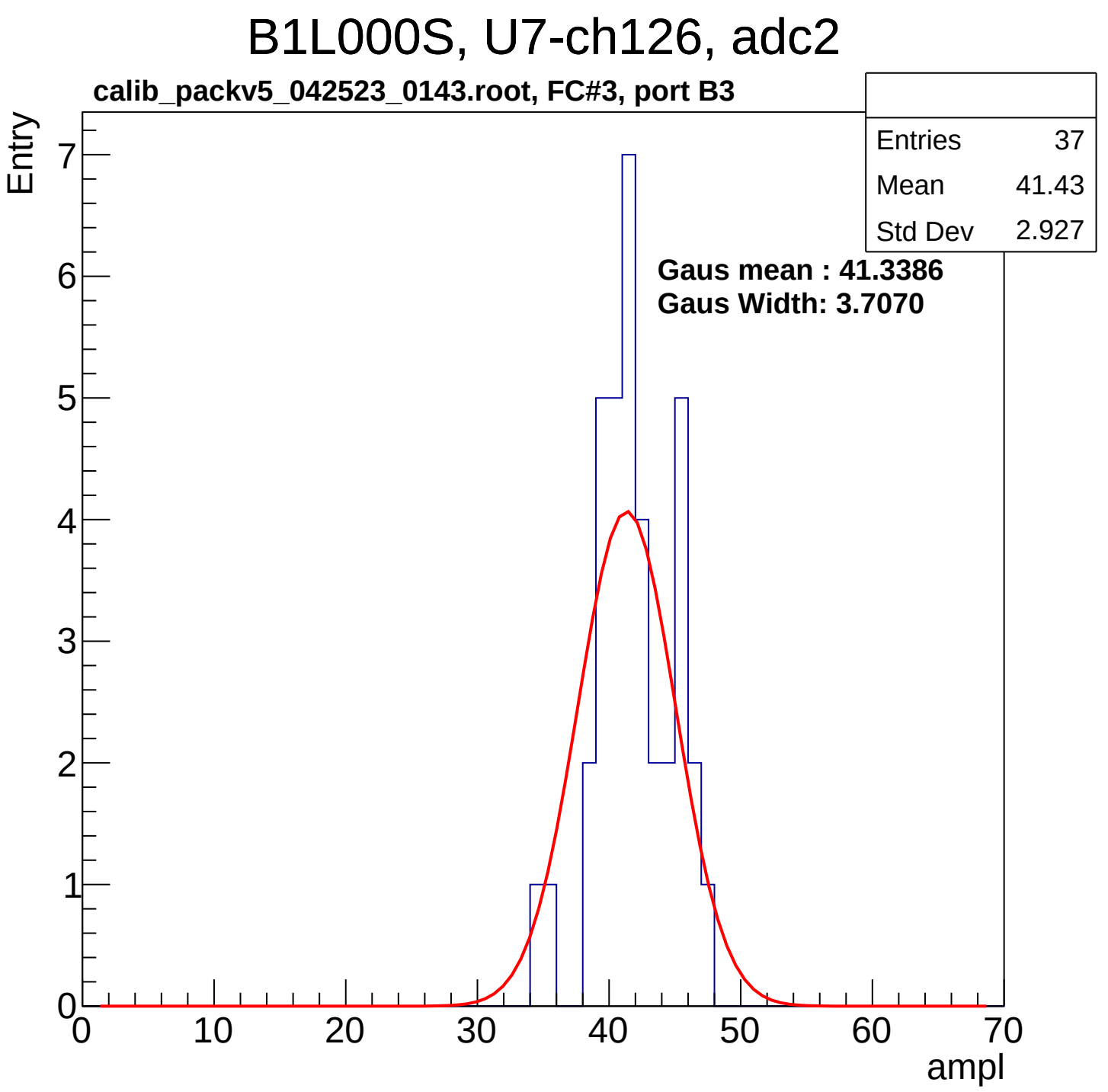
20
30
40
50
60
70


B1L000S, U7-ch126, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 37
Mean 41.43
Std Dev 2.927
Gaus mean : 41.3386
Gaus Width: 3.7070
Entry
ampl
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4
5
6
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0
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50
60
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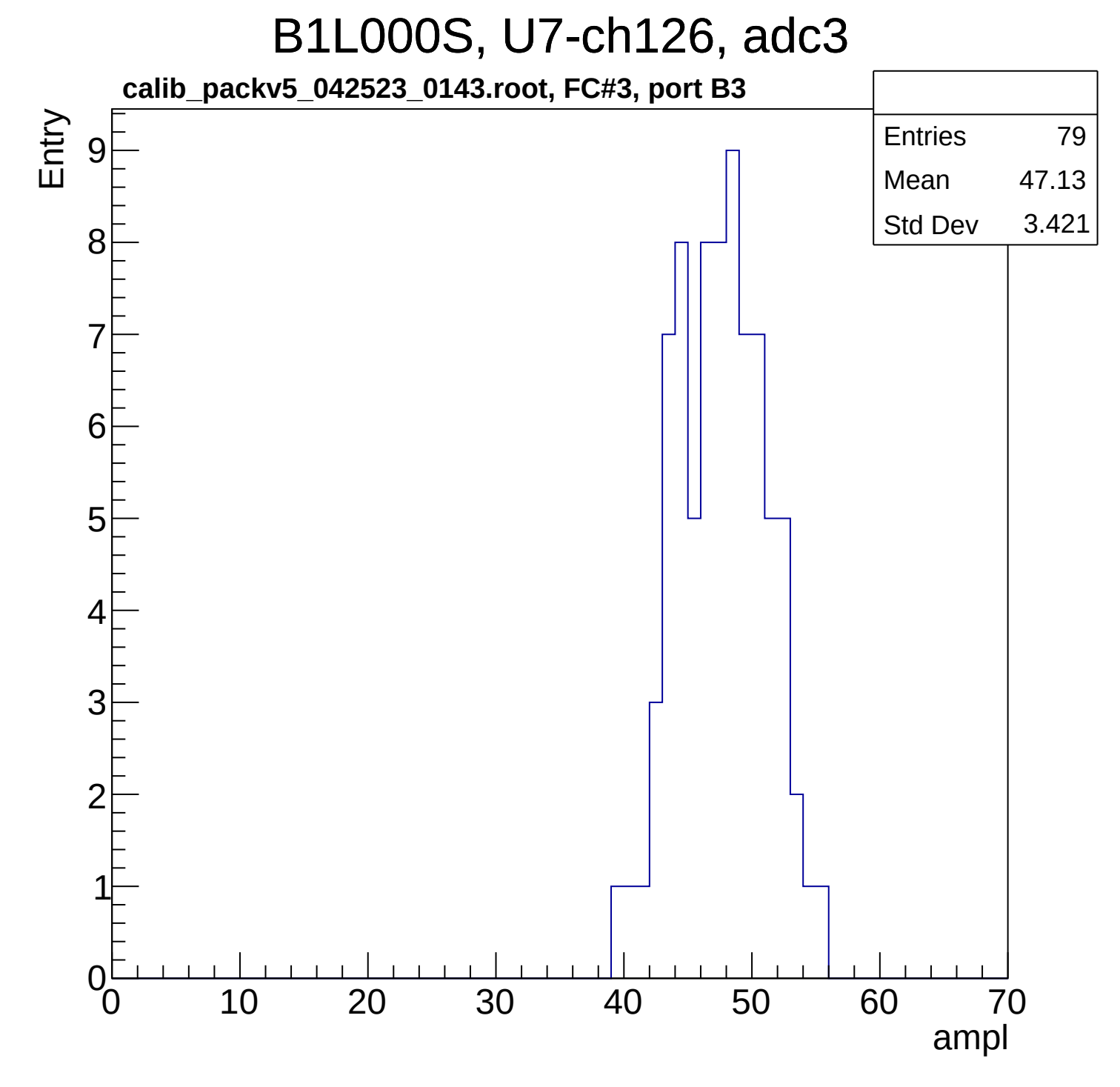
B1L000S, U7-ch126, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 79
Mean 47.13
Std Dev 3.421
Entry
ampl
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3
4
5
6
7
8
9
0
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50
60
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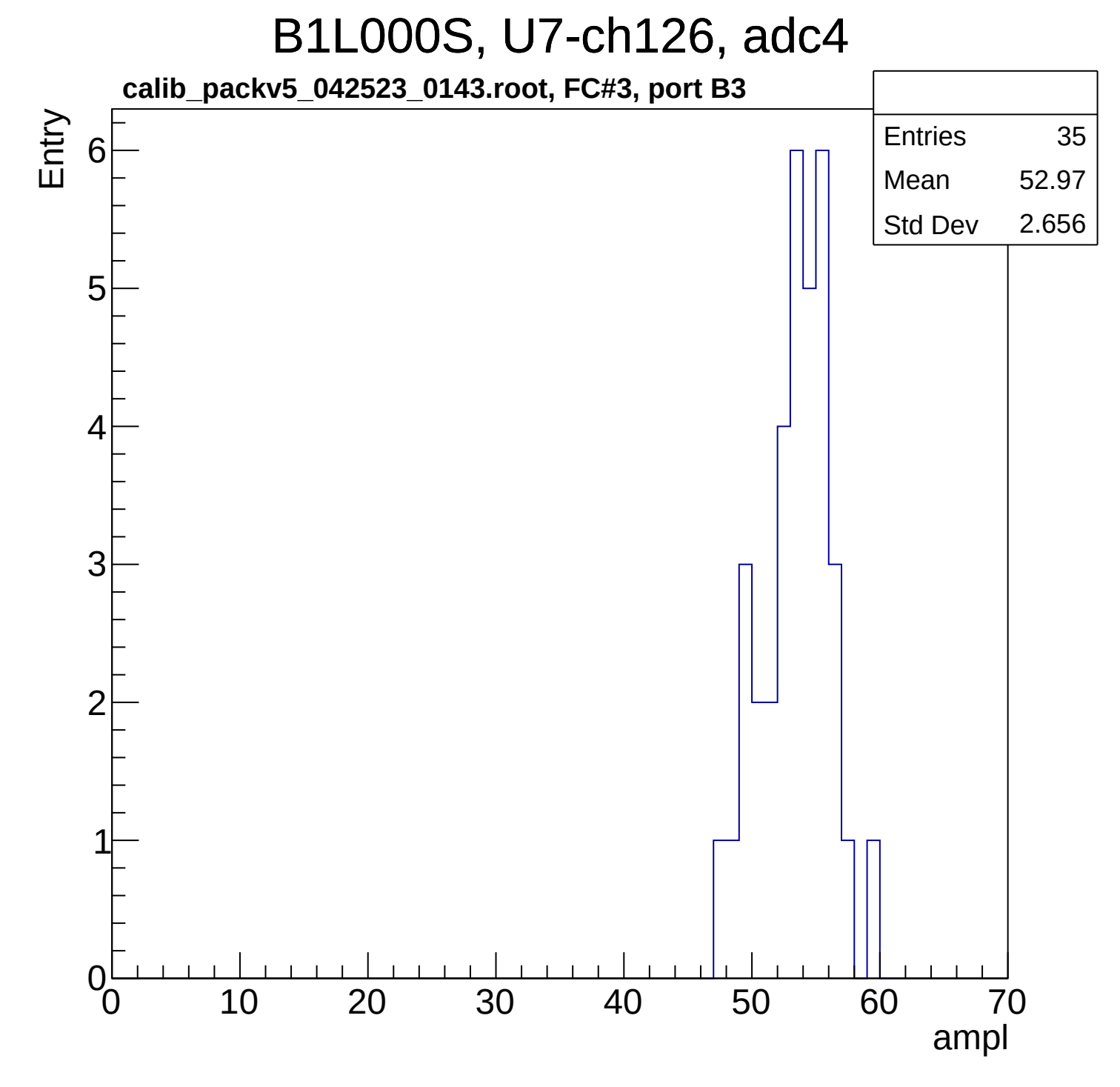
B1L000S, U7-ch126, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 35
Mean 52.97
Std Dev 2.656
Entry
ampl
0
1
2
3
4
5
6
0
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20
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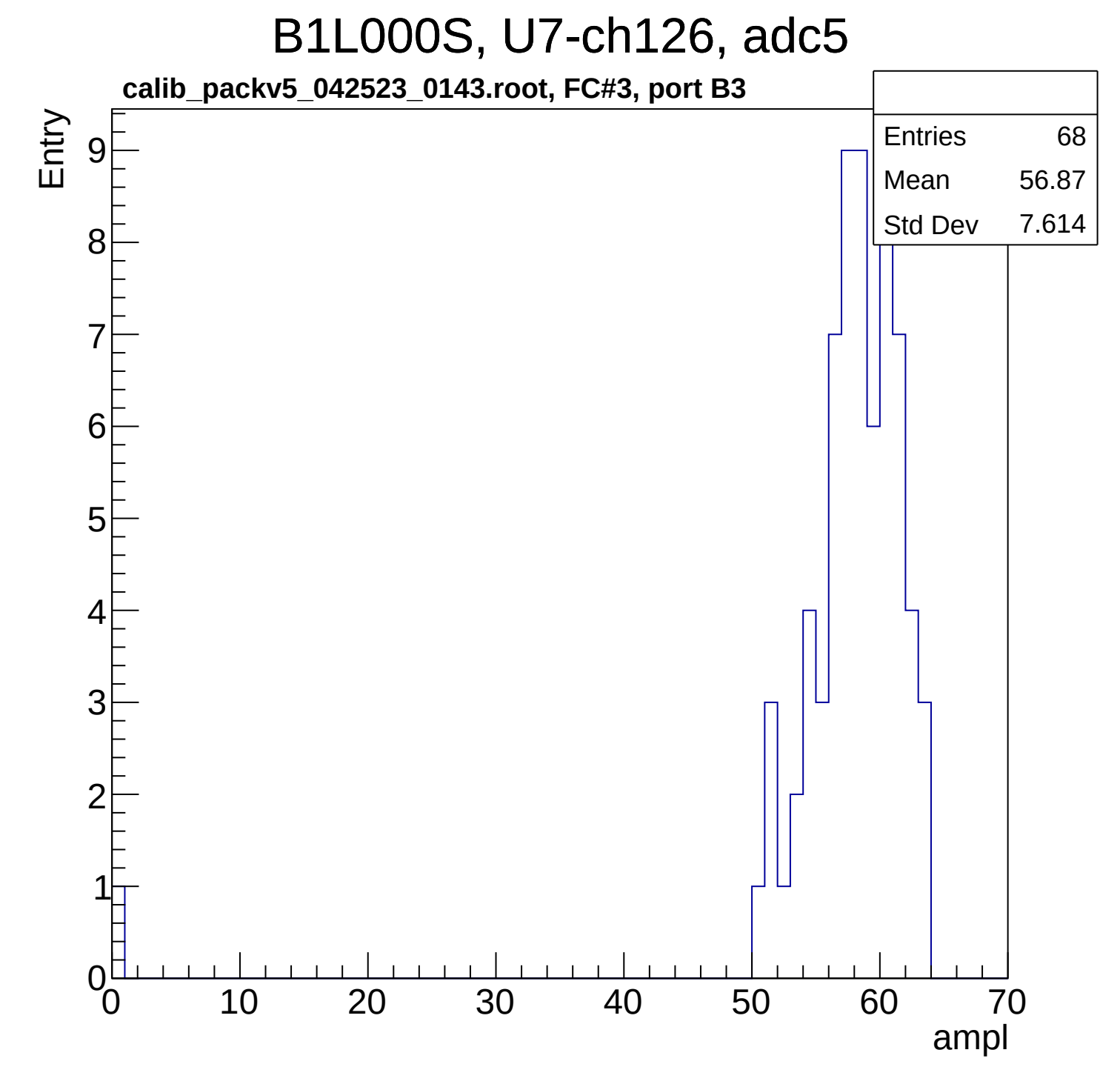
40
50
60
70


B1L000S, U7-ch126, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 68
Mean 56.87
Std Dev 7.614
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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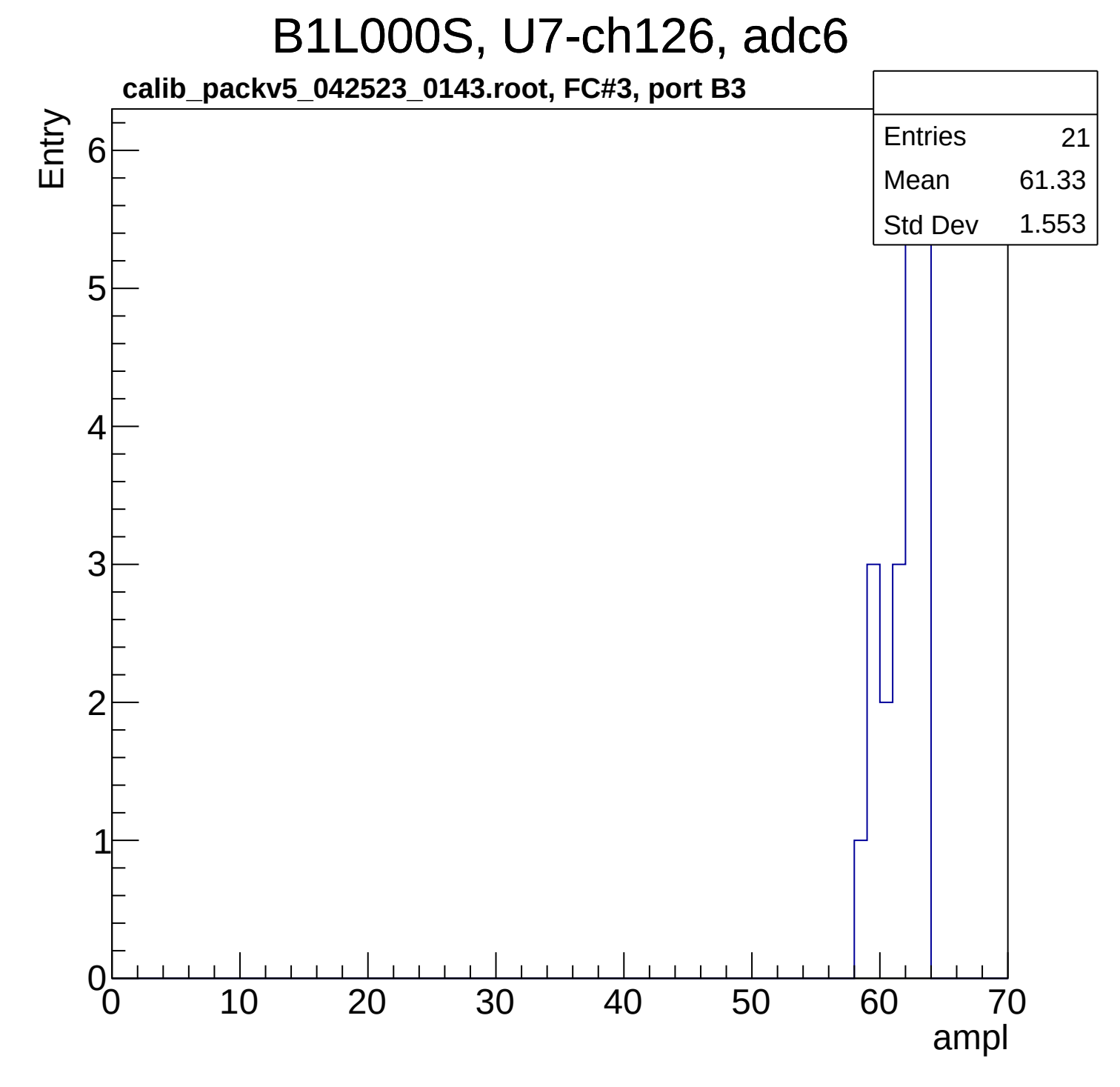
B1L000S, U7-ch126, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 21
Mean 61.33
Std Dev 1.553
Entry
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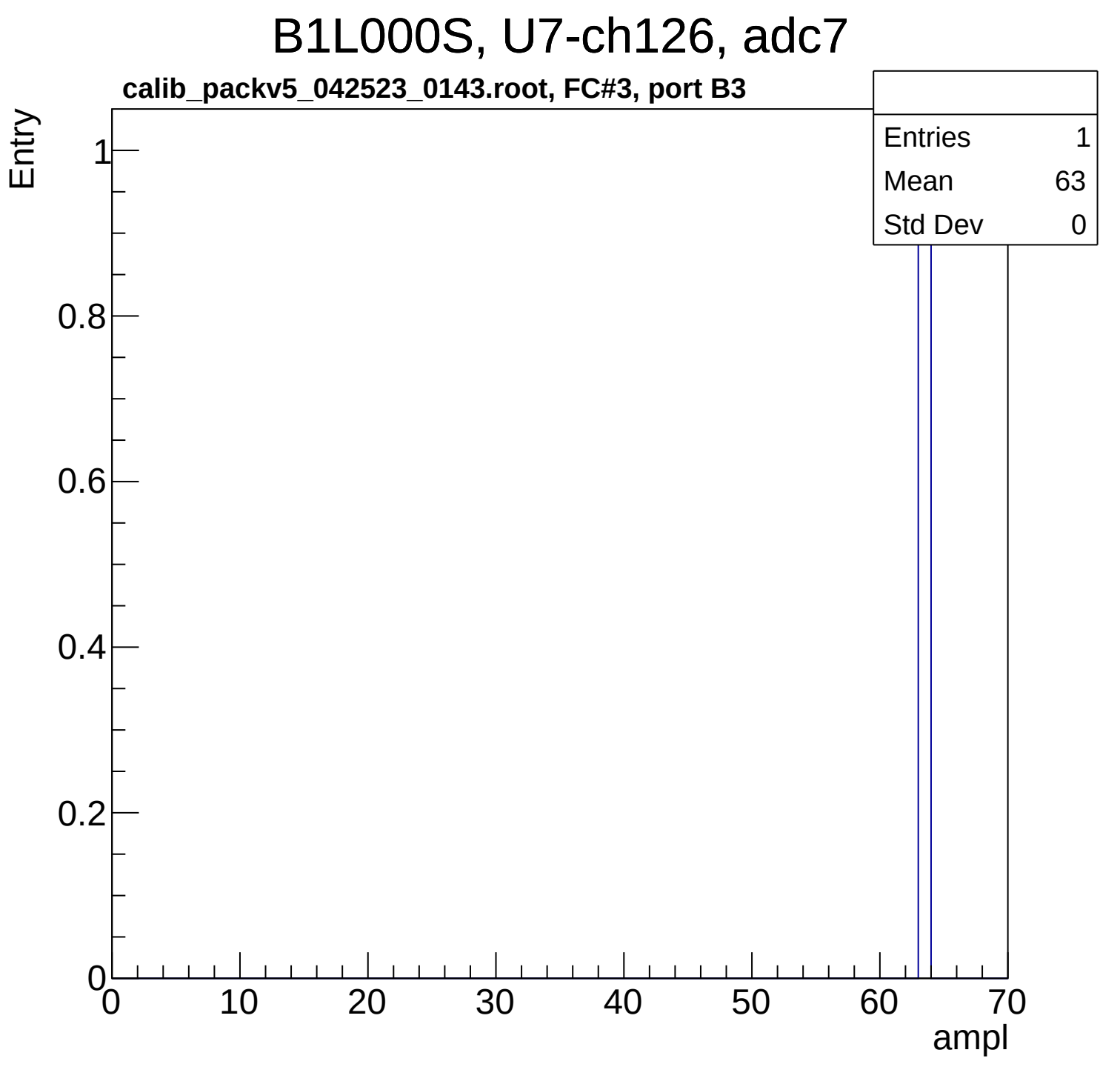
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch126, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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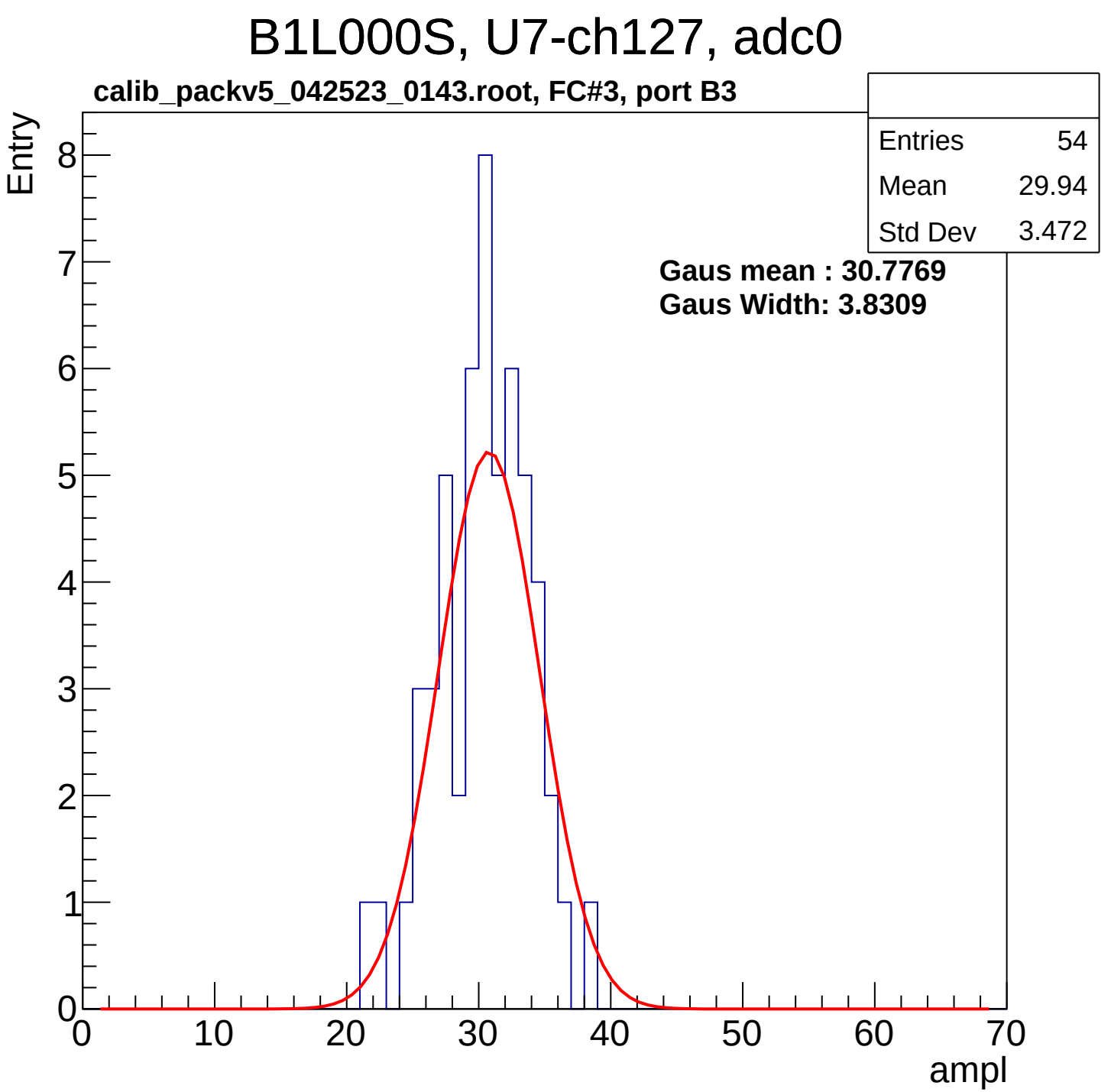
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch127, adc0
calib_packv5_042523_0143.root, FC#3, port B3
Entries 54
Mean 29.94
Std Dev 3.472
Gaus mean : 30.7769
Gaus Width: 3.8309
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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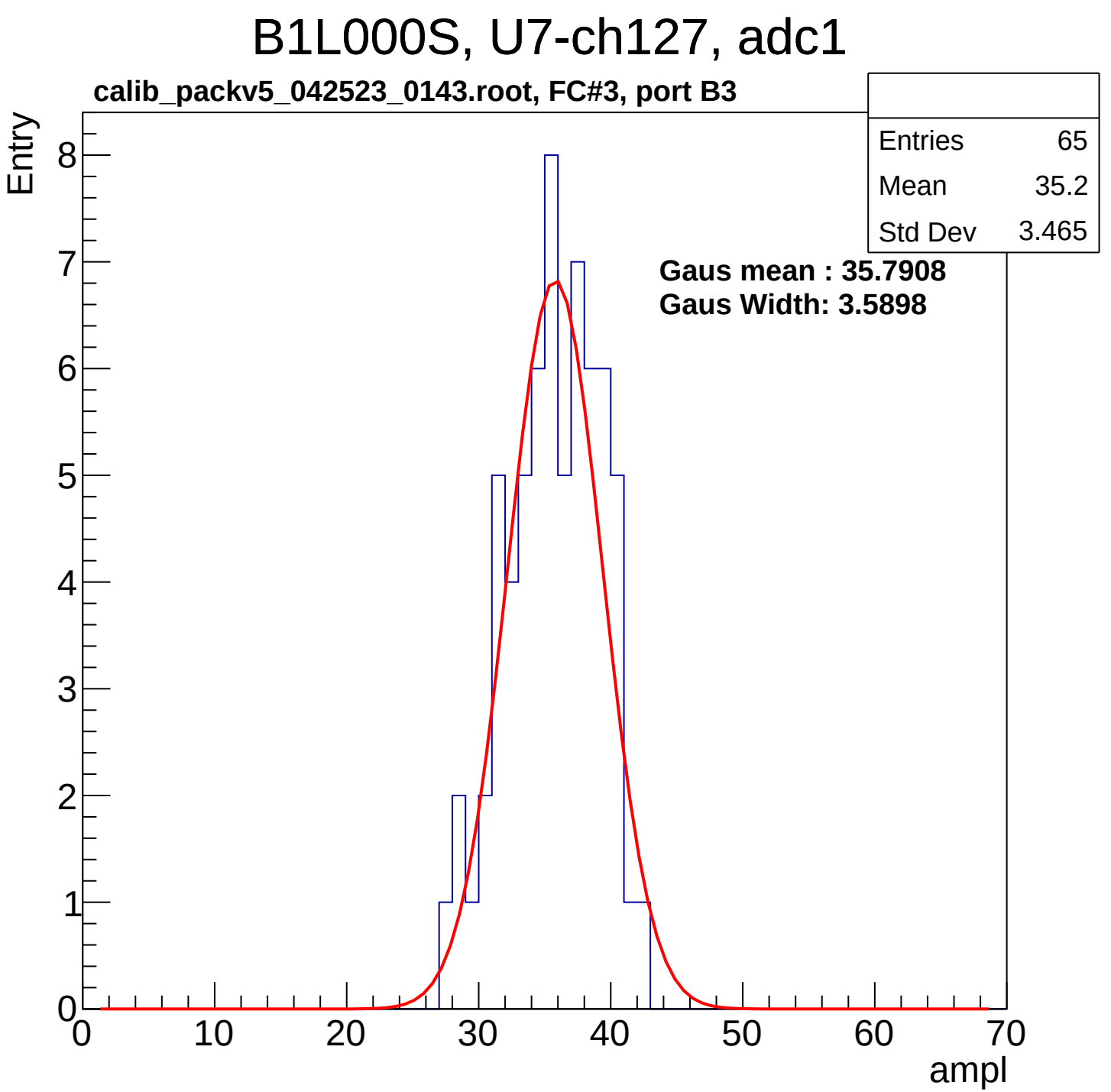
B1L000S, U7-ch127, adc1
calib_packv5_042523_0143.root, FC#3, port B3
Entries 65
Mean 35.2
Std Dev 3.465
Gaus mean : 35.7908
Gaus Width: 3.5898
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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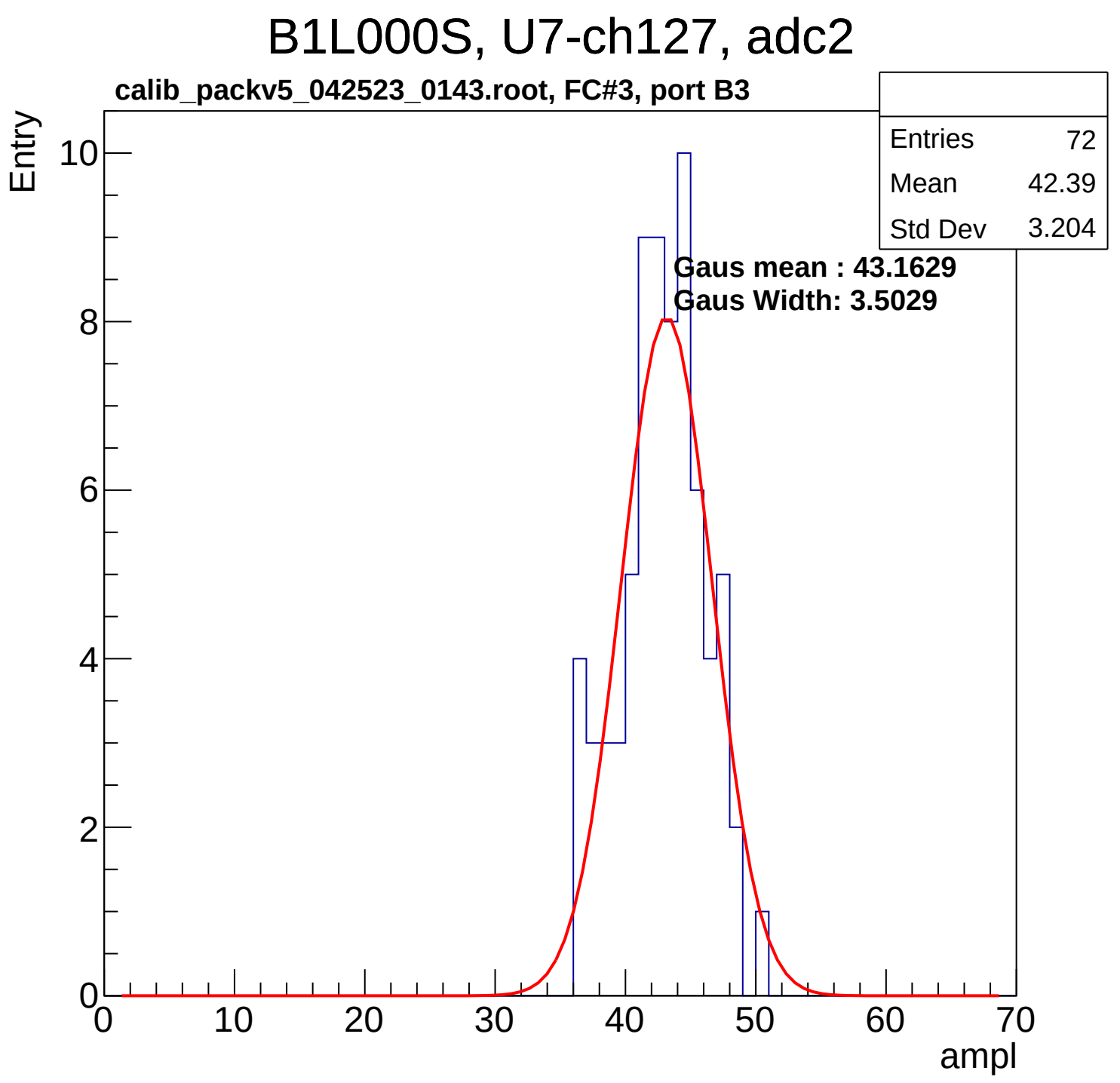
B1L000S, U7-ch127, adc2
calib_packv5_042523_0143.root, FC#3, port B3
Entries 72
Mean 42.39
Std Dev 3.204
Gaus mean : 43.1629
Gaus Width: 3.5029
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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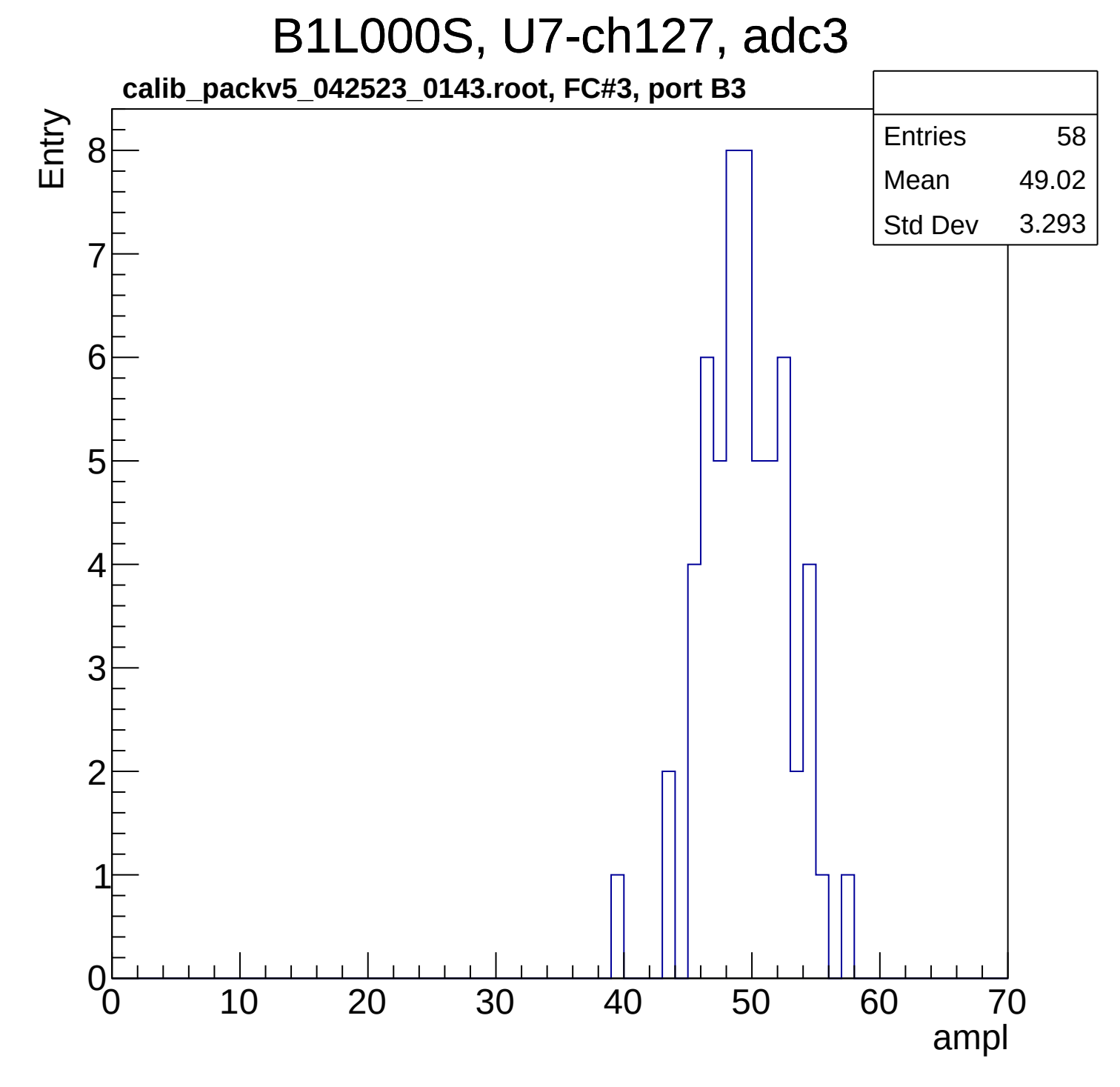
B1L000S, U7-ch127, adc3
calib_packv5_042523_0143.root, FC#3, port B3
Entries 58
Mean 49.02
Std Dev 3.293
Entry
ampl
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2
3
4
5
6
7
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0
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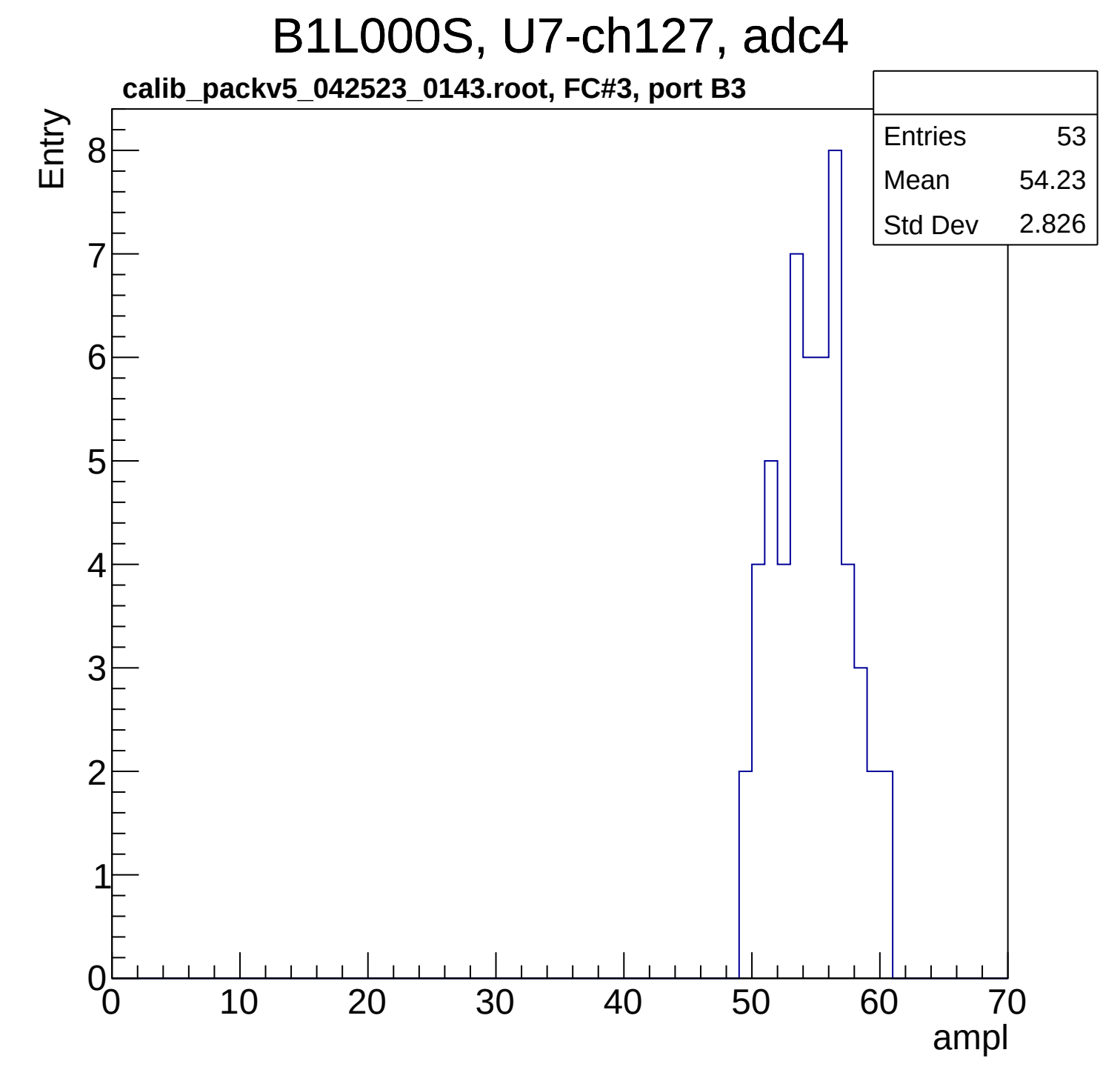
B1L000S, U7-ch127, adc4
calib_packv5_042523_0143.root, FC#3, port B3
Entries 53
Mean 54.23
Std Dev 2.826
Entry
ampl
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4
5
6
7
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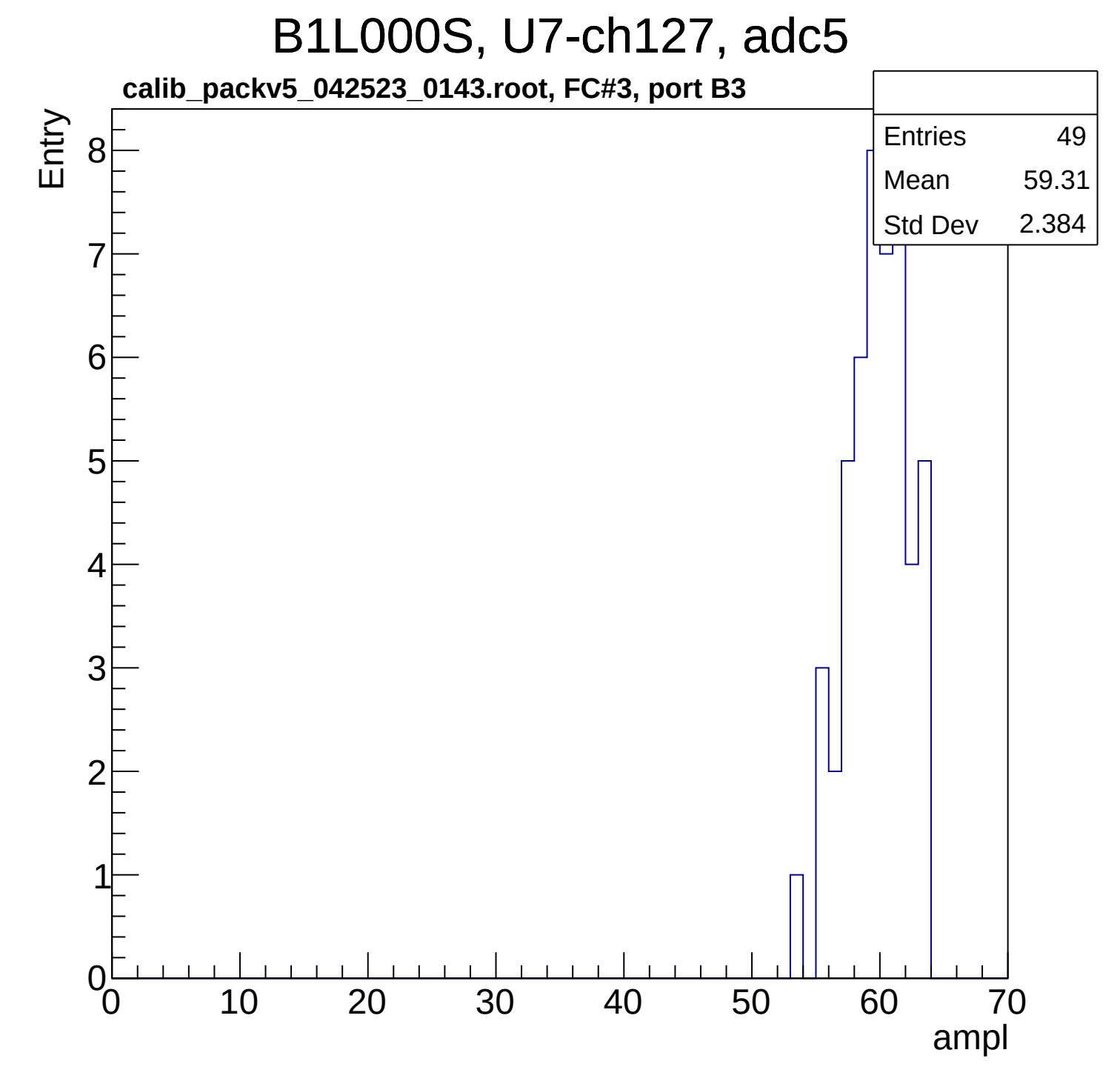
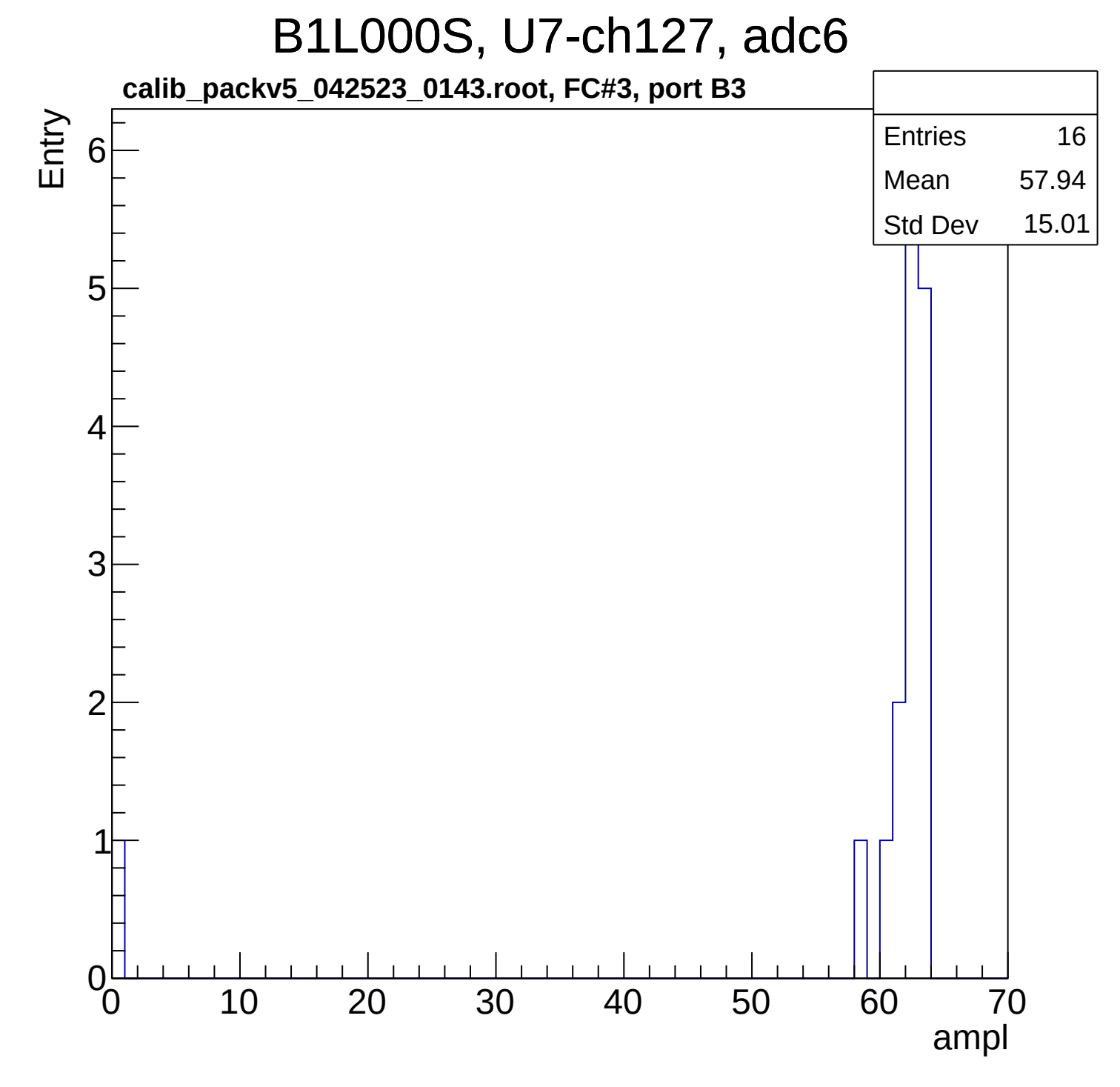
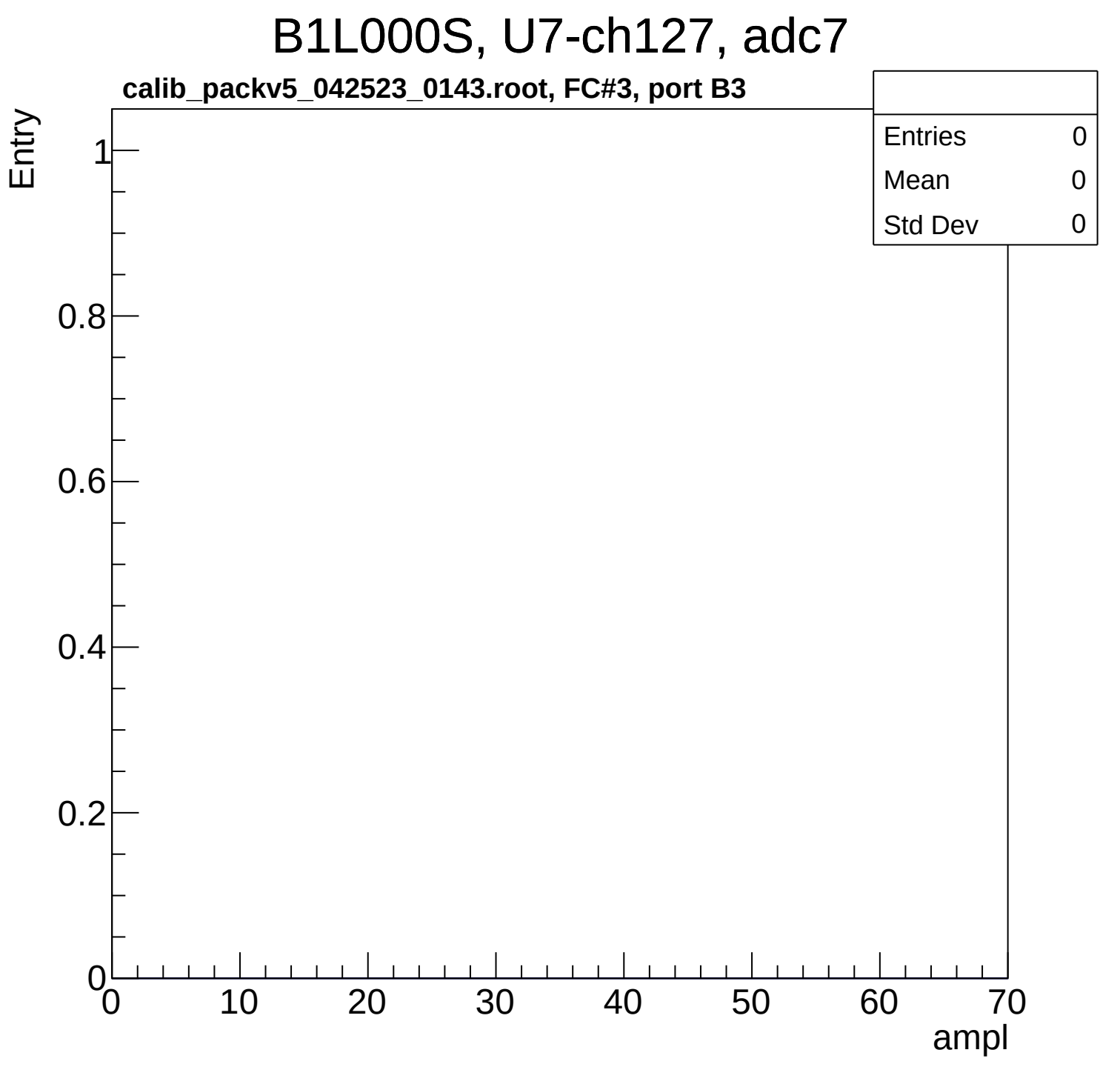
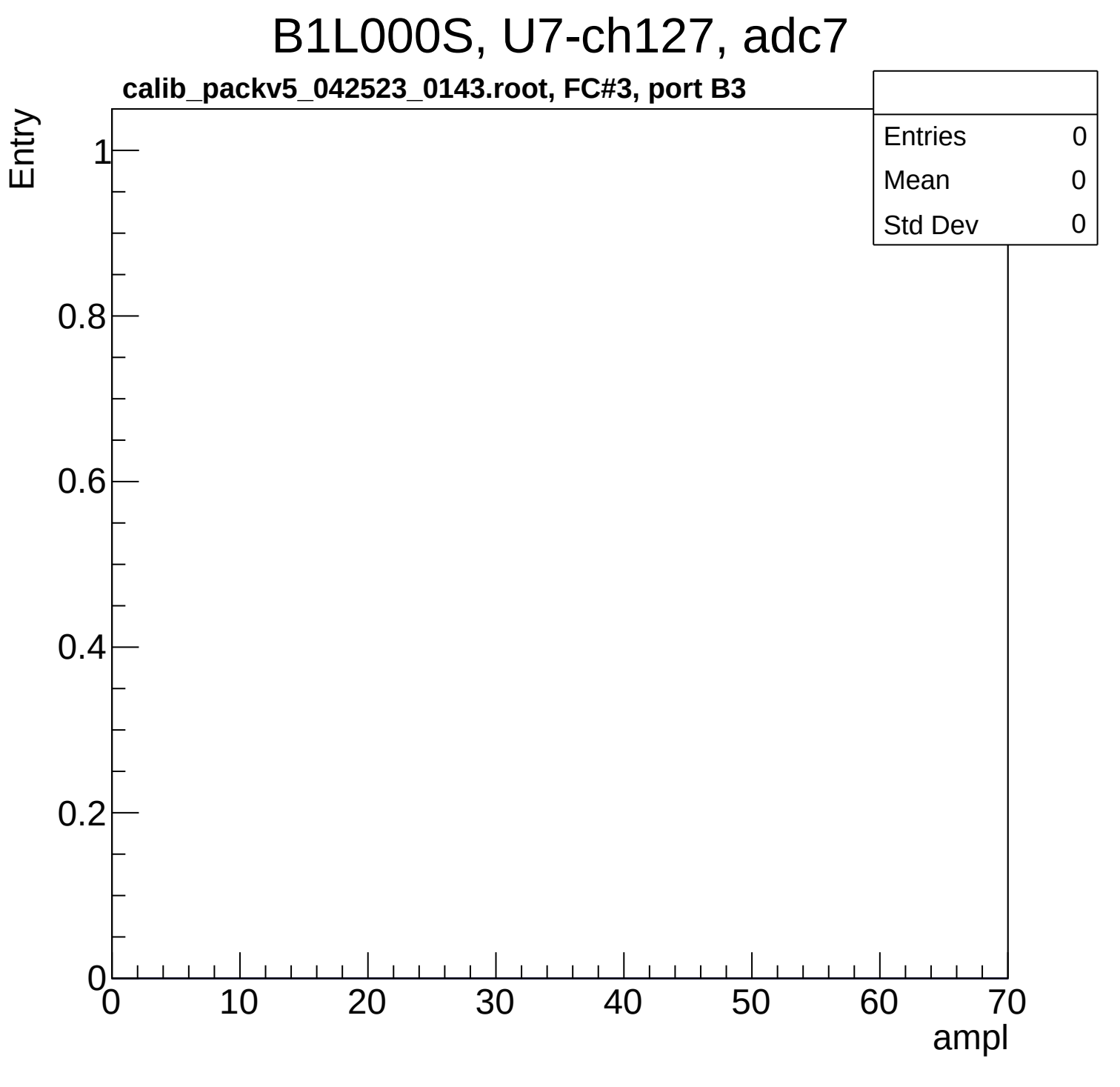

B1L000S, U7-ch127, adc5
calib_packv5_042523_0143.root, FC#3, port B3
Entries 49
Mean 59.31
Std Dev 2.384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L000S, U7-ch127, adc6
calib_packv5_042523_0143.root, FC#3, port B3
Entries 16
Mean 57.94
Std Dev 15.01
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L000S, U7-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L000S, U7-ch127, adc7
calib_packv5_042523_0143.root, FC#3, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl