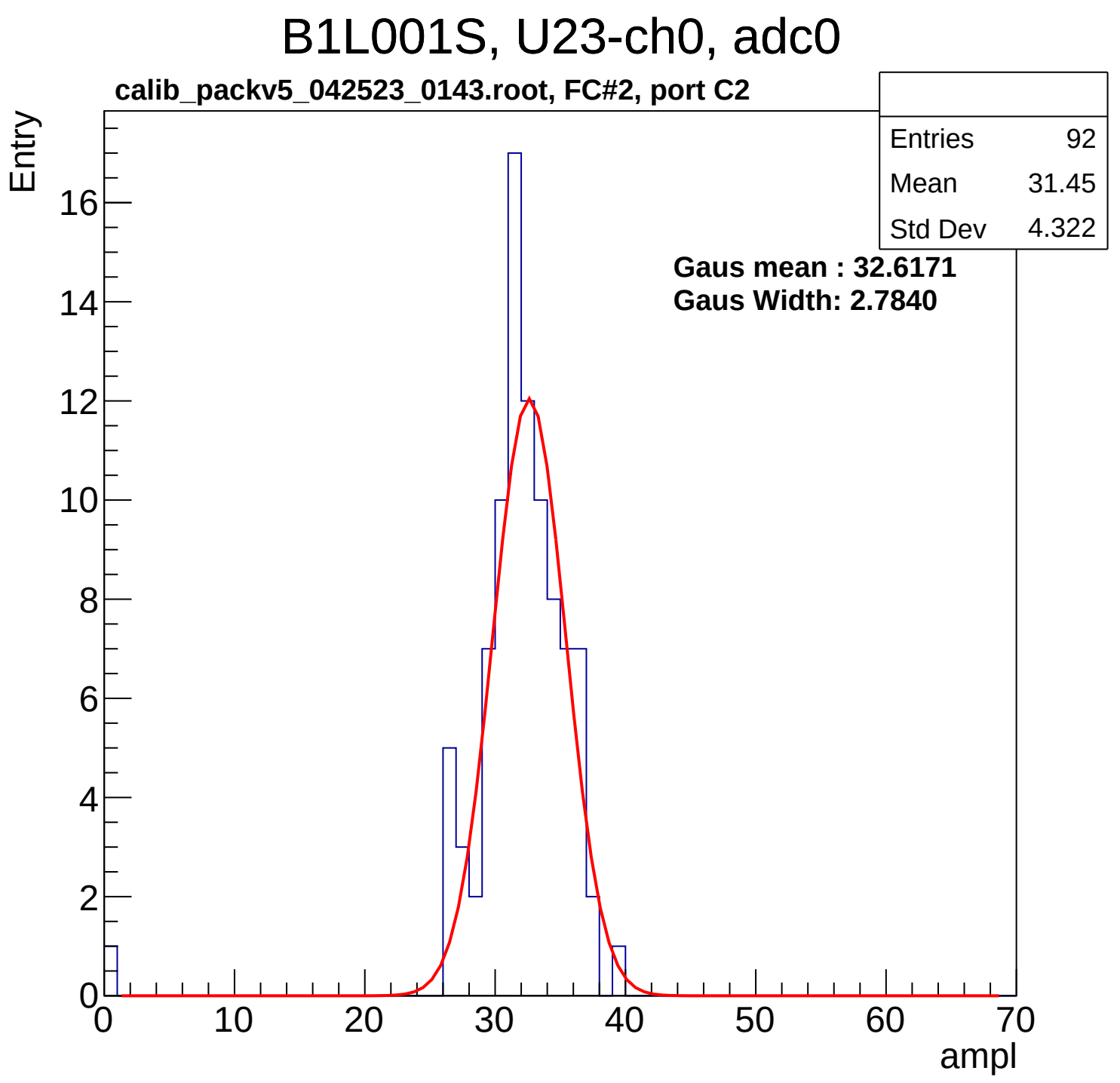
B1L001S, U23-ch0, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 92
Mean 31.45
Std Dev 4.322
Gaus mean : 32.6171
Gaus Width: 2.7840
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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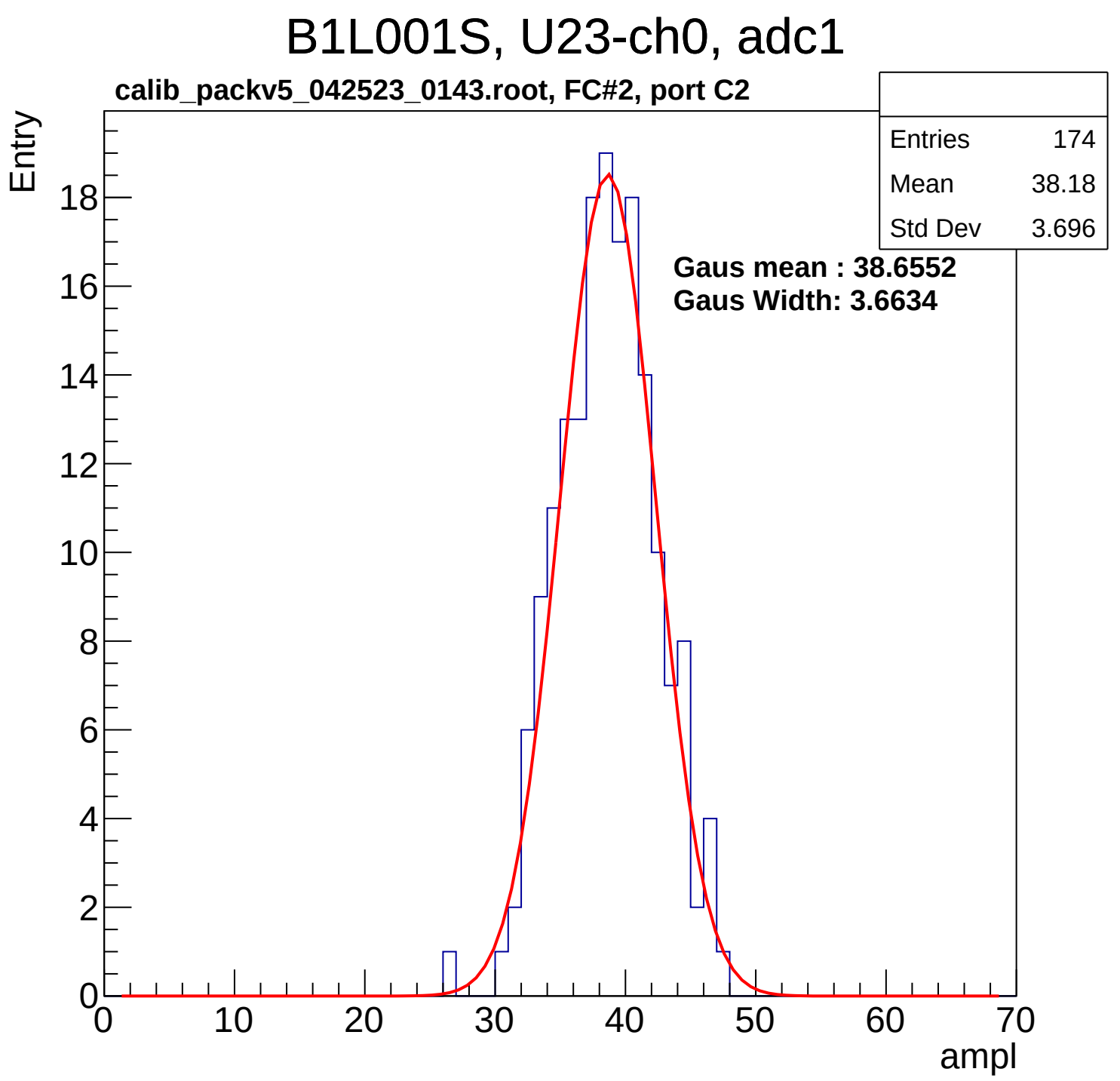
B1L001S, U23-ch0, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 174
Mean 38.18
Std Dev 3.696
Gaus mean : 38.6552
Gaus Width: 3.6634
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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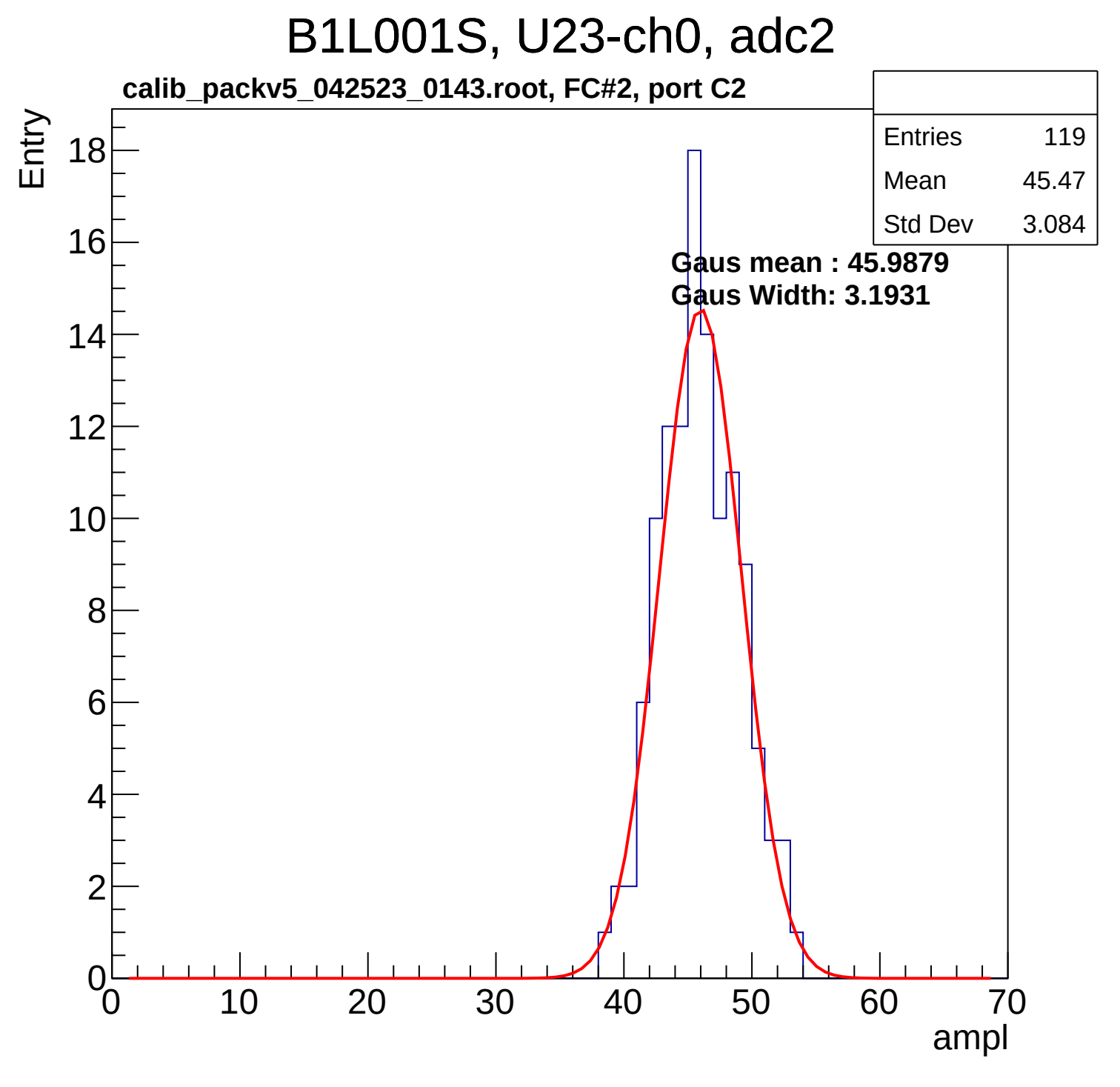
B1L001S, U23-ch0, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 45.47
Std Dev 3.084
Gaus mean : 45.9879
Gaus Width: 3.1931
Entry
ampl
0
2
4
6
8
10
12
14
16
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0
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20
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40
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60
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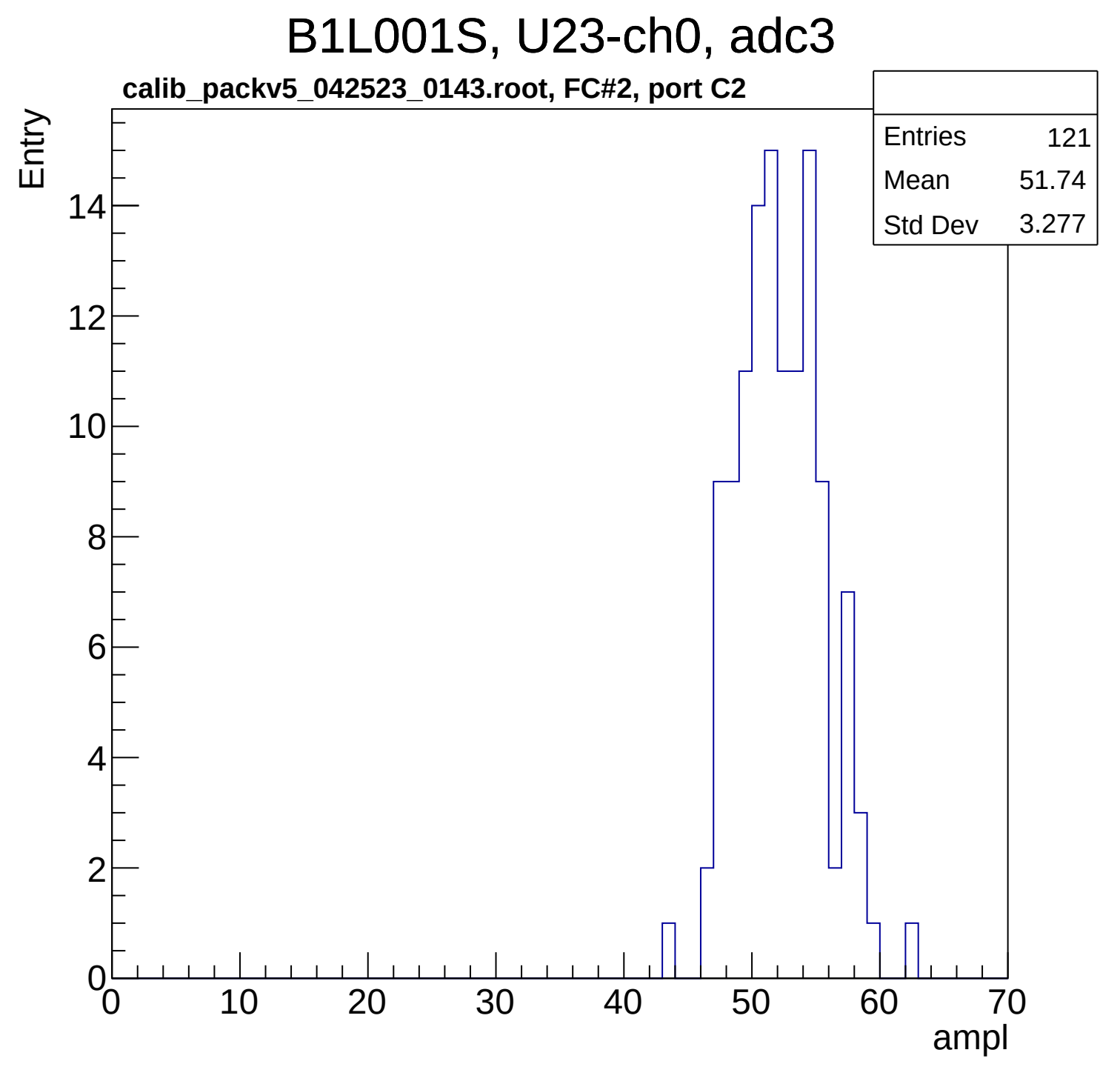
B1L001S, U23-ch0, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 51.74
Std Dev 3.277
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
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60
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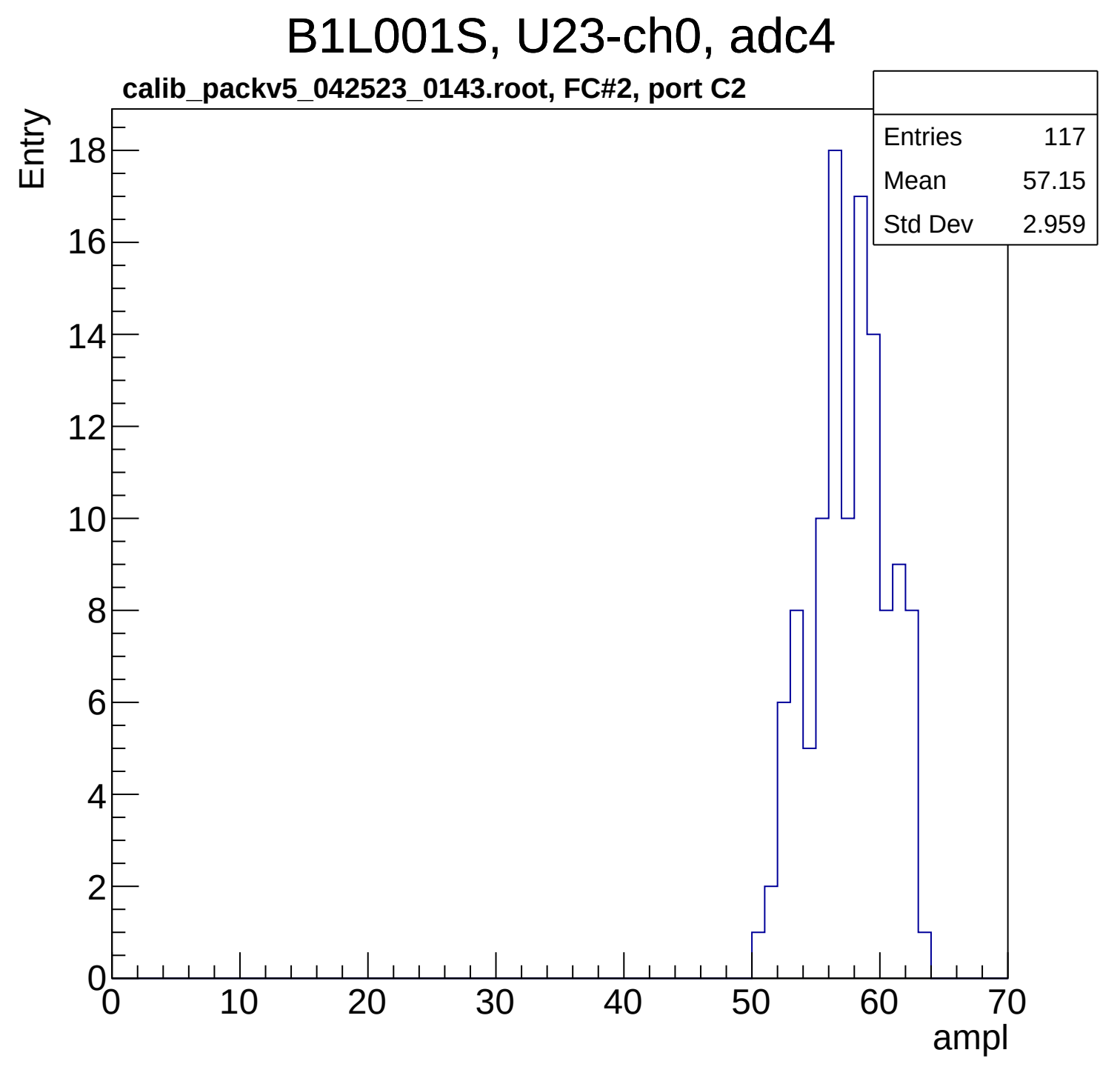
B1L001S, U23-ch0, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 57.15
Std Dev 2.959
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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20
30
40
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60
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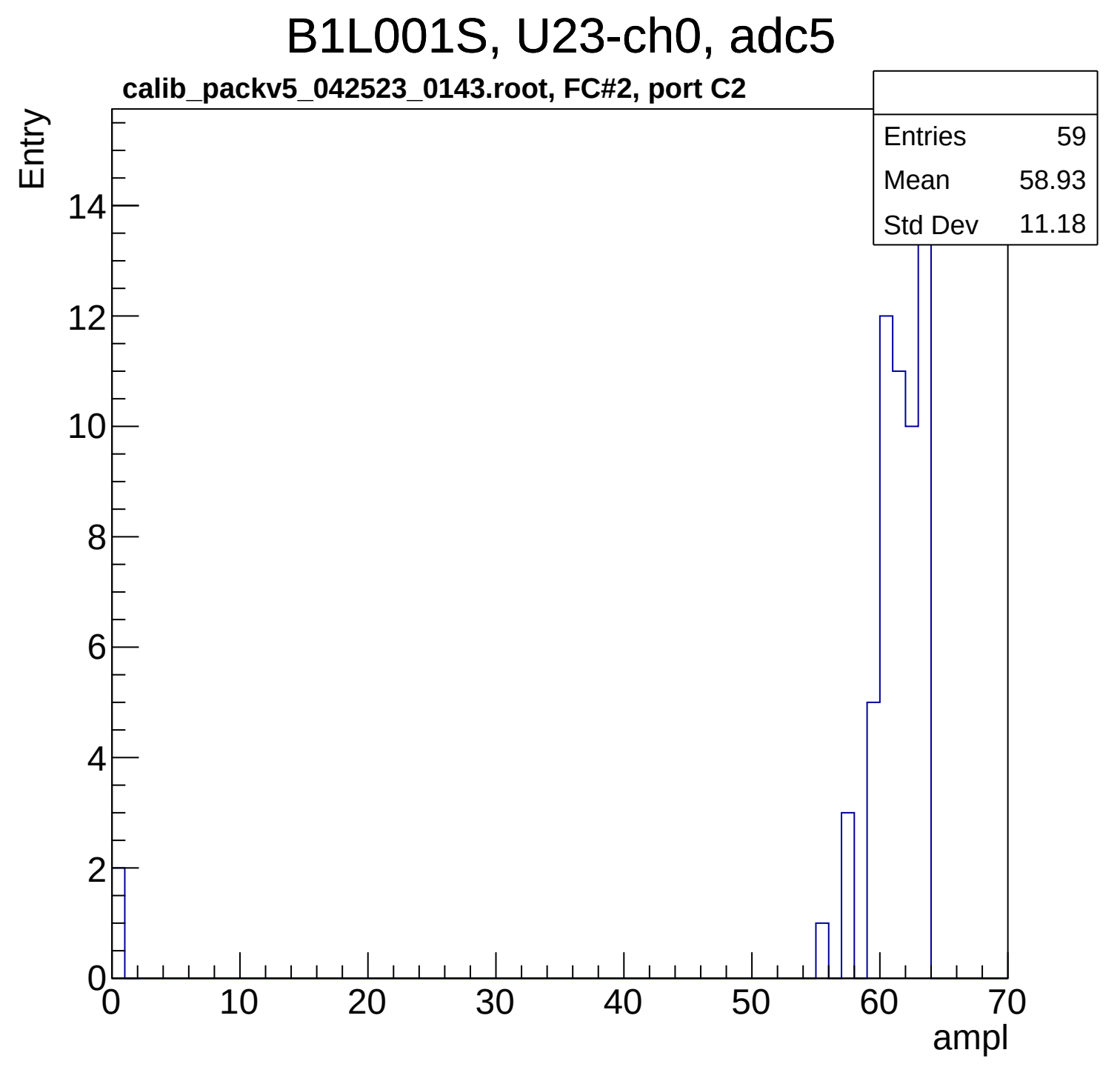
B1L001S, U23-ch0, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 59
Mean 58.93
Std Dev 11.18
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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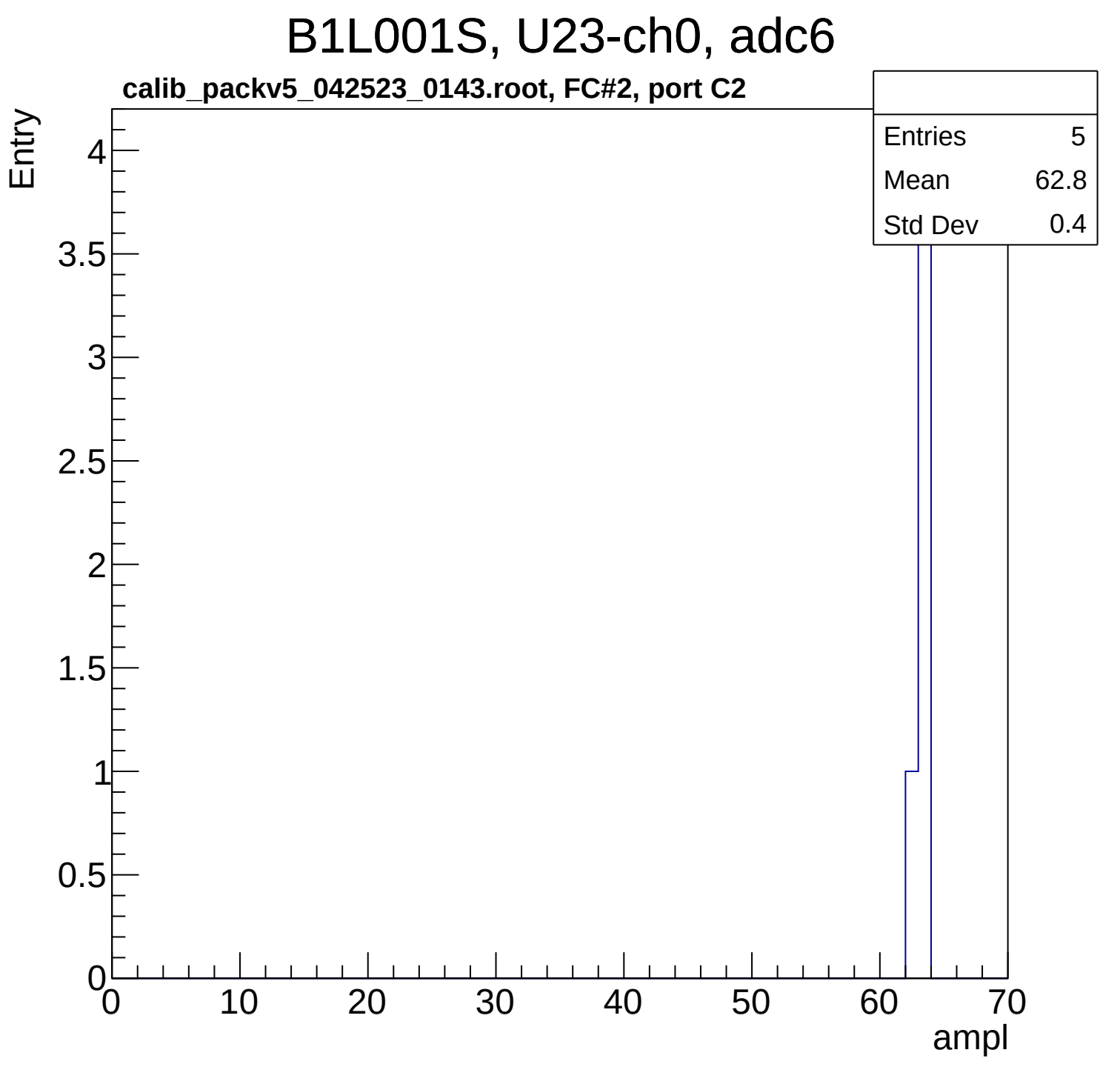
B1L001S, U23-ch0, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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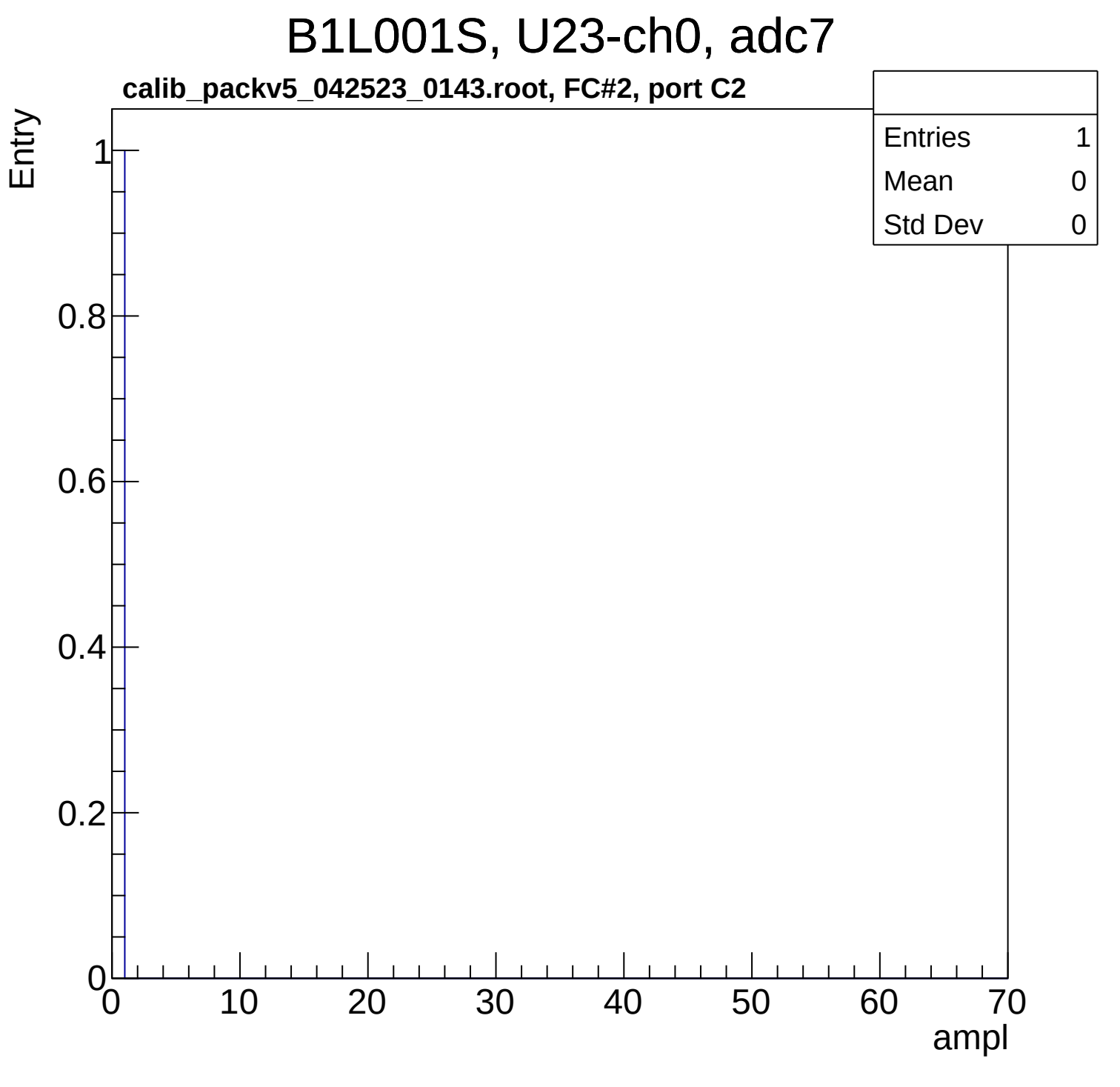
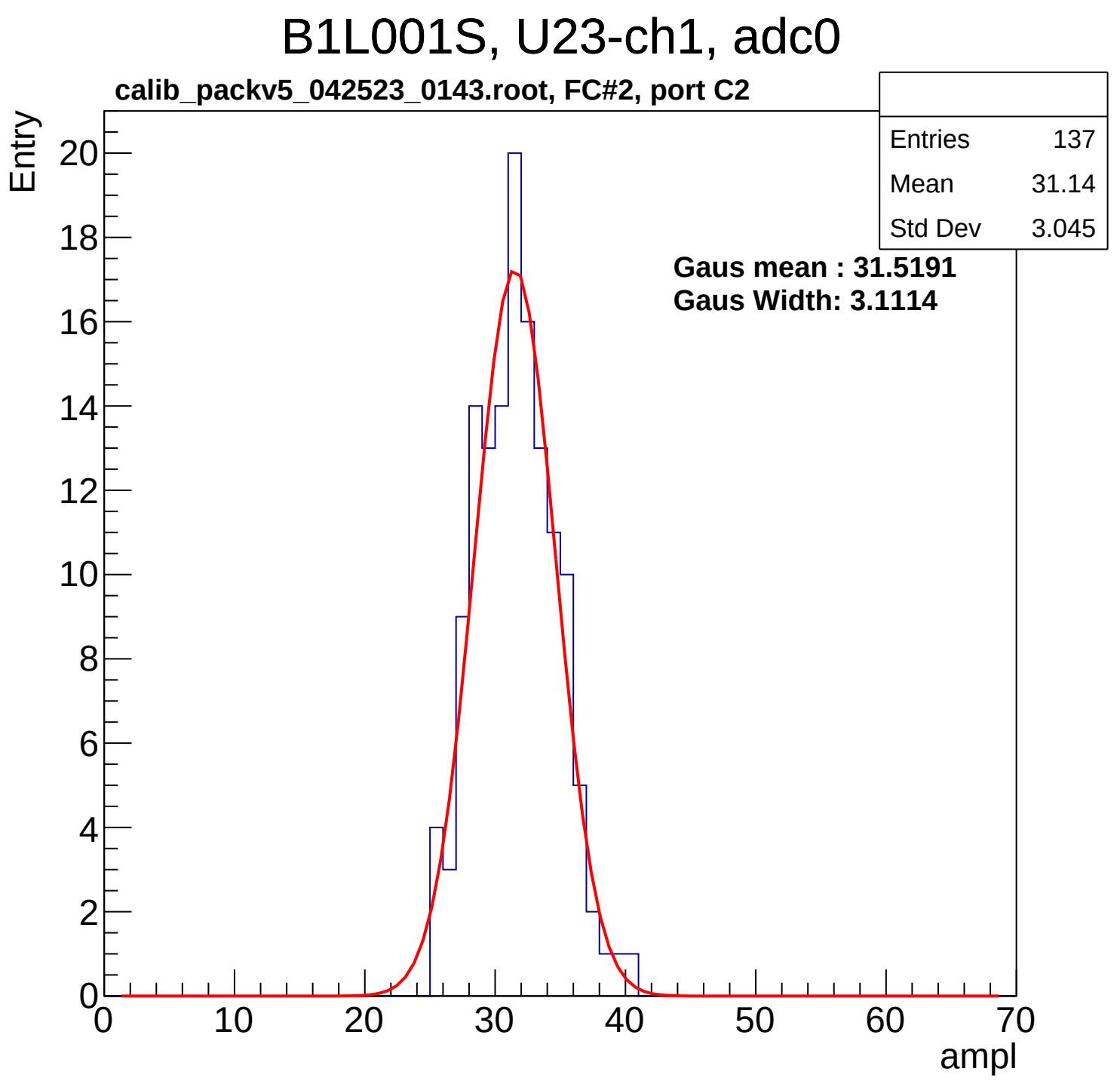
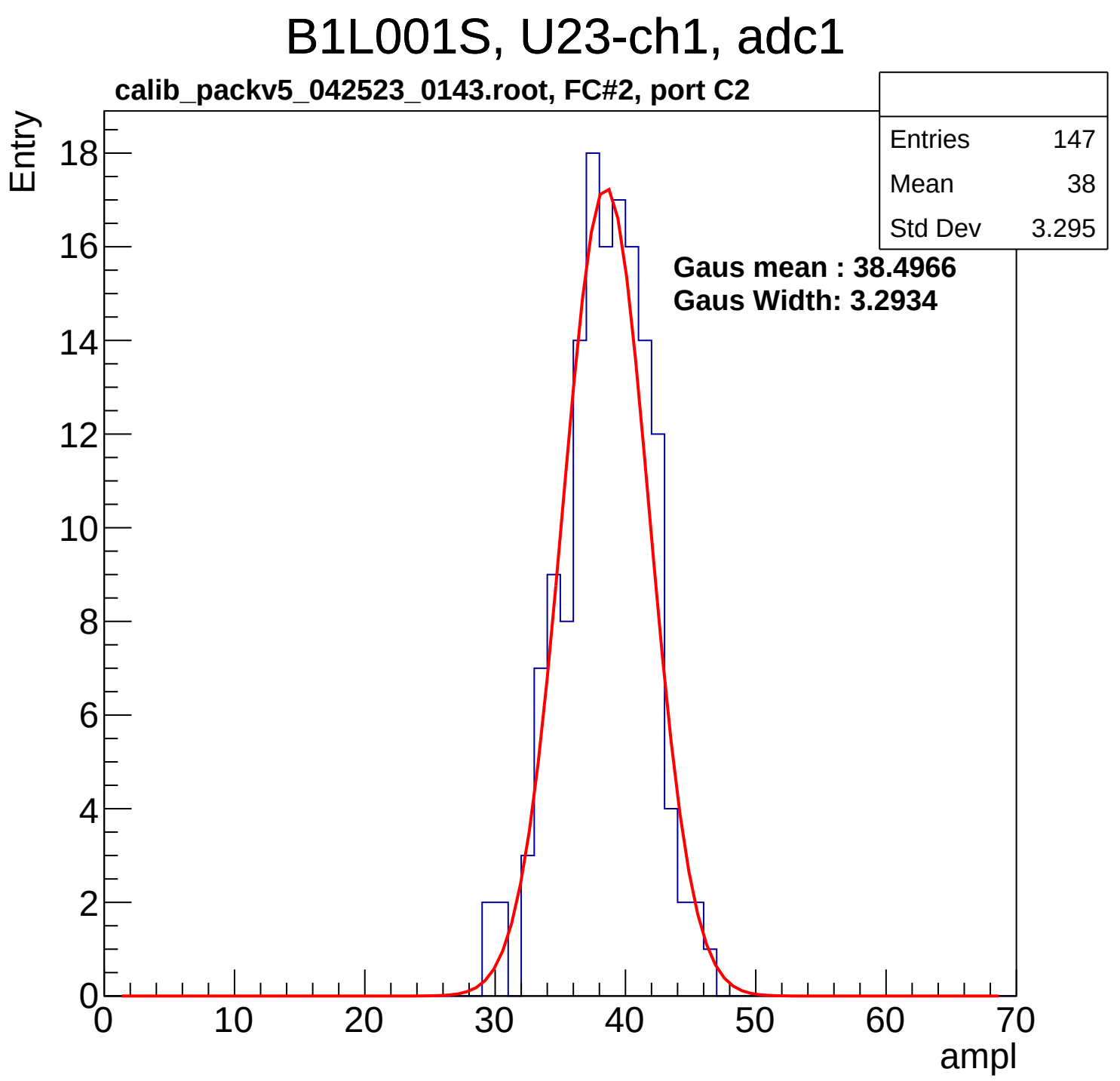

B1L001S, U23-ch0, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch1, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 31.14
Std Dev 3.045
Gaus mean : 31.5191
Gaus Width: 3.1114
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch1, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 38
Std Dev 3.295
Gaus mean : 38.4966
Gaus Width: 3.2934
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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60
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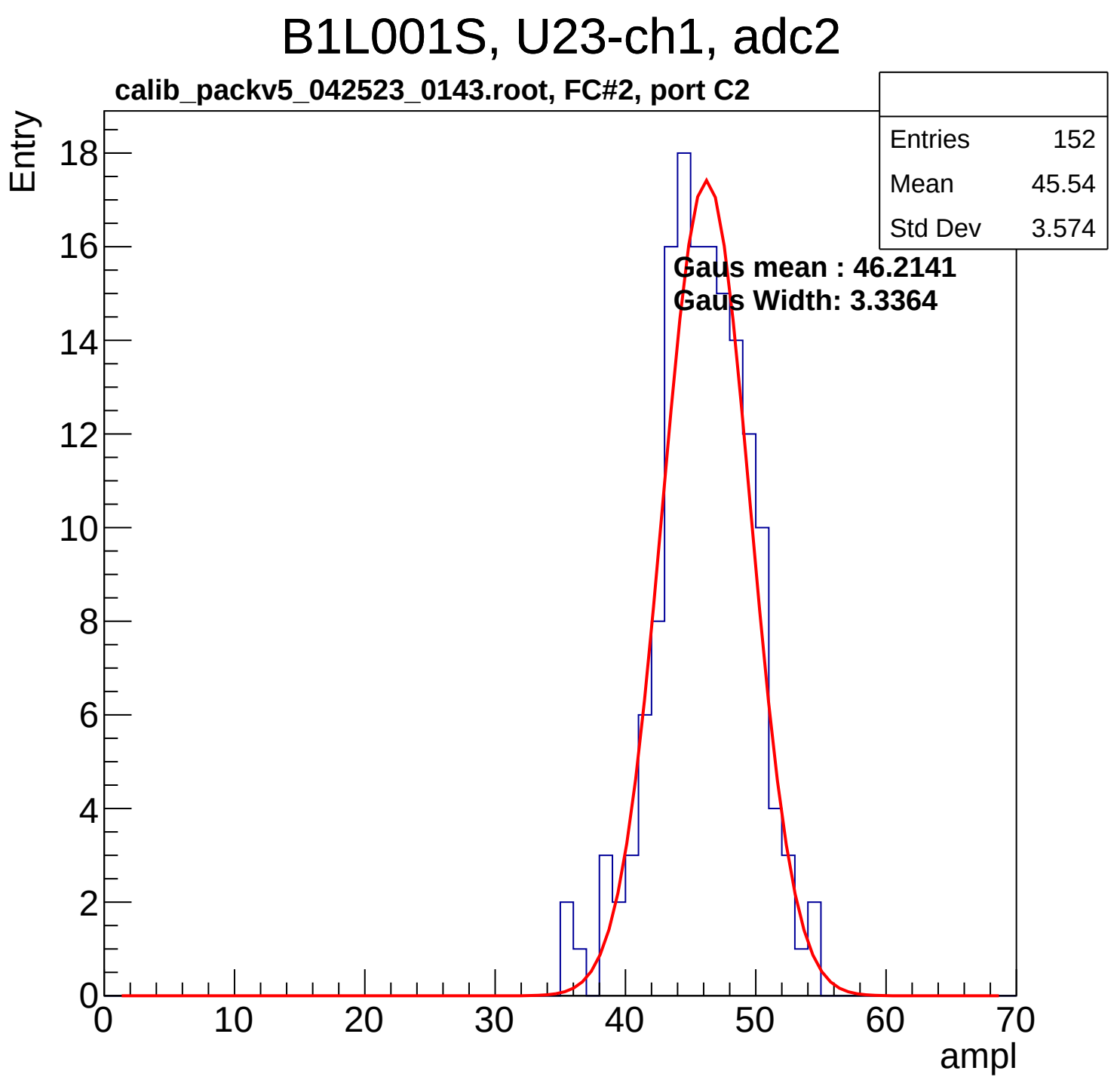
B1L001S, U23-ch1, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 45.54
Std Dev 3.574
Gaus mean : 46.2141
Gaus Width: 3.3364
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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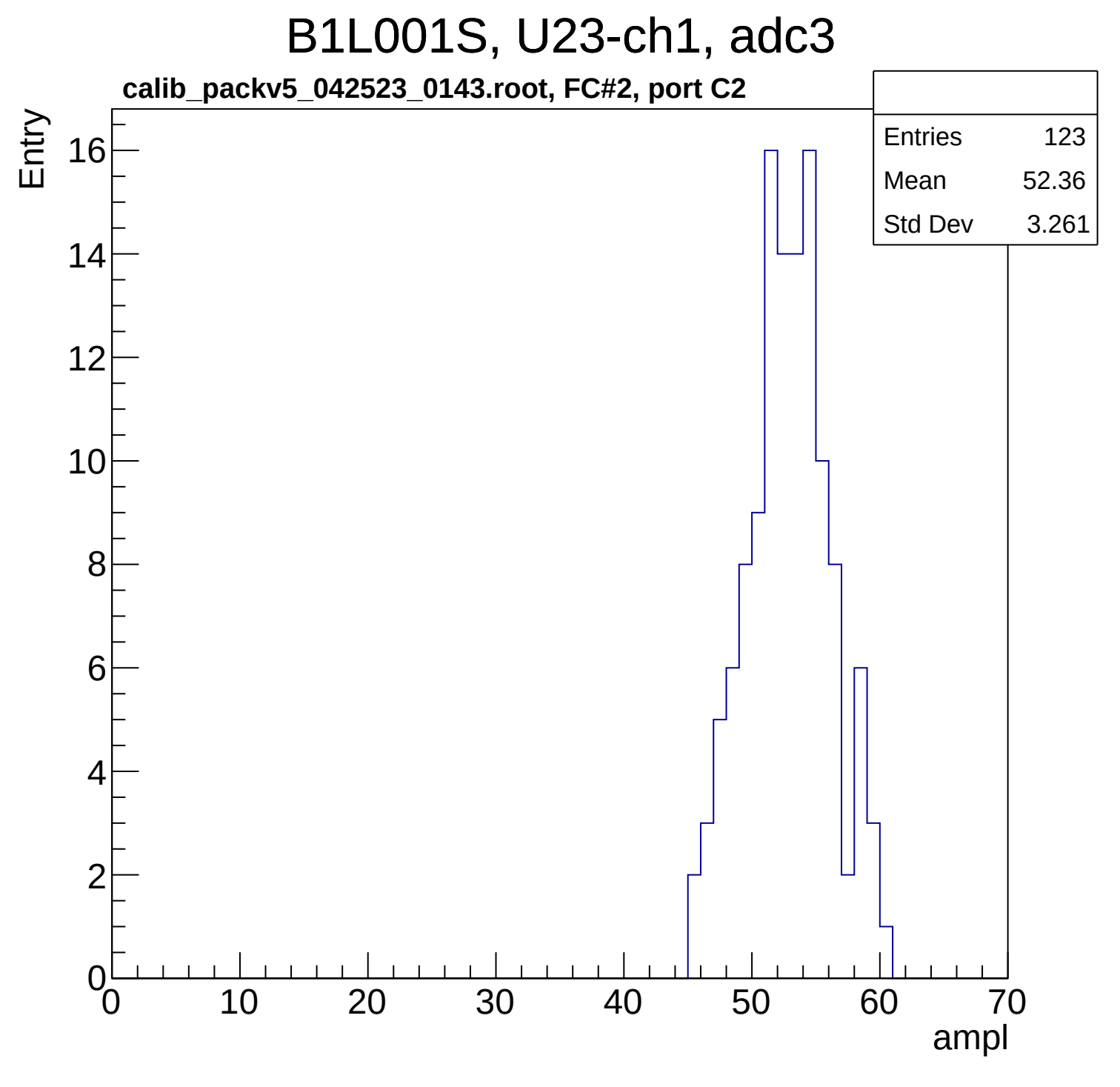
B1L001S, U23-ch1, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 52.36
Std Dev 3.261
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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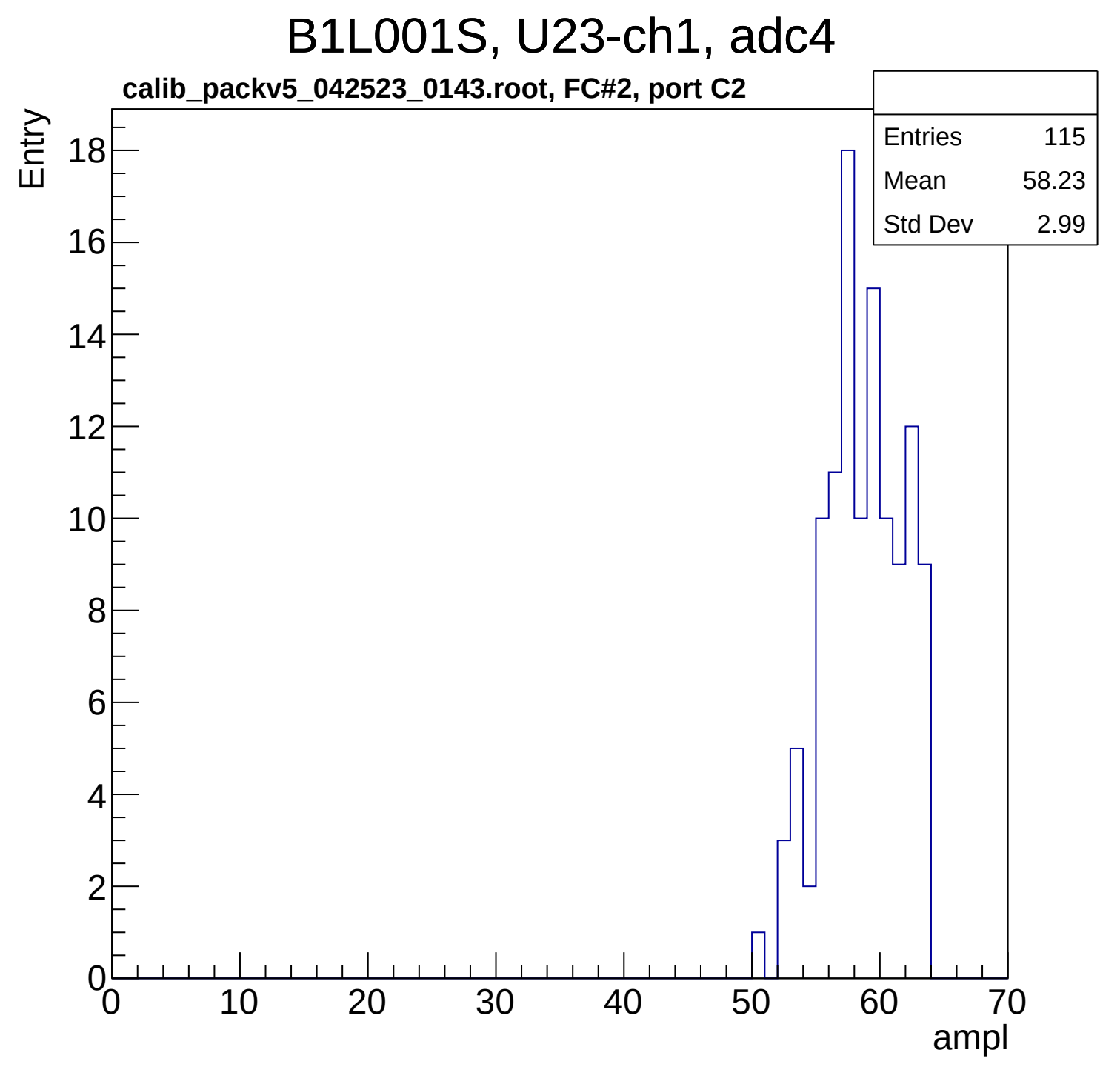
B1L001S, U23-ch1, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 58.23
Std Dev 2.99
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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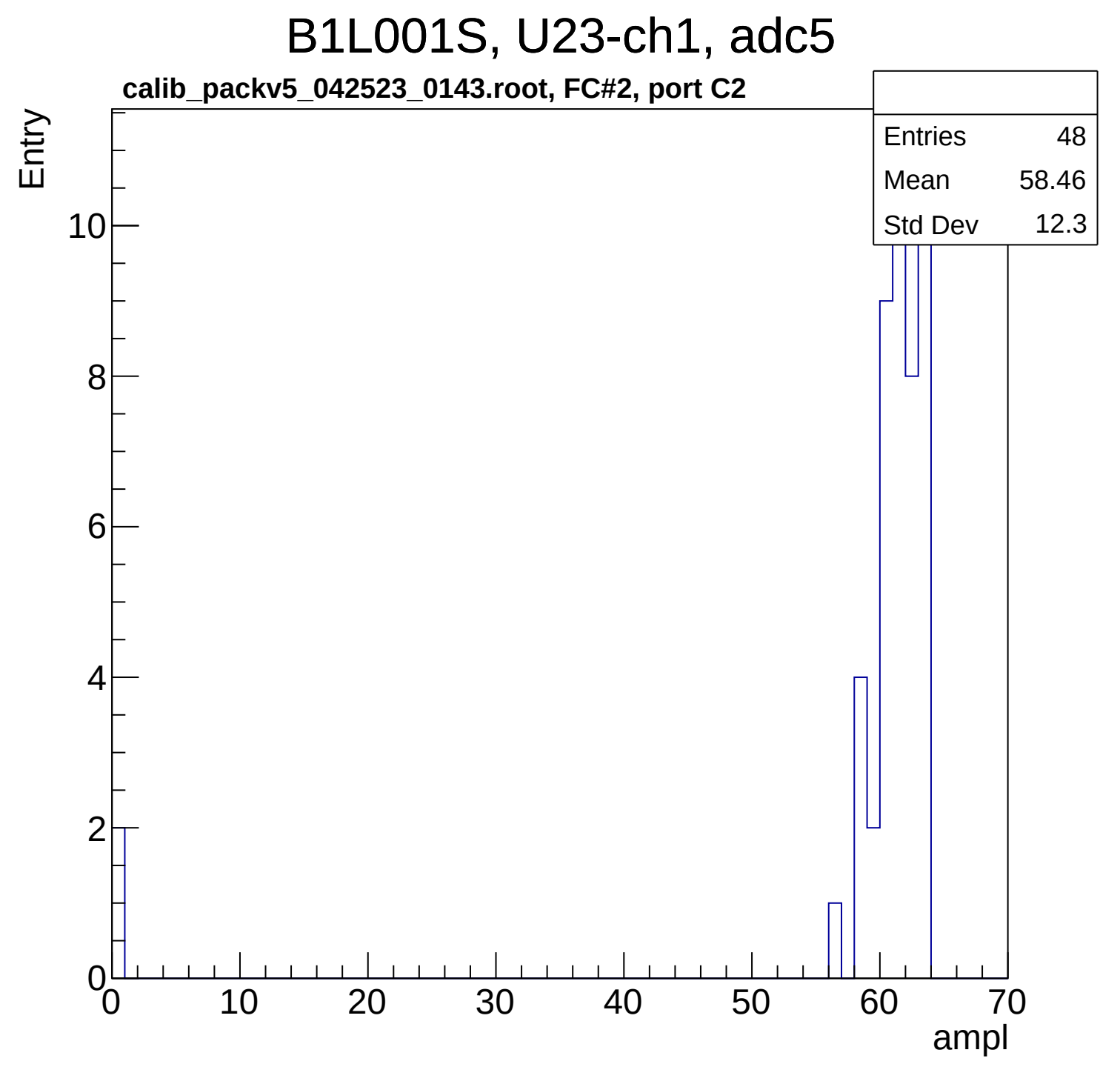
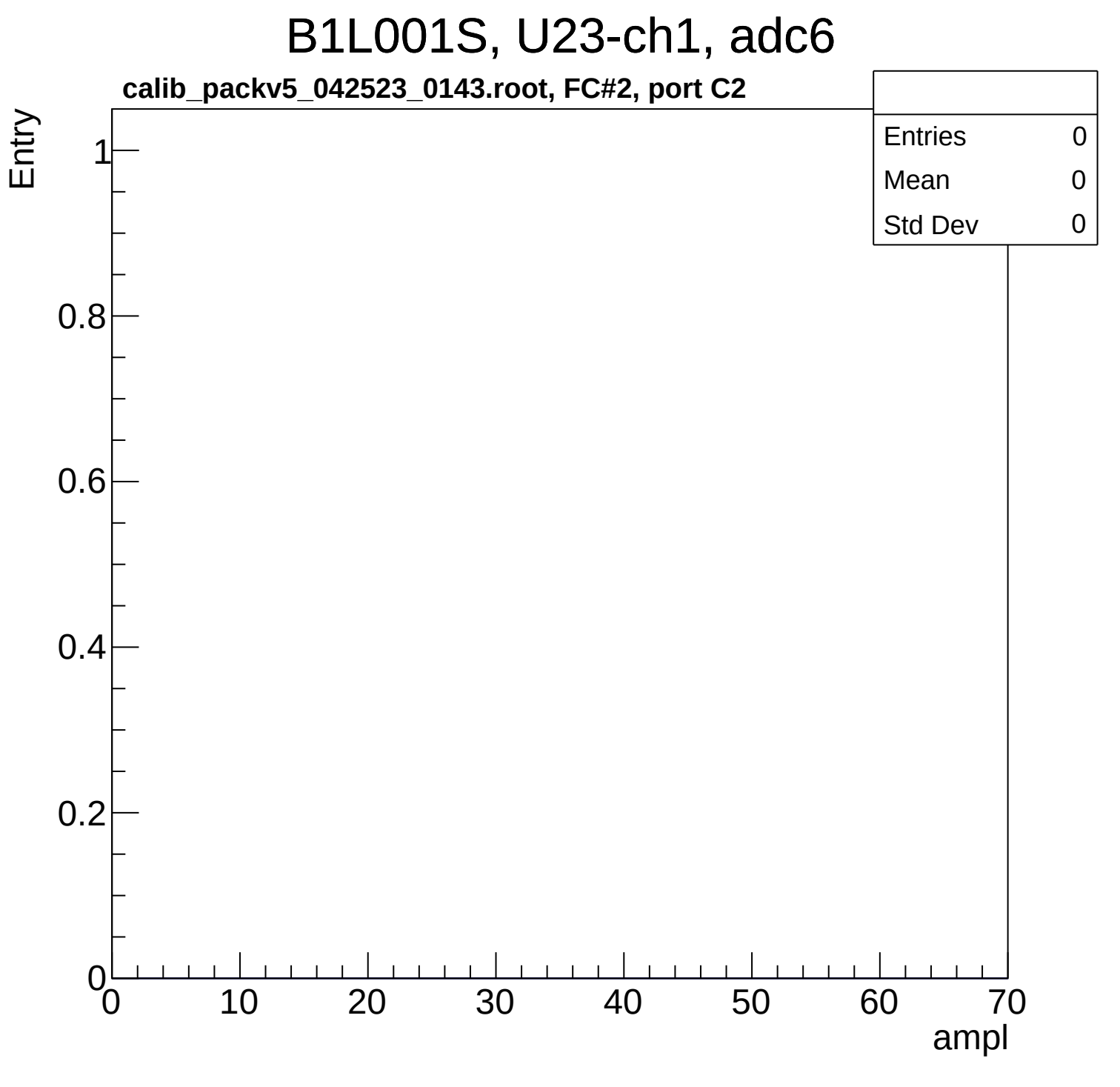
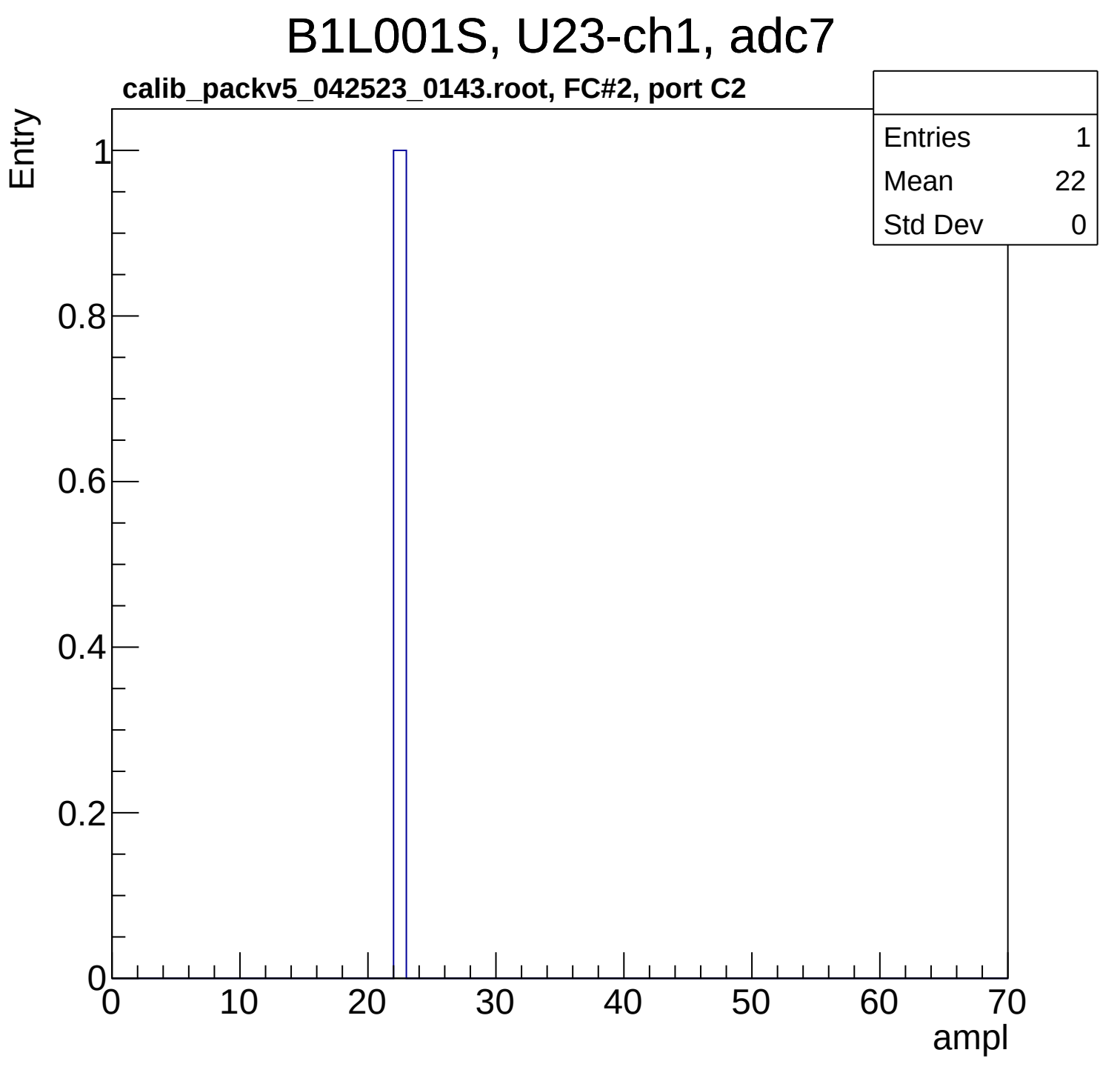

B1L001S, U23-ch1, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 48
Mean 58.46
Std Dev 12.3
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L001S, U23-ch1, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch1, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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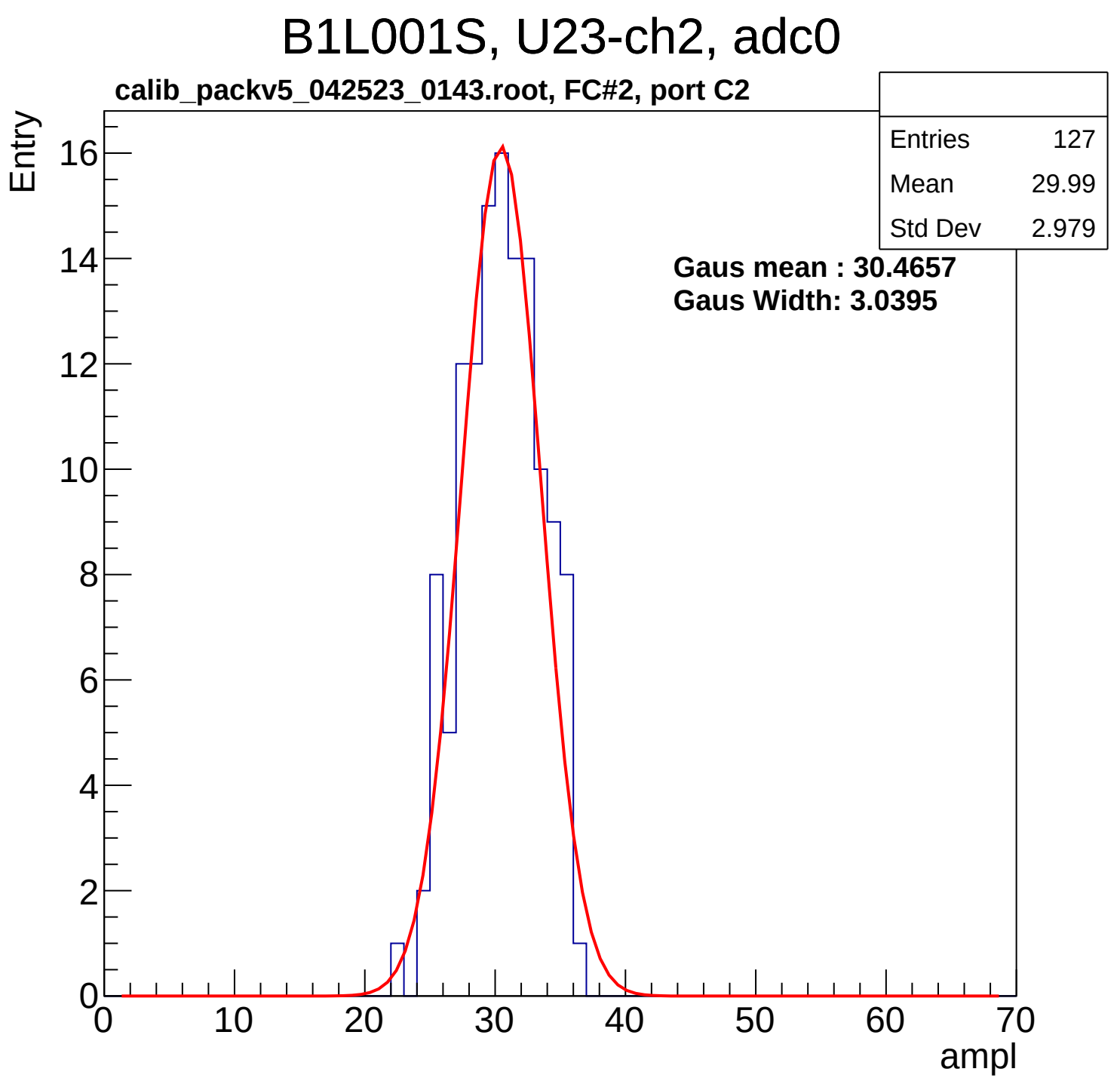
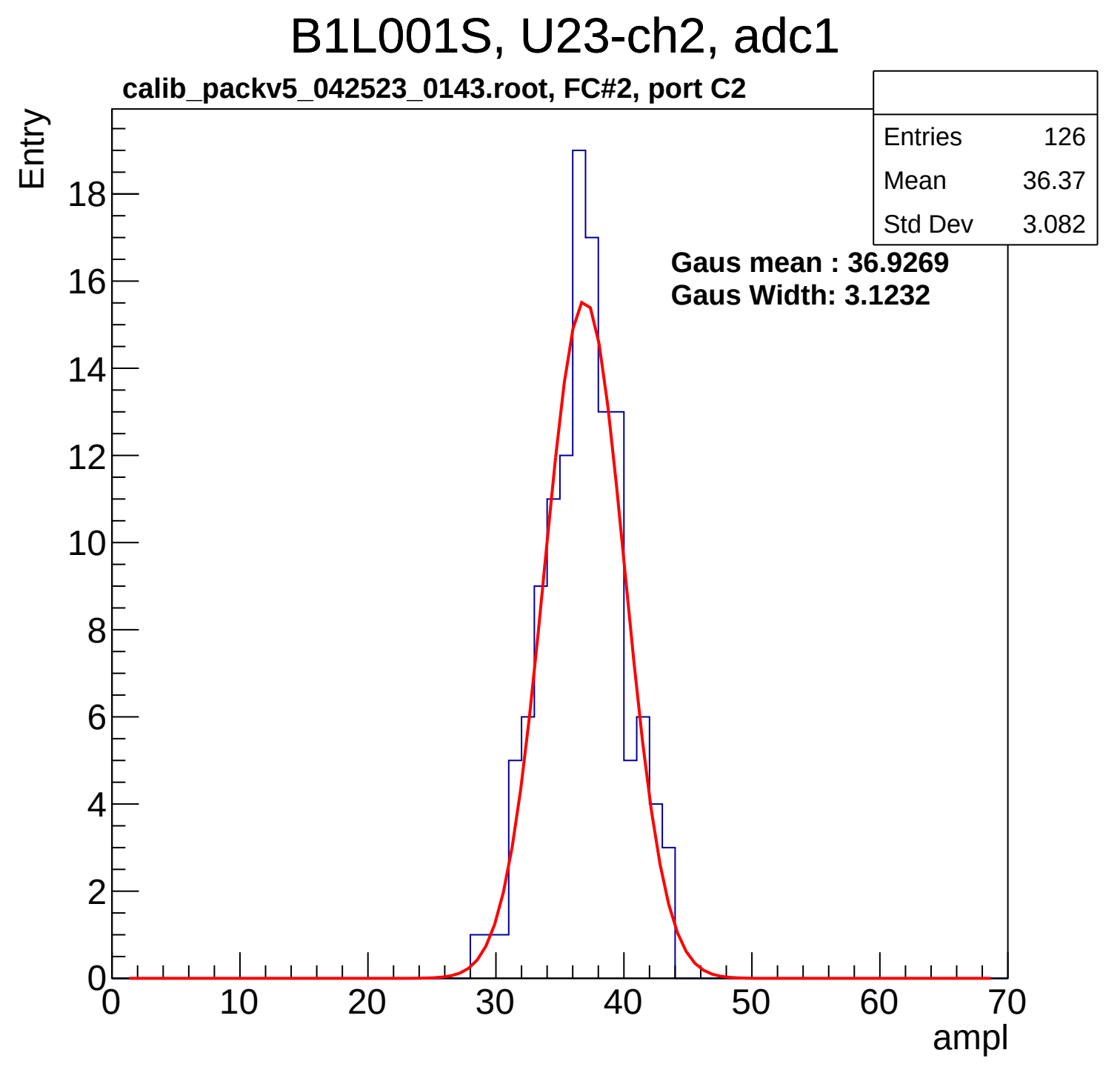
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch2, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 29.99
Std Dev 2.979
Gaus mean : 30.4657
Gaus Width: 3.0395
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch2, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 36.37
Std Dev 3.082
Gaus mean : 36.9269
Gaus Width: 3.1232
Entry
ampl
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4
6
8
10
12
14
16
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0
10
20
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40
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60
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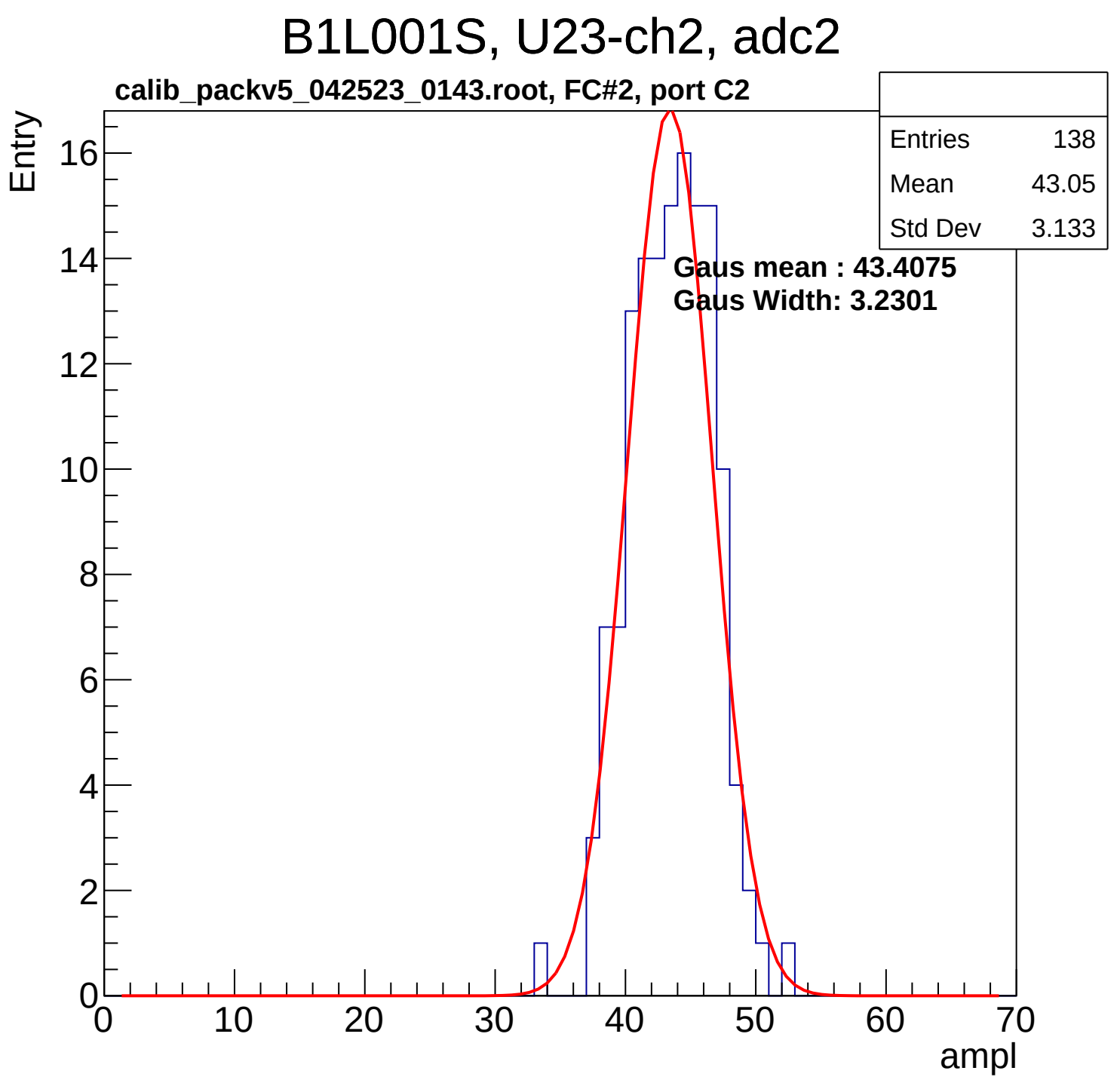
B1L001S, U23-ch2, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.05
Std Dev 3.133
Gaus mean : 43.4075
Gaus Width: 3.2301
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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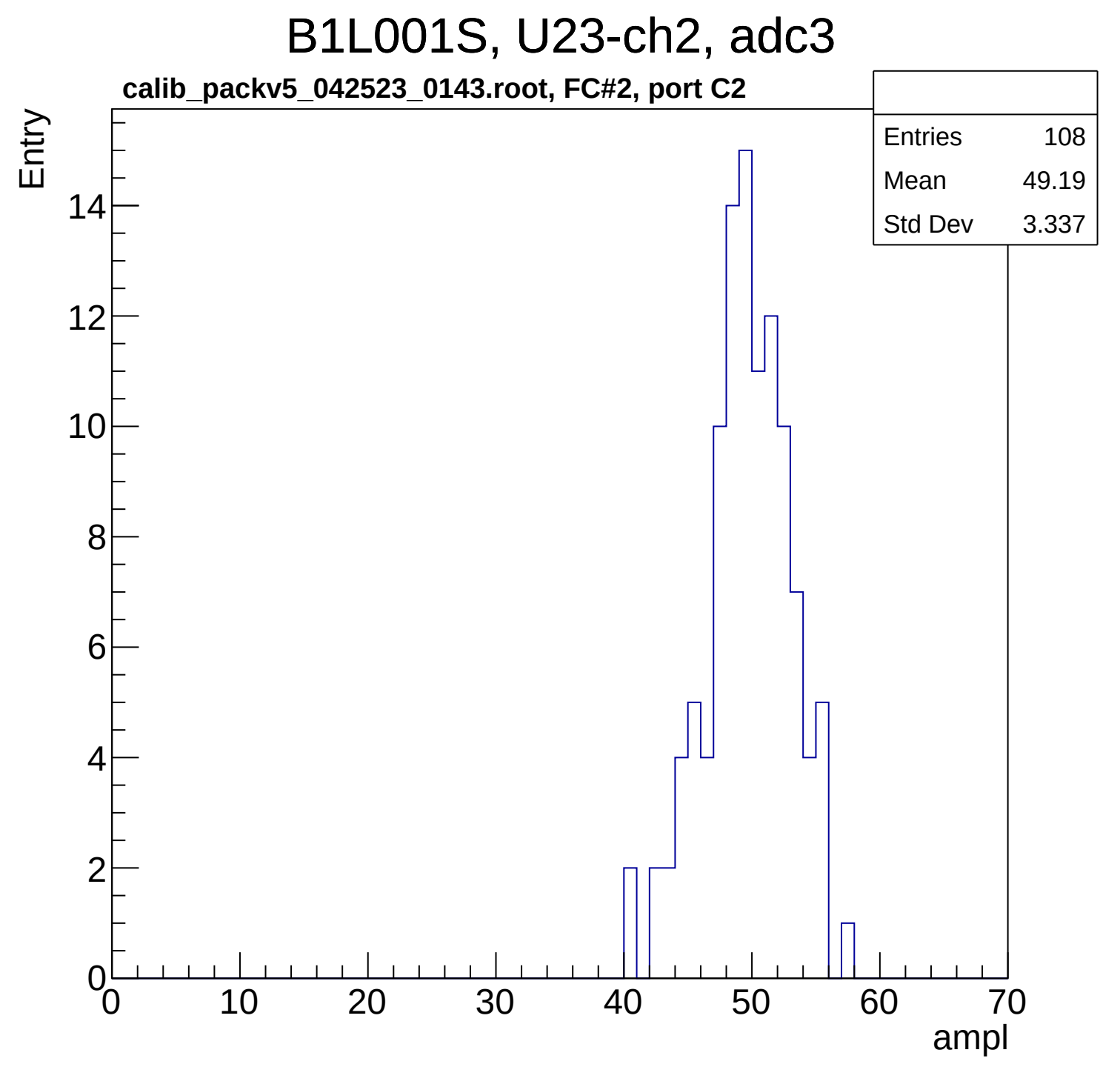
B1L001S, U23-ch2, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 49.19
Std Dev 3.337
Entry
ampl
0
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6
8
10
12
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0
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40
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60
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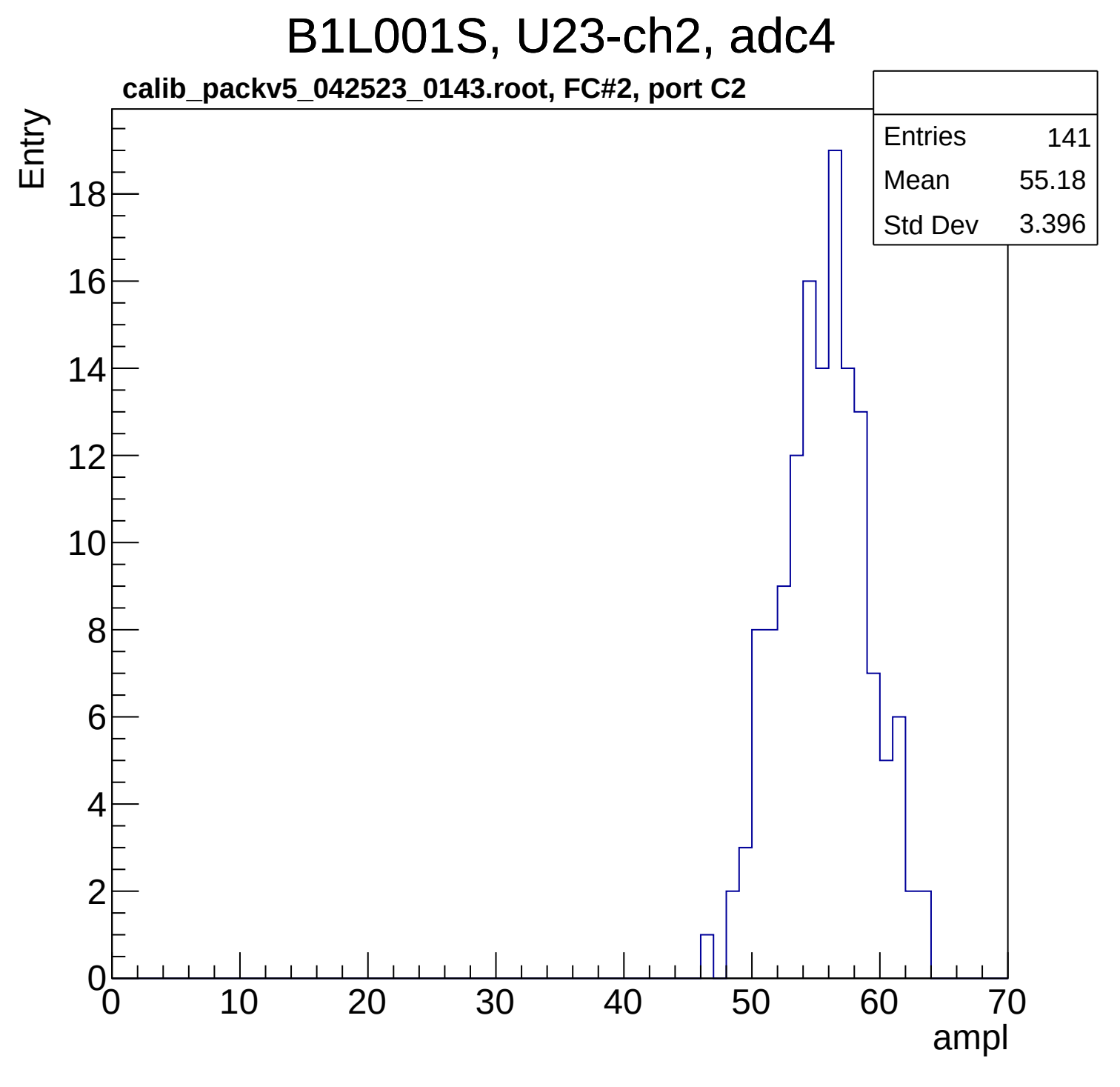
B1L001S, U23-ch2, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 55.18
Std Dev 3.396
Entry
ampl
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14
16
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0
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60
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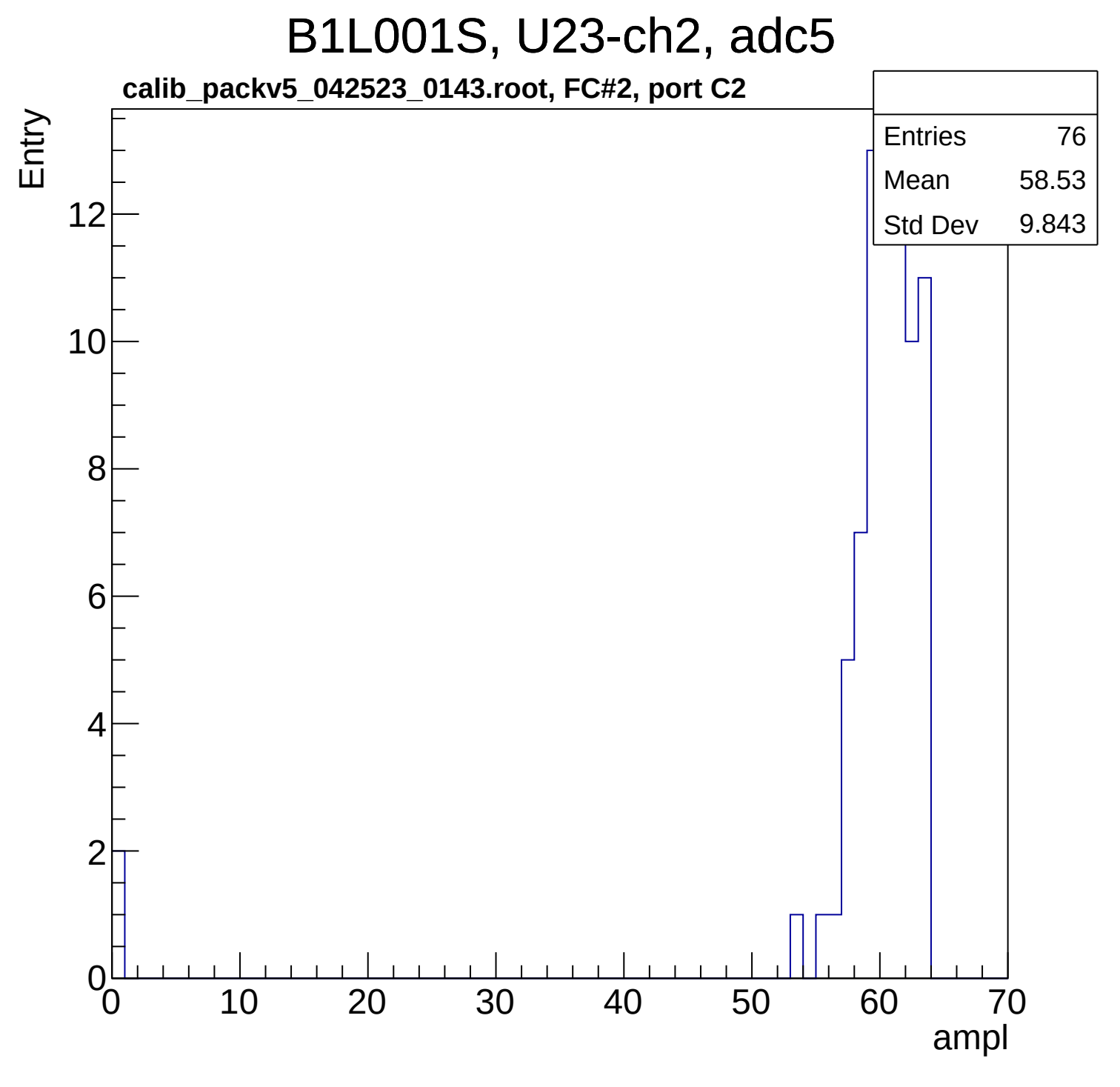
B1L001S, U23-ch2, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 58.53
Std Dev 9.843
Entry
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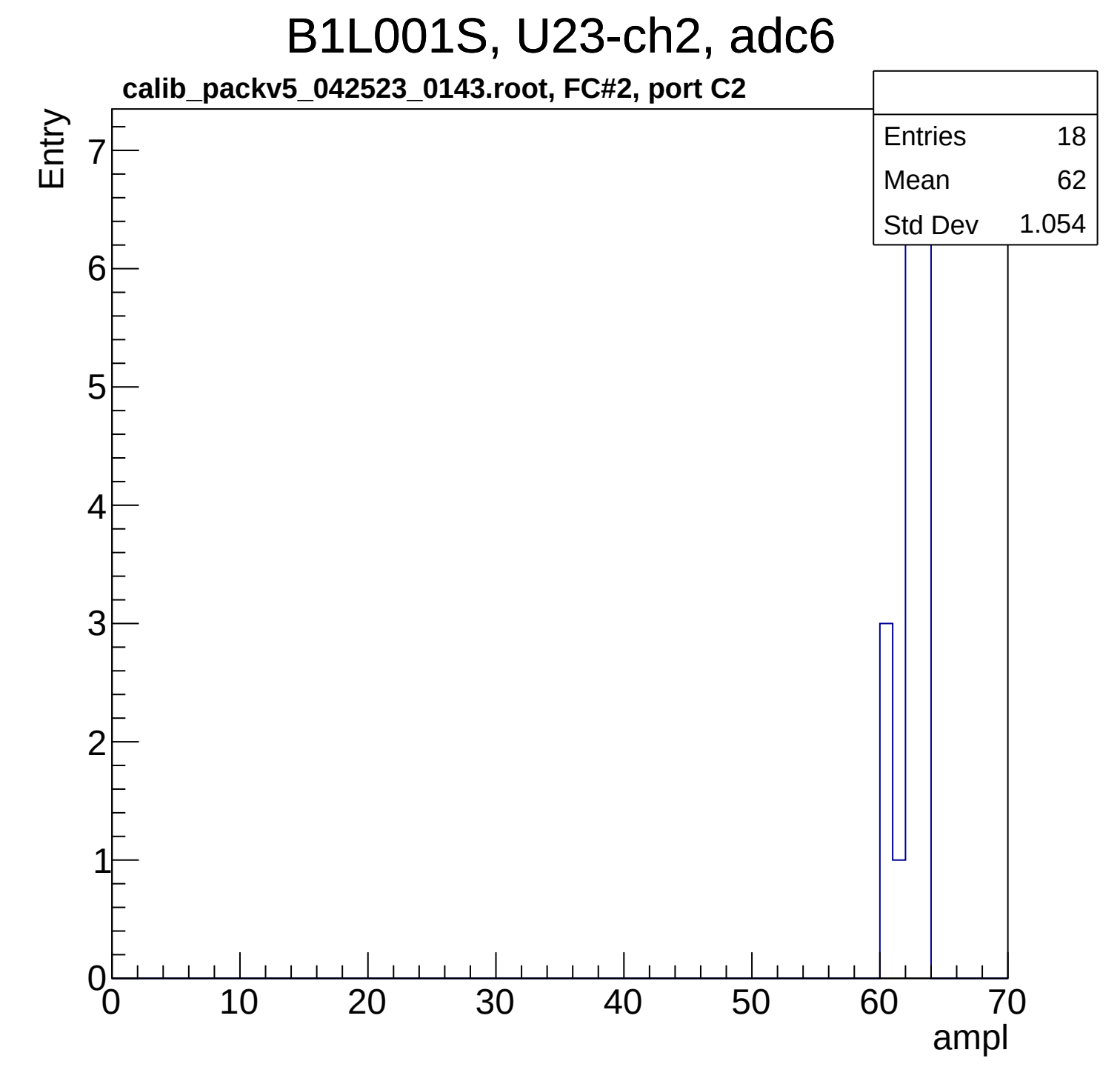
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch2, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 18
Mean 62
Std Dev 1.054
Entry
ampl
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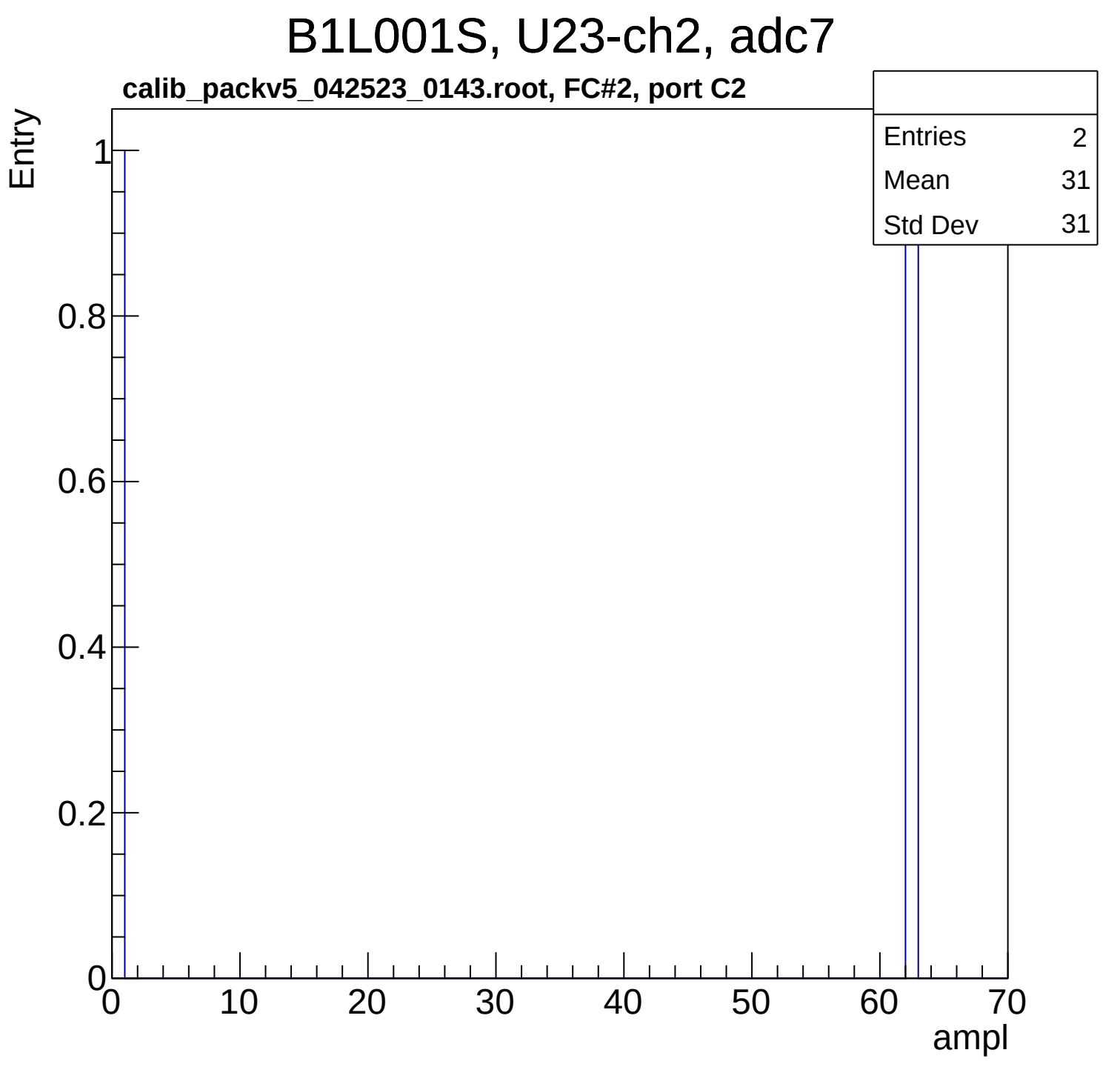
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U23-ch2, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31
Std Dev 31
Entry
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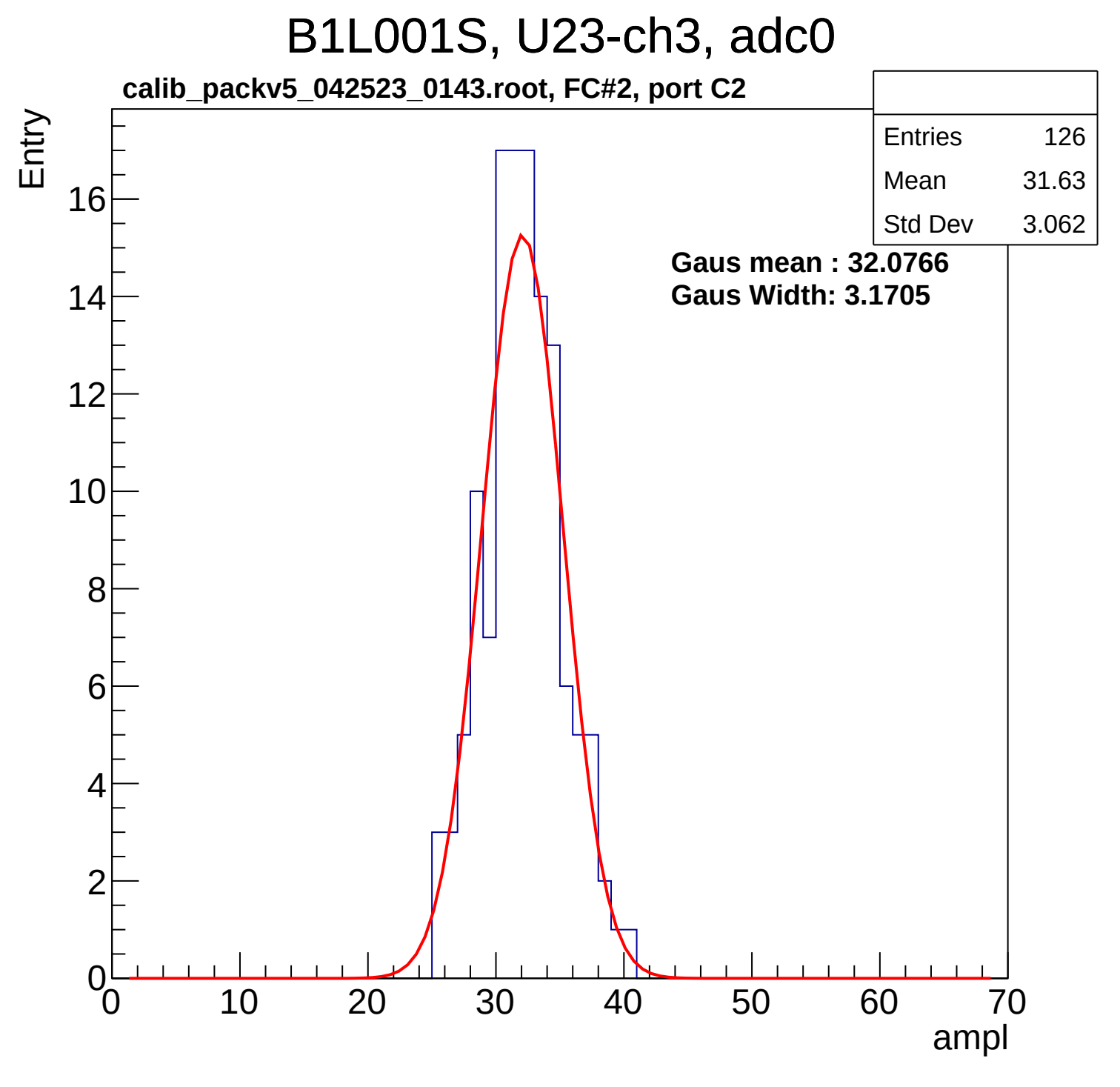
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch3, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 31.63
Std Dev 3.062
Gaus mean : 32.0766
Gaus Width: 3.1705
Entry
ampl
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2
4
6
8
10
12
14
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0
10
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40
50
60
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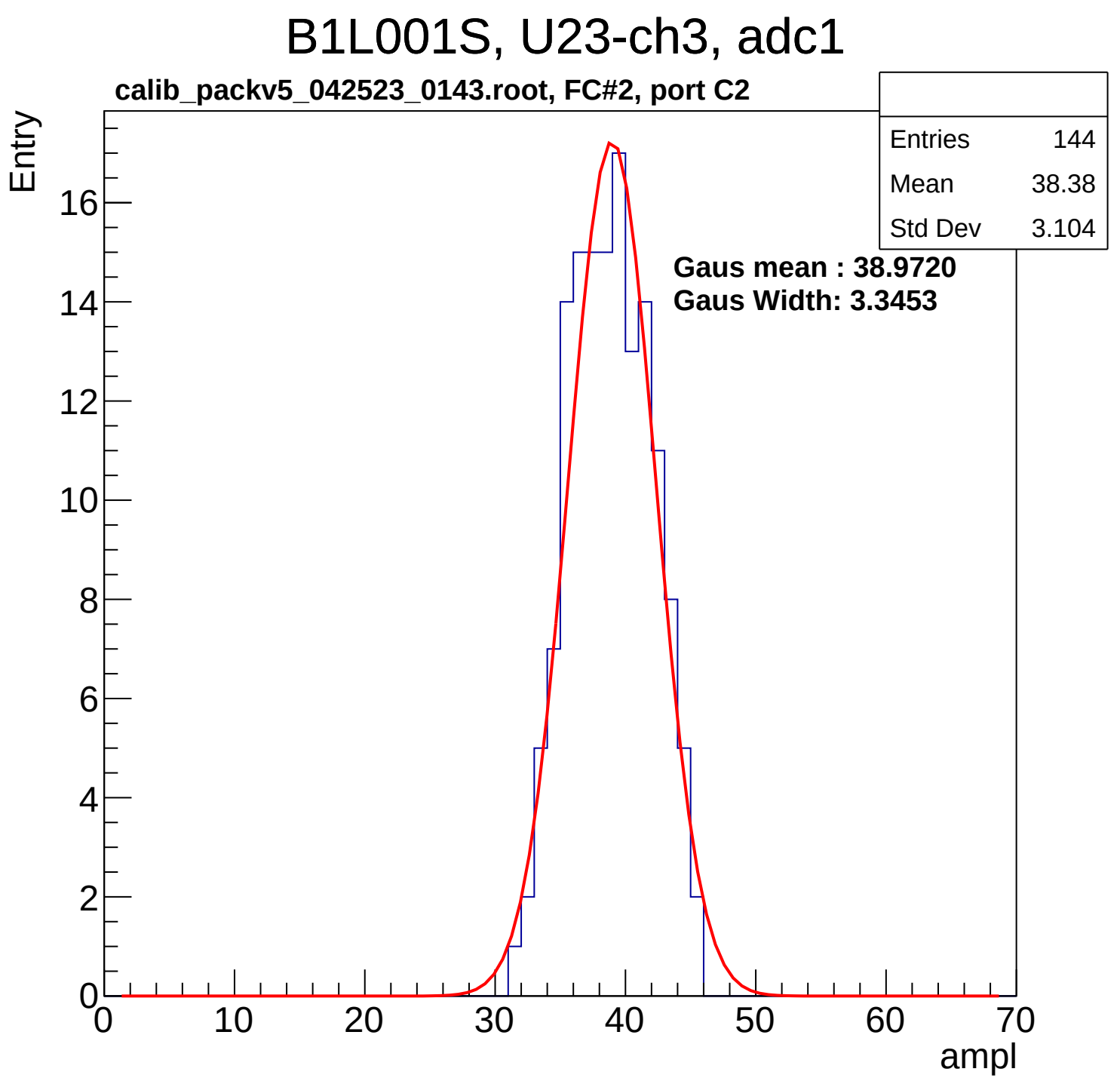
B1L001S, U23-ch3, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 38.38
Std Dev 3.104
Gaus mean : 38.9720
Gaus Width: 3.3453
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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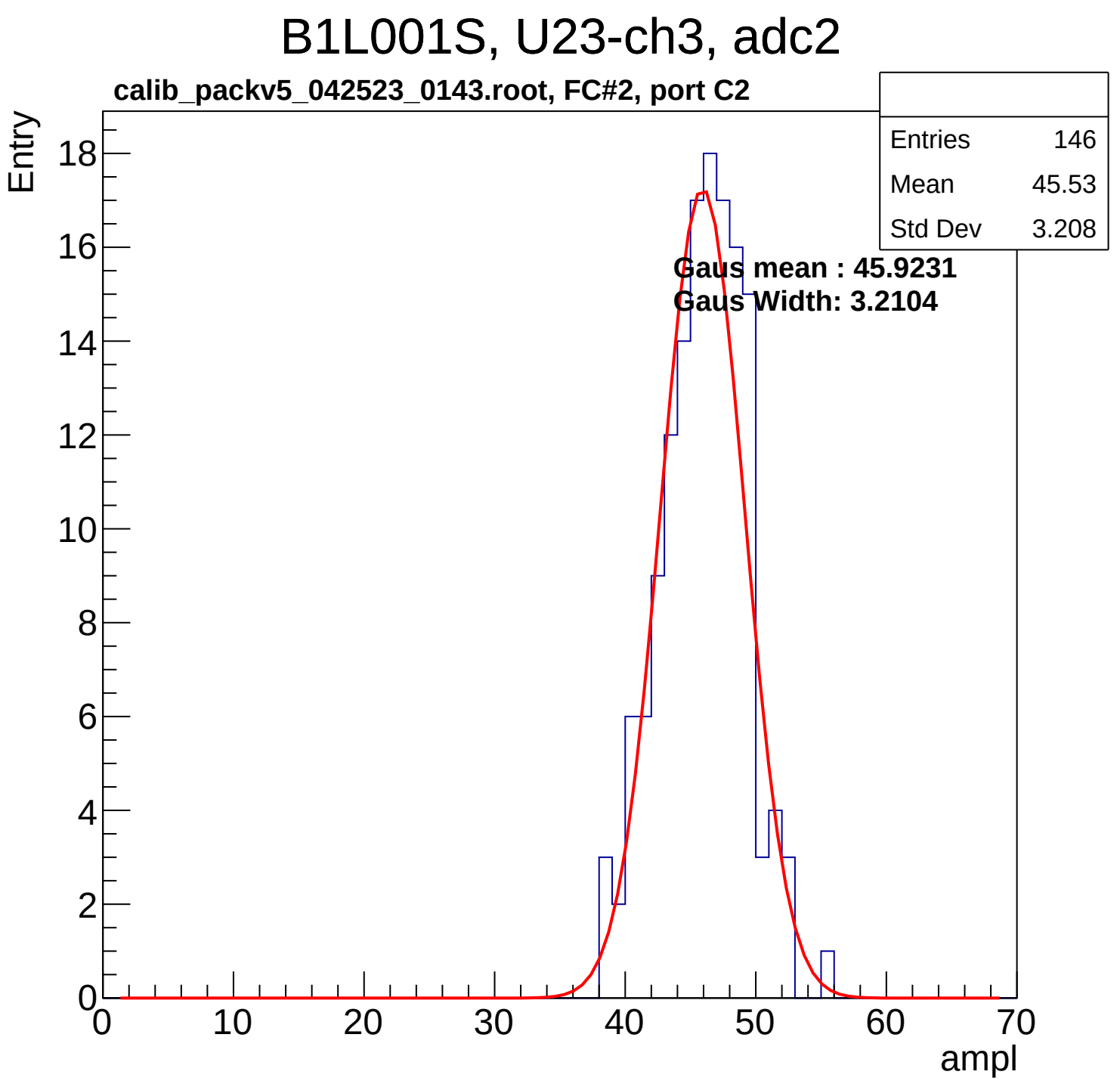
B1L001S, U23-ch3, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 45.53
Std Dev 3.208
Gaus mean : 45.9231
Gaus Width: 3.2104
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
40
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60
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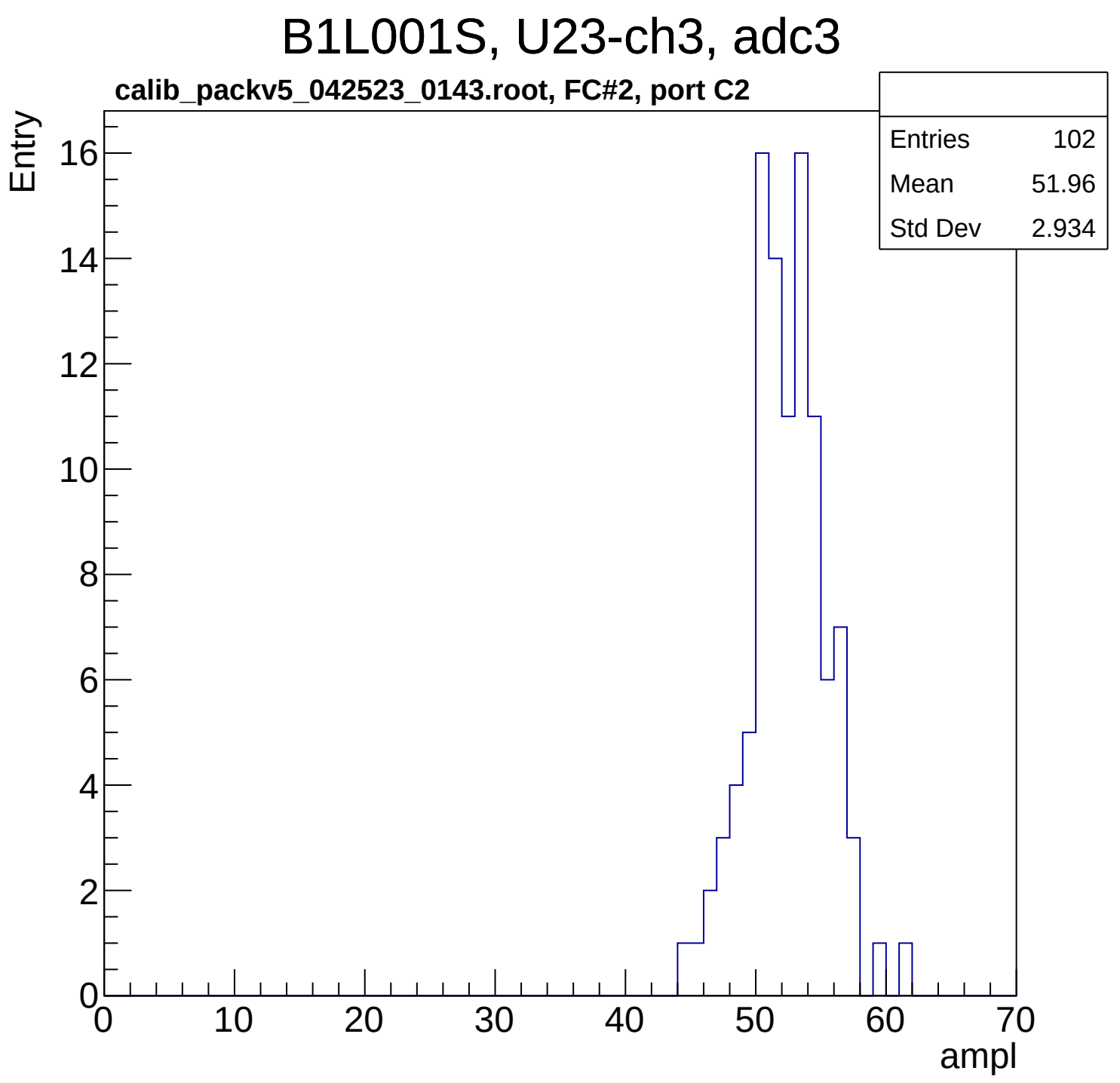
B1L001S, U23-ch3, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 51.96
Std Dev 2.934
Entry
ampl
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6
8
10
12
14
16
0
10
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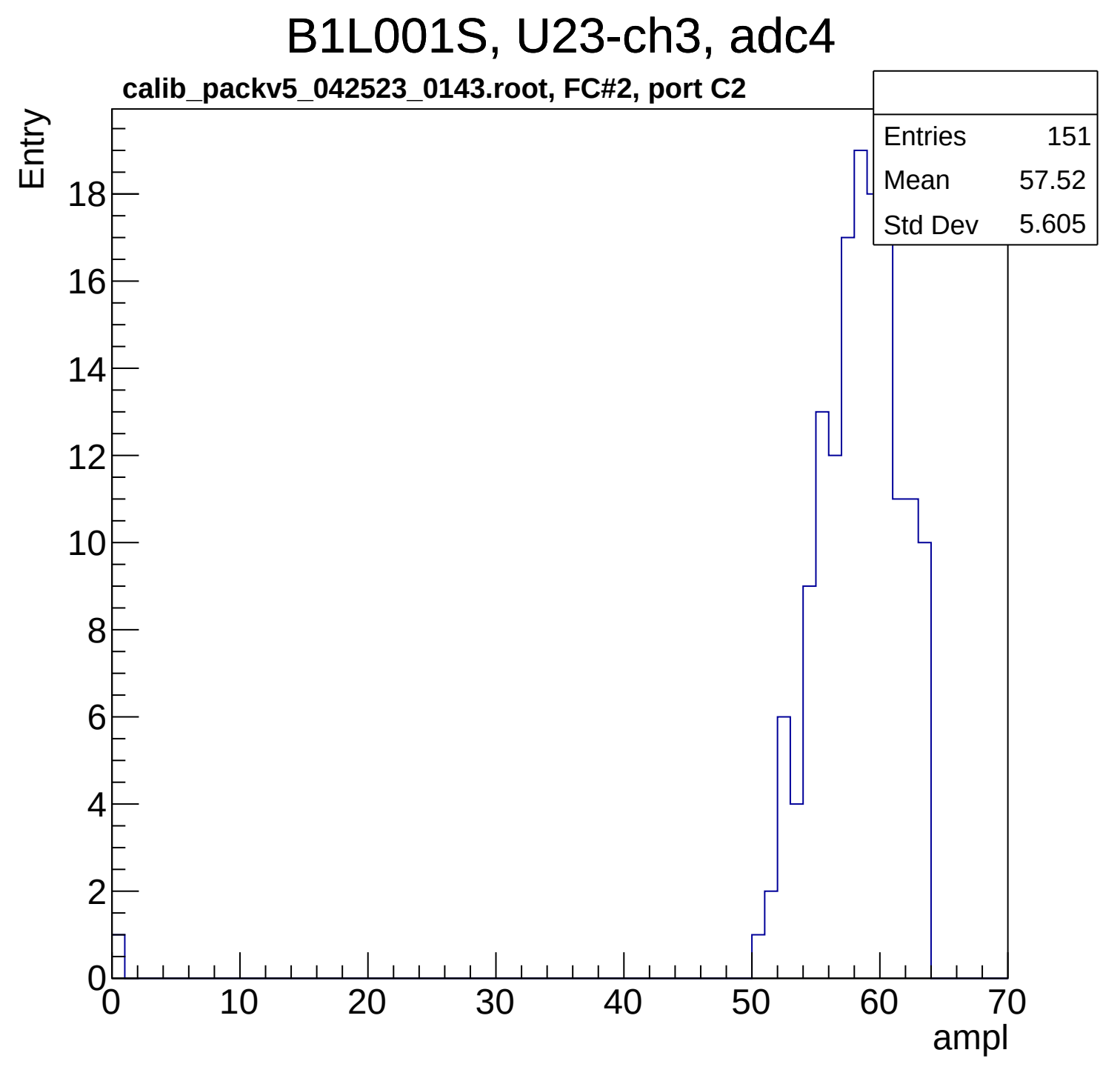
B1L001S, U23-ch3, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 57.52
Std Dev 5.605
Entry
ampl
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6
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12
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16
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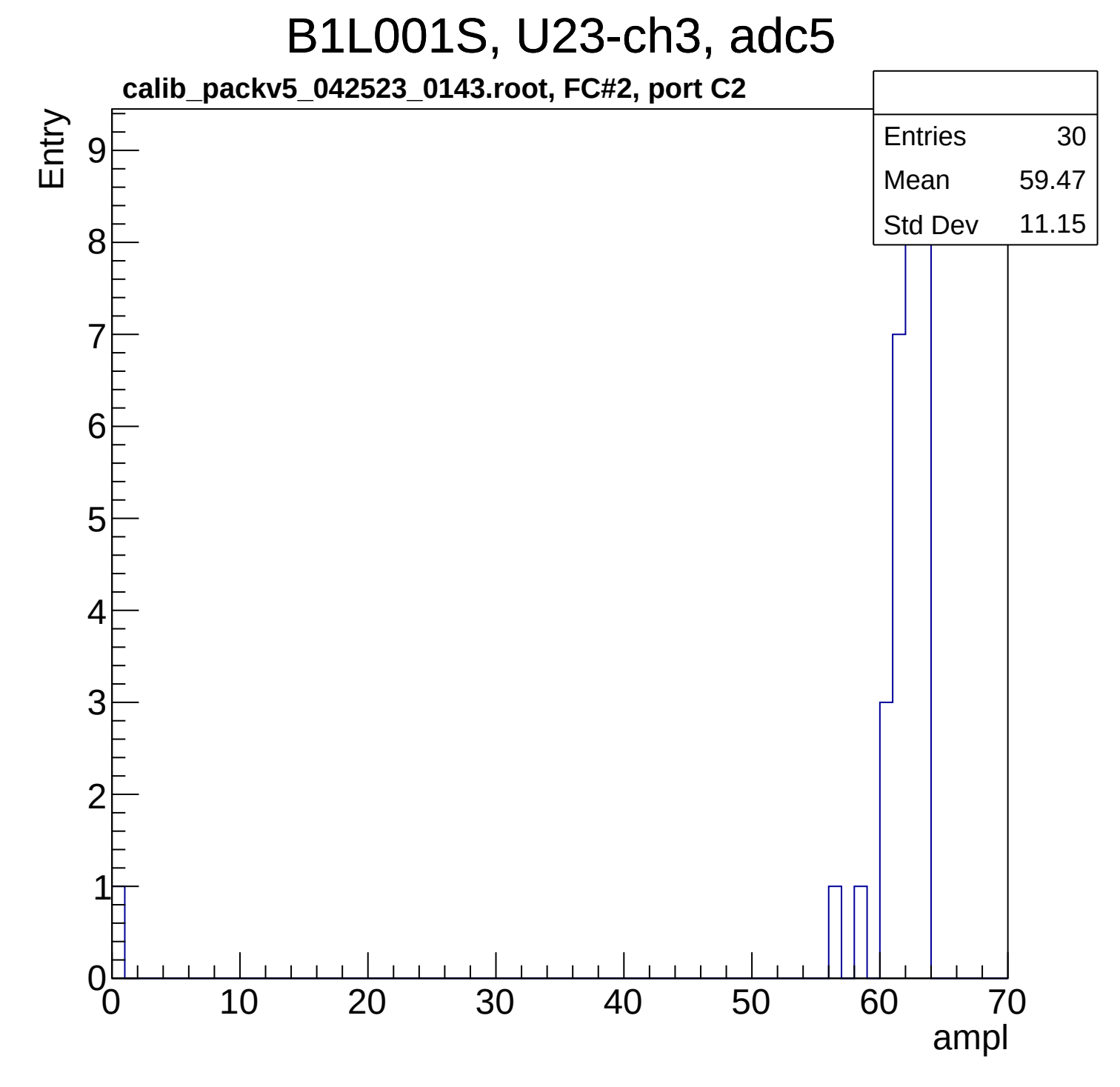
B1L001S, U23-ch3, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 30
Mean 59.47
Std Dev 11.15
Entry
ampl
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2
3
4
5
6
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8
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0
10
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40
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60
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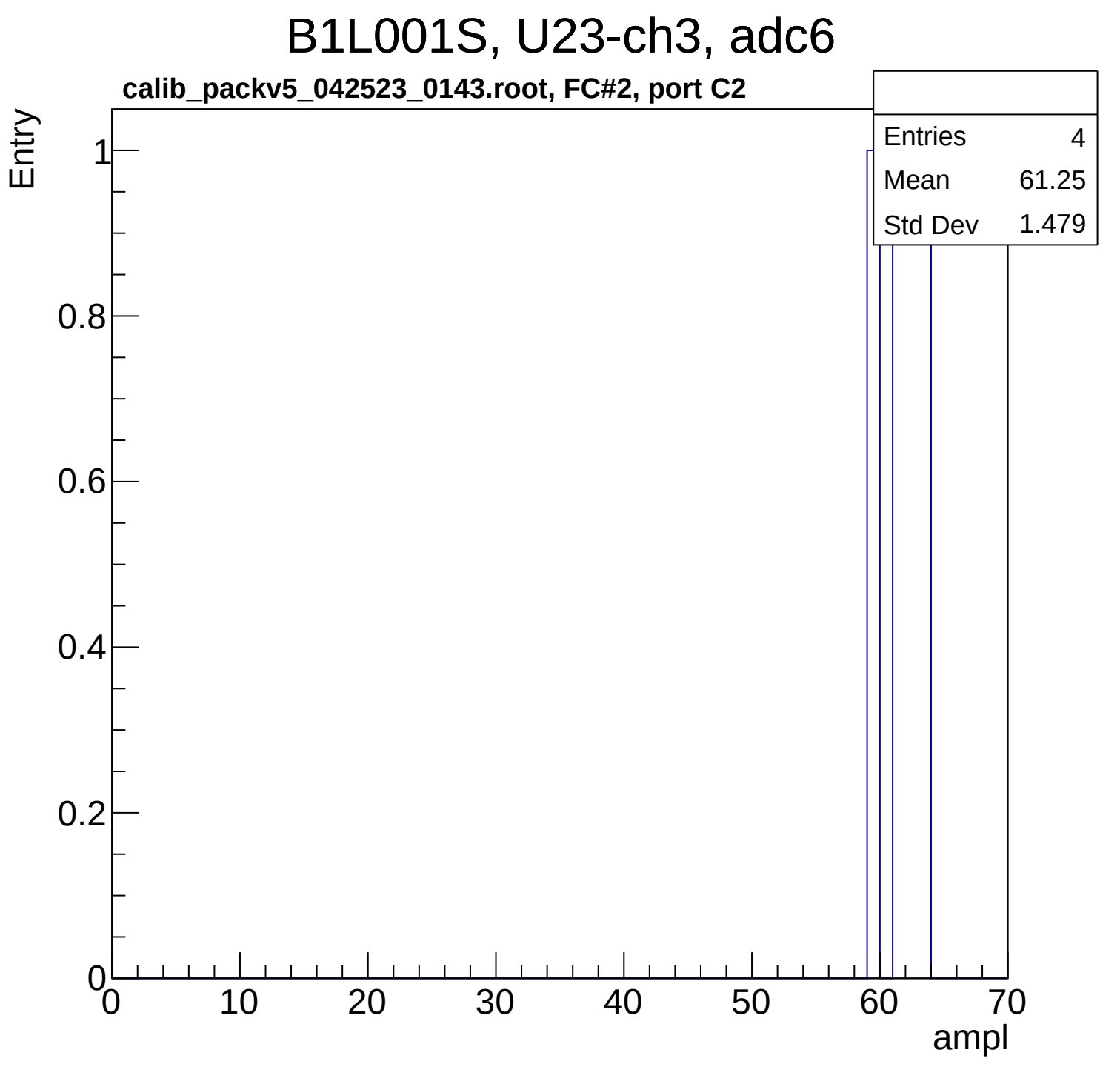
B1L001S, U23-ch3, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.25
Std Dev 1.479
Entry
ampl
0
0.2
0.4
0.6
0.8
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0
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60
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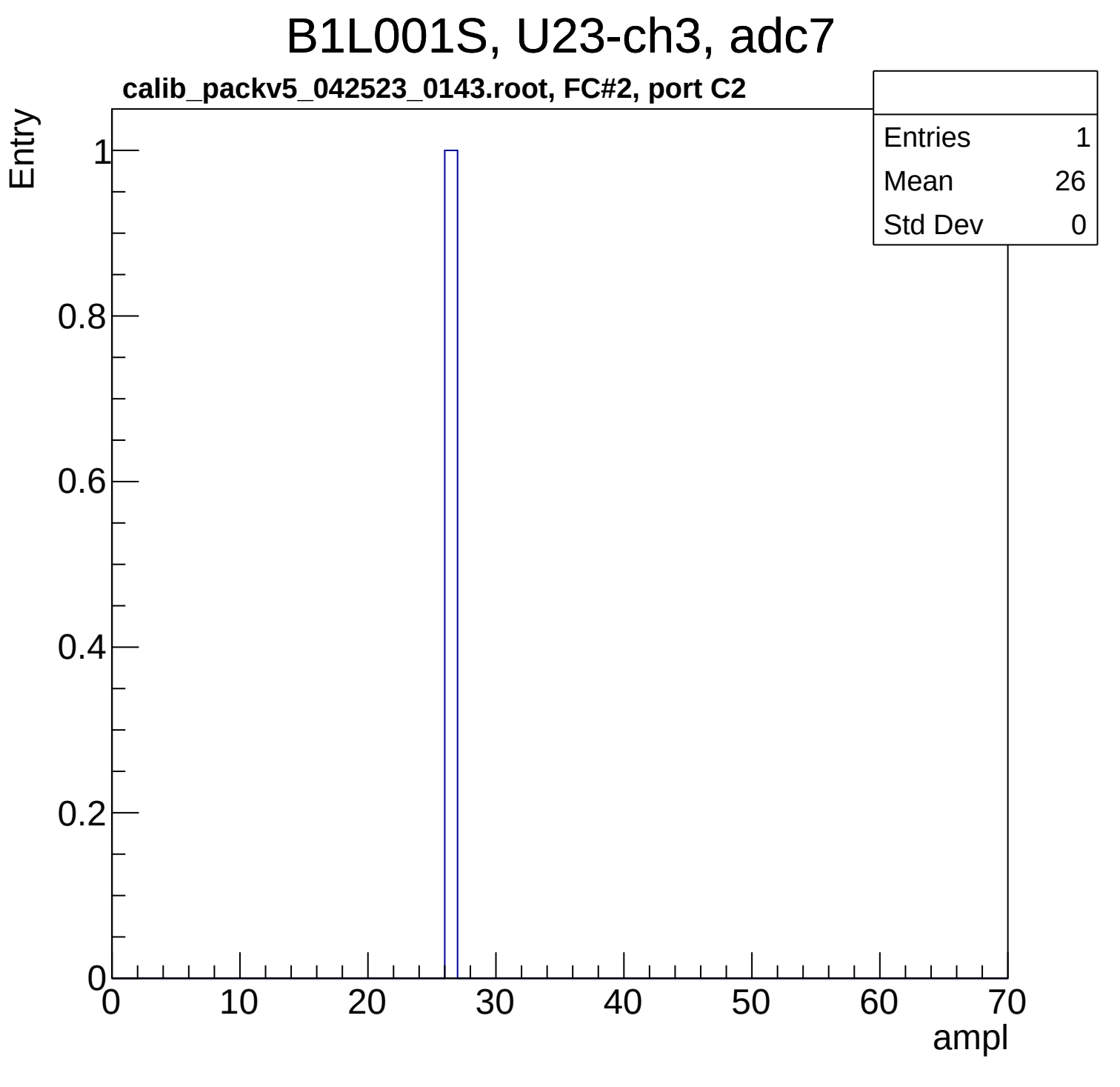
B1L001S, U23-ch3, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 26
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
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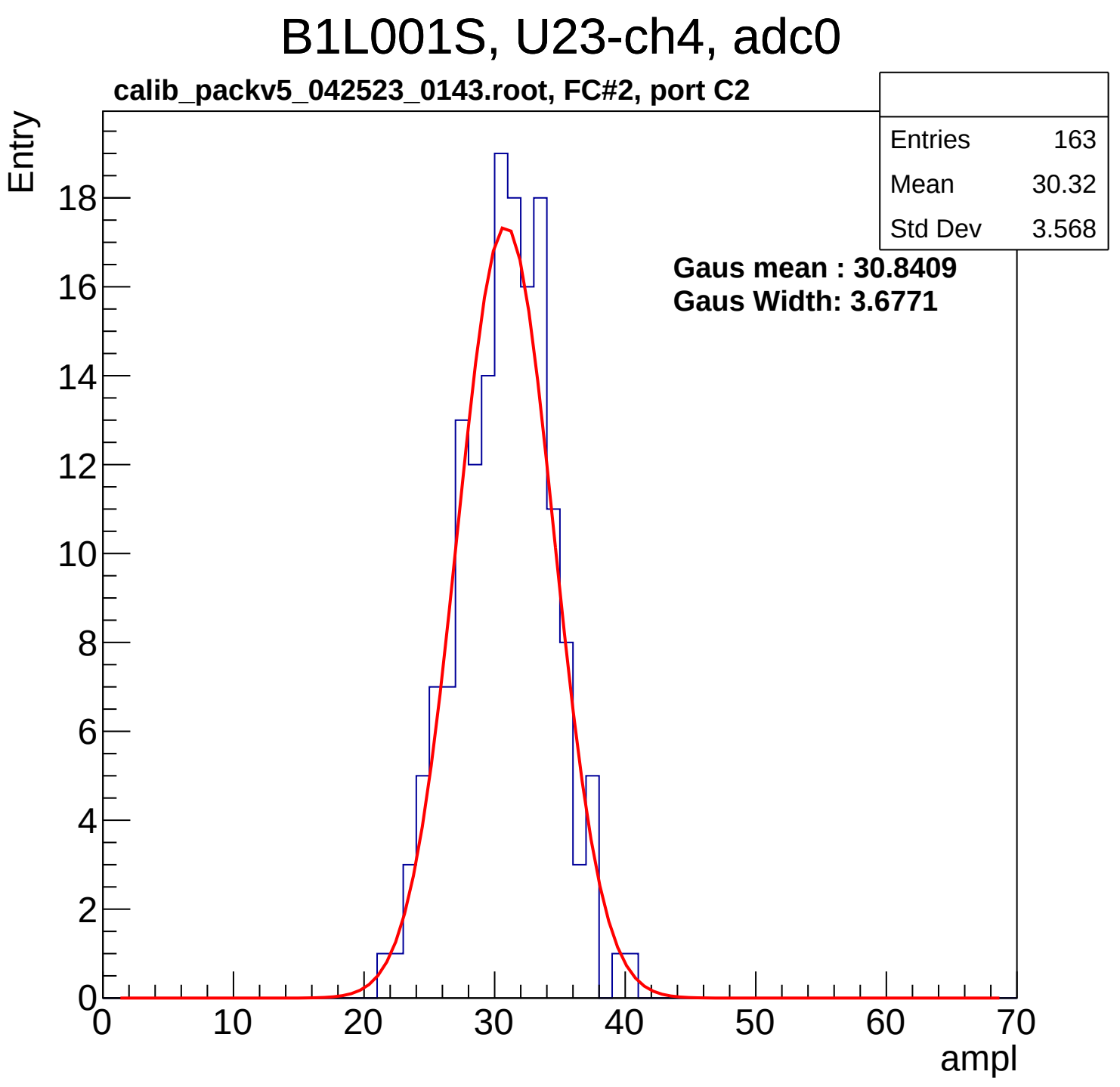
B1L001S, U23-ch4, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 30.32
Std Dev 3.568
Gaus mean : 30.8409
Gaus Width: 3.6771
Entry
ampl
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2
4
6
8
10
12
14
16
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0
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20
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40
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60
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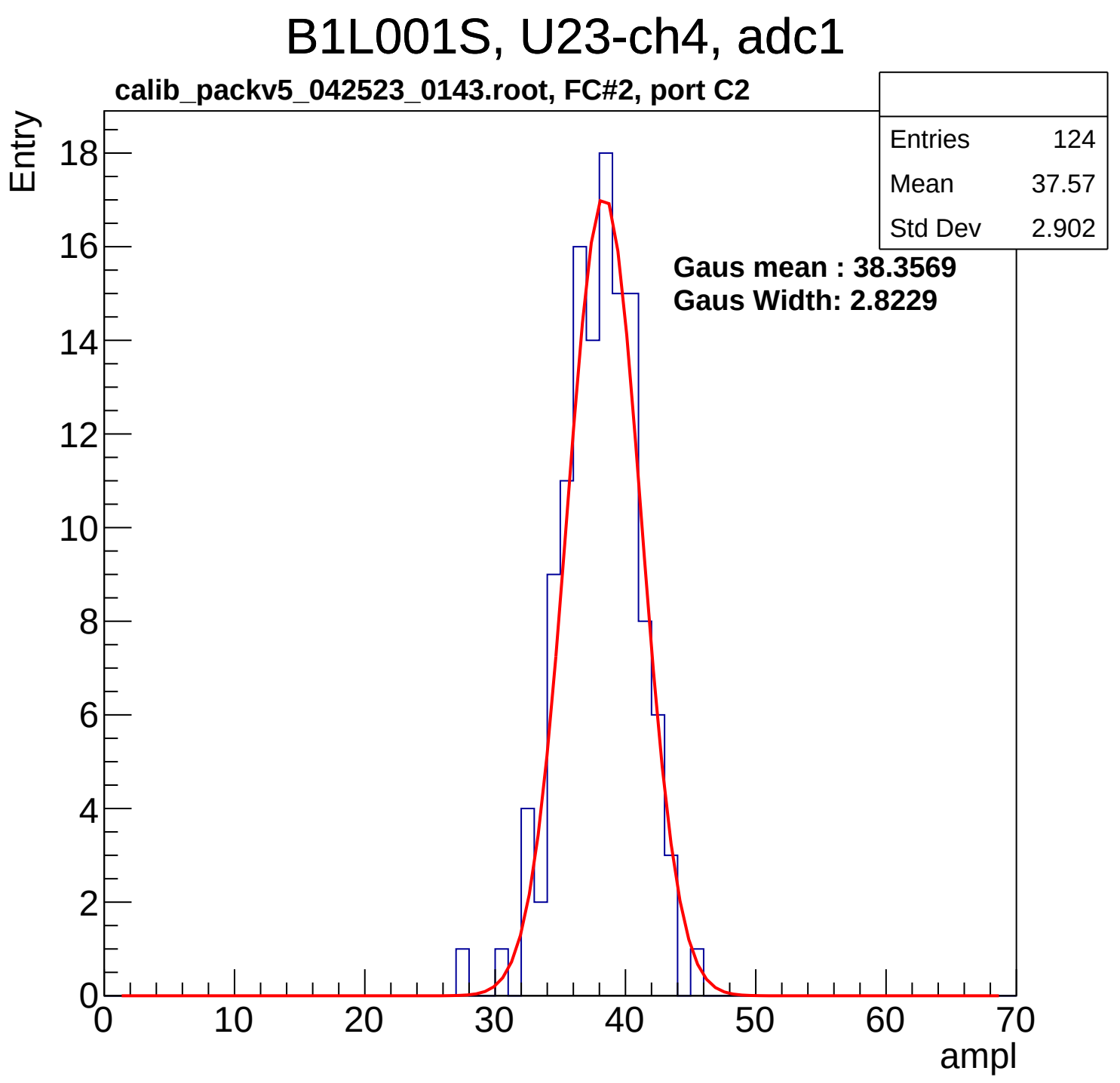
B1L001S, U23-ch4, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 37.57
Std Dev 2.902
Gaus mean : 38.3569
Gaus Width: 2.8229
Entry
ampl
0
2
4
6
8
10
12
14
16
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0
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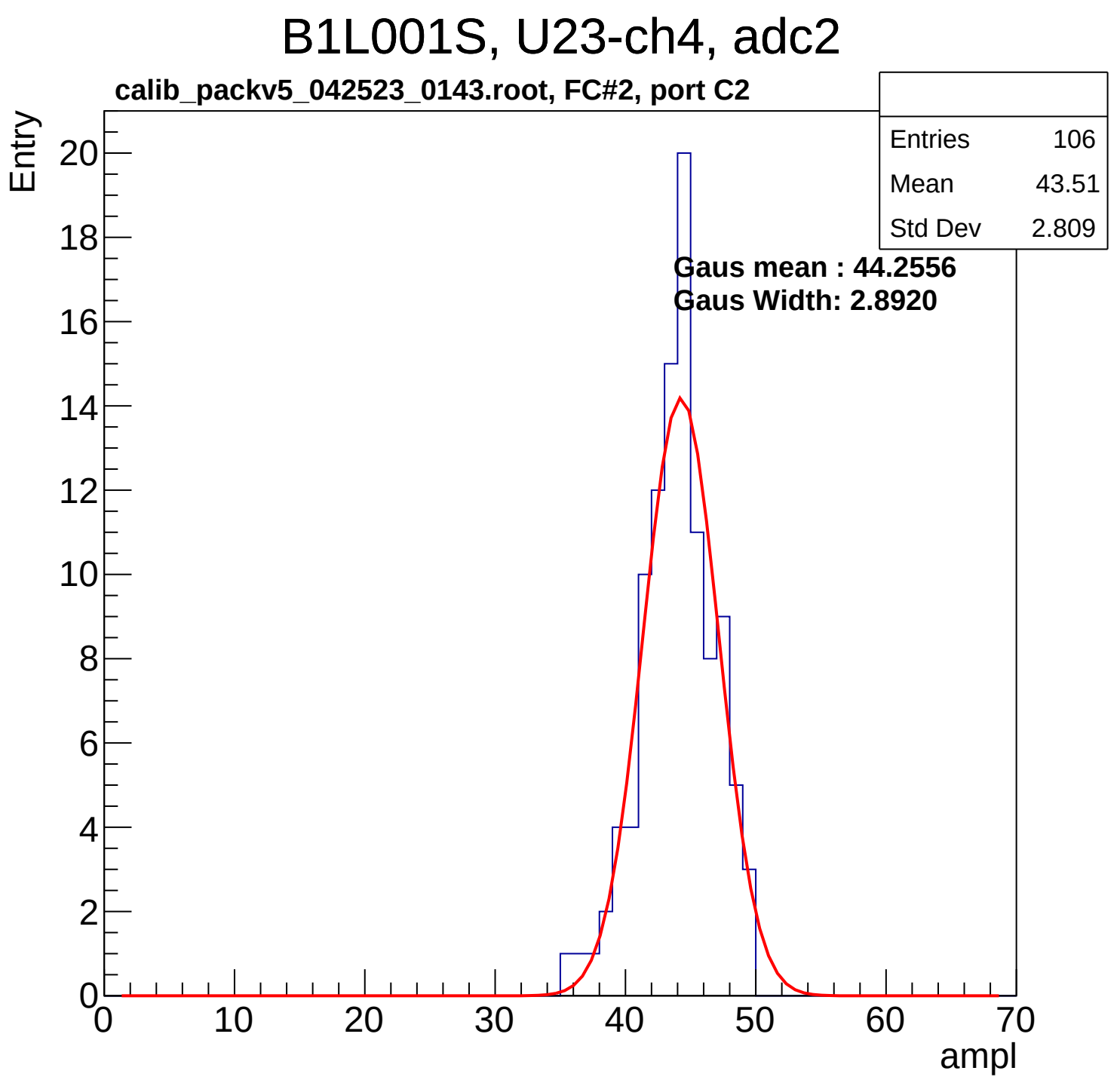
B1L001S, U23-ch4, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 43.51
Std Dev 2.809
Gaus mean : 44.2556
Gaus Width: 2.8920
Entry
ampl
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4
6
8
10
12
14
16
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0
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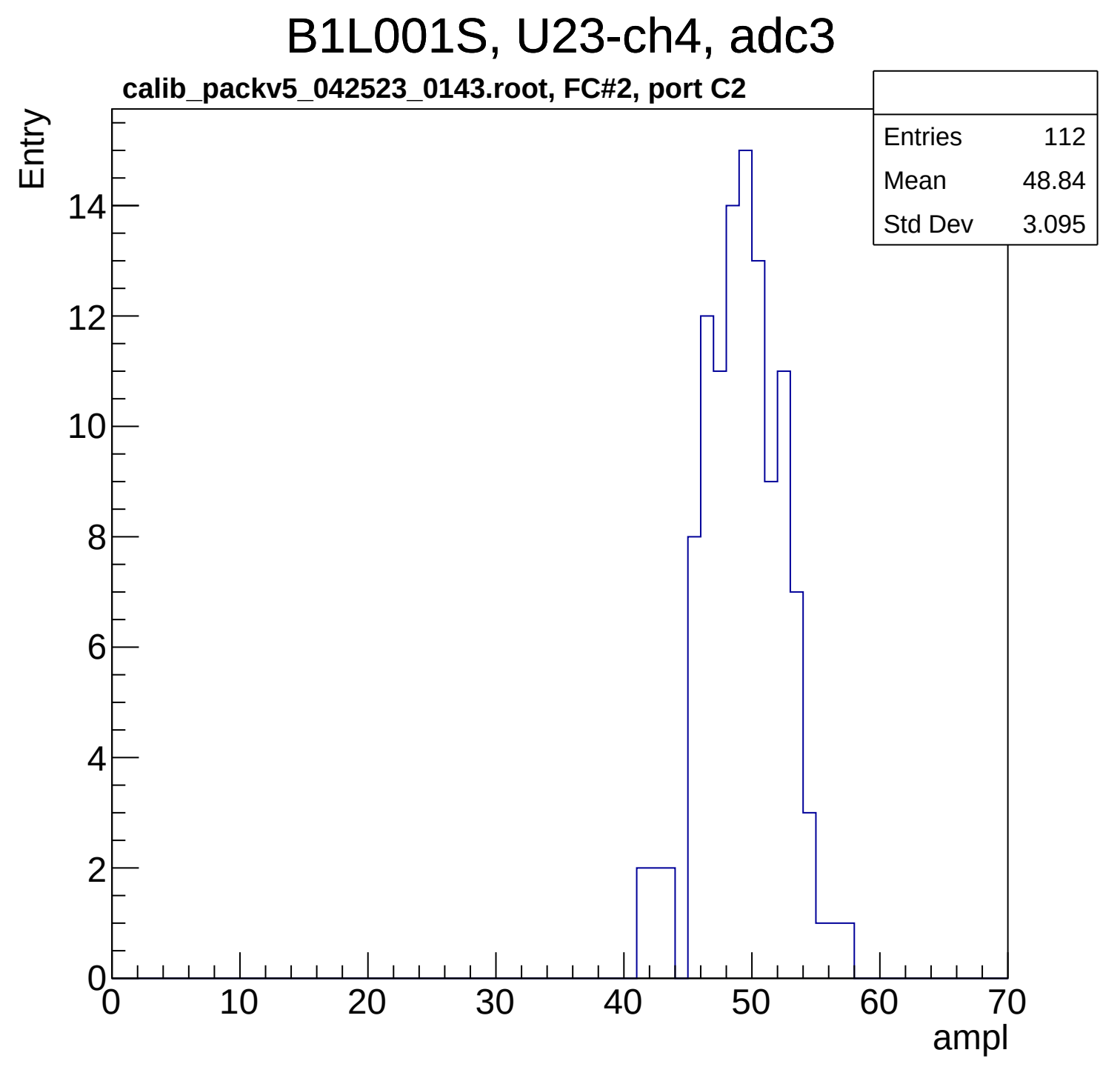
B1L001S, U23-ch4, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 48.84
Std Dev 3.095
Entry
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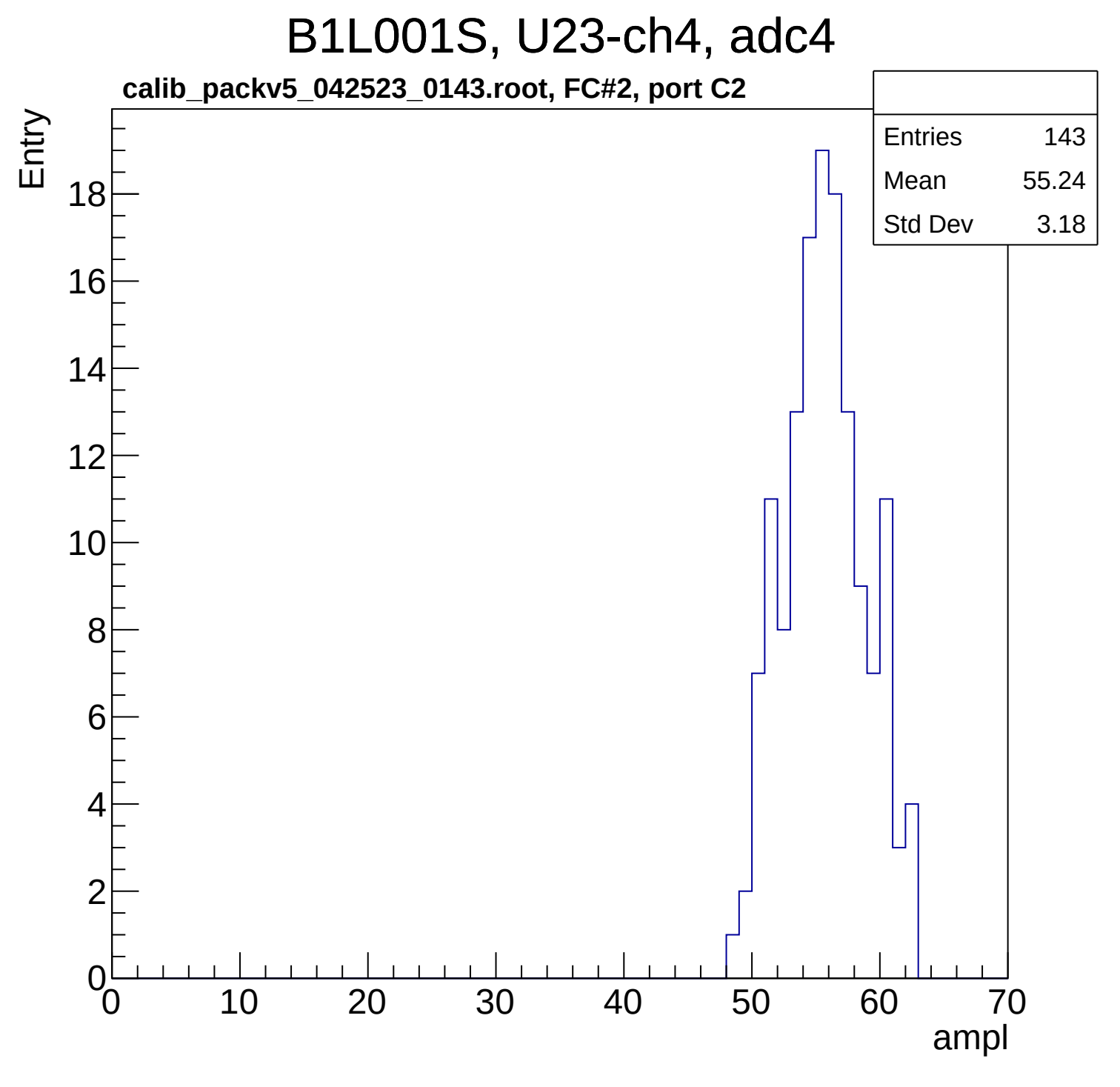
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch4, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 55.24
Std Dev 3.18
Entry
ampl
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2
4
6
8
10
12
14
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0
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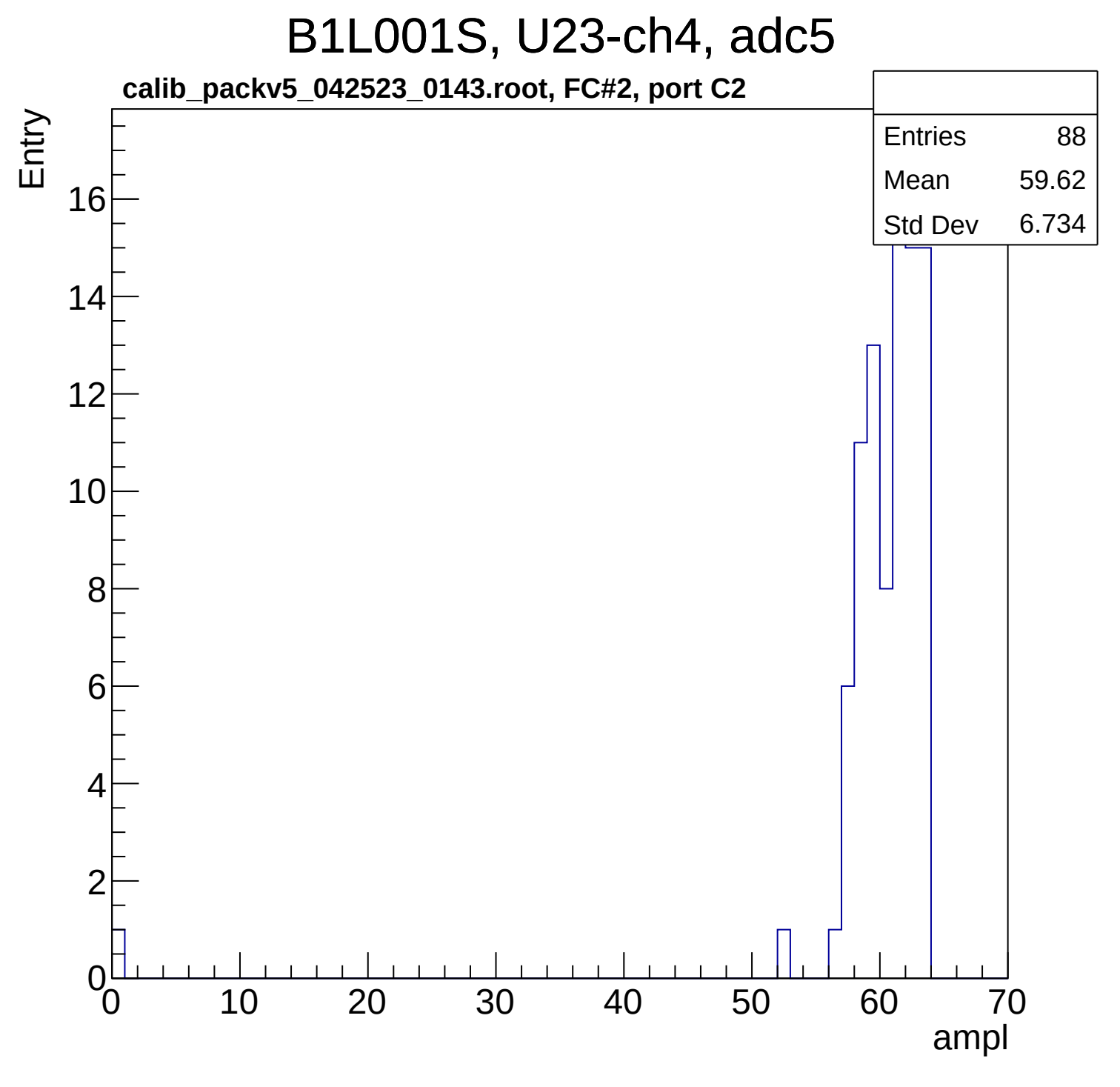
B1L001S, U23-ch4, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.62
Std Dev 6.734
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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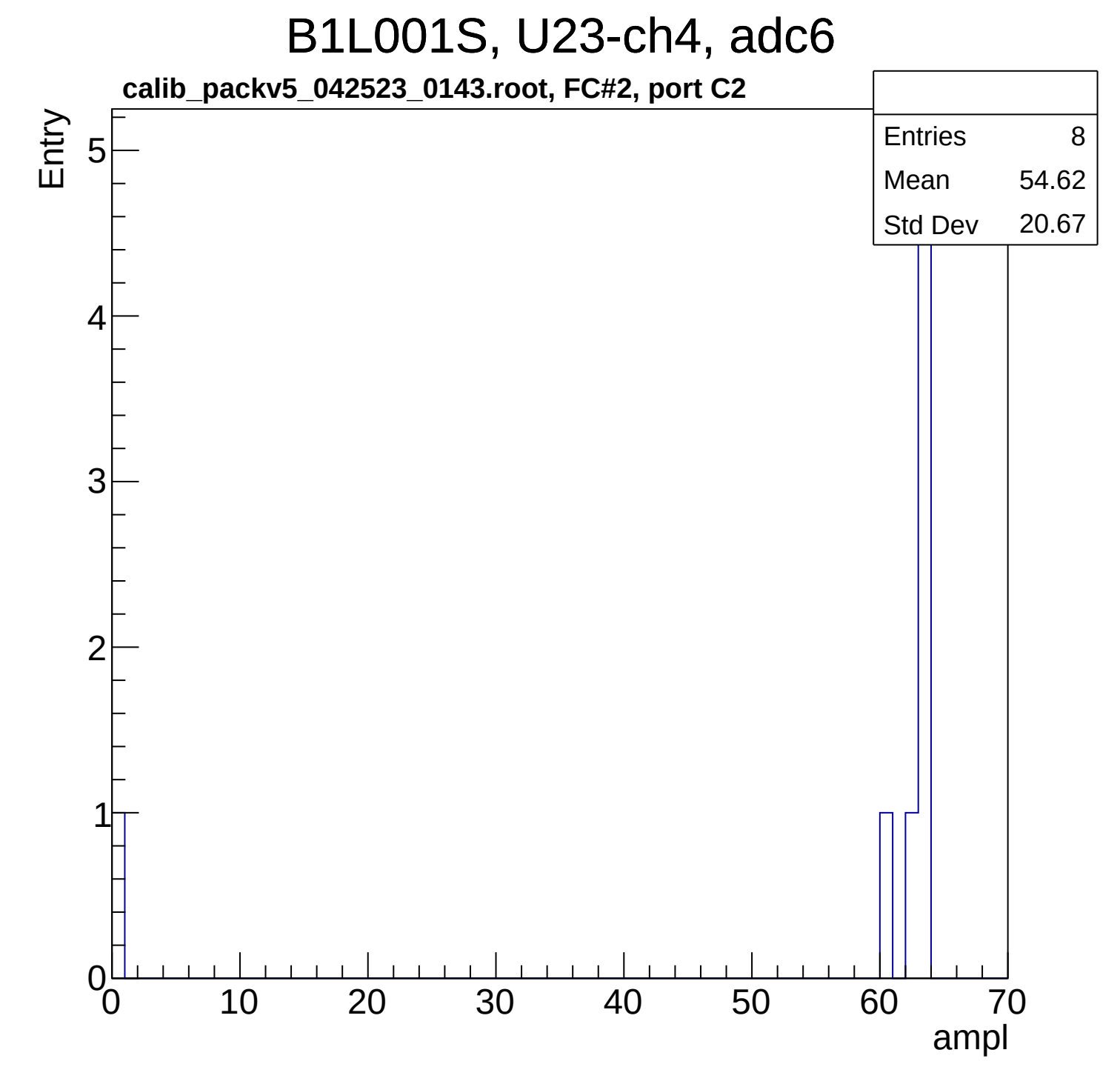
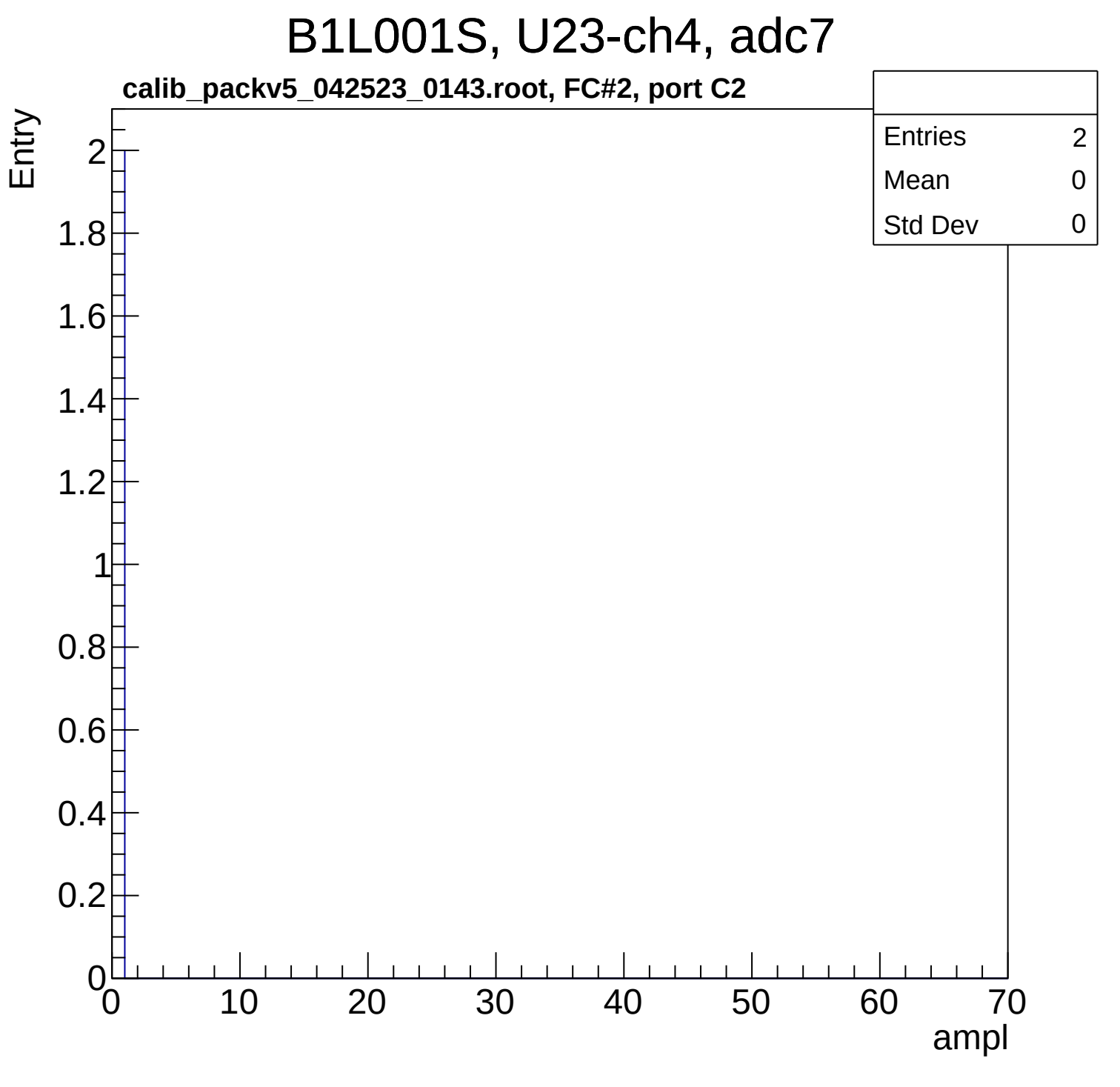
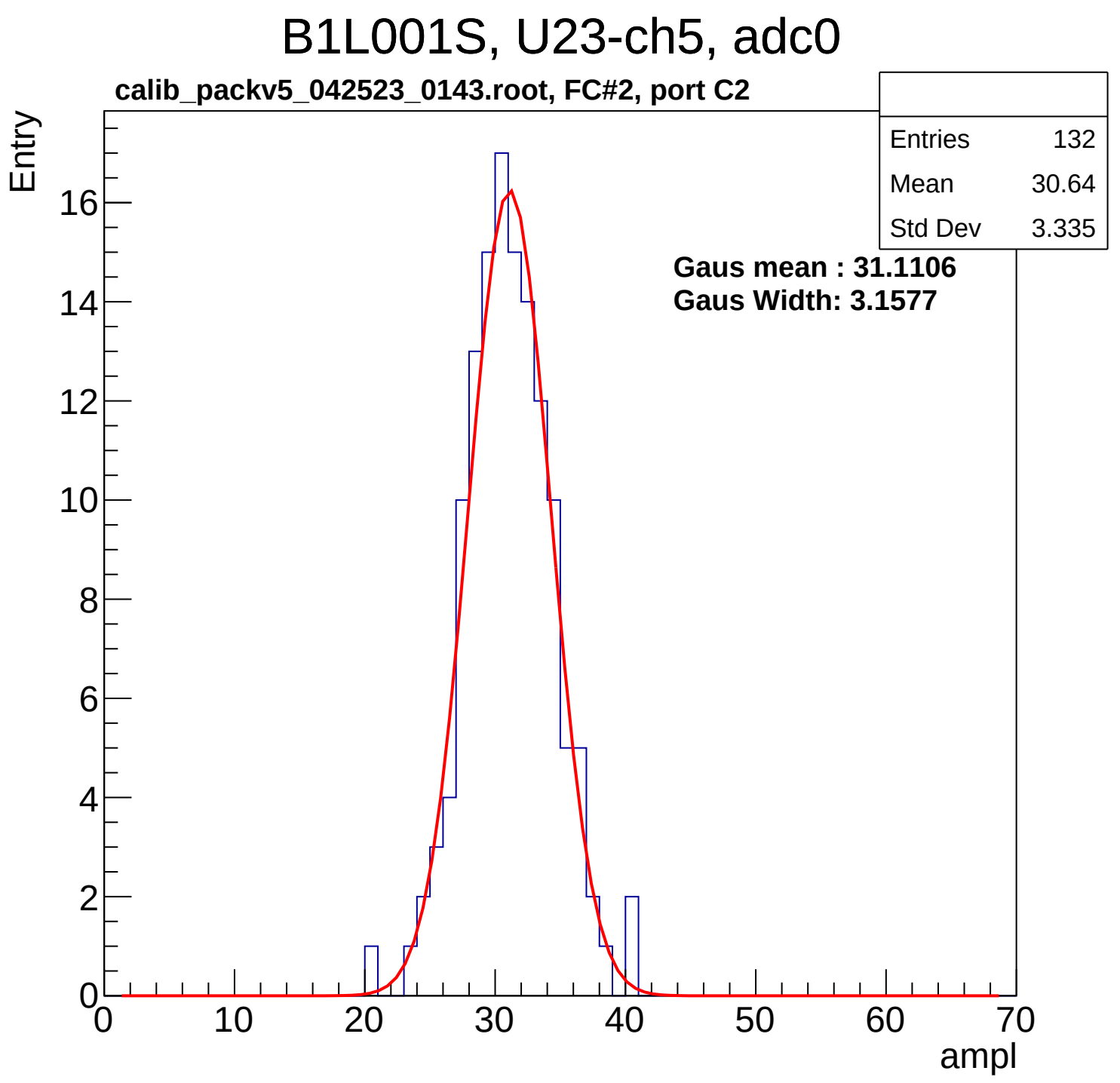

B1L001S, U23-ch4, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.62
Std Dev 20.67
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L001S, U23-ch4, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch5, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 30.64
Std Dev 3.335
Gaus mean : 31.1106
Gaus Width: 3.1577
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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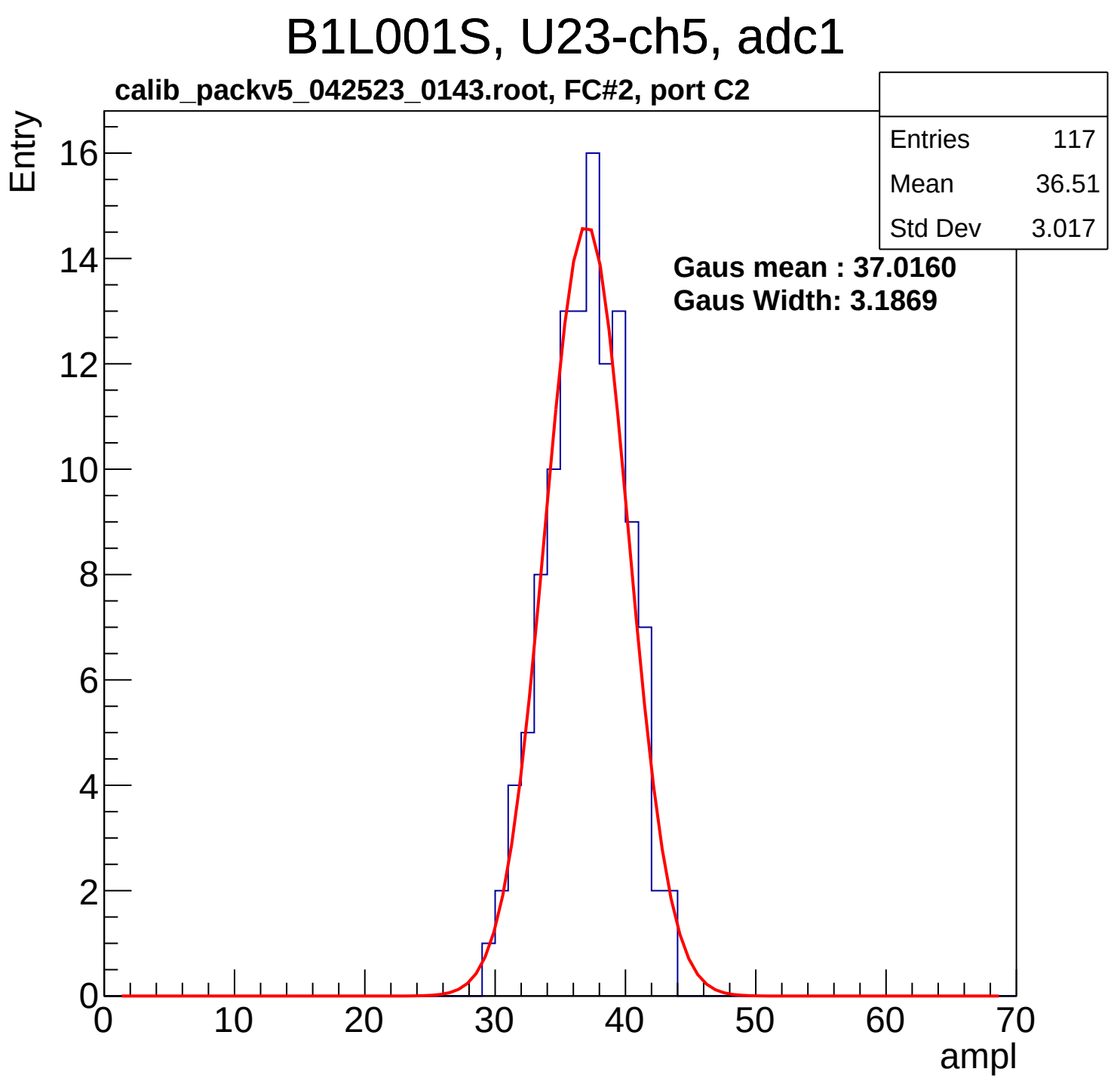
B1L001S, U23-ch5, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 36.51
Std Dev 3.017
Gaus mean : 37.0160
Gaus Width: 3.1869
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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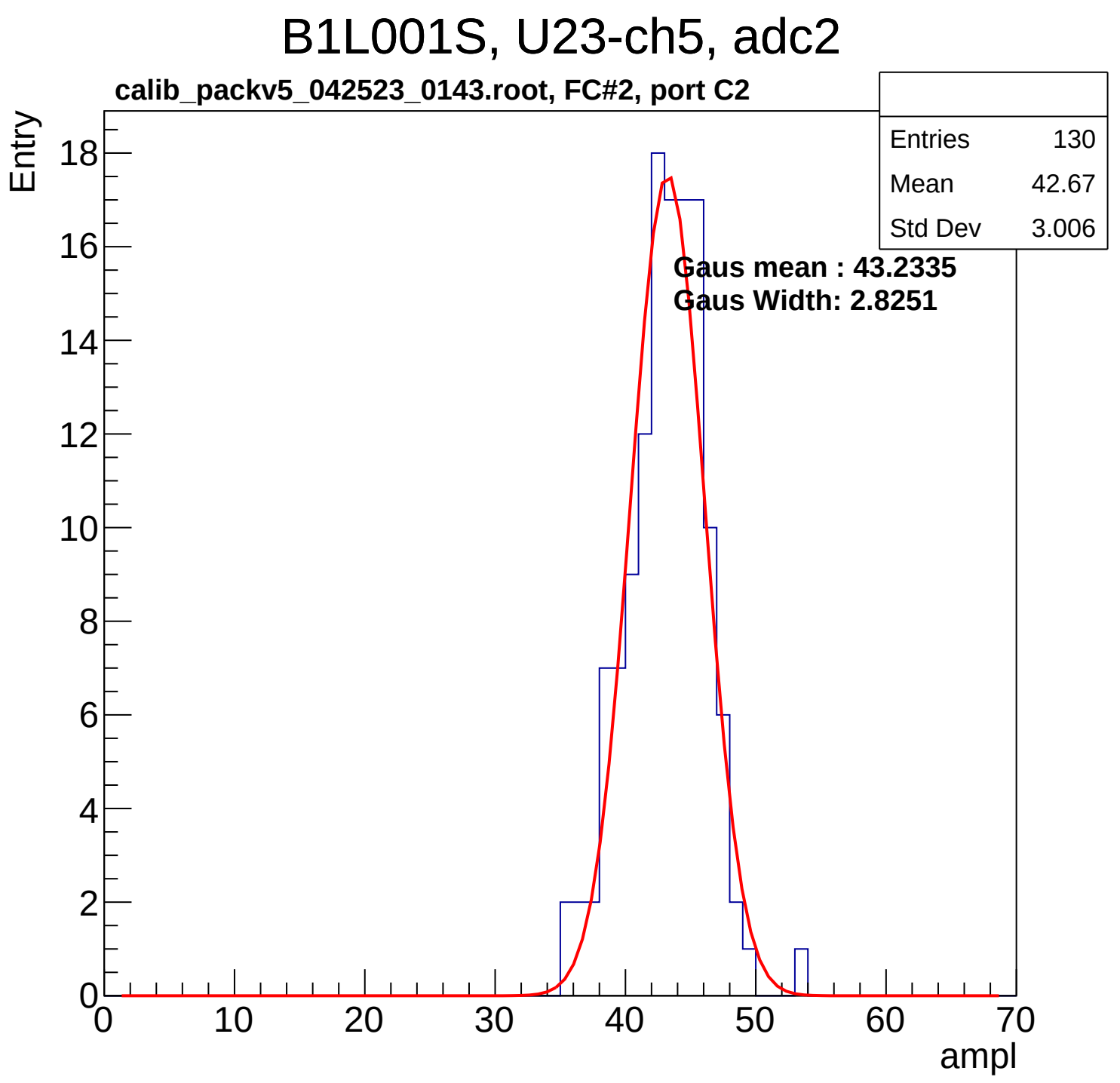
B1L001S, U23-ch5, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.67
Std Dev 3.006
Gaus mean : 43.2335
Gaus Width: 2.8251
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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60
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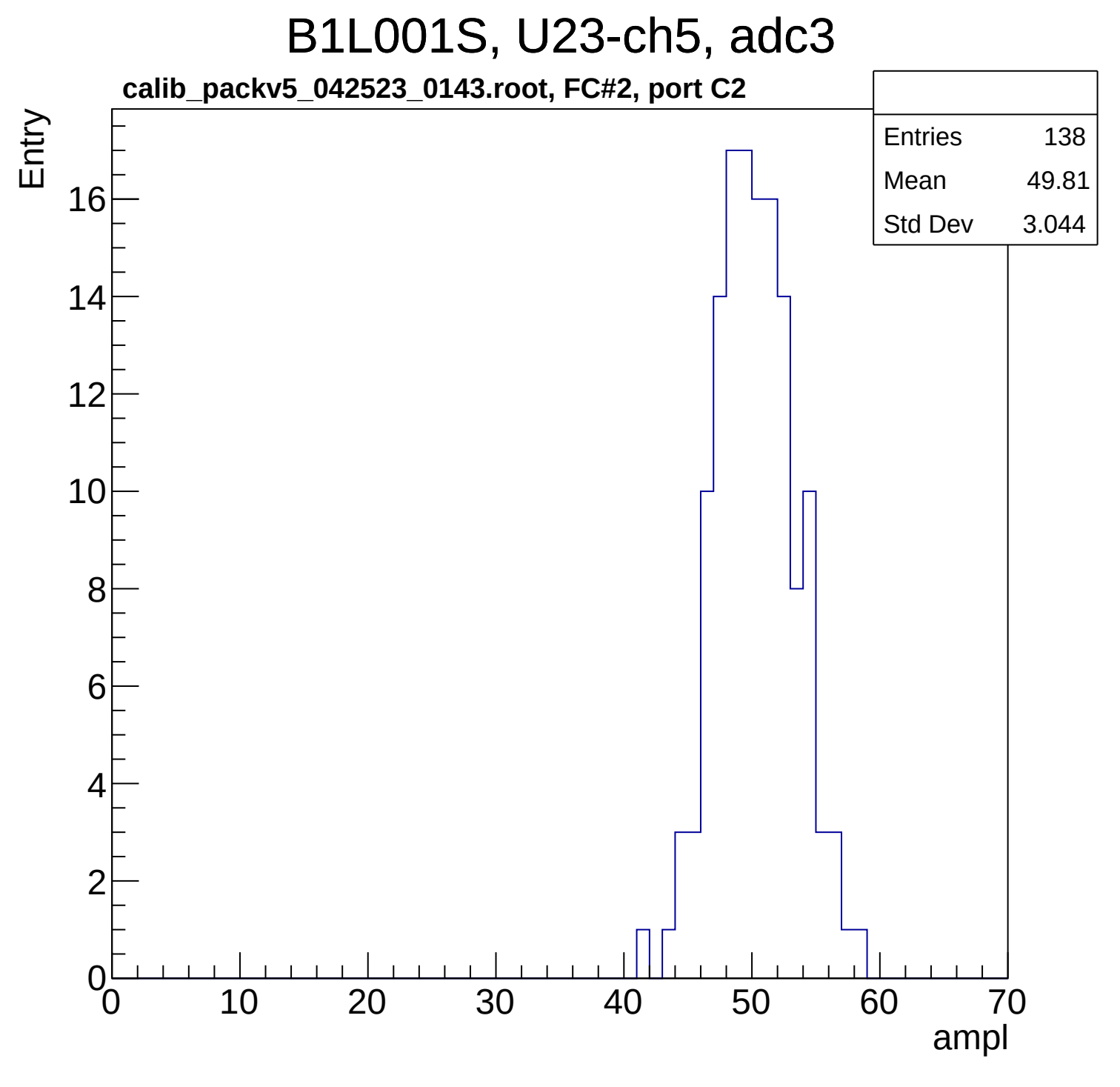
B1L001S, U23-ch5, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 49.81
Std Dev 3.044
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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60
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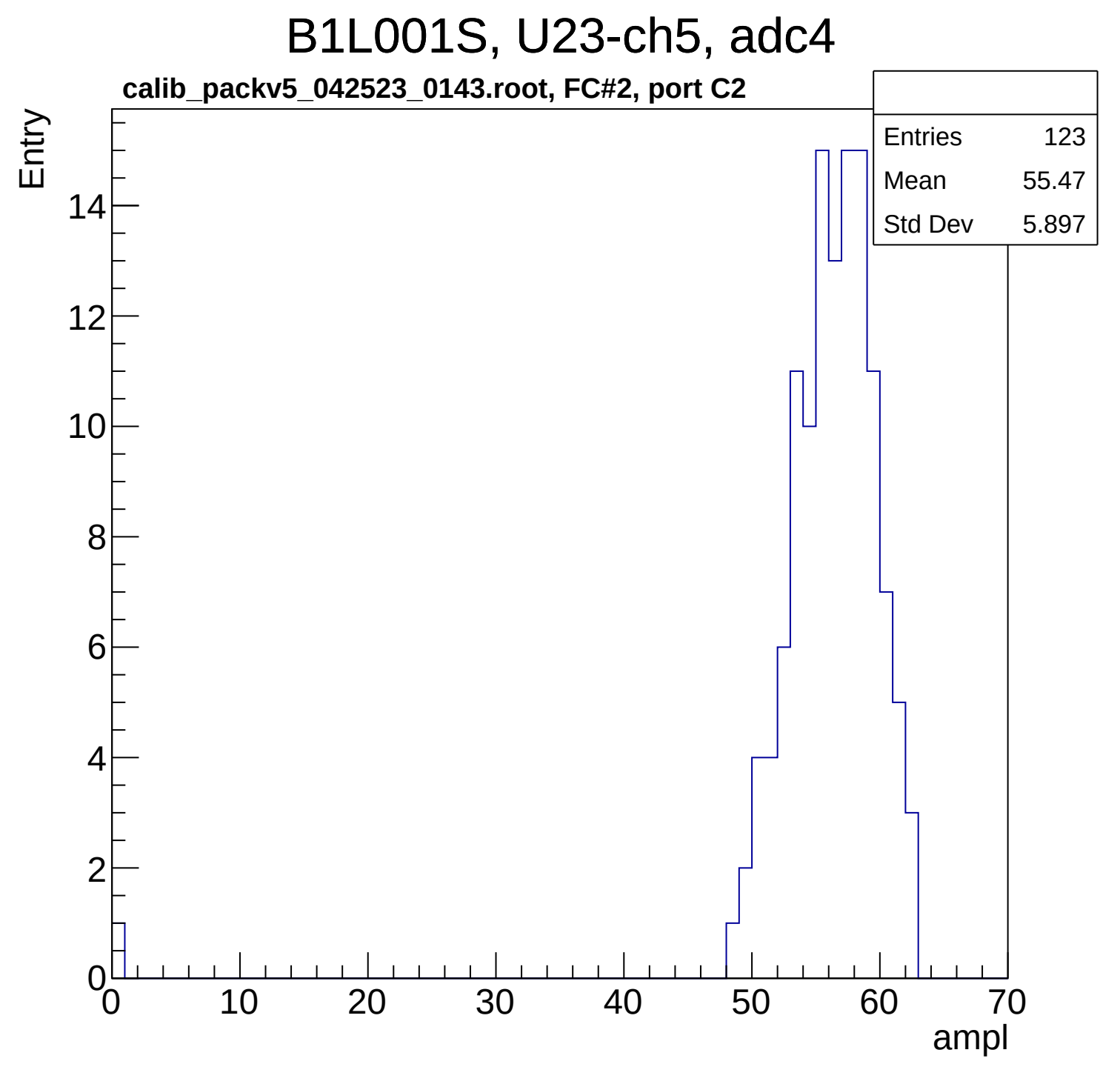
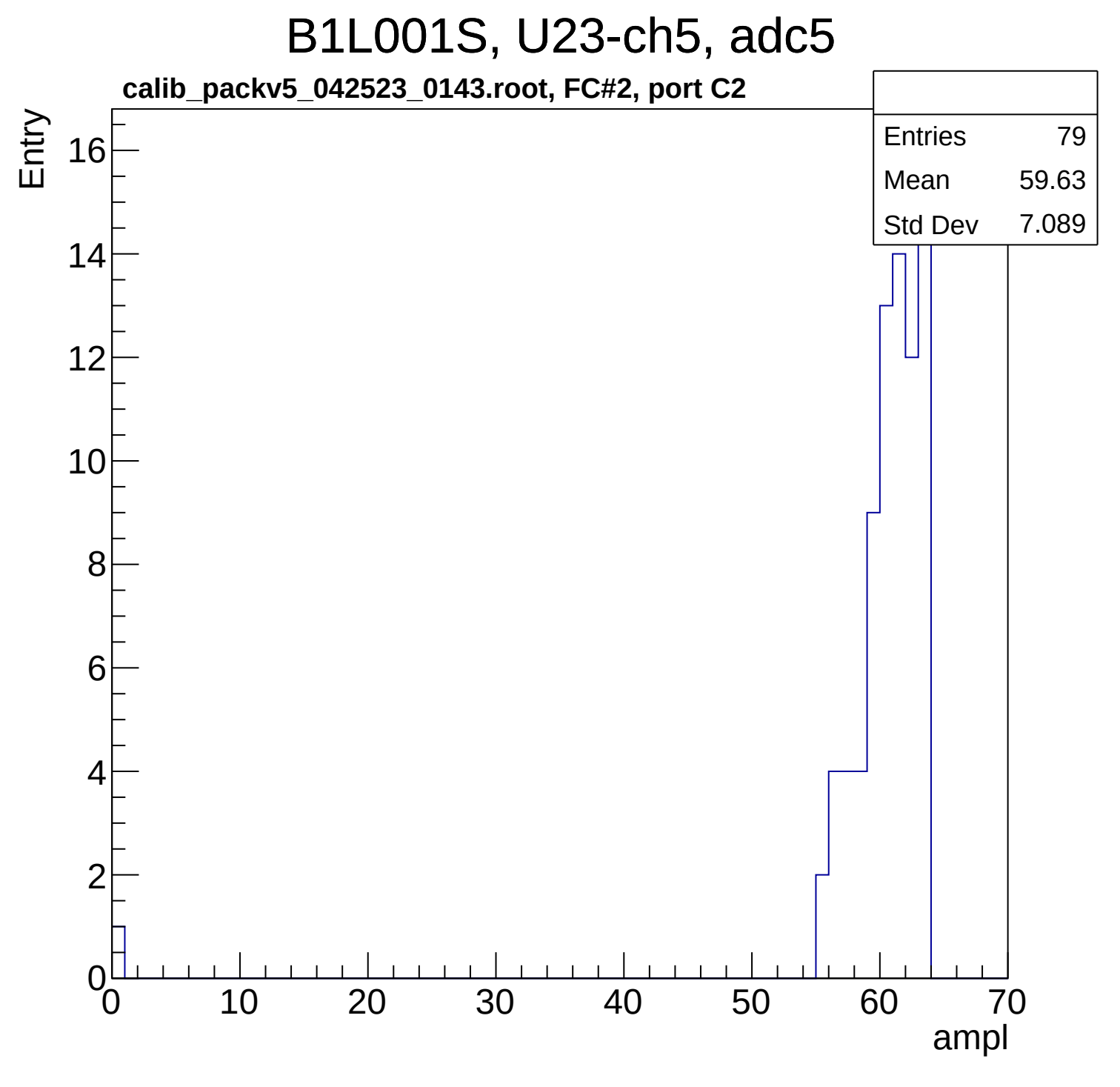

B1L001S, U23-ch5, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 55.47
Std Dev 5.897
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch5, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 79
Mean 59.63
Std Dev 7.089
Entry
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4
6
8
10
12
14
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10
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30
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50
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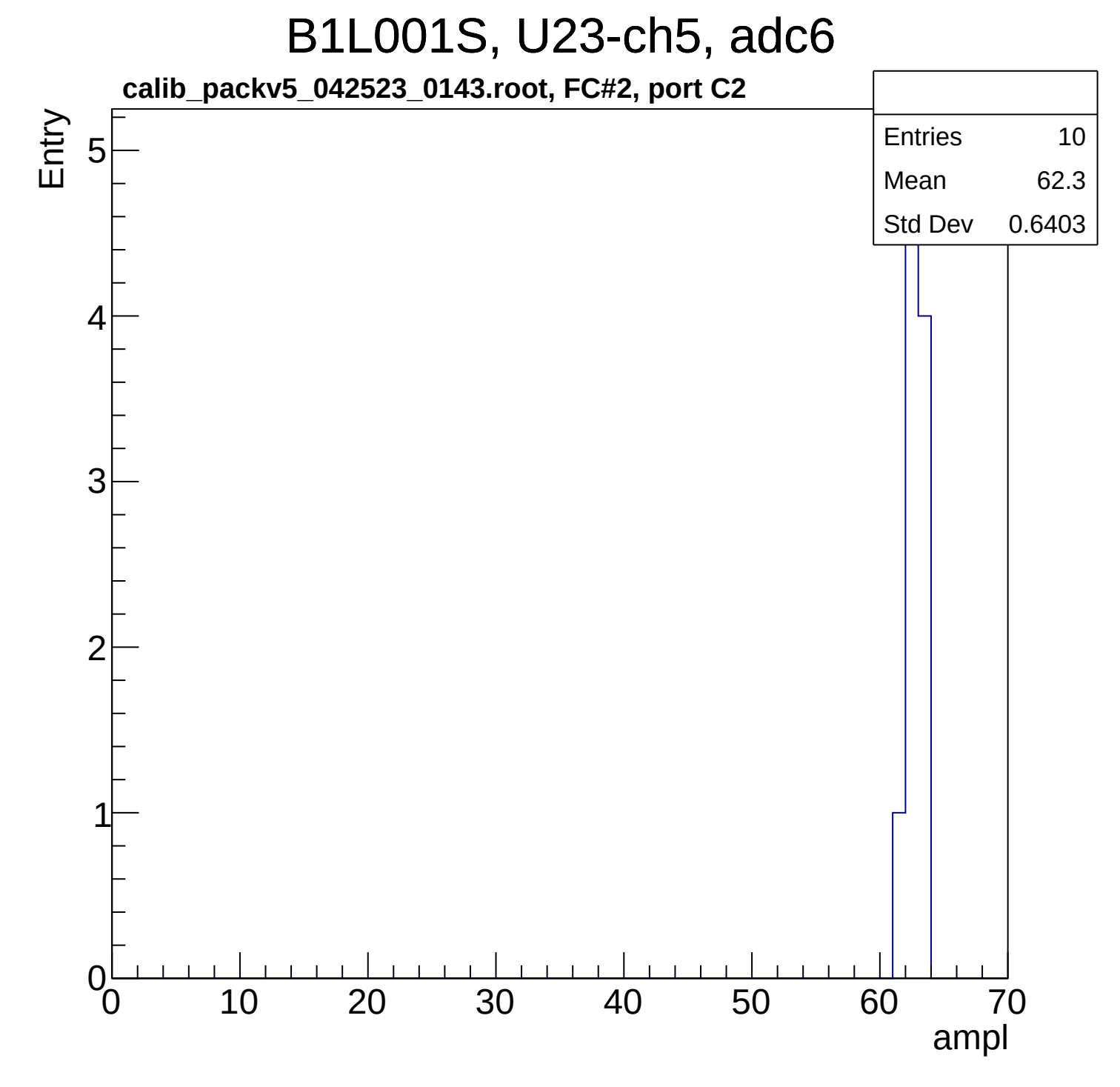
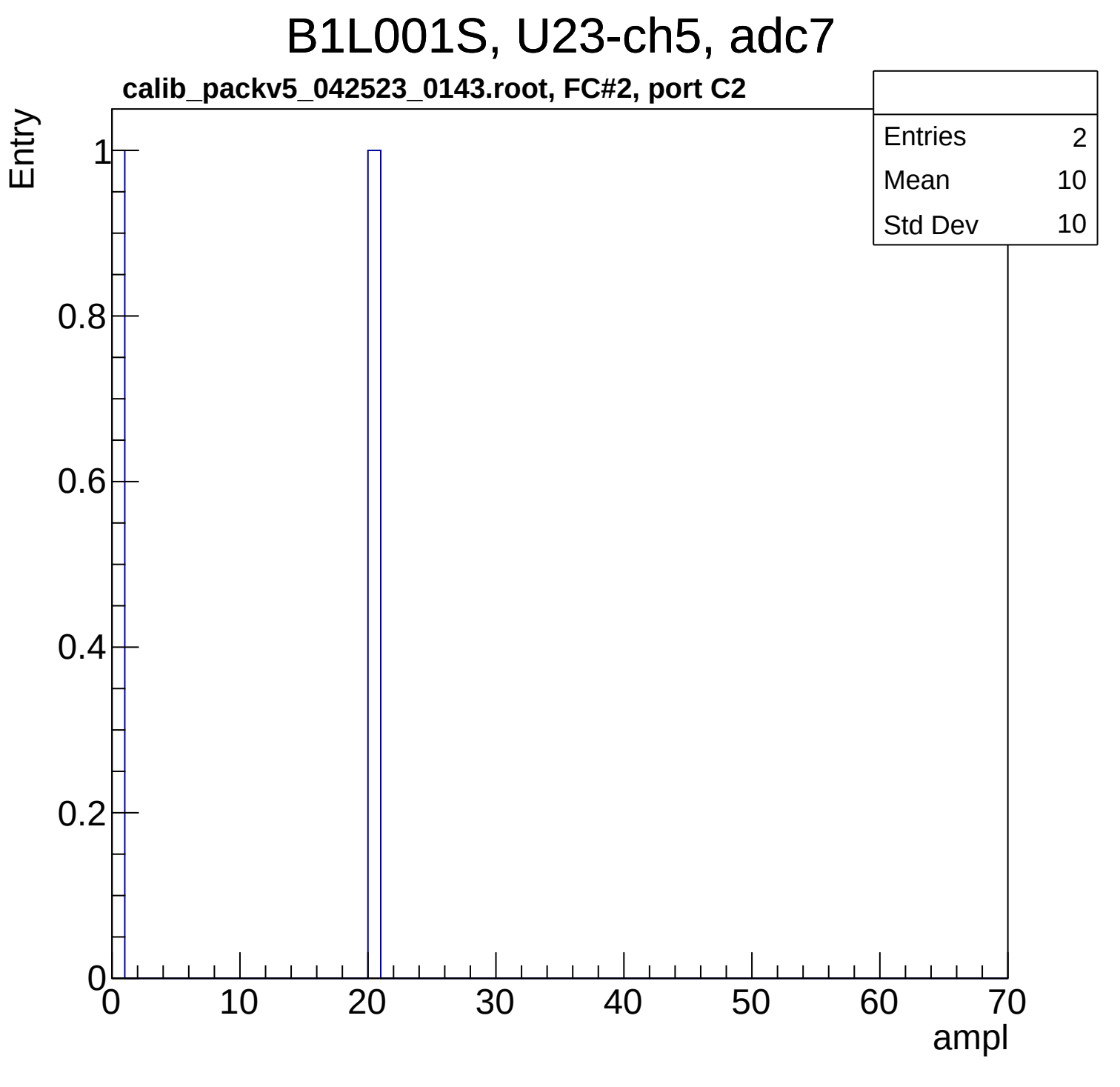
70
ampl


B1L001S, U23-ch5, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 62.3
Std Dev 0.6403
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch5, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10
Std Dev 10
Entry
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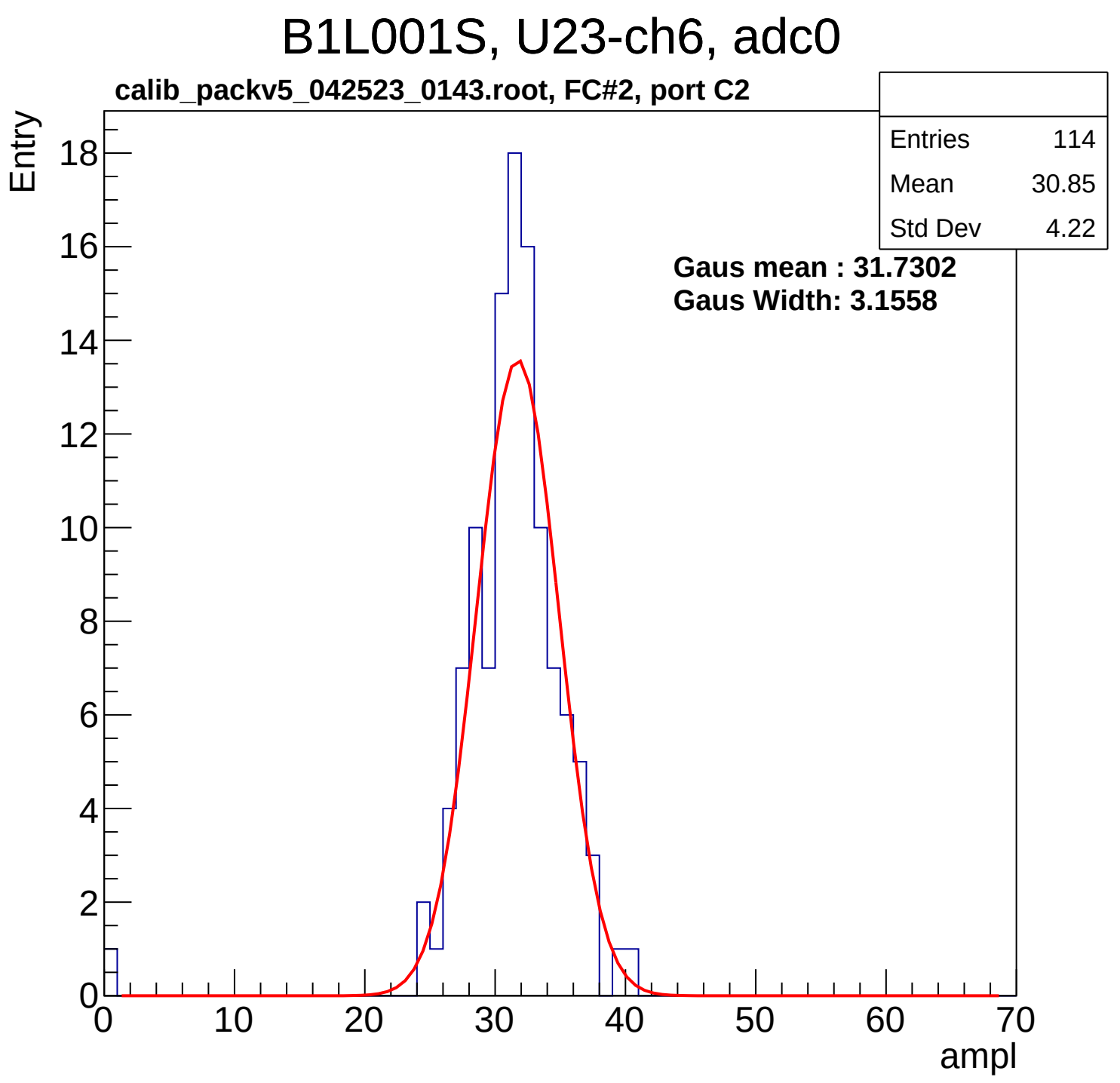
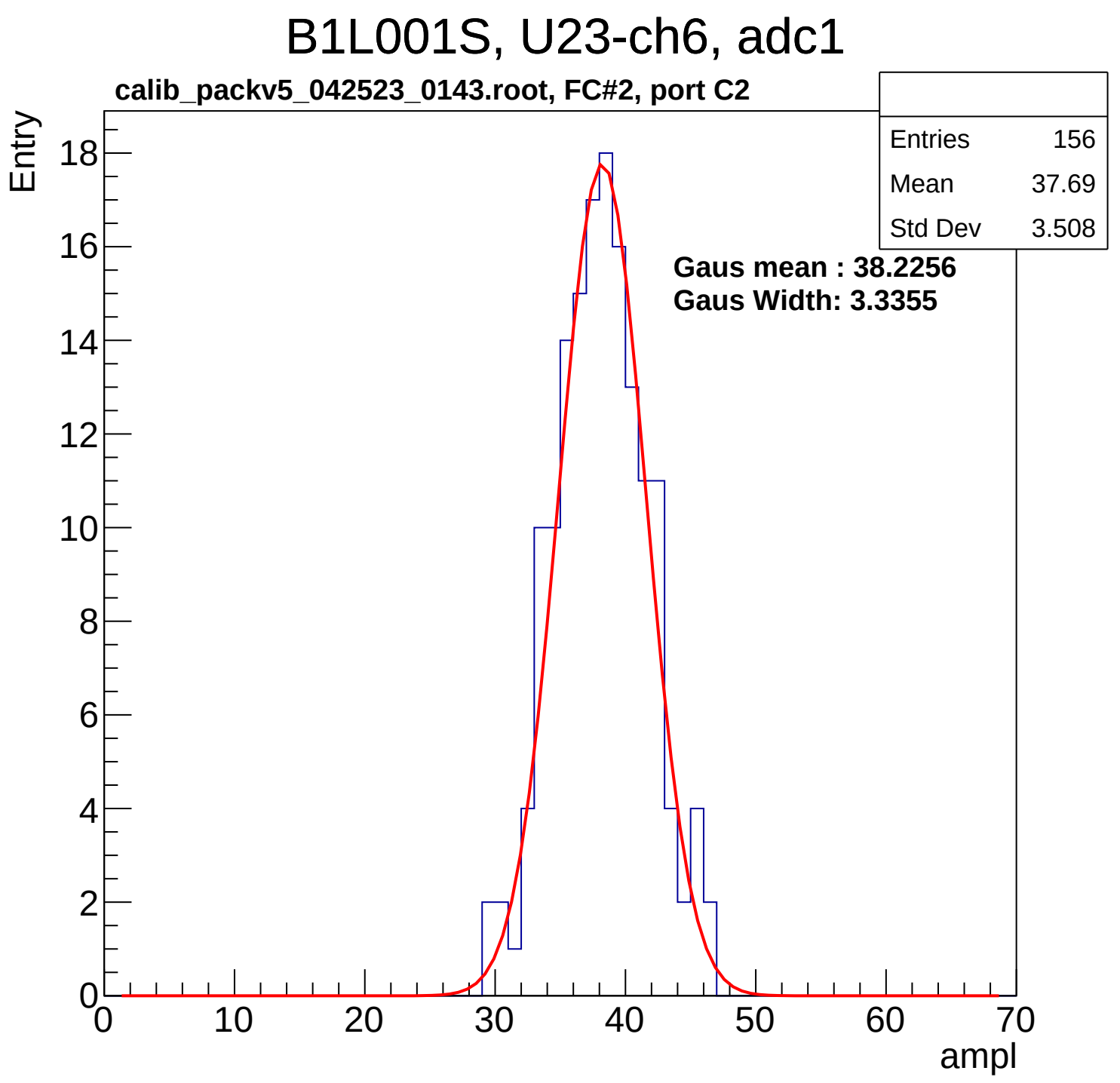
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch6, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 30.85
Std Dev 4.22
Gaus mean : 31.7302
Gaus Width: 3.1558
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch6, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 37.69
Std Dev 3.508
Gaus mean : 38.2256
Gaus Width: 3.3355
Entry
ampl
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2
4
6
8
10
12
14
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0
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20
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60
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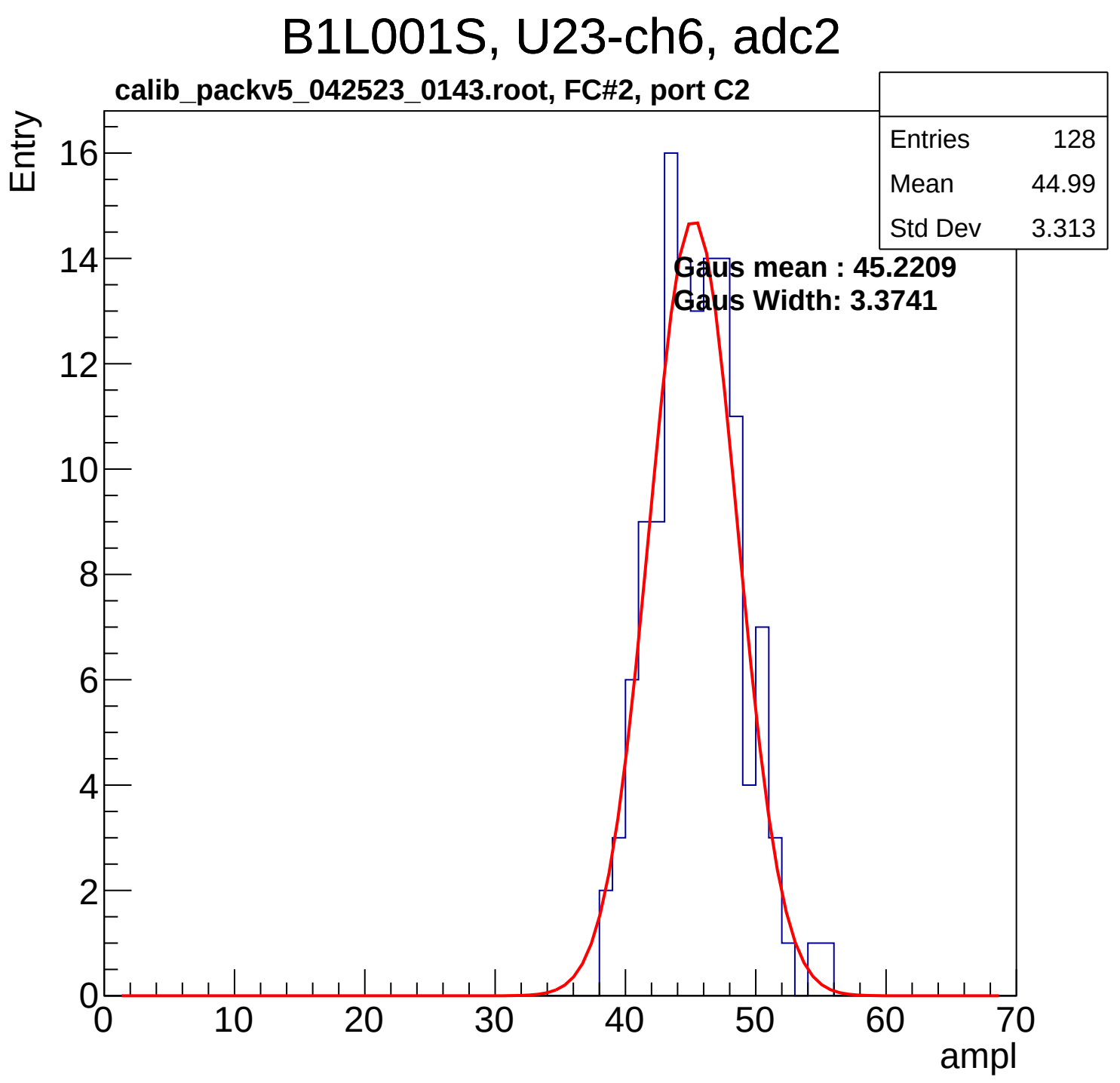
B1L001S, U23-ch6, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 44.99
Std Dev 3.313
Gaus mean : 45.2209
Gaus Width: 3.3741
Entry
ampl
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6
8
10
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60
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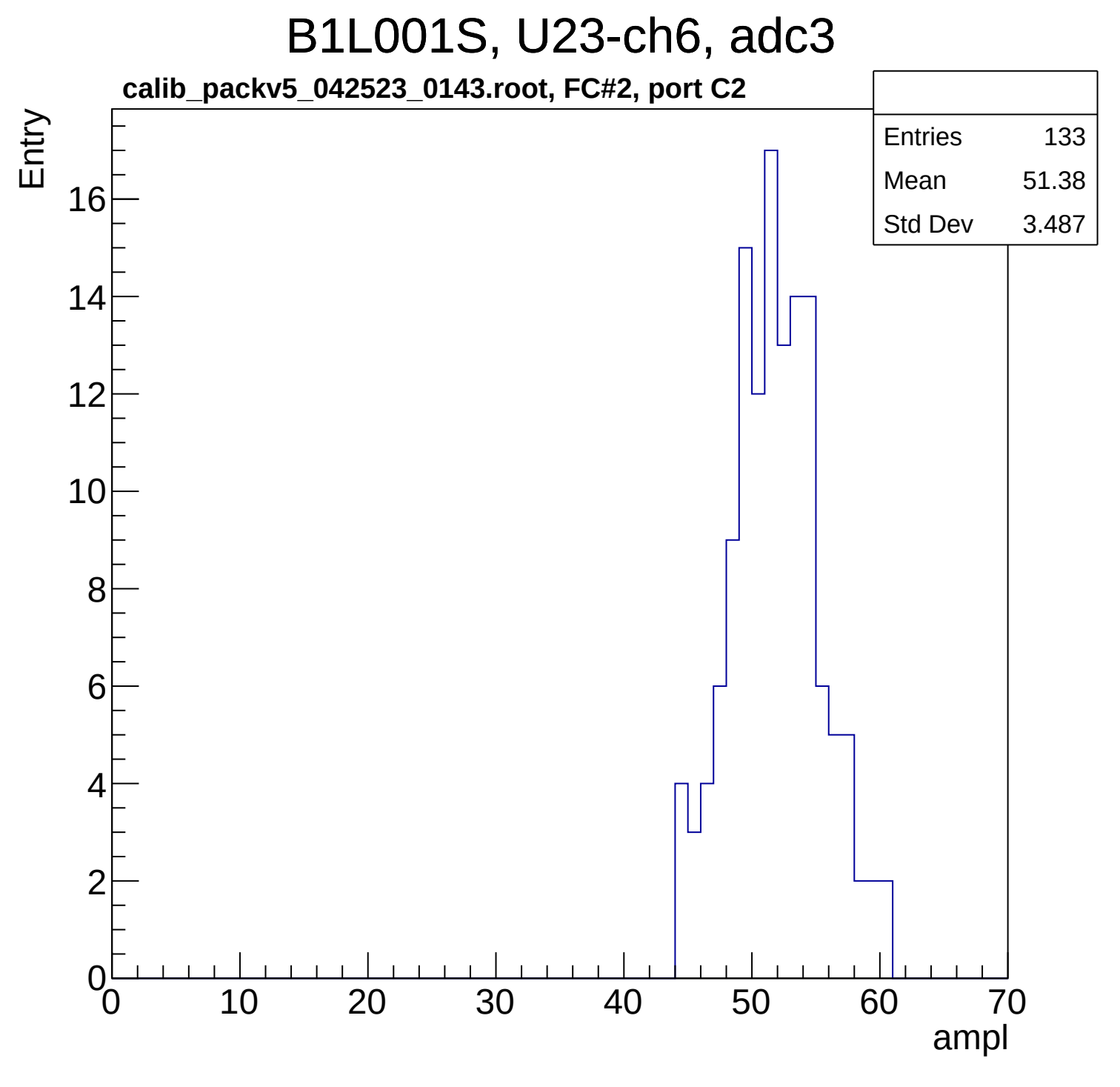
B1L001S, U23-ch6, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 51.38
Std Dev 3.487
Entry
ampl
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4
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8
10
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0
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40
50
60
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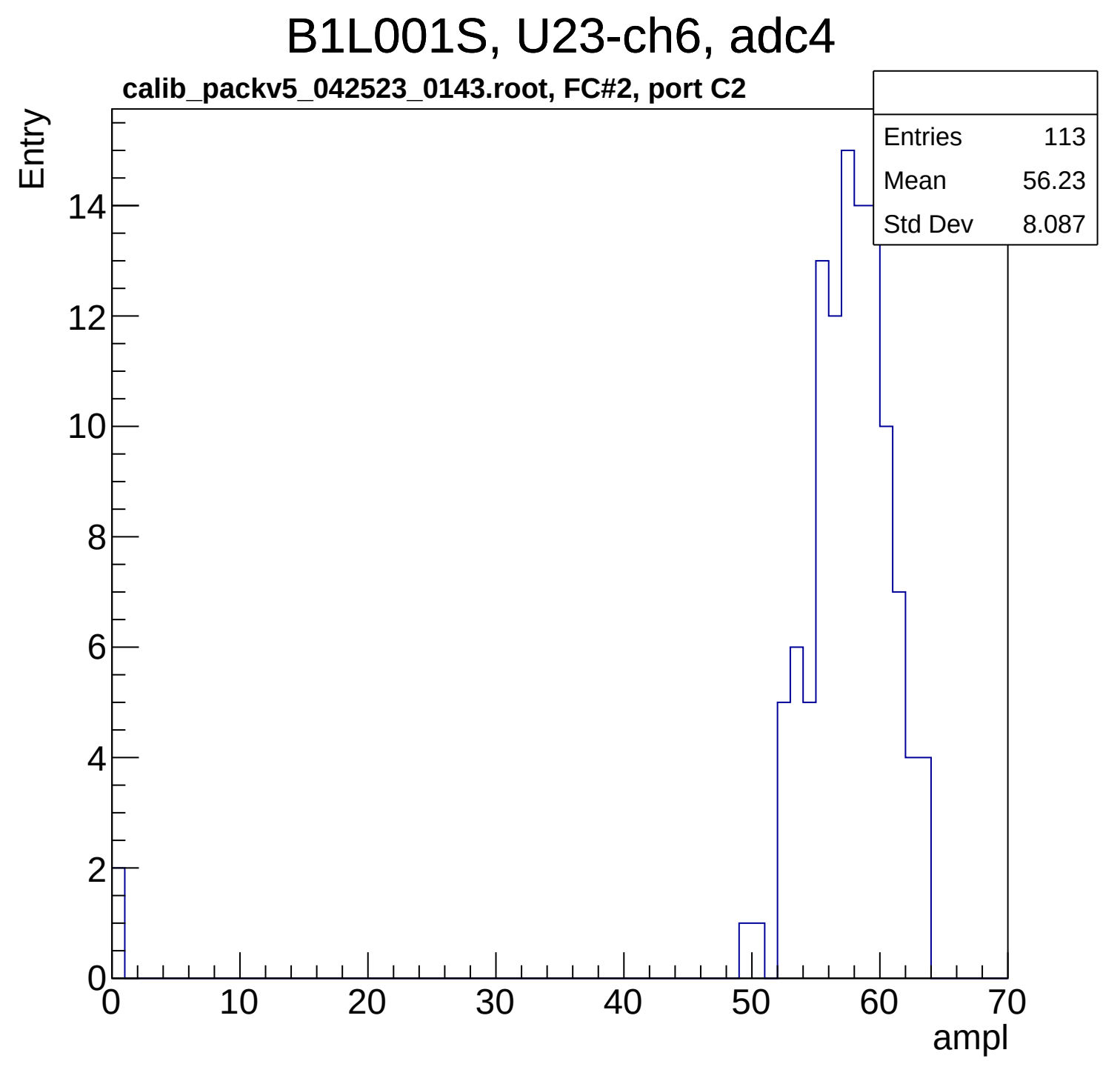
B1L001S, U23-ch6, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 56.23
Std Dev 8.087
Entry
ampl
0
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8
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0
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60
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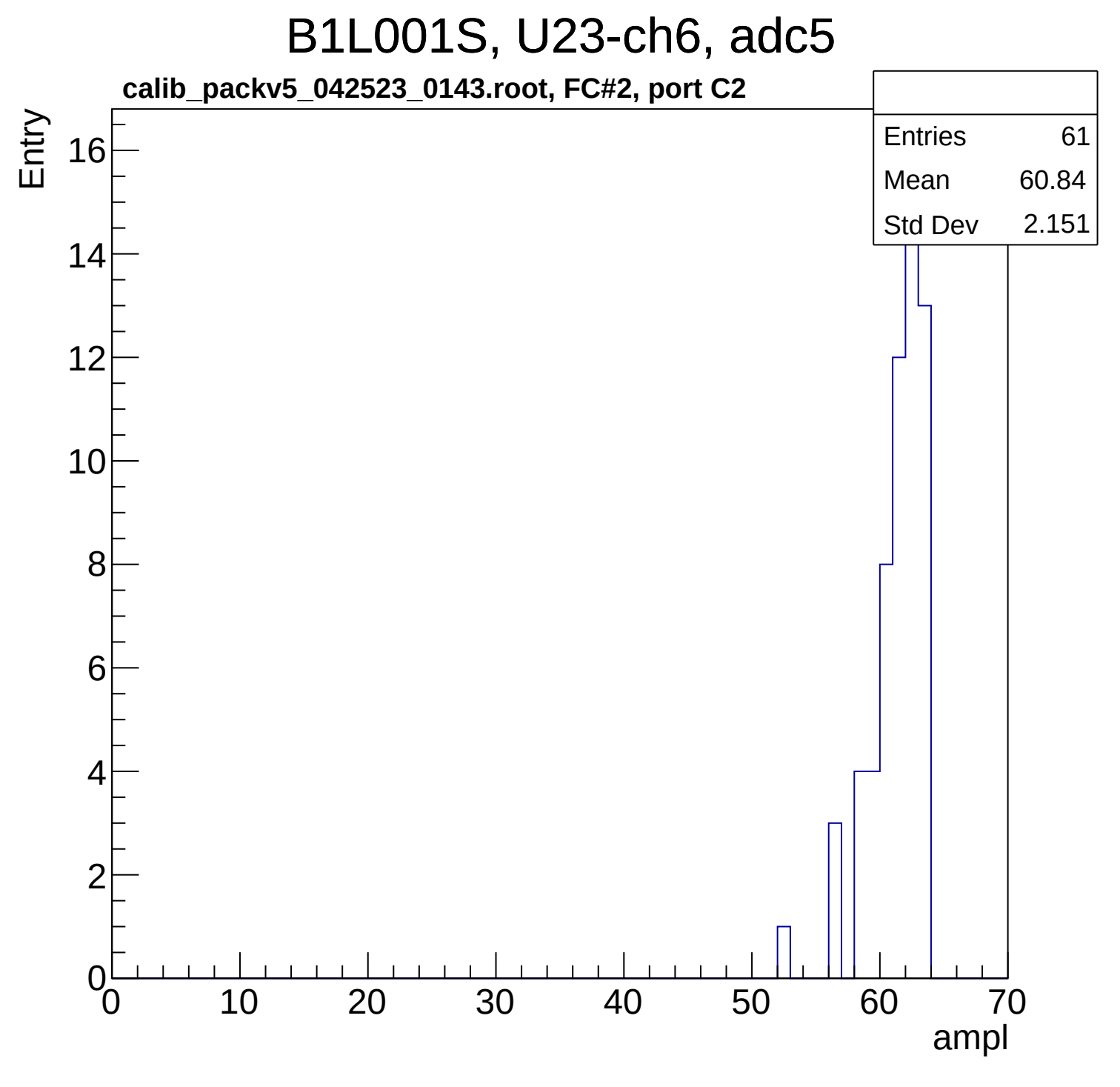
B1L001S, U23-ch6, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 60.84
Std Dev 2.151
Entry
ampl
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2
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10
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0
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60
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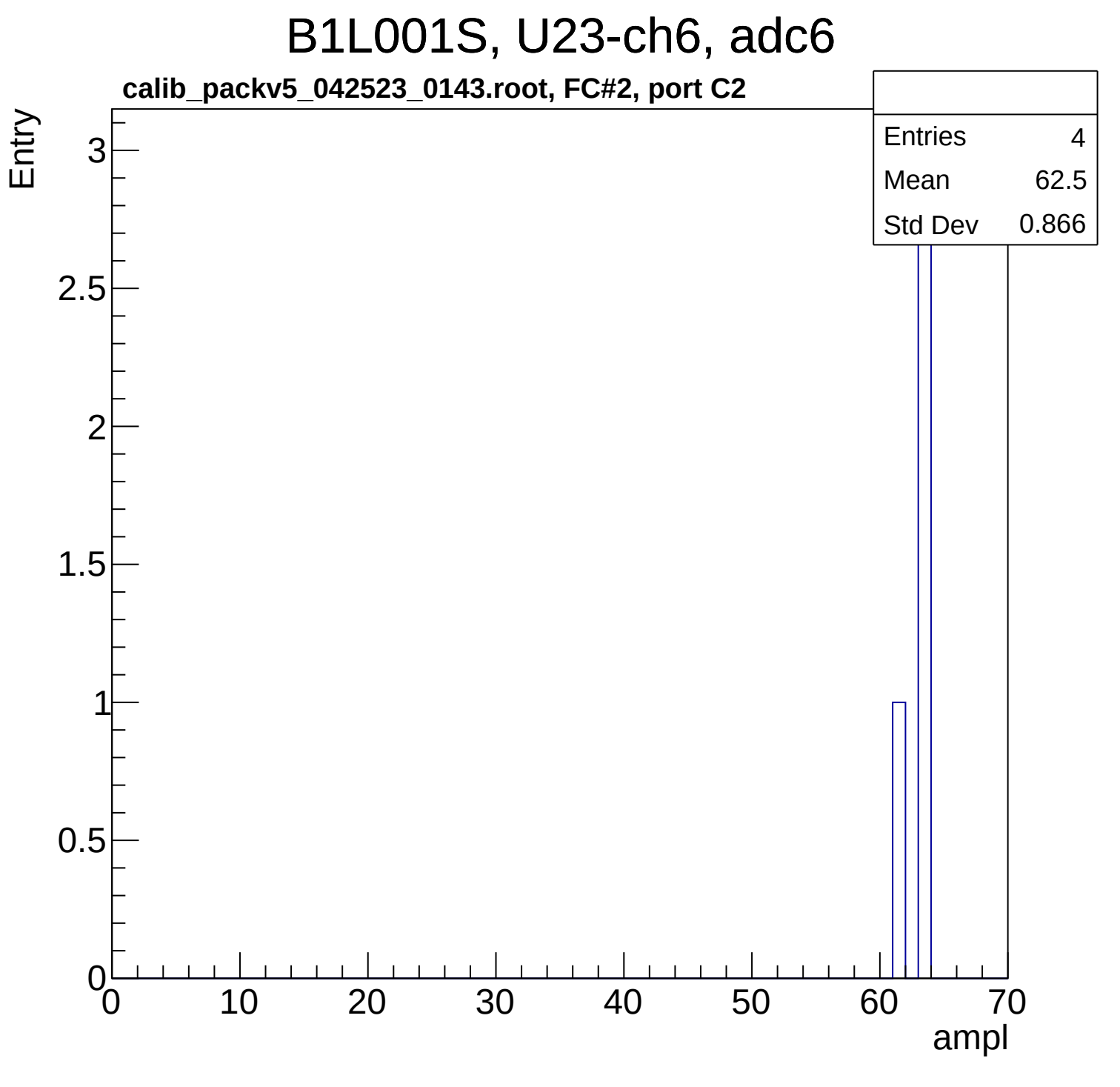
B1L001S, U23-ch6, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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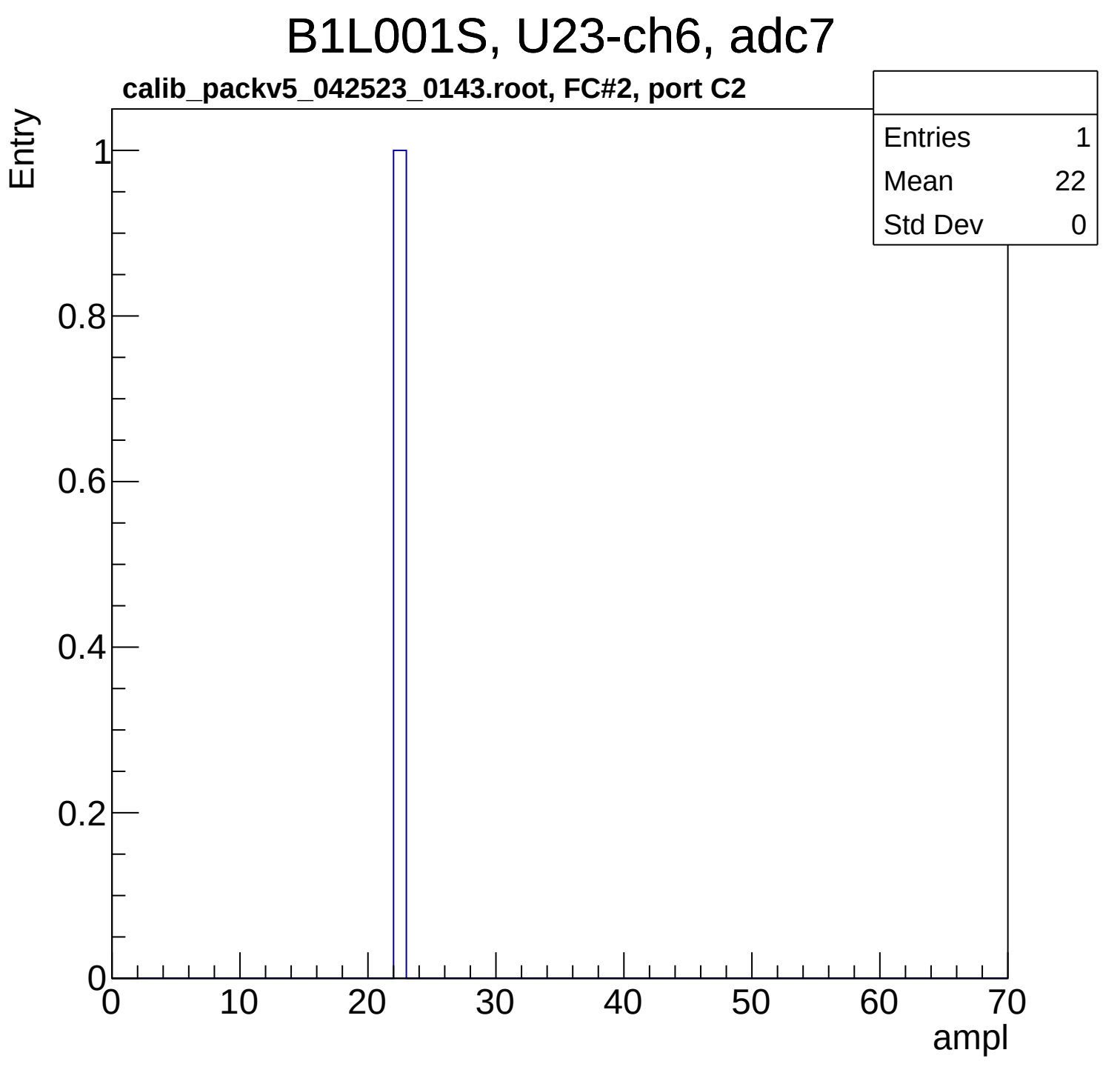
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch6, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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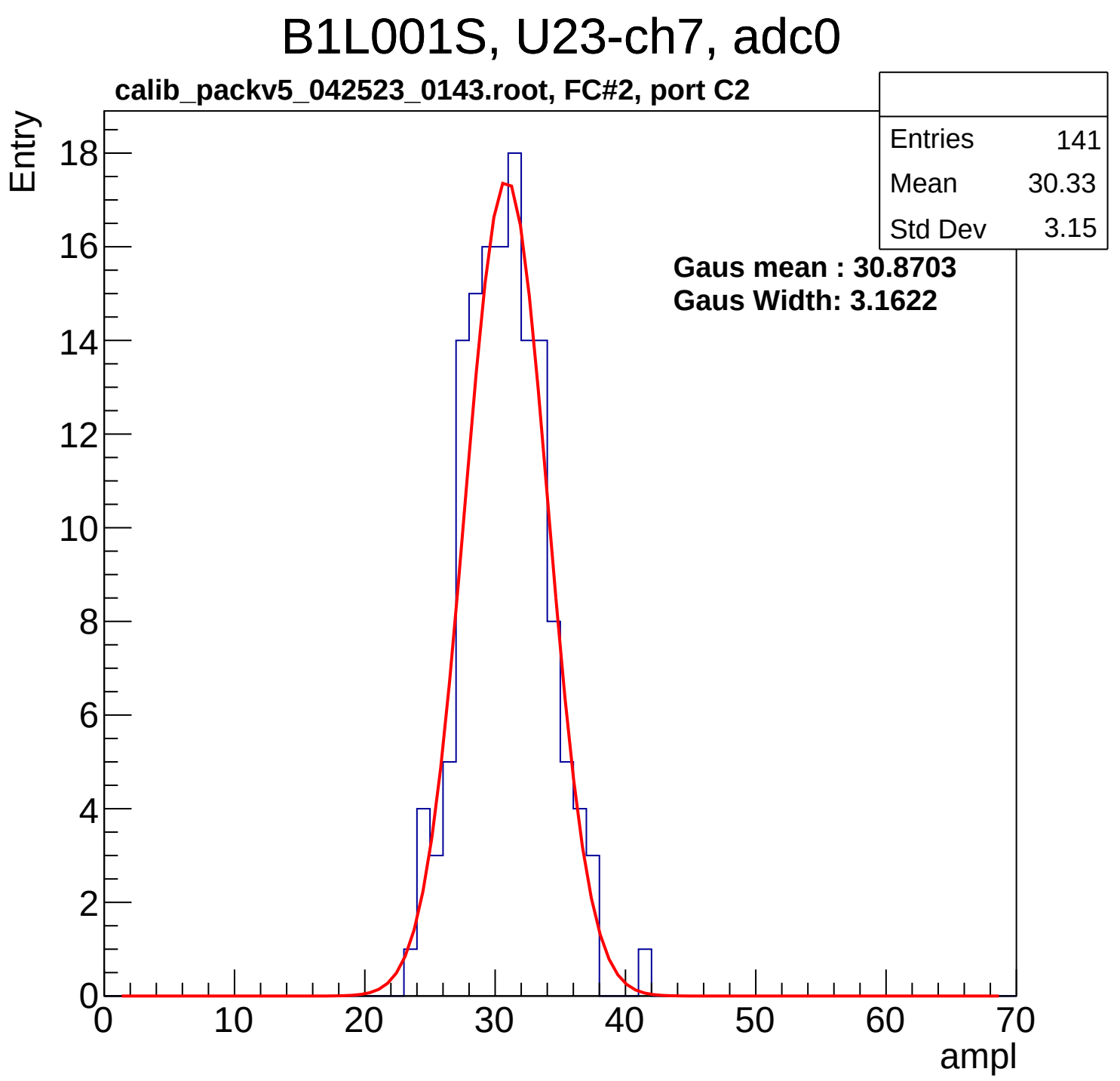
50
60
70
ampl


B1L001S, U23-ch7, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 30.33
Std Dev 3.15
Gaus mean : 30.8703
Gaus Width: 3.1622
Entry
ampl
0
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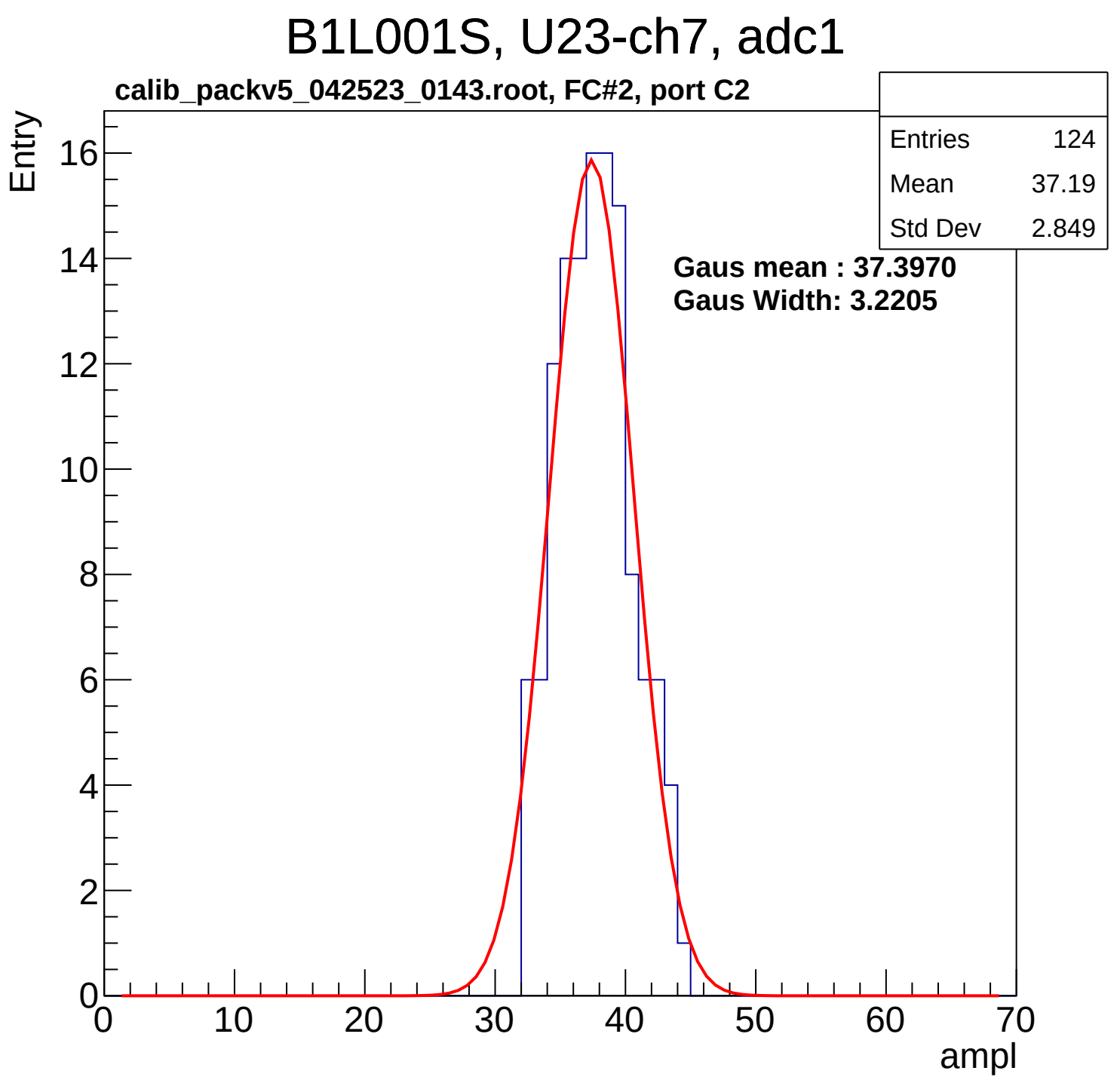
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch7, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 37.19
Std Dev 2.849
Gaus mean : 37.3970
Gaus Width: 3.2205
Entry
ampl
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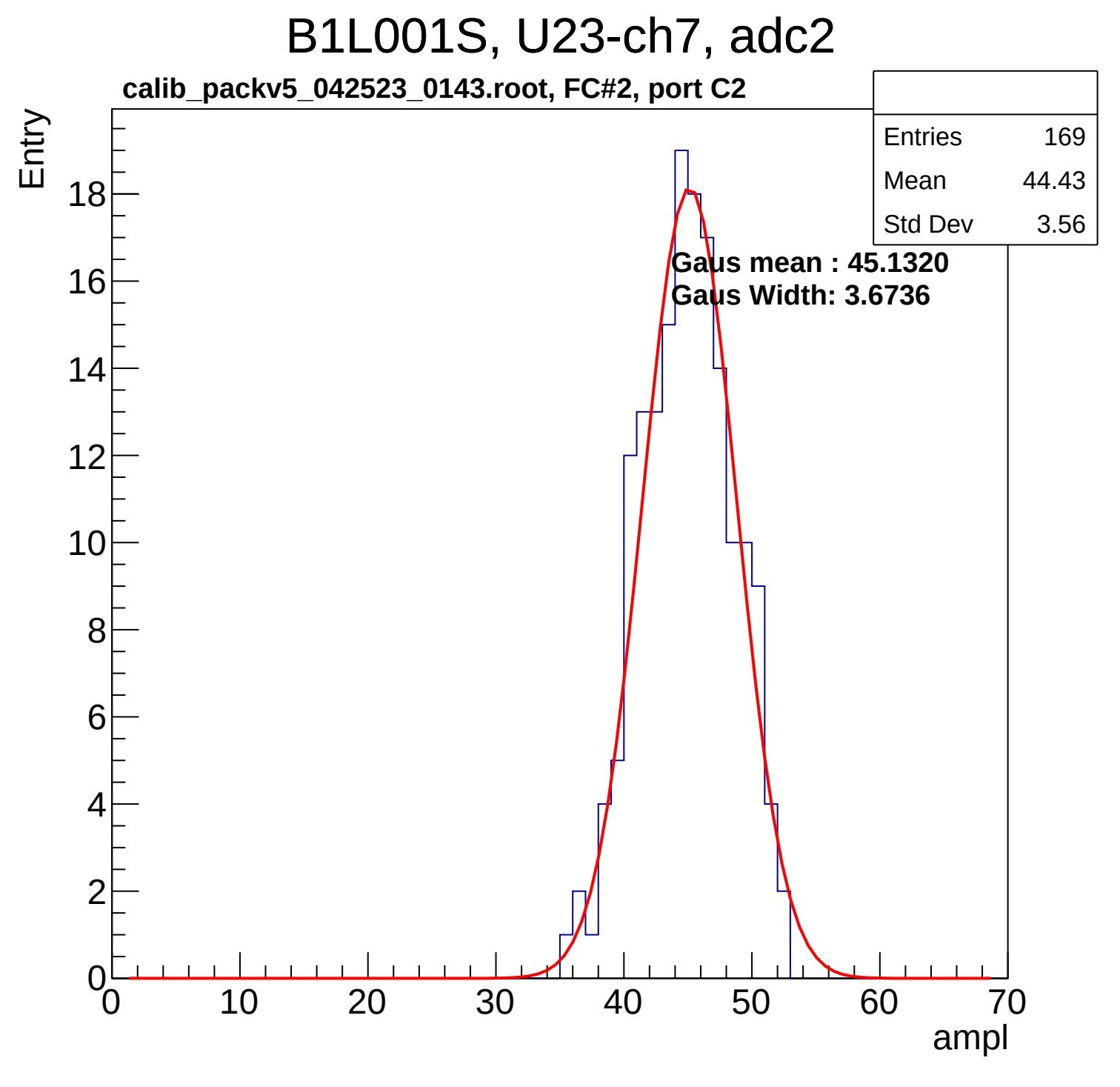
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch7, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 169
Mean 44.43
Std Dev 3.56
Gaus mean : 45.1320
Gaus Width: 3.6736
Entry
ampl
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4
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12
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0
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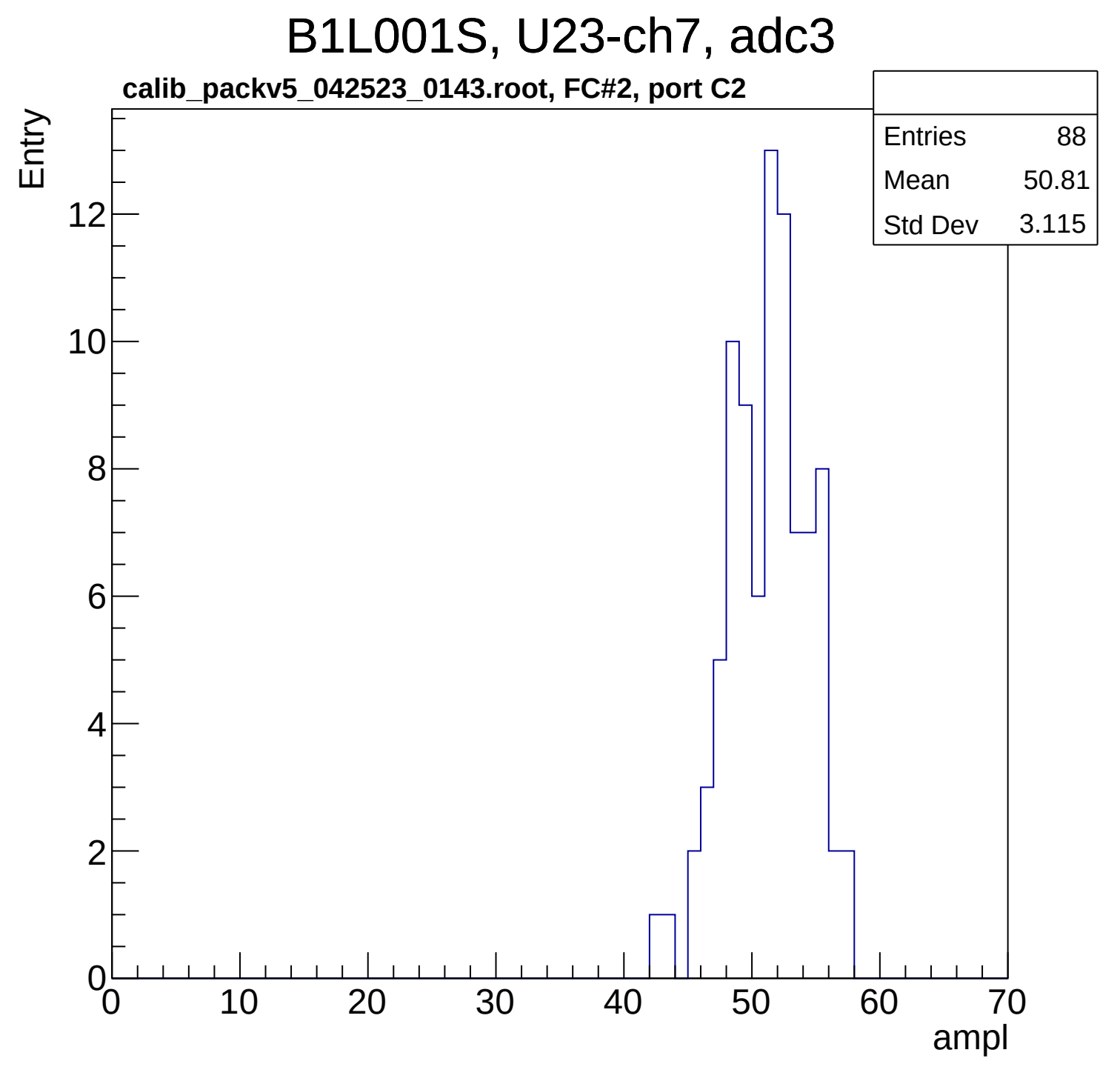
B1L001S, U23-ch7, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 50.81
Std Dev 3.115
Entry
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10
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20
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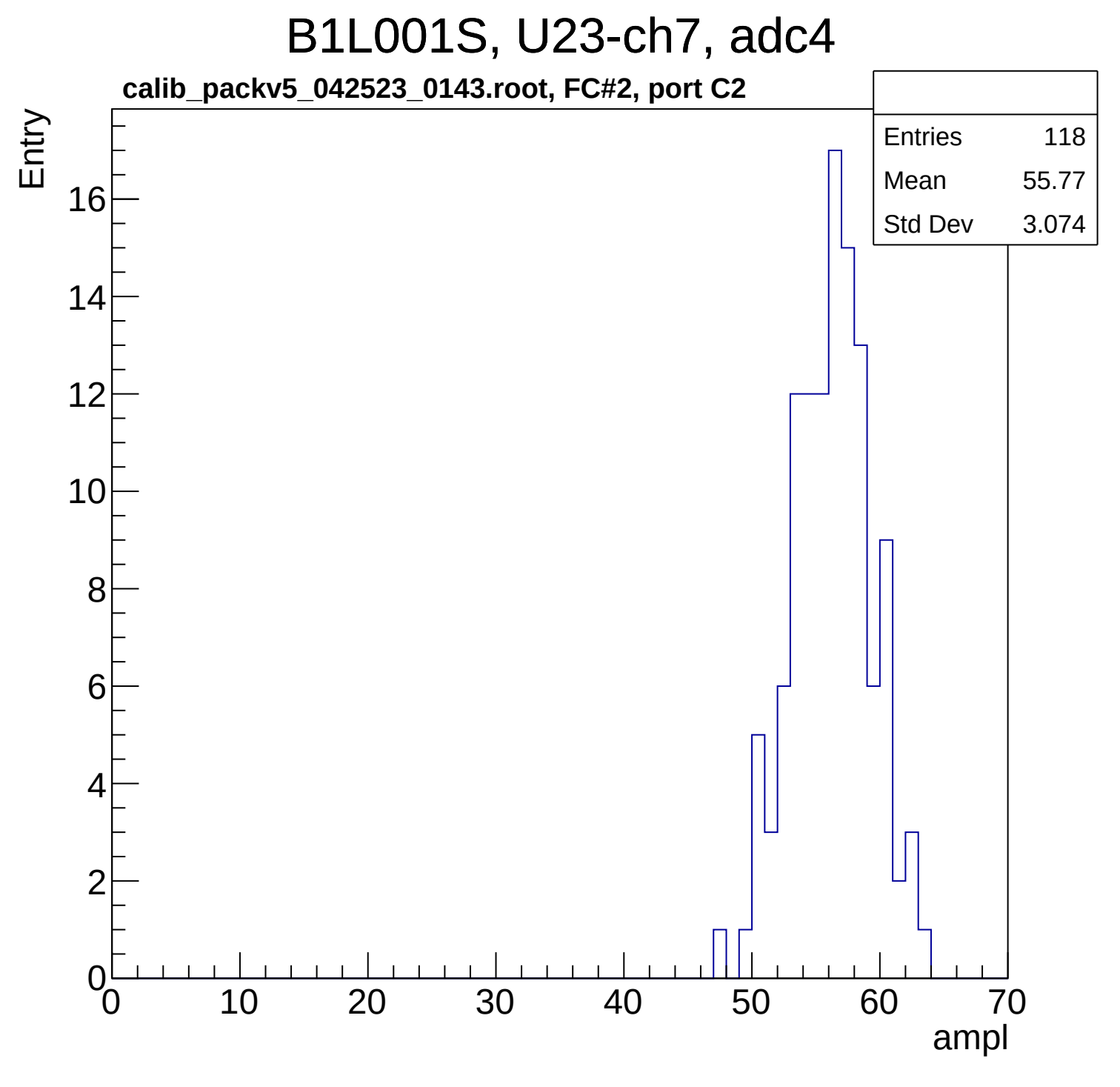
50
60
70
ampl


B1L001S, U23-ch7, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 55.77
Std Dev 3.074
Entry
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12
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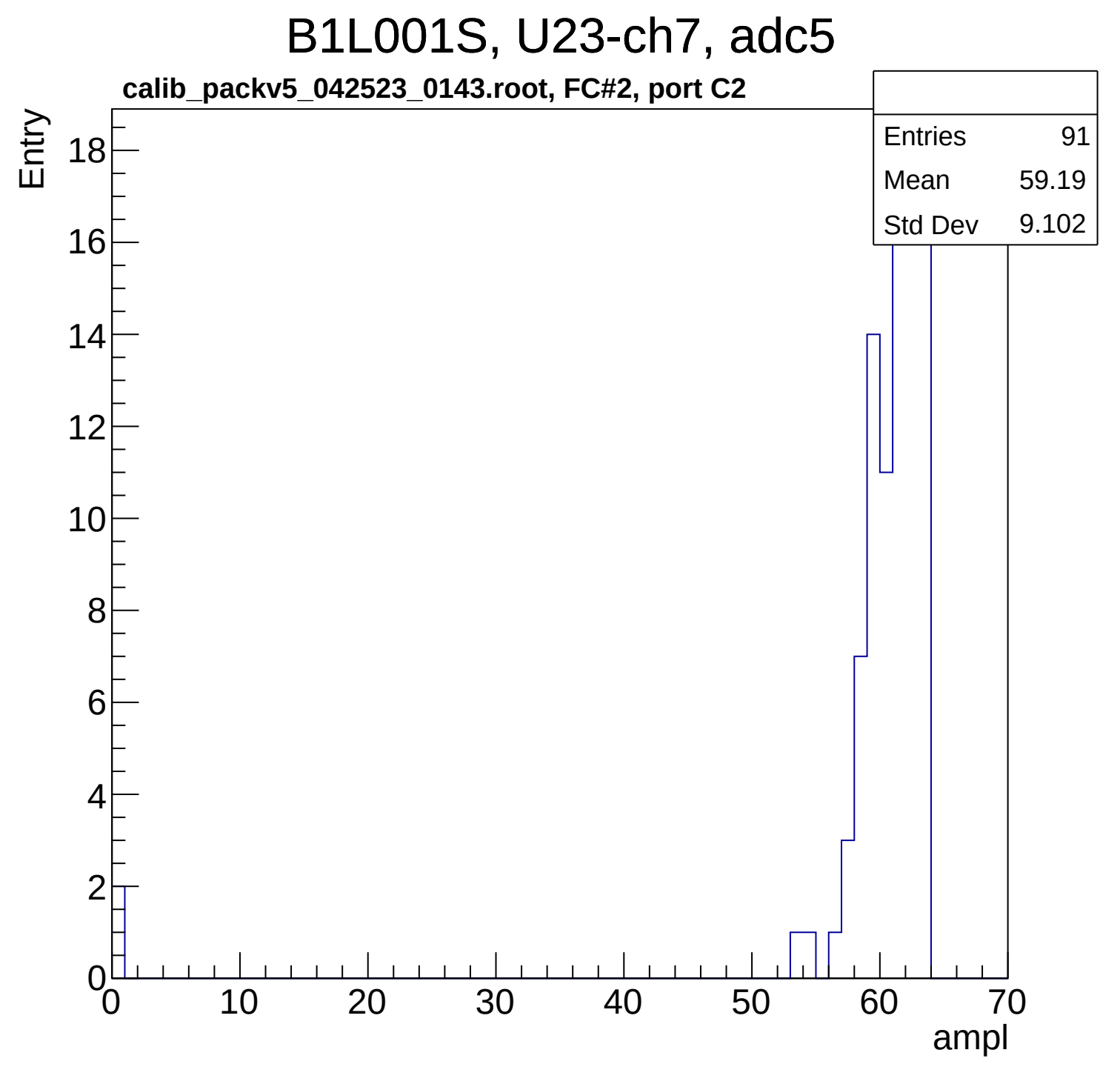
60
70
ampl


B1L001S, U23-ch7, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.19
Std Dev 9.102
Entry
ampl
0
2
4
6
8
10
12
14
16
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0
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60
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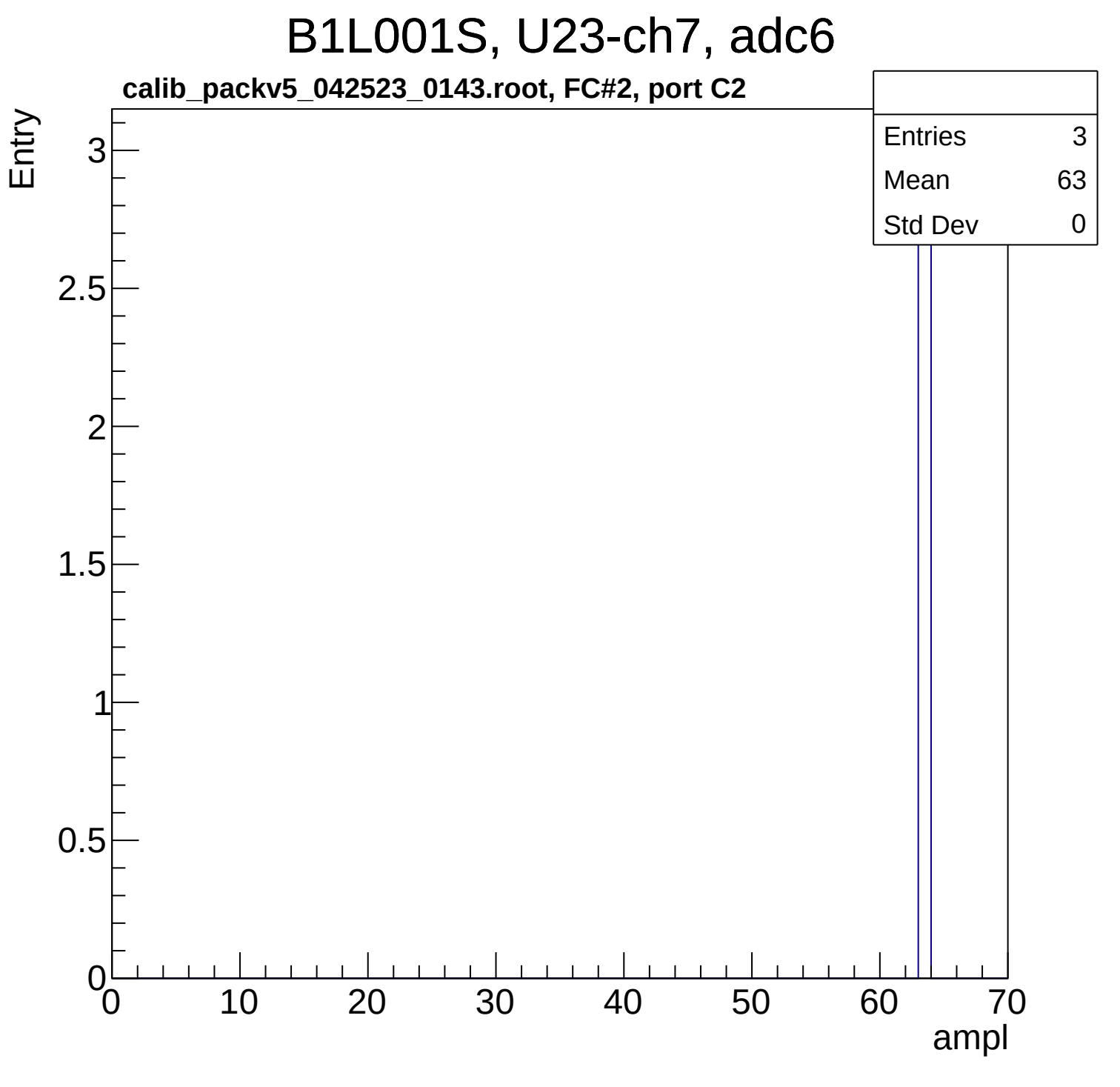
B1L001S, U23-ch7, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
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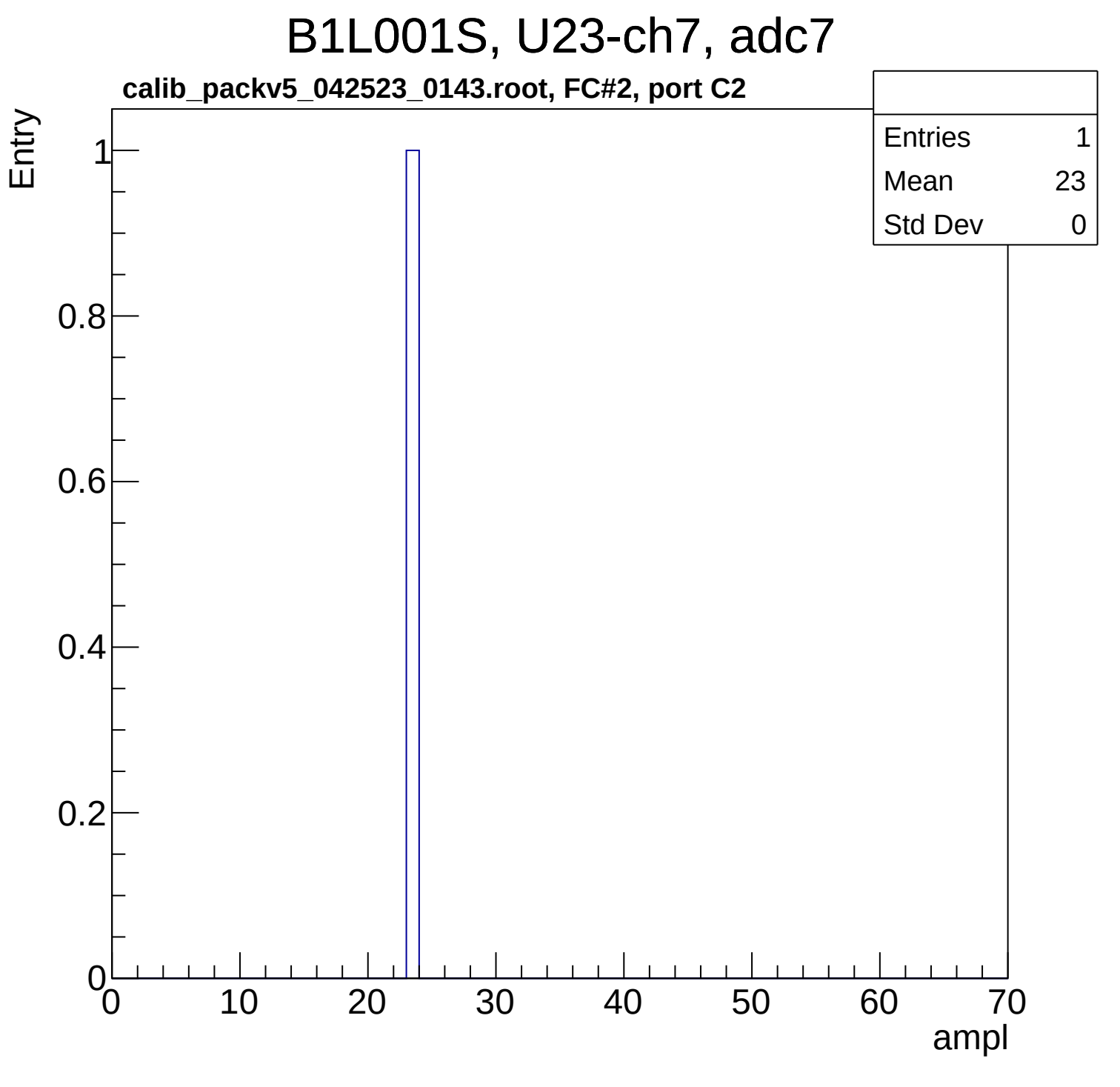
40
50
60
70
ampl


B1L001S, U23-ch7, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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0
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20
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60
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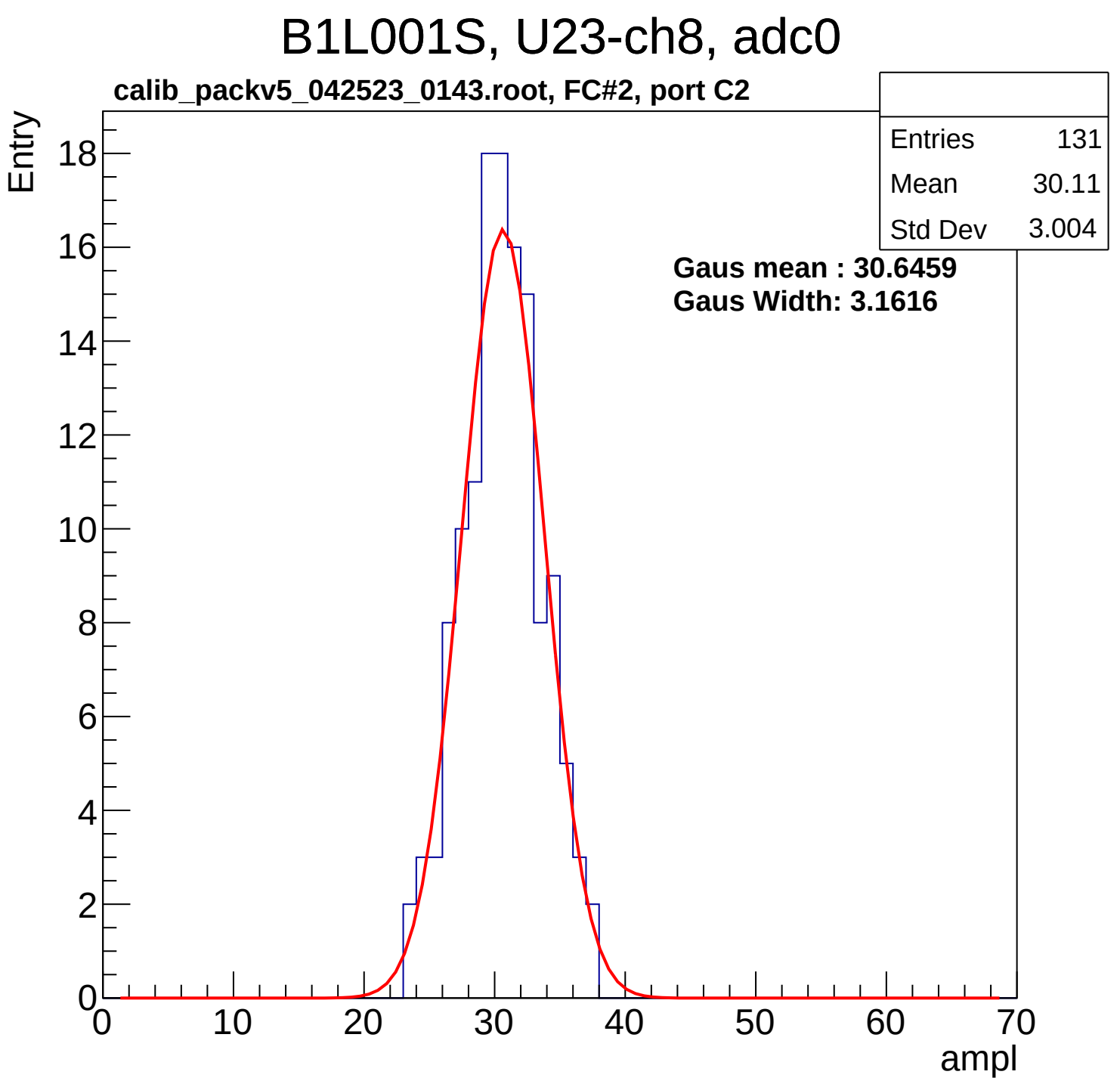
B1L001S, U23-ch8, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 30.11
Std Dev 3.004
Gaus mean : 30.6459
Gaus Width: 3.1616
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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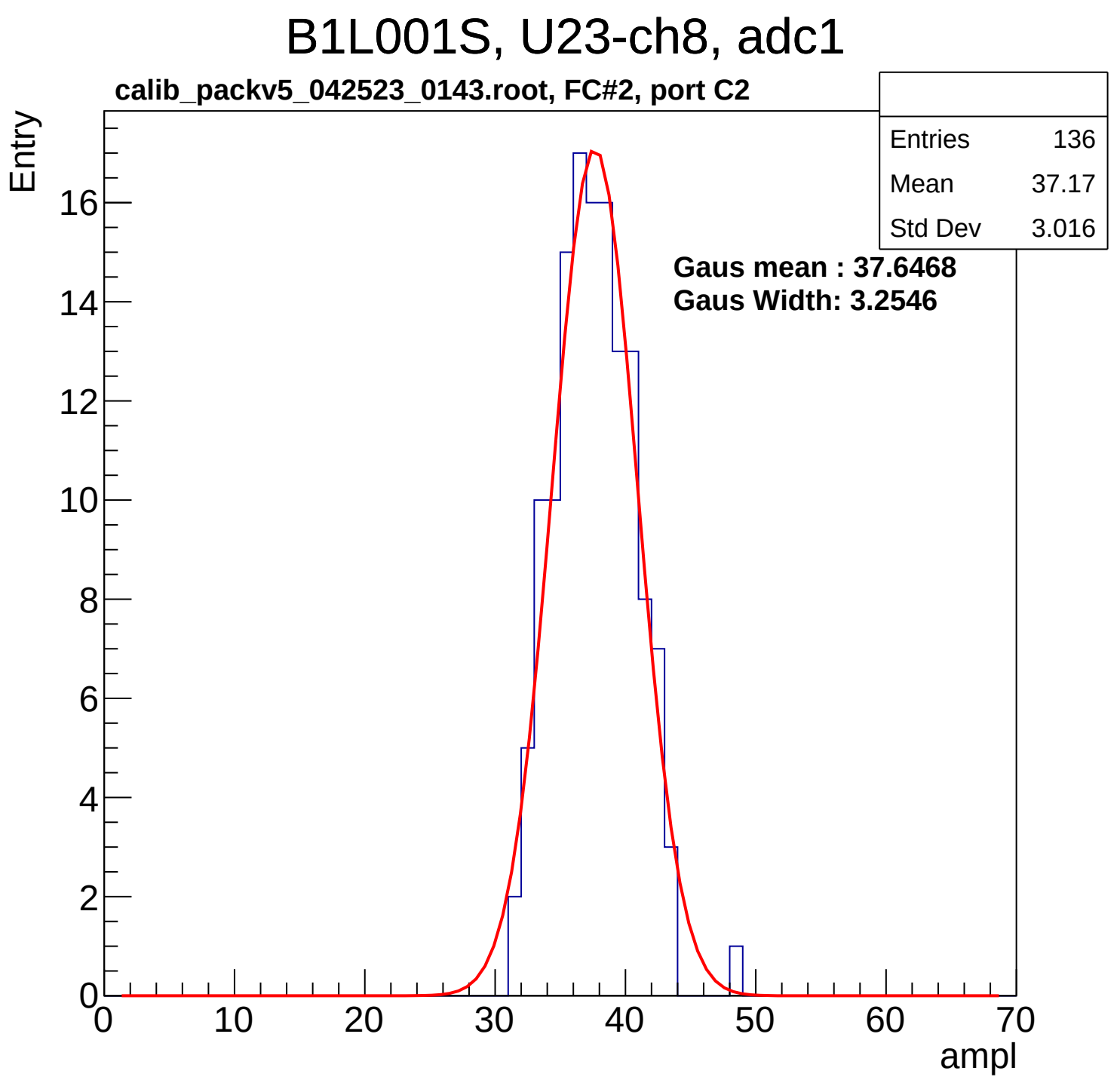
B1L001S, U23-ch8, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 37.17
Std Dev 3.016
Gaus mean : 37.6468
Gaus Width: 3.2546
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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40
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60
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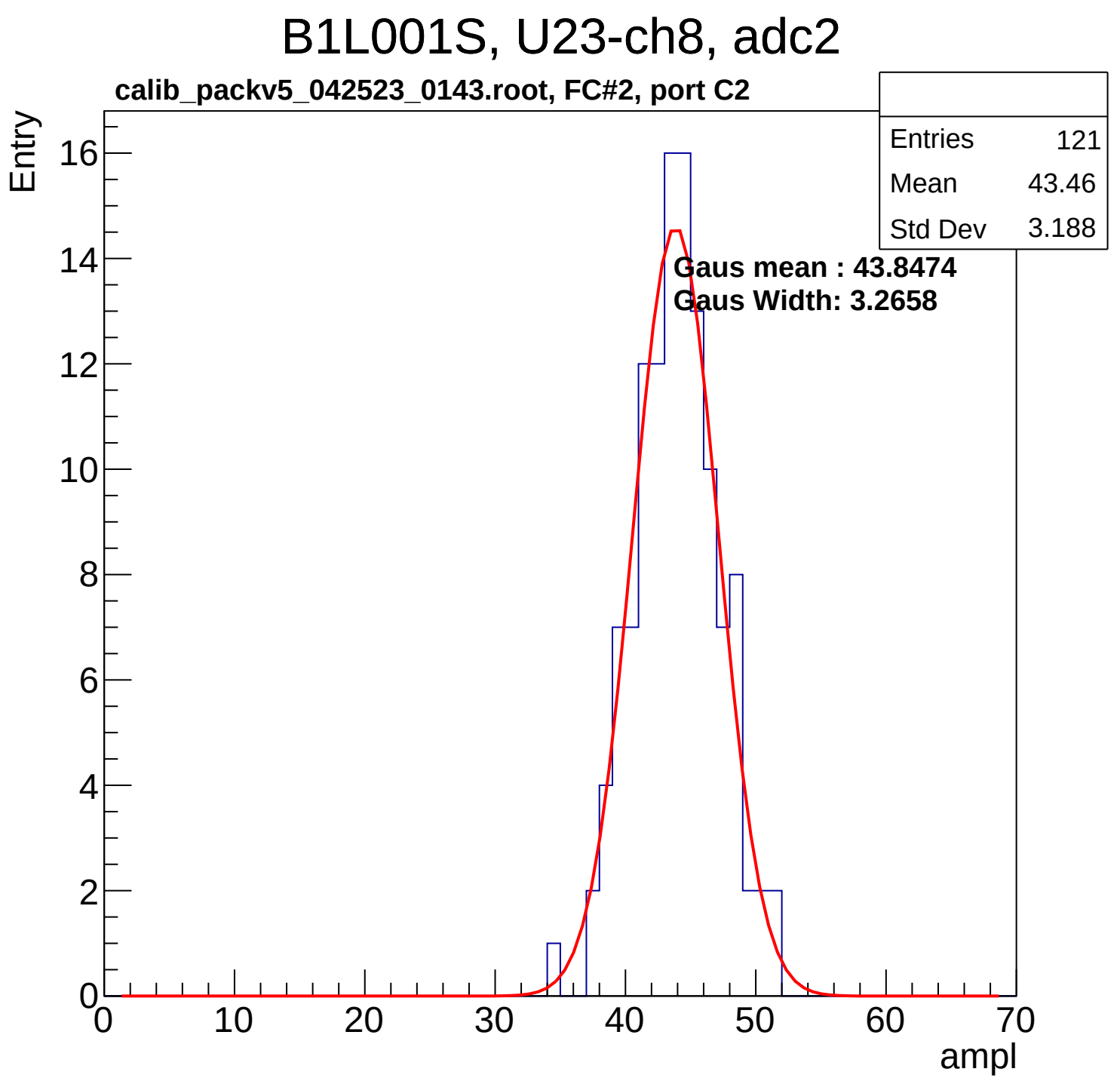
B1L001S, U23-ch8, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 43.46
Std Dev 3.188
Gaus mean : 43.8474
Gaus Width: 3.2658
Entry
ampl
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2
4
6
8
10
12
14
16
0
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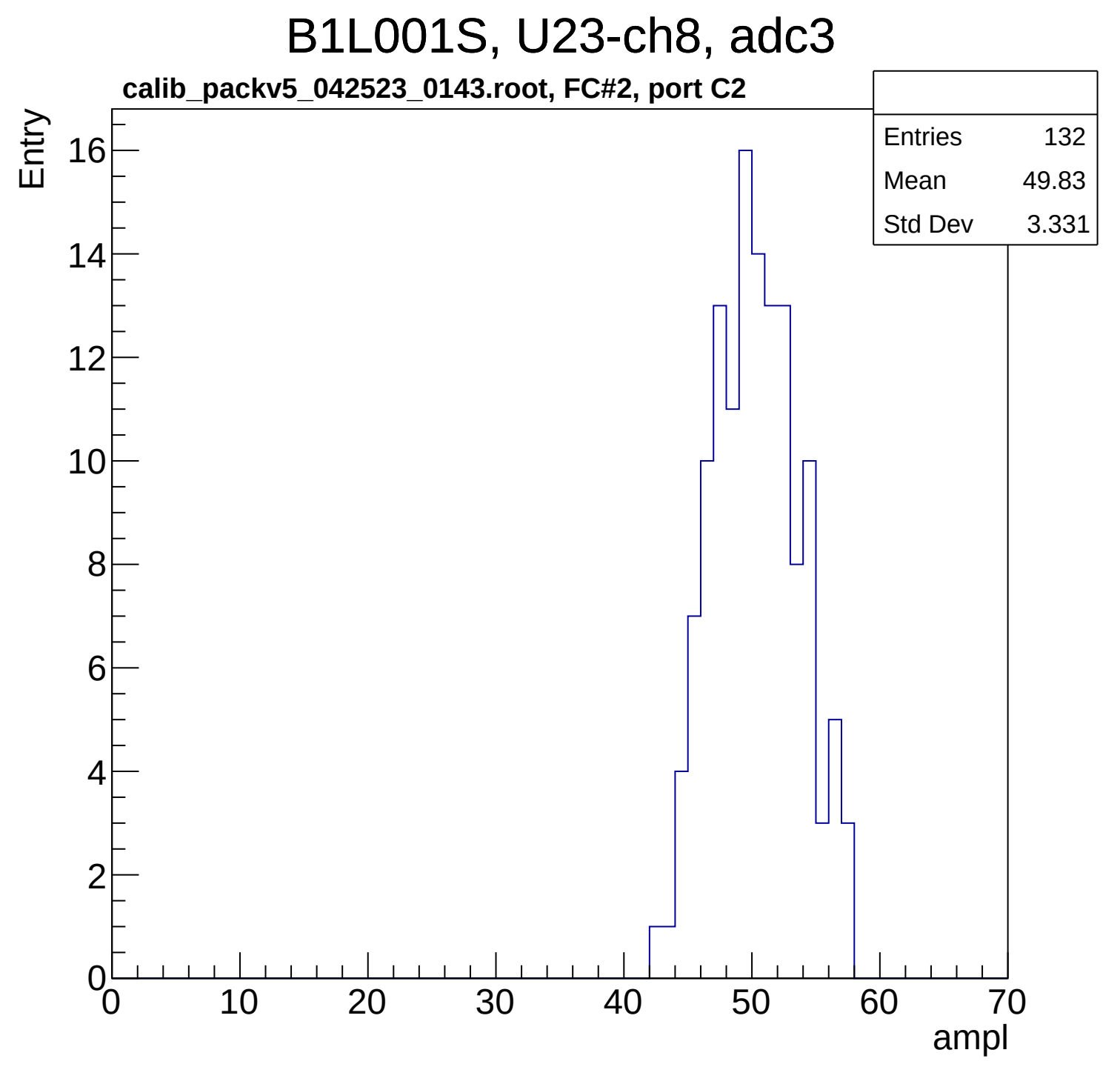
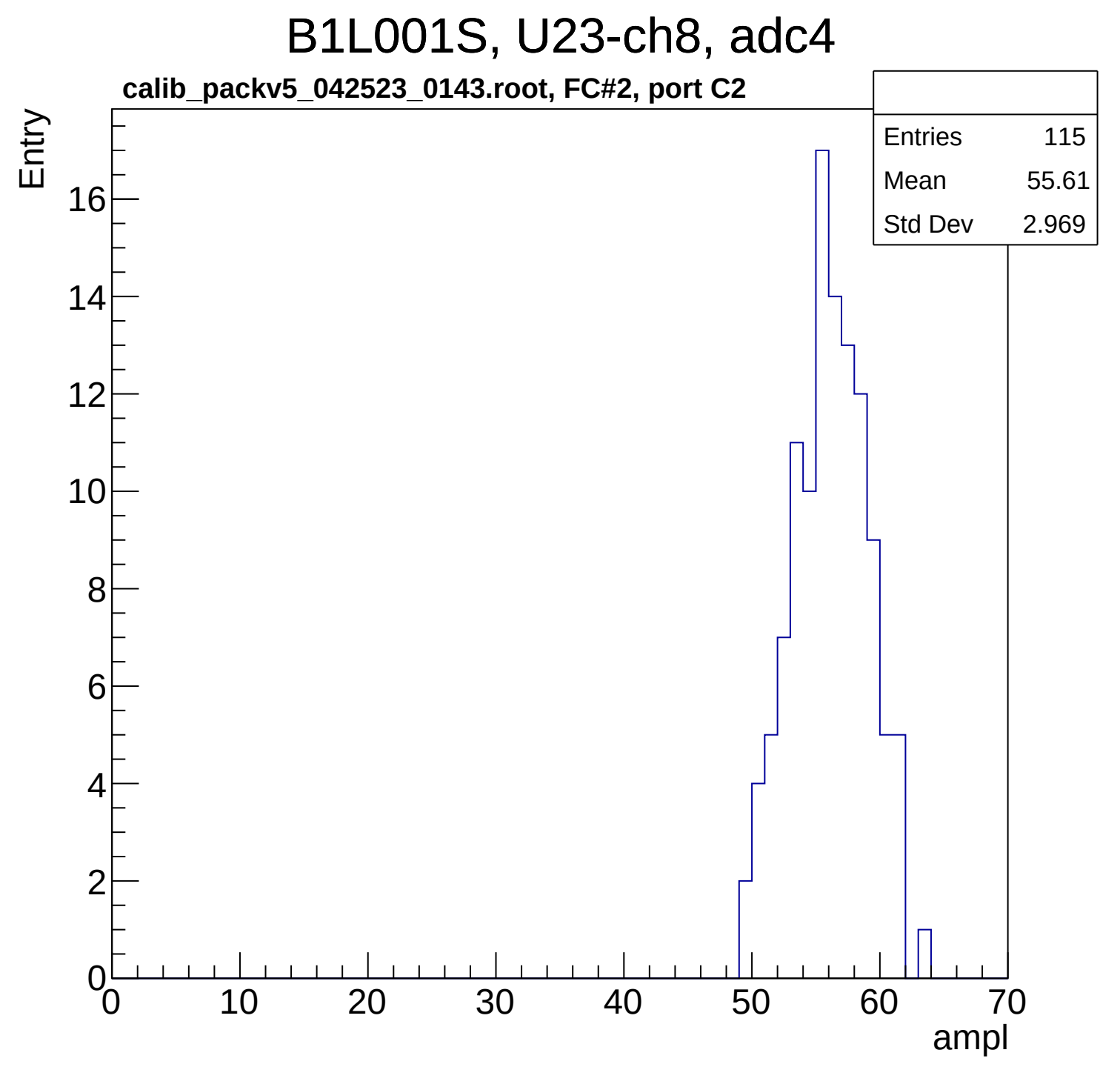

B1L001S, U23-ch8, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 49.83
Std Dev 3.331
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch8, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 55.61
Std Dev 2.969
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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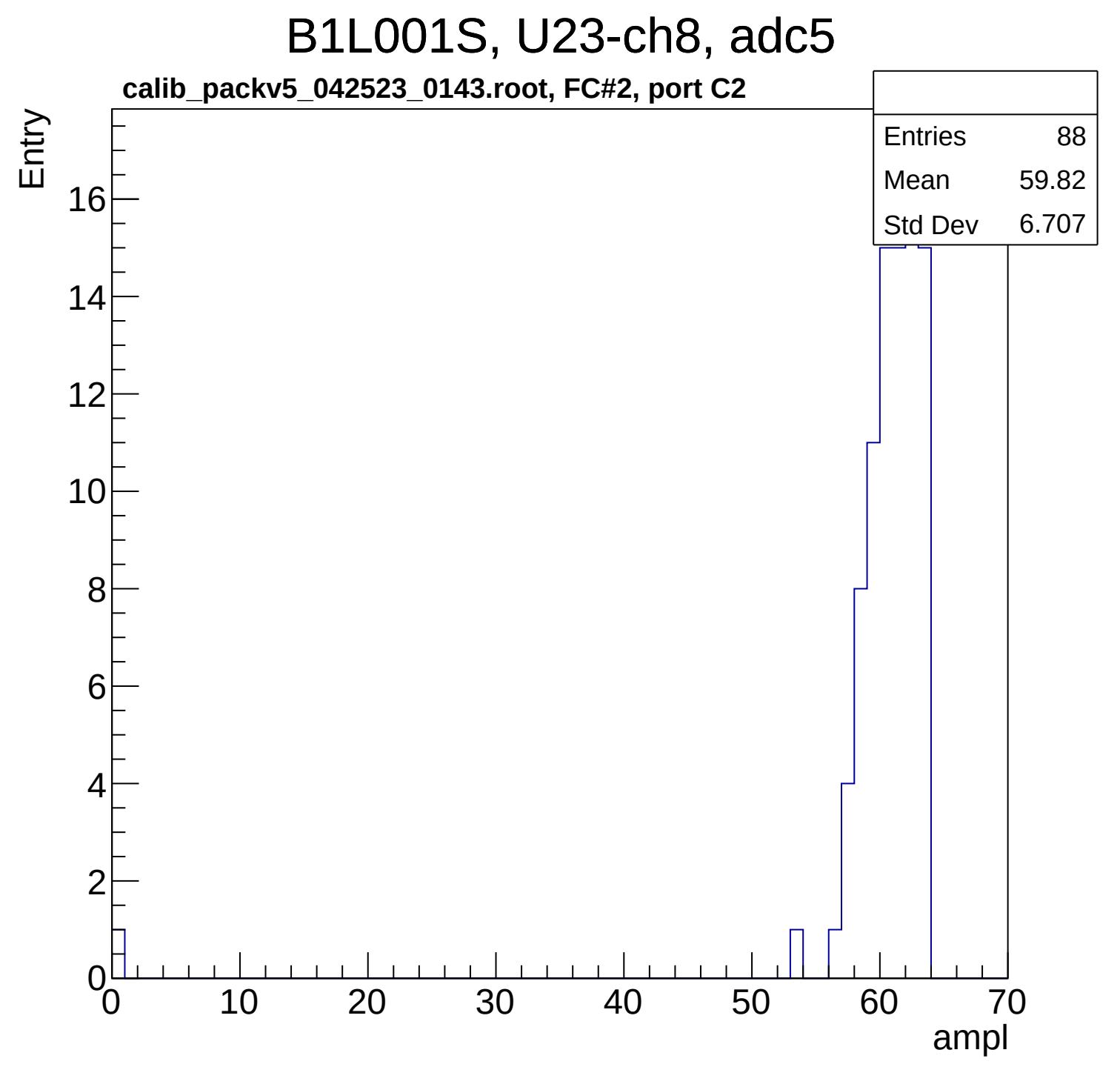
B1L001S, U23-ch8, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.82
Std Dev 6.707
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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60
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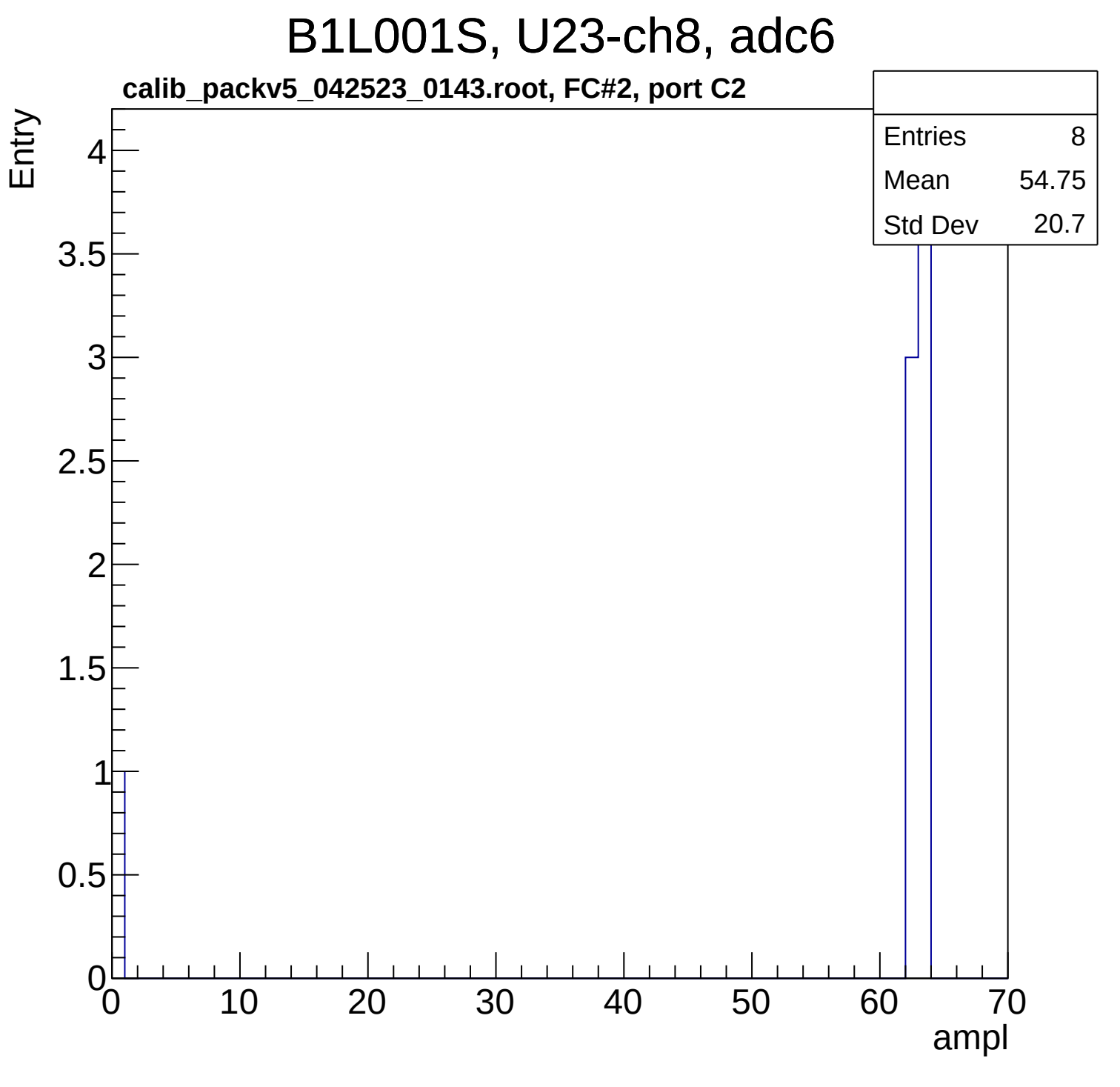
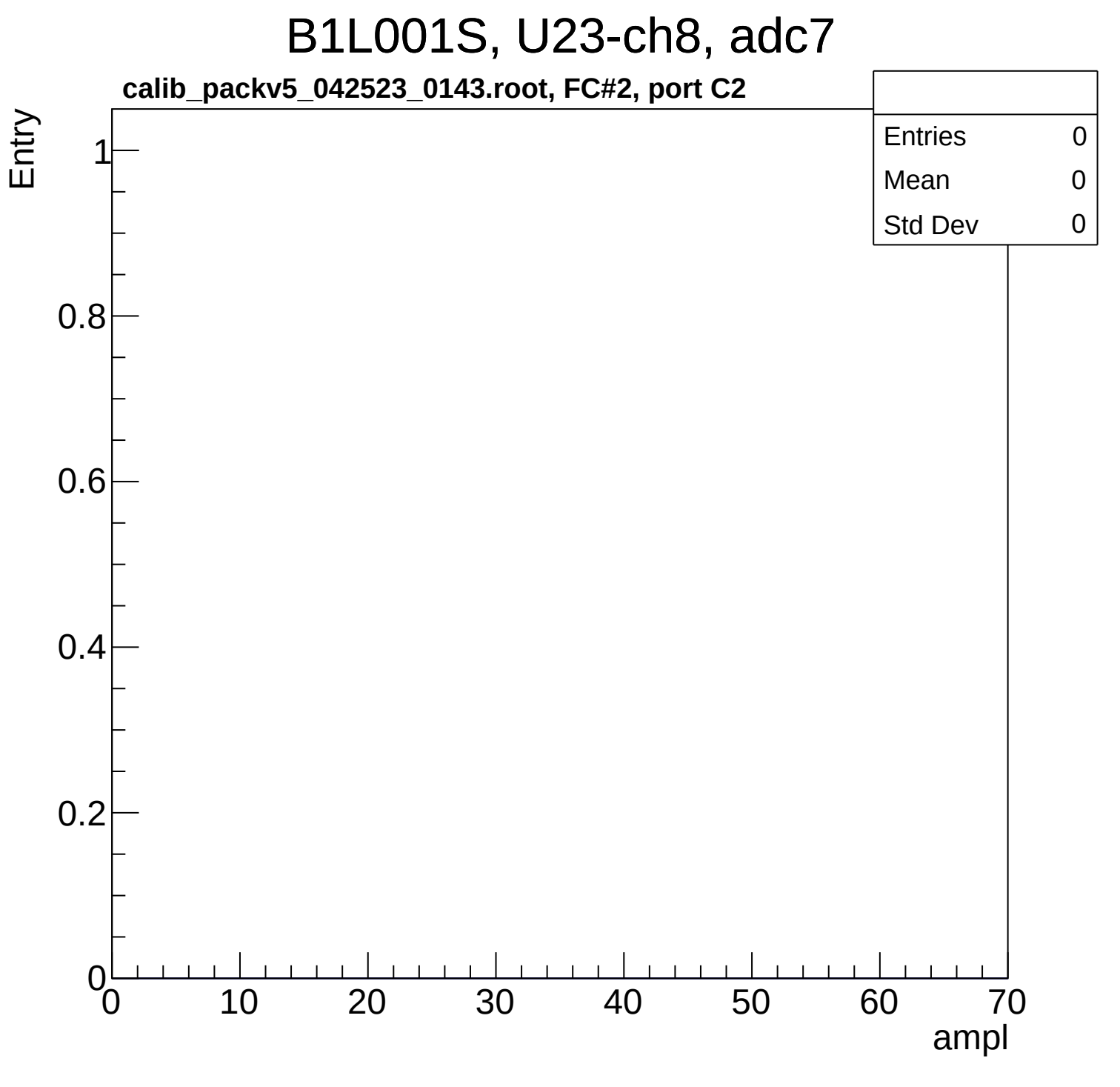

B1L001S, U23-ch8, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.75
Std Dev 20.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch8, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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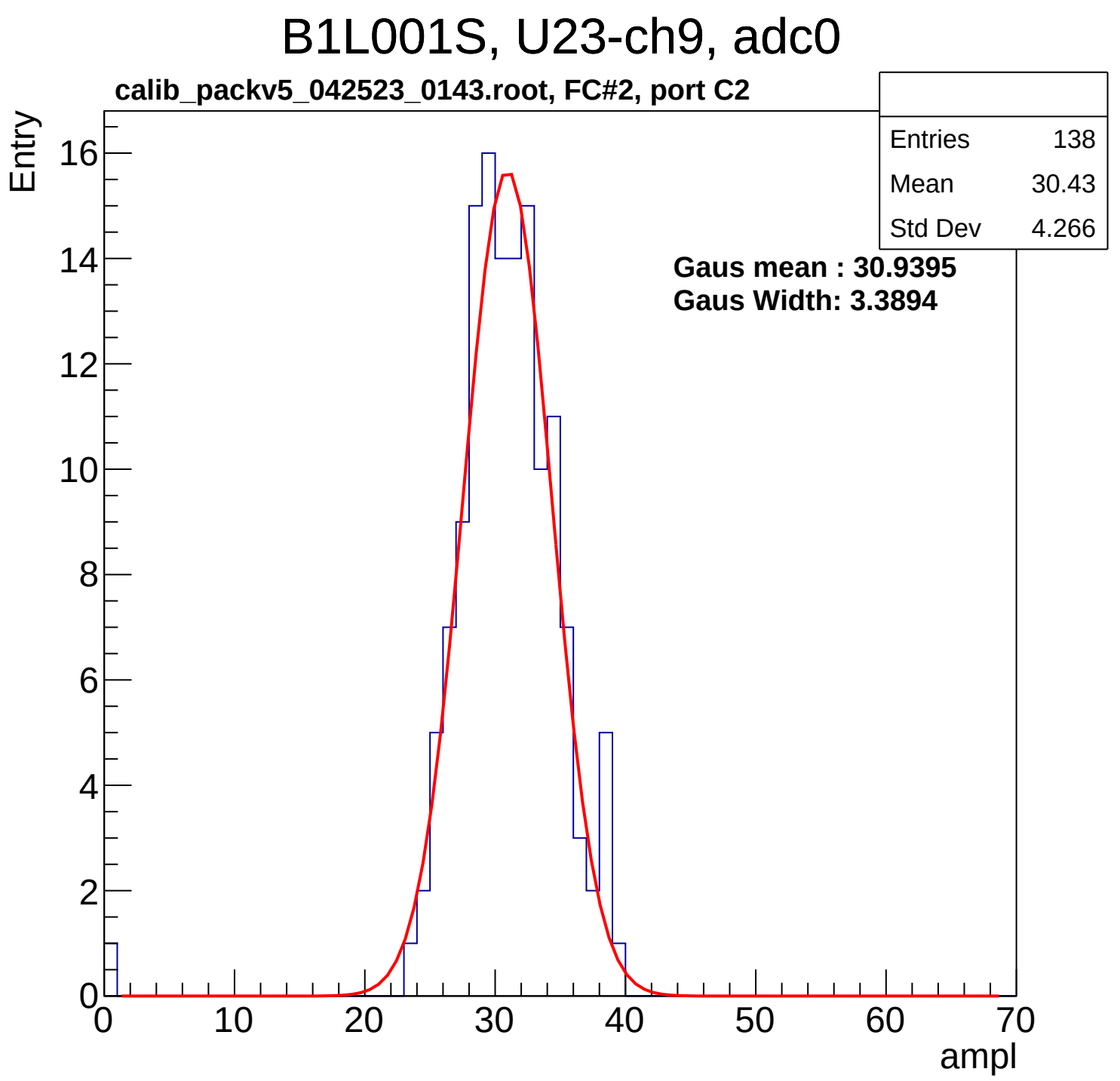
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch9, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 30.43
Std Dev 4.266
Gaus mean : 30.9395
Gaus Width: 3.3894
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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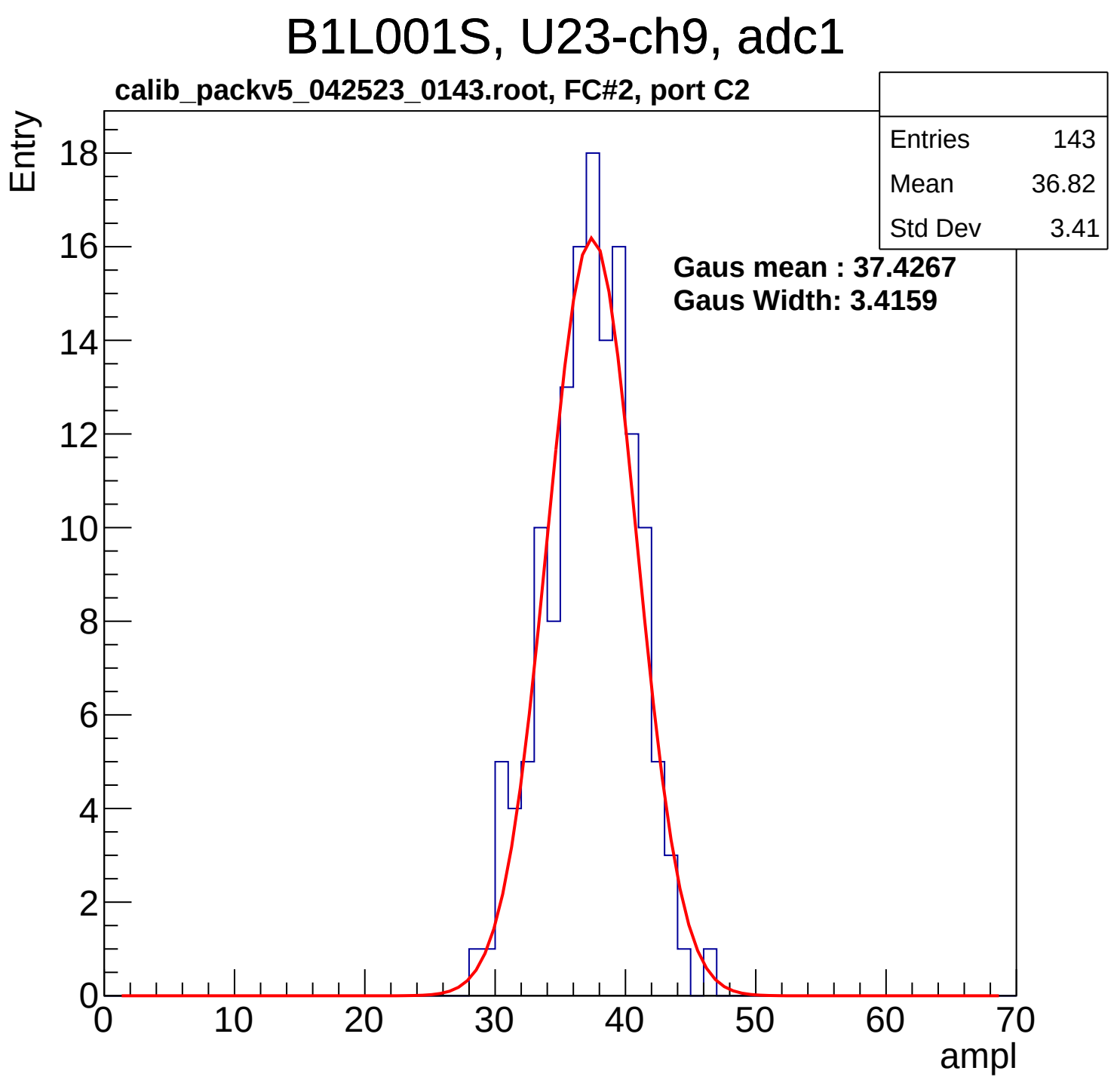
B1L001S, U23-ch9, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 36.82
Std Dev 3.41
Gaus mean : 37.4267
Gaus Width: 3.4159
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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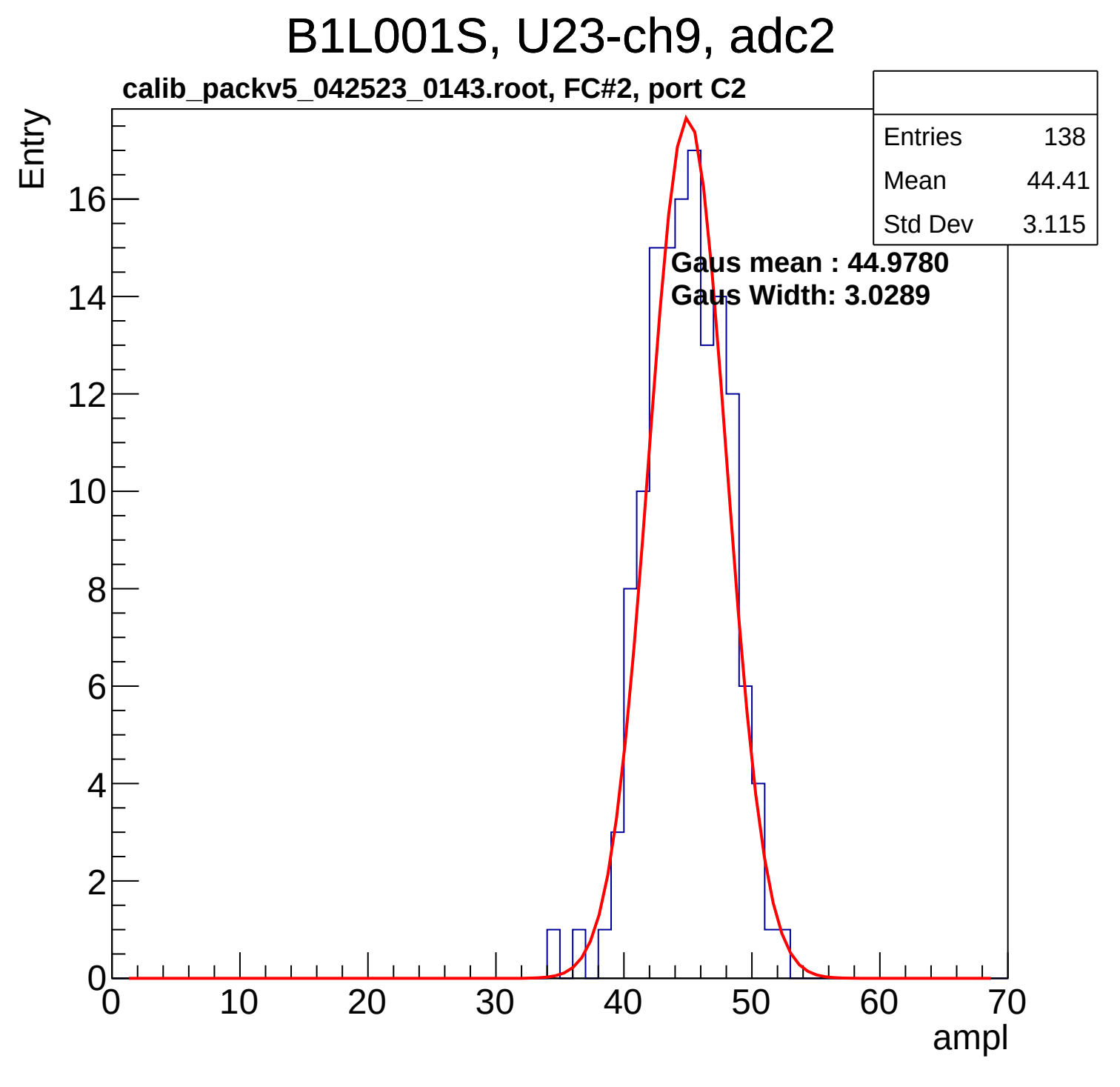
B1L001S, U23-ch9, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 44.41
Std Dev 3.115
Gaus mean : 44.9780
Gaus Width: 3.0289
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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60
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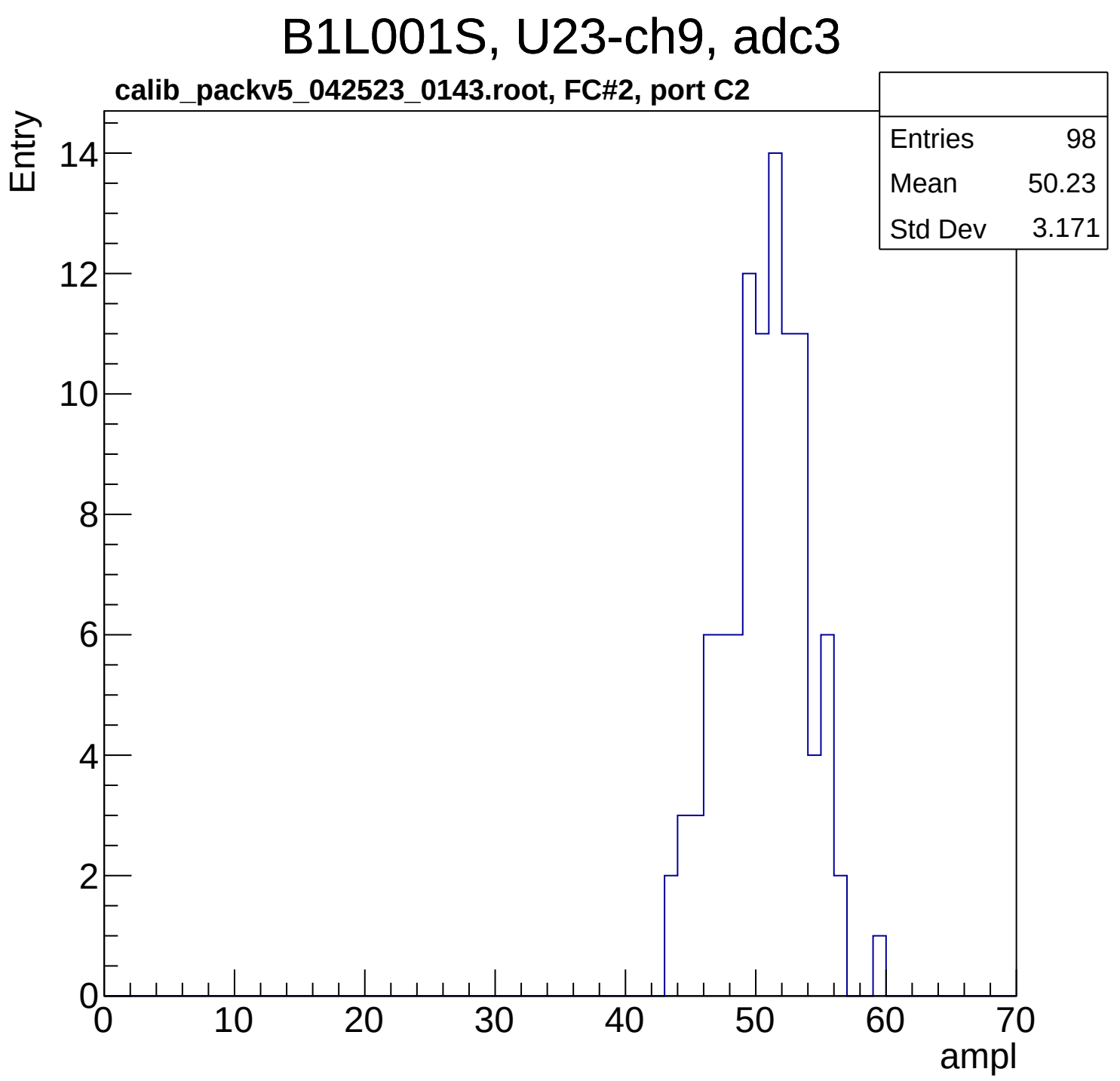
B1L001S, U23-ch9, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 50.23
Std Dev 3.171
Entry
ampl
0
2
4
6
8
10
12
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0
10
20
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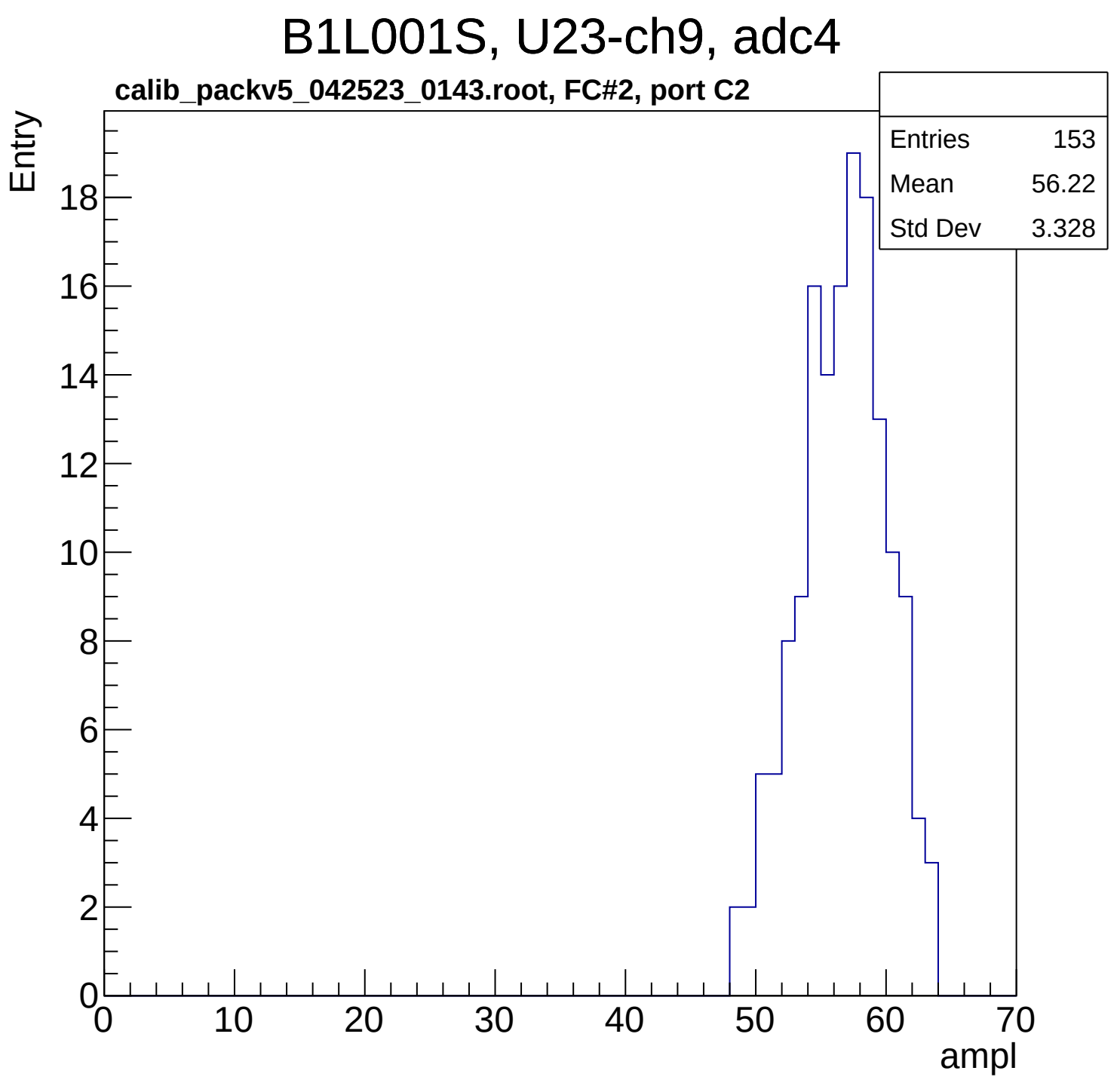
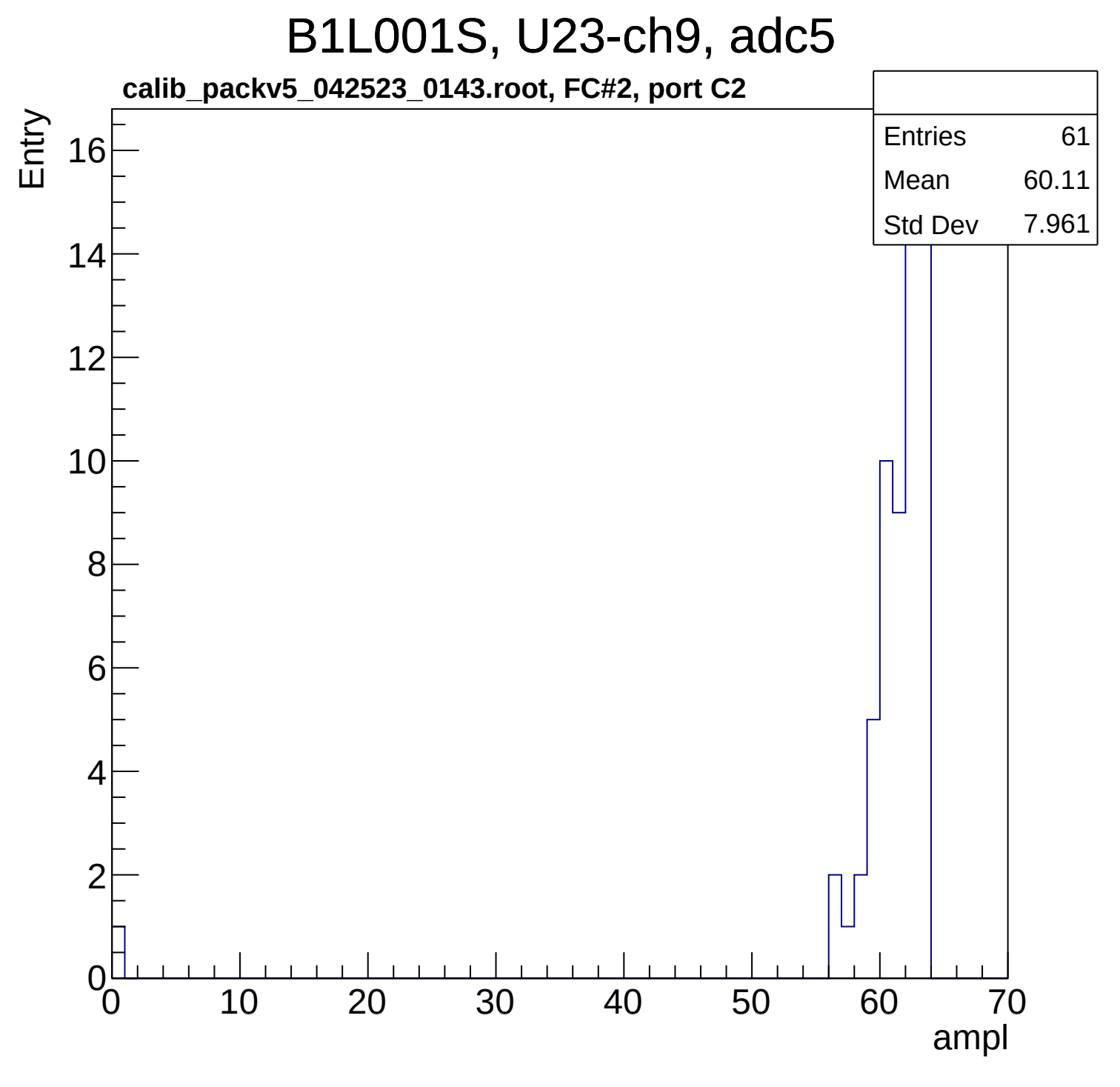
40
50
60
70


B1L001S, U23-ch9, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 56.22
Std Dev 3.328
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch9, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 60.11
Std Dev 7.961
Entry
ampl
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2
4
6
8
10
12
14
16
0
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40
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60
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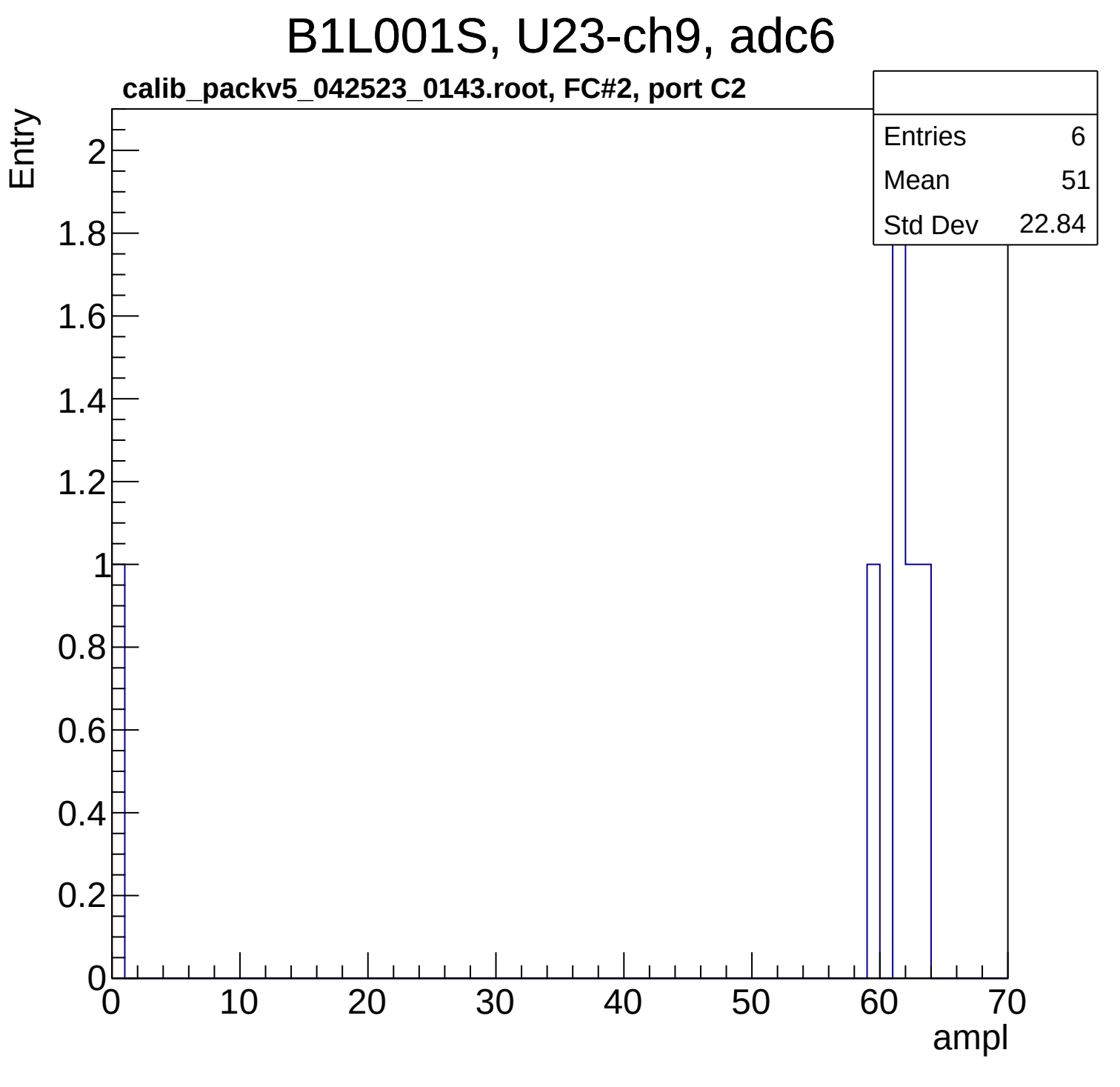
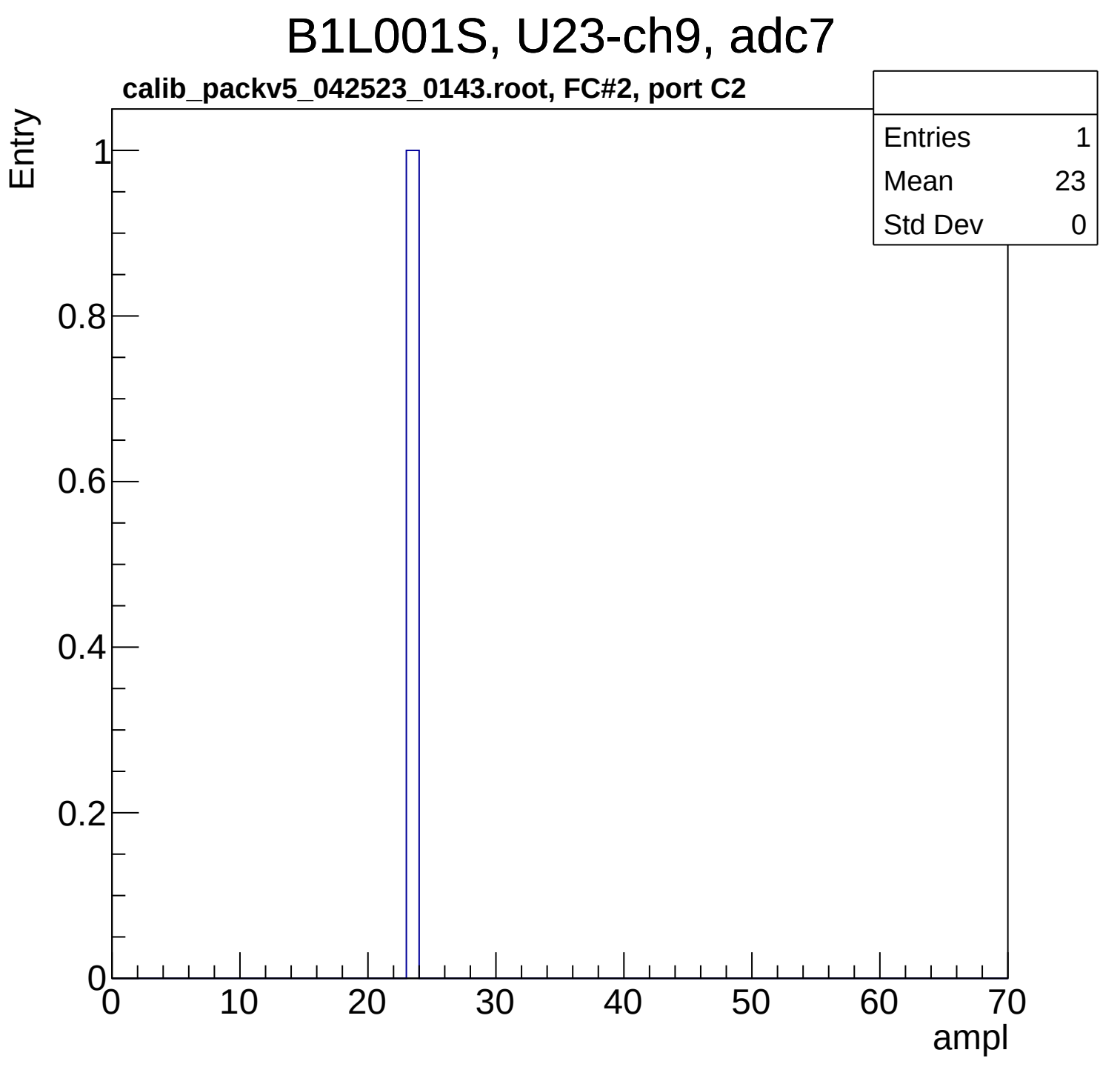

B1L001S, U23-ch9, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 51
Std Dev 22.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch9, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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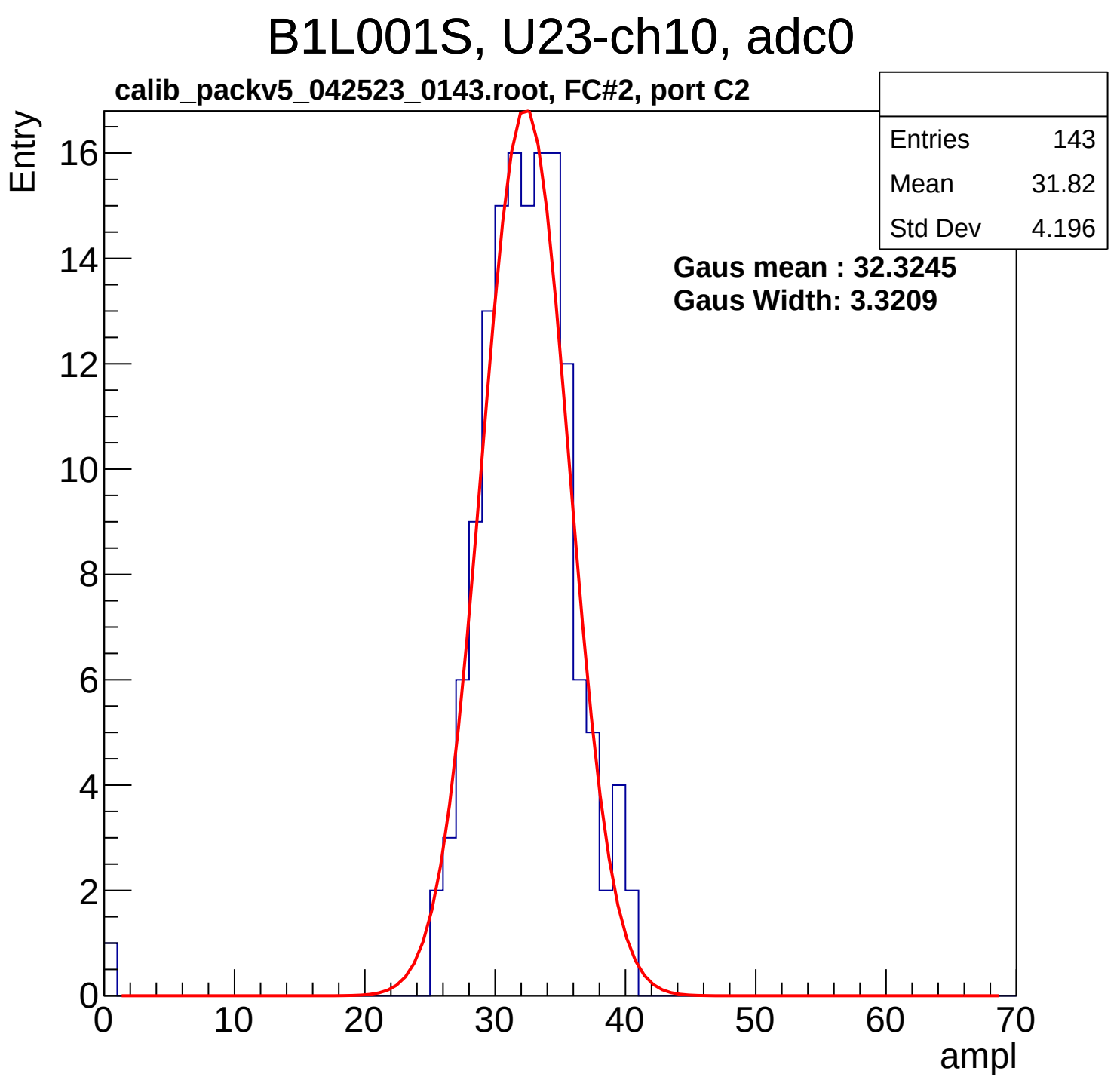
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch10, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 31.82
Std Dev 4.196
Gaus mean : 32.3245
Gaus Width: 3.3209
Entry
ampl
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2
4
6
8
10
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14
16
0
10
20
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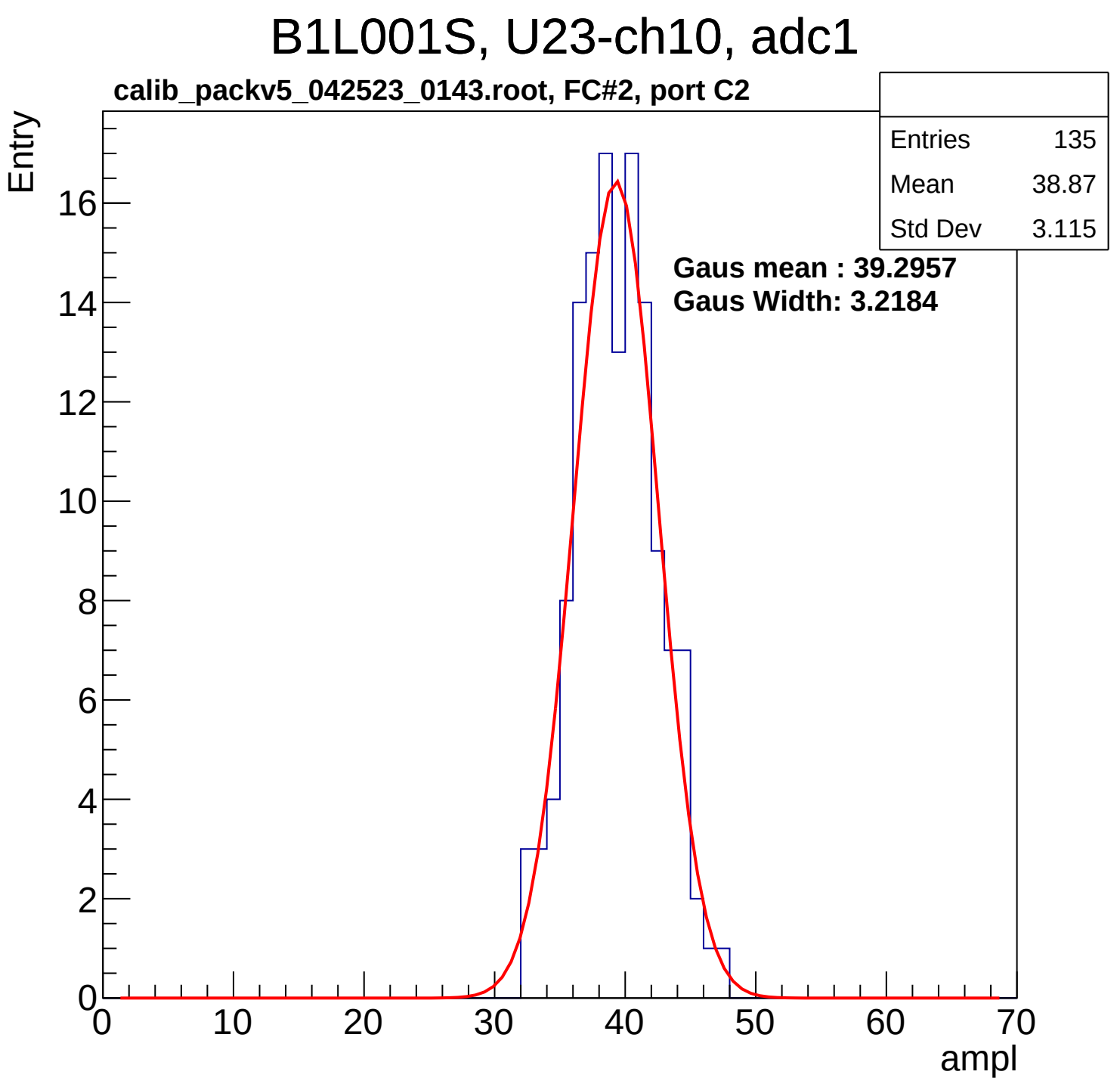
40
50
60
70


B1L001S, U23-ch10, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 38.87
Std Dev 3.115
Gaus mean : 39.2957
Gaus Width: 3.2184
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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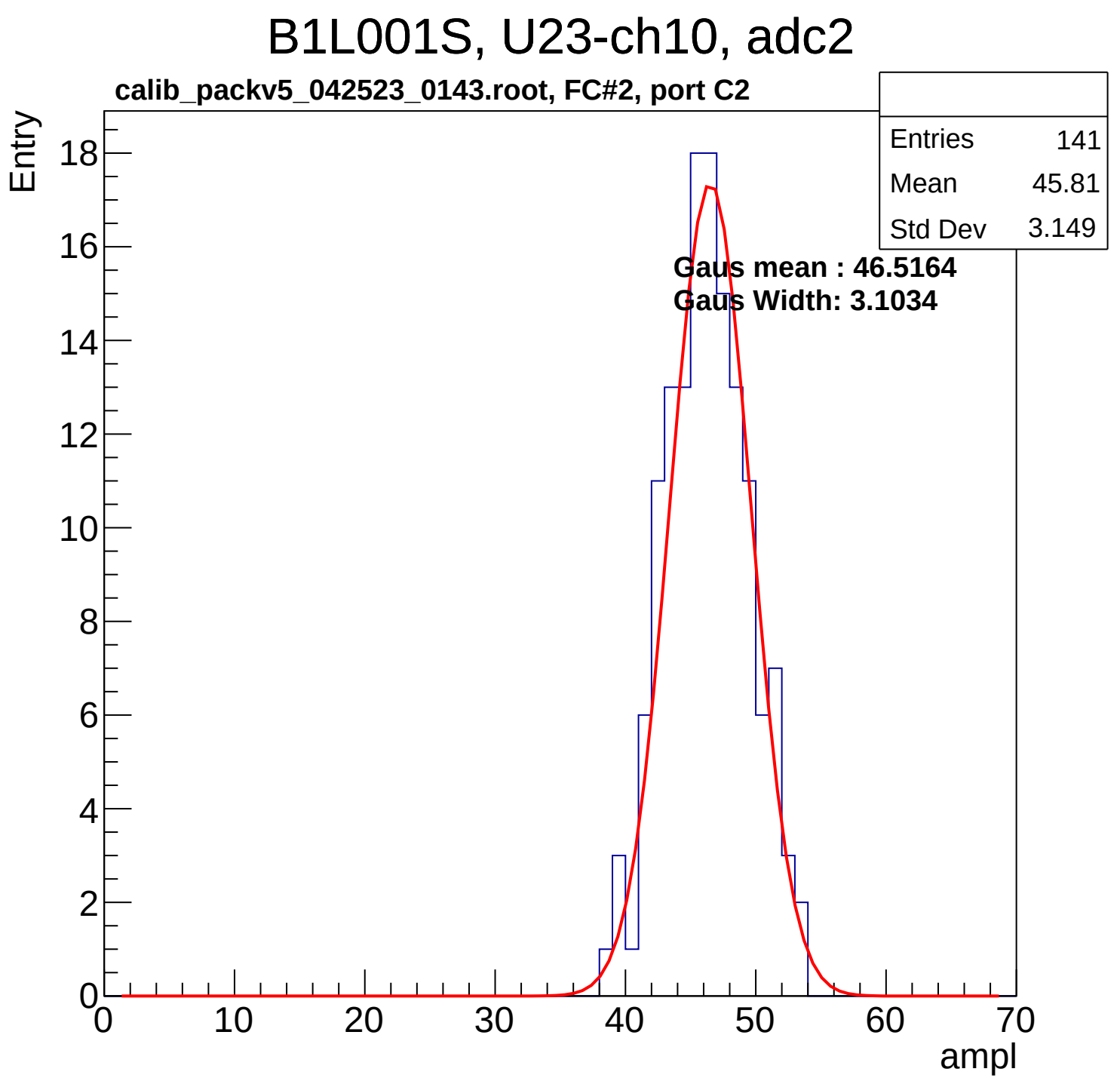
B1L001S, U23-ch10, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 45.81
Std Dev 3.149
Gaus mean : 46.5164
Gaus Width: 3.1034
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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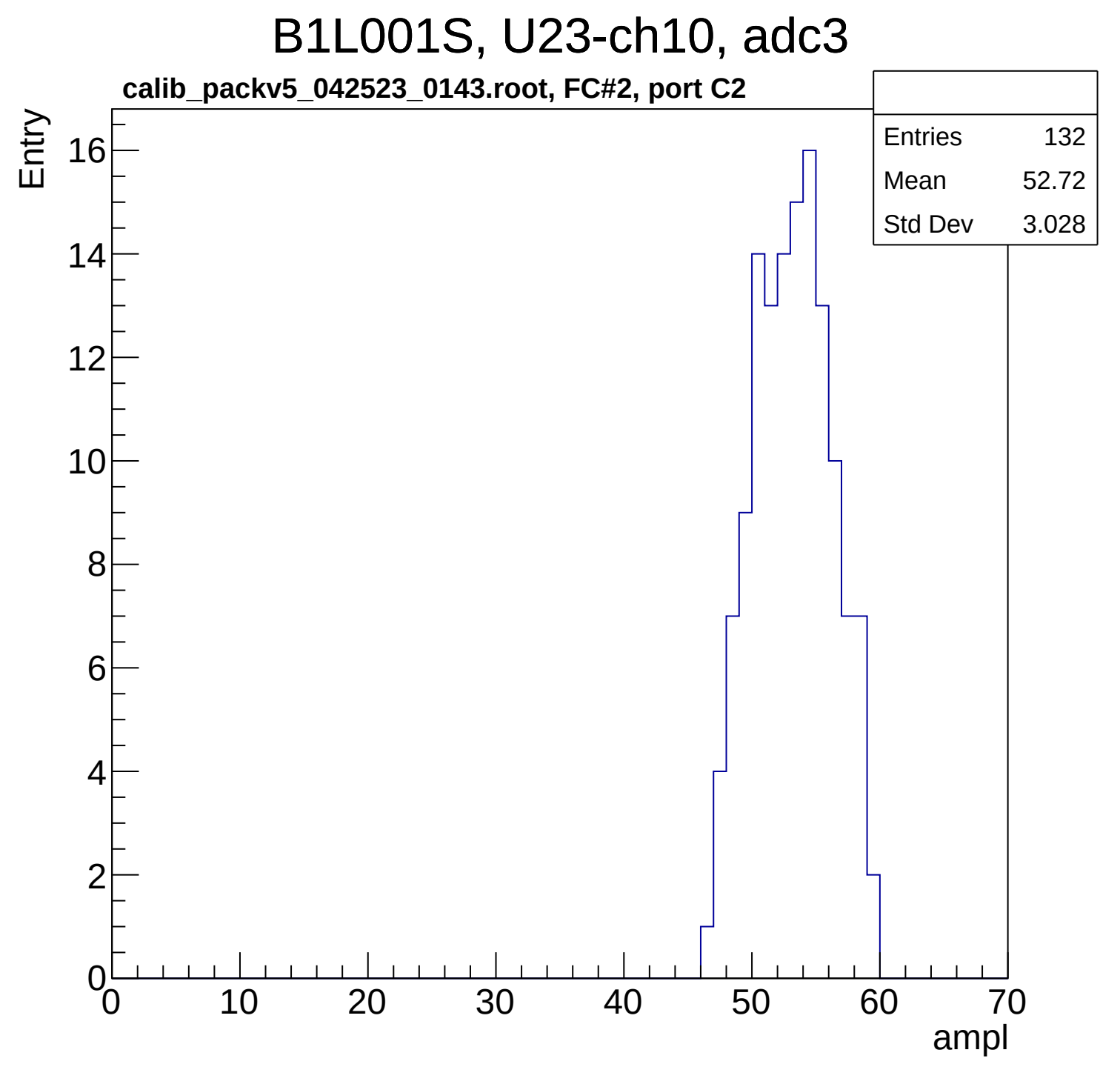
B1L001S, U23-ch10, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 52.72
Std Dev 3.028
Entry
ampl
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4
6
8
10
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14
16
0
10
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60
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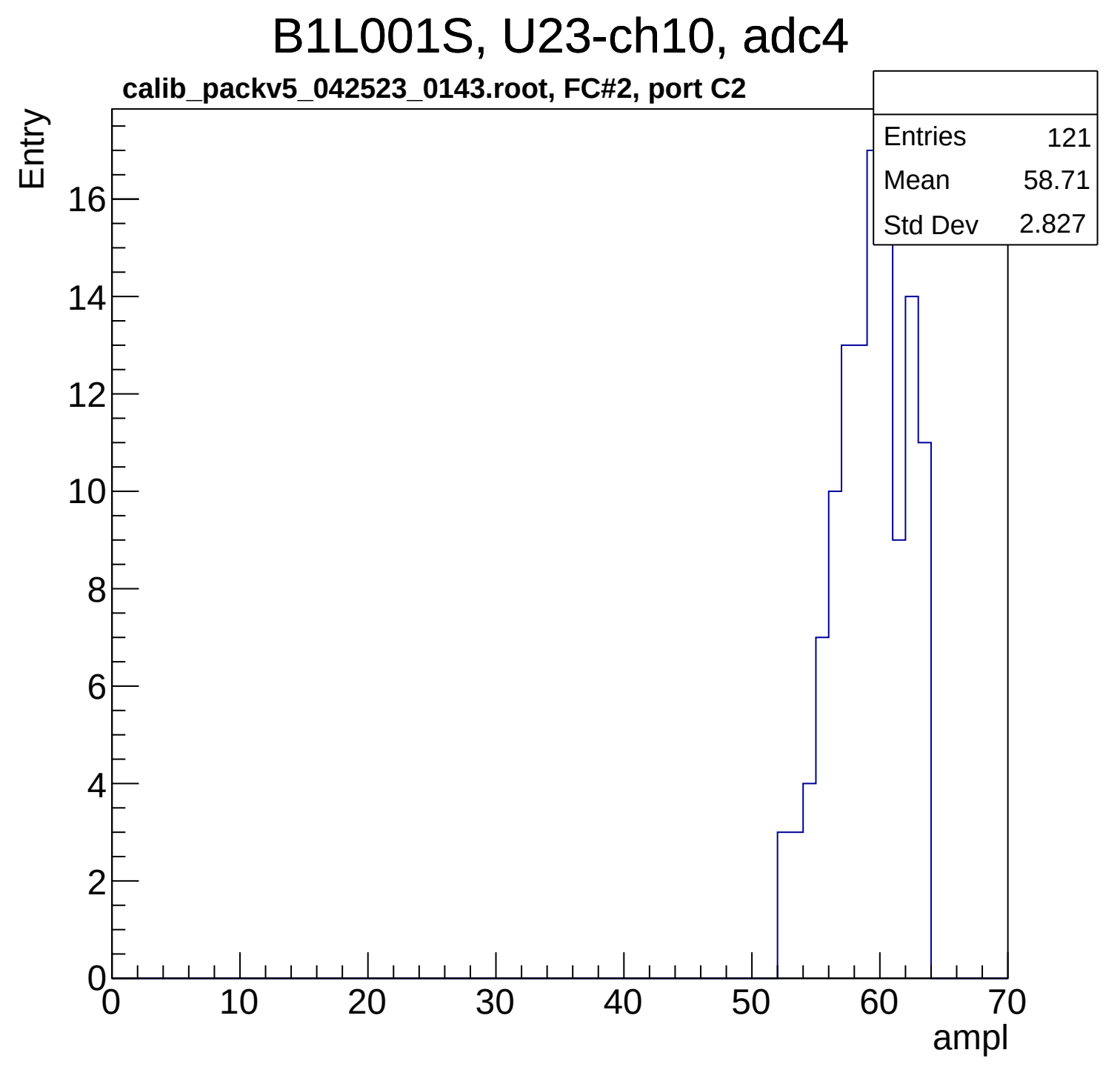
B1L001S, U23-ch10, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 58.71
Std Dev 2.827
Entry
ampl
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8
10
12
14
16
0
10
20
30
40
50
60
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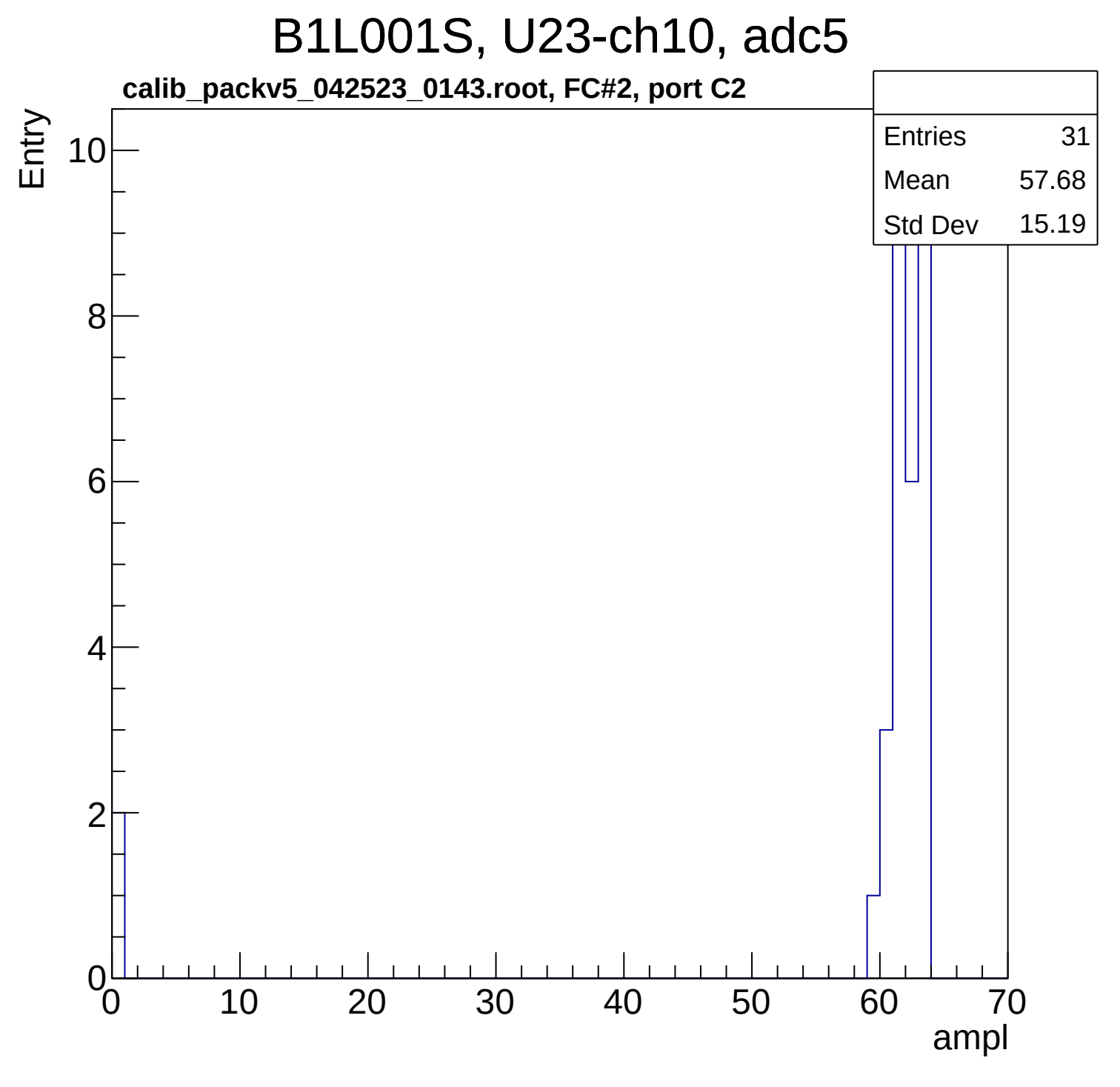
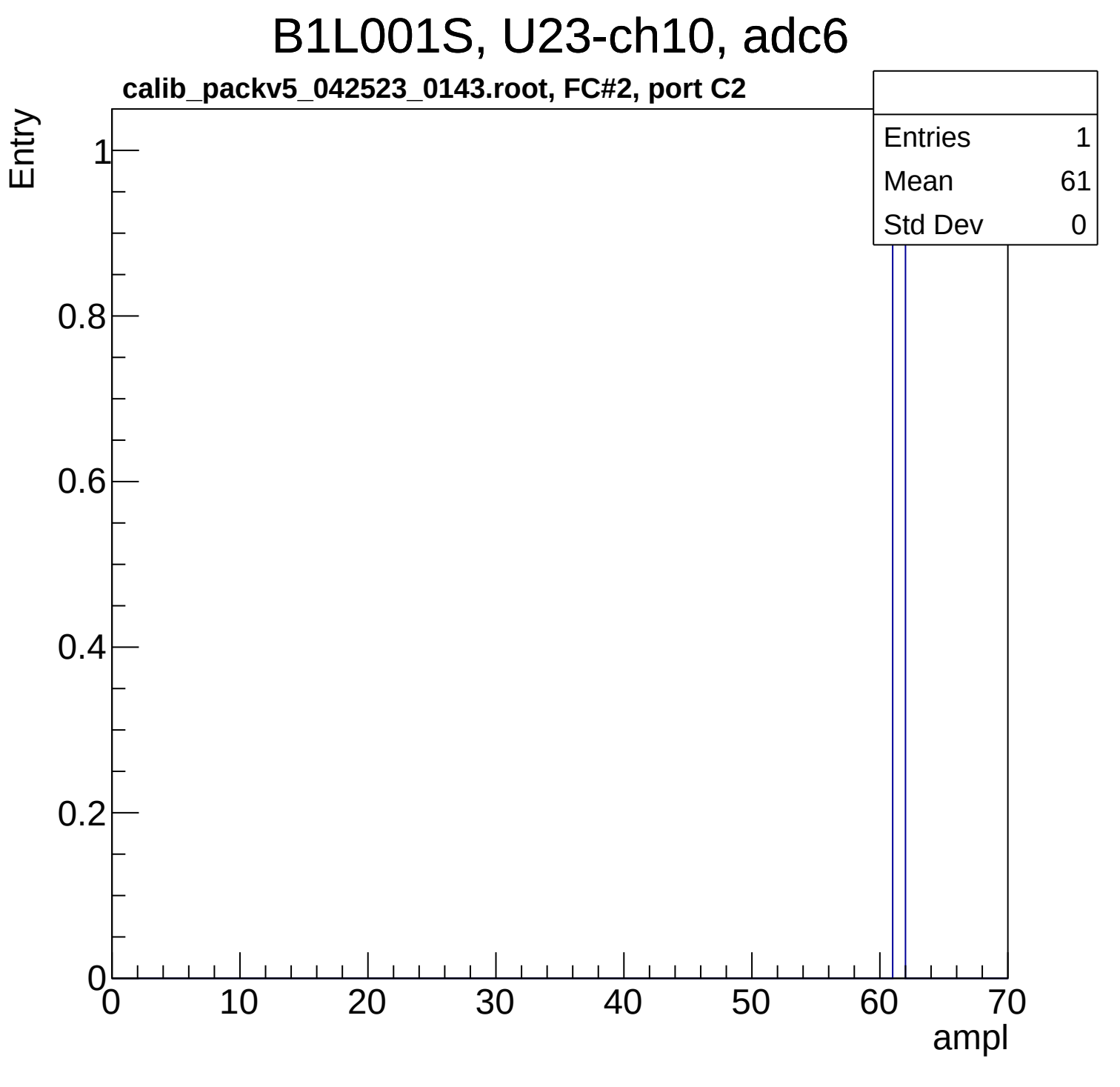
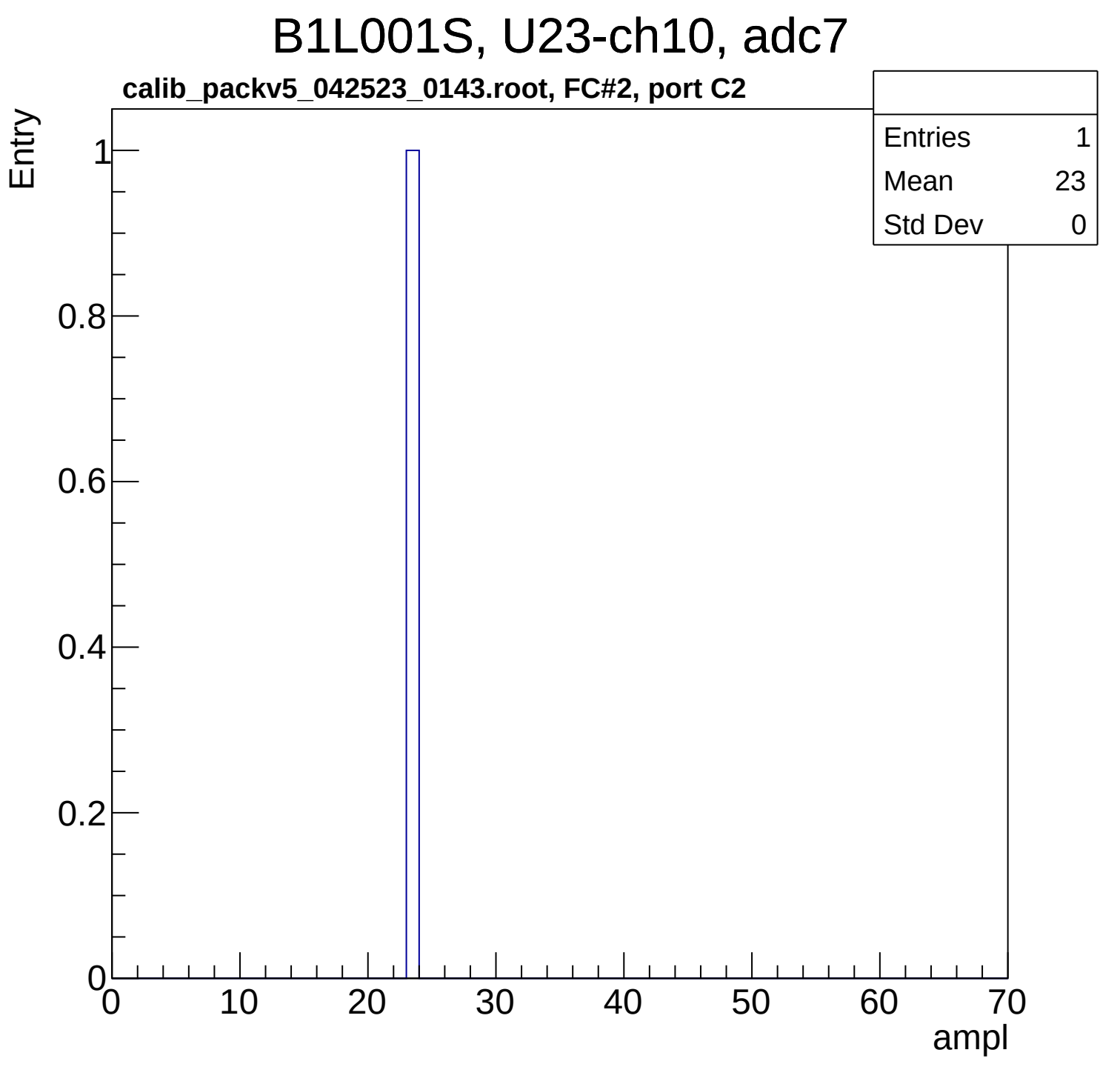

B1L001S, U23-ch10, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 31
Mean 57.68
Std Dev 15.19
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch10, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch10, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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0
10
20
30
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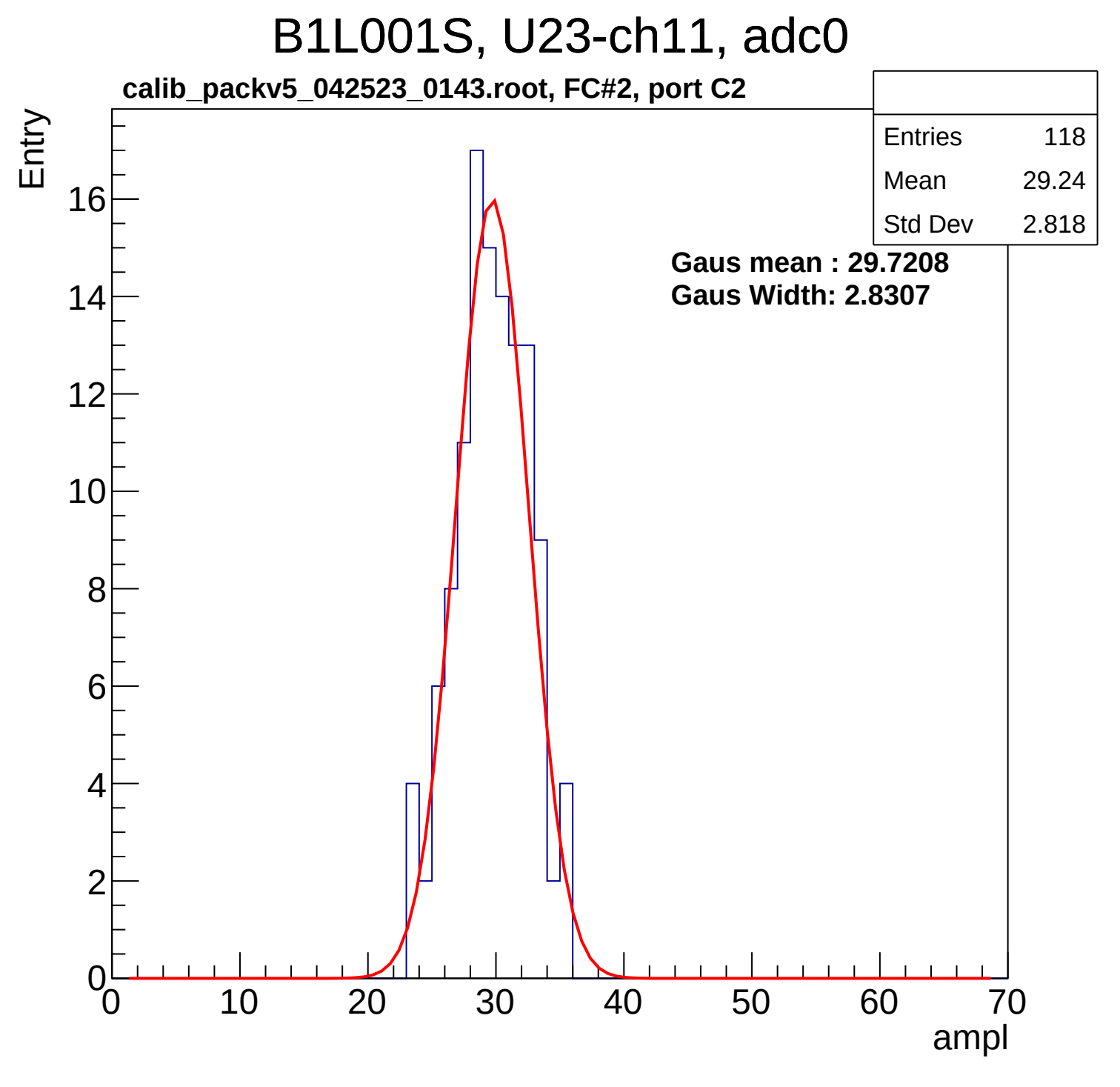
50
60
70
ampl


B1L001S, U23-ch11, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 29.24
Std Dev 2.818
Gaus mean : 29.7208
Gaus Width: 2.8307
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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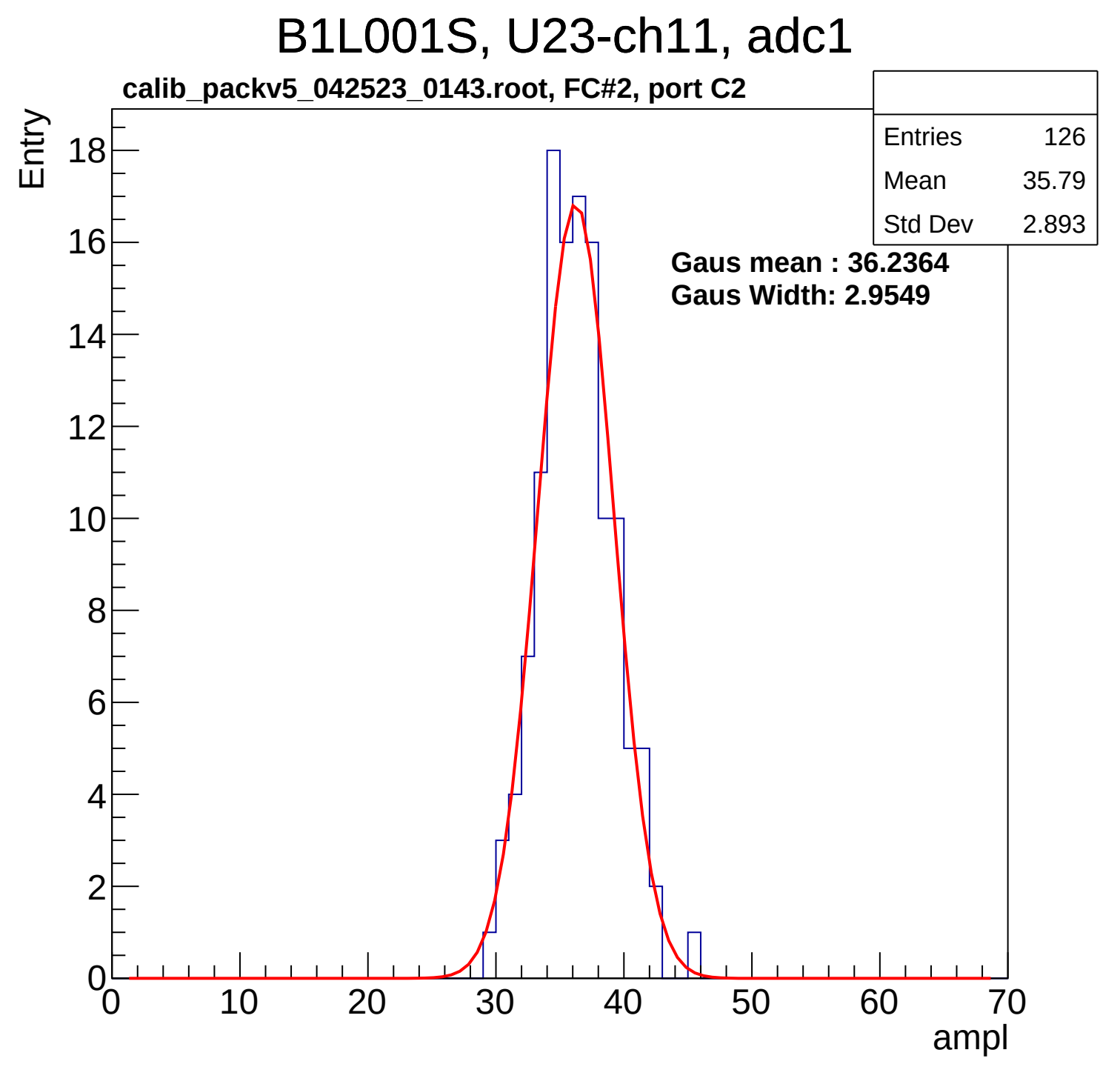
B1L001S, U23-ch11, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 35.79
Std Dev 2.893
Gaus mean : 36.2364
Gaus Width: 2.9549
Entry
ampl
0
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4
6
8
10
12
14
16
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0
10
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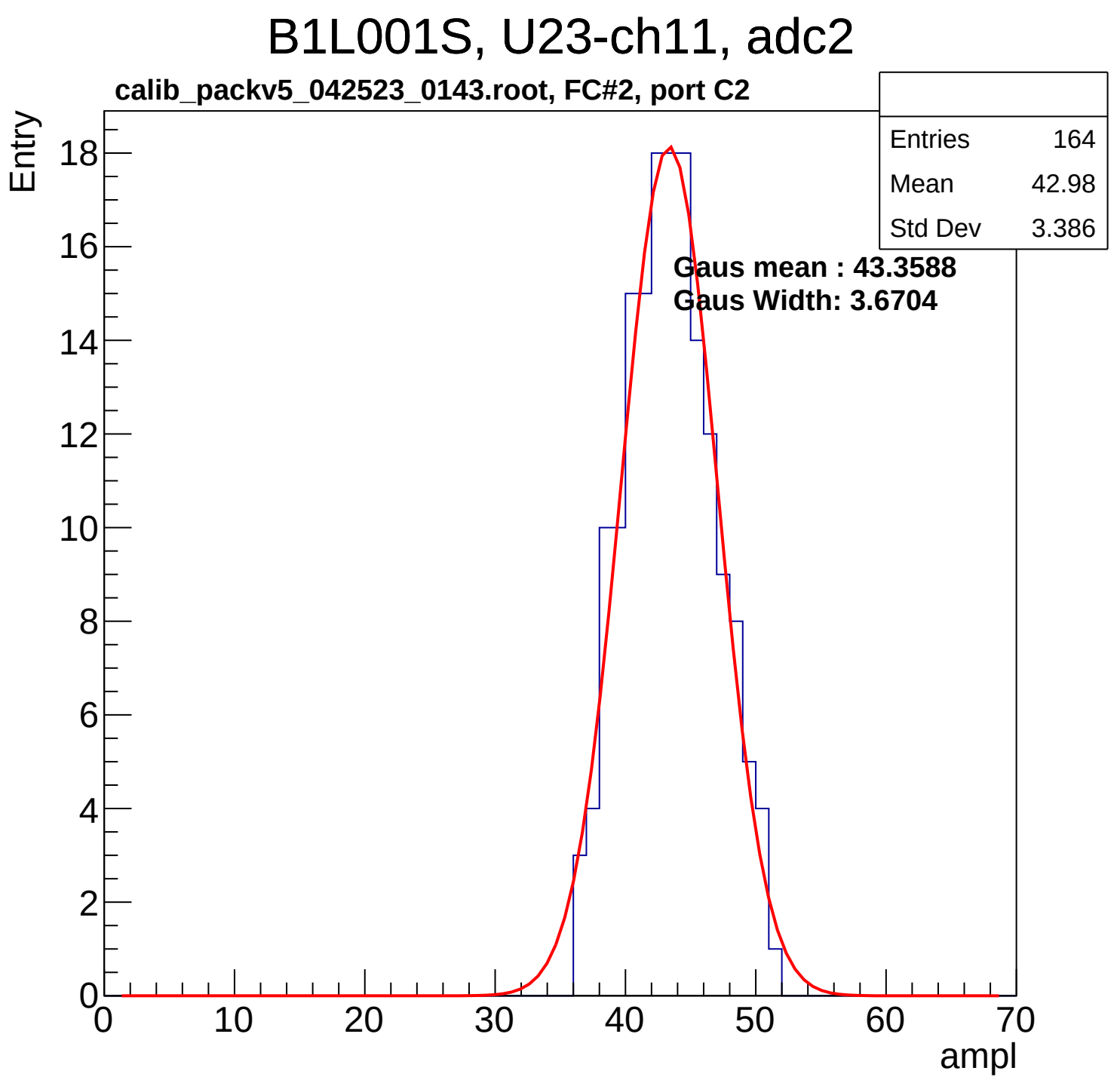
B1L001S, U23-ch11, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 164
Mean 42.98
Std Dev 3.386
Gaus mean : 43.3588
Gaus Width: 3.6704
Entry
ampl
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4
6
8
10
12
14
16
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0
10
20
30
40
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60
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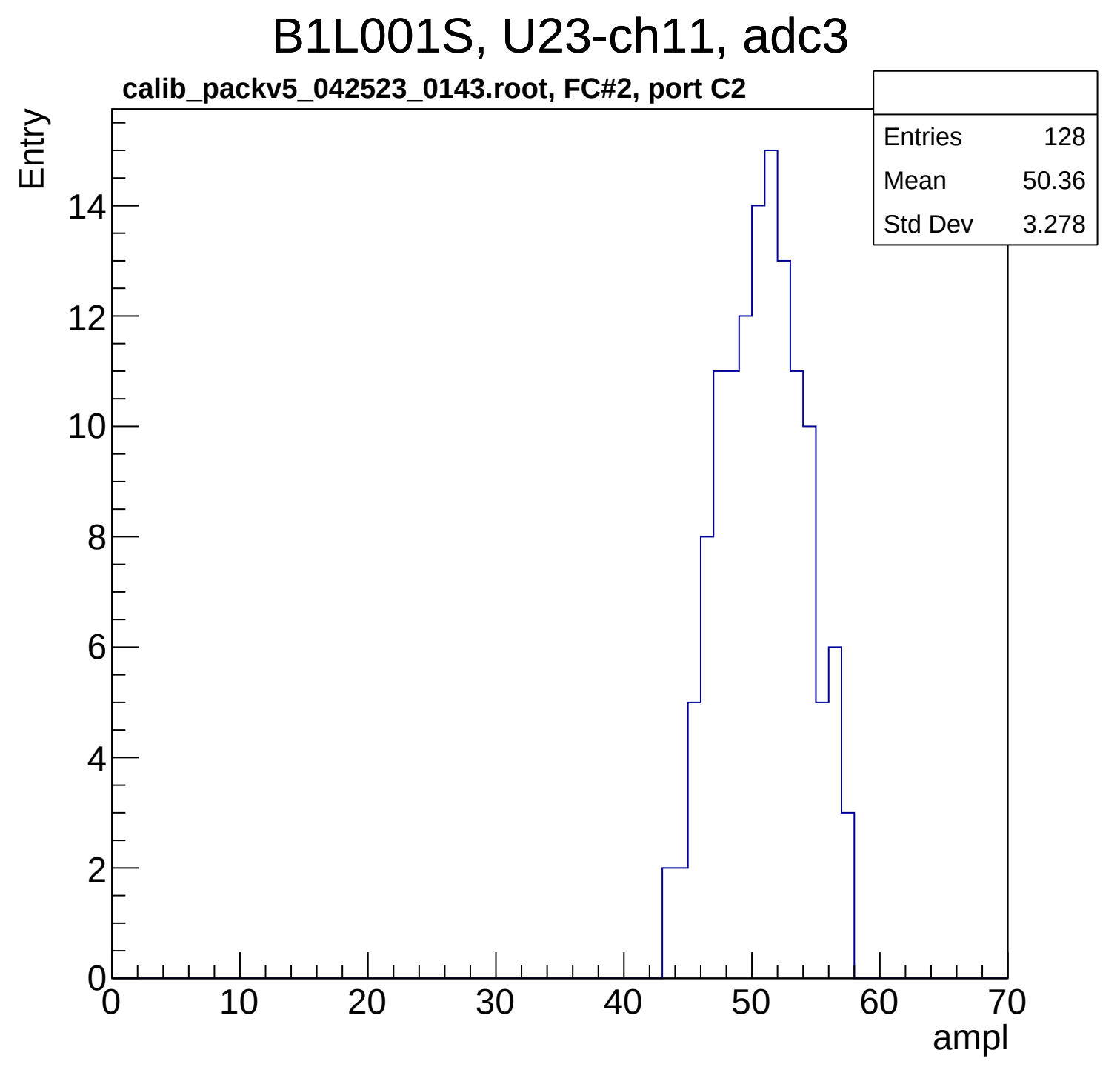
B1L001S, U23-ch11, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 50.36
Std Dev 3.278
Entry
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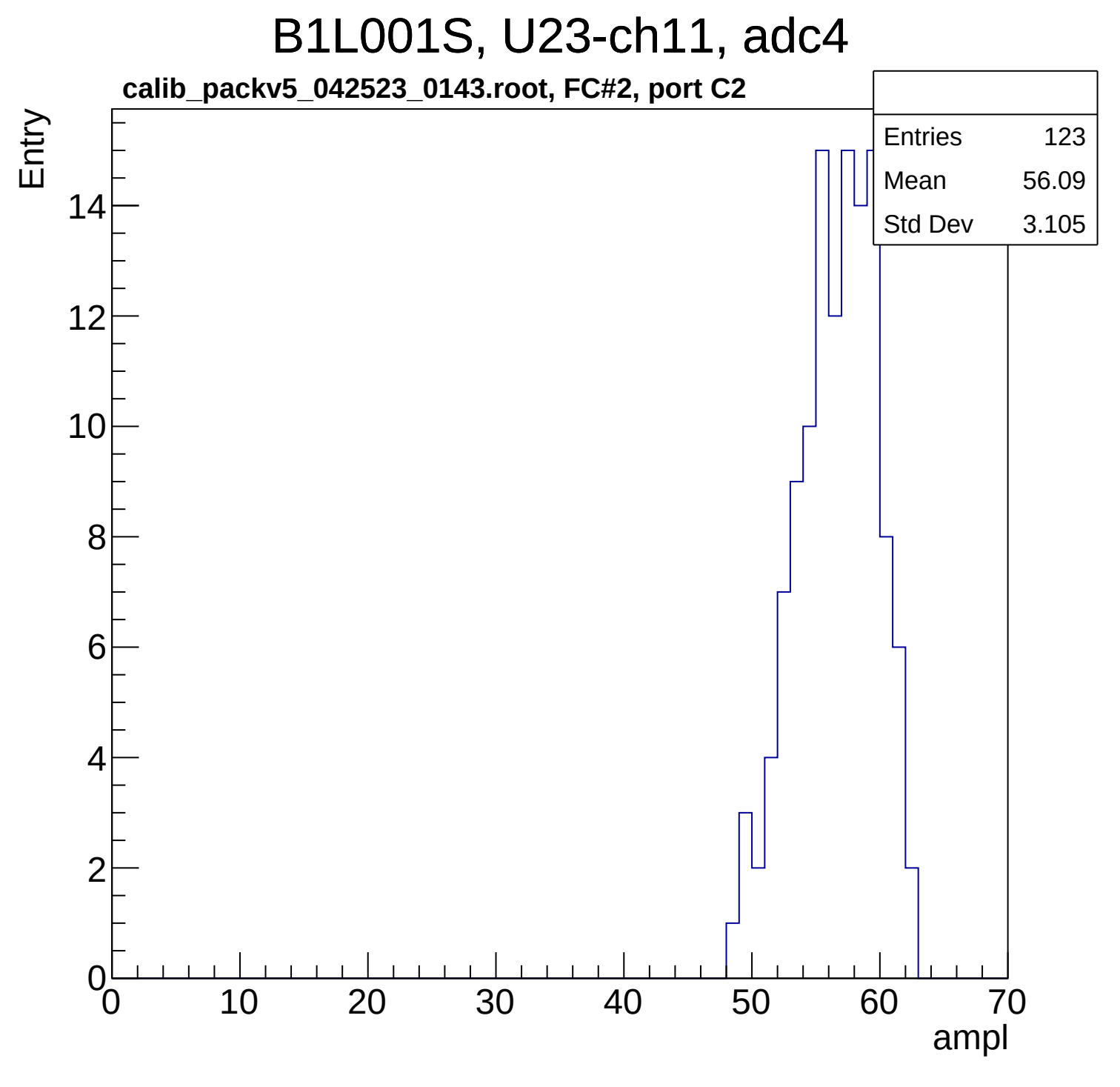
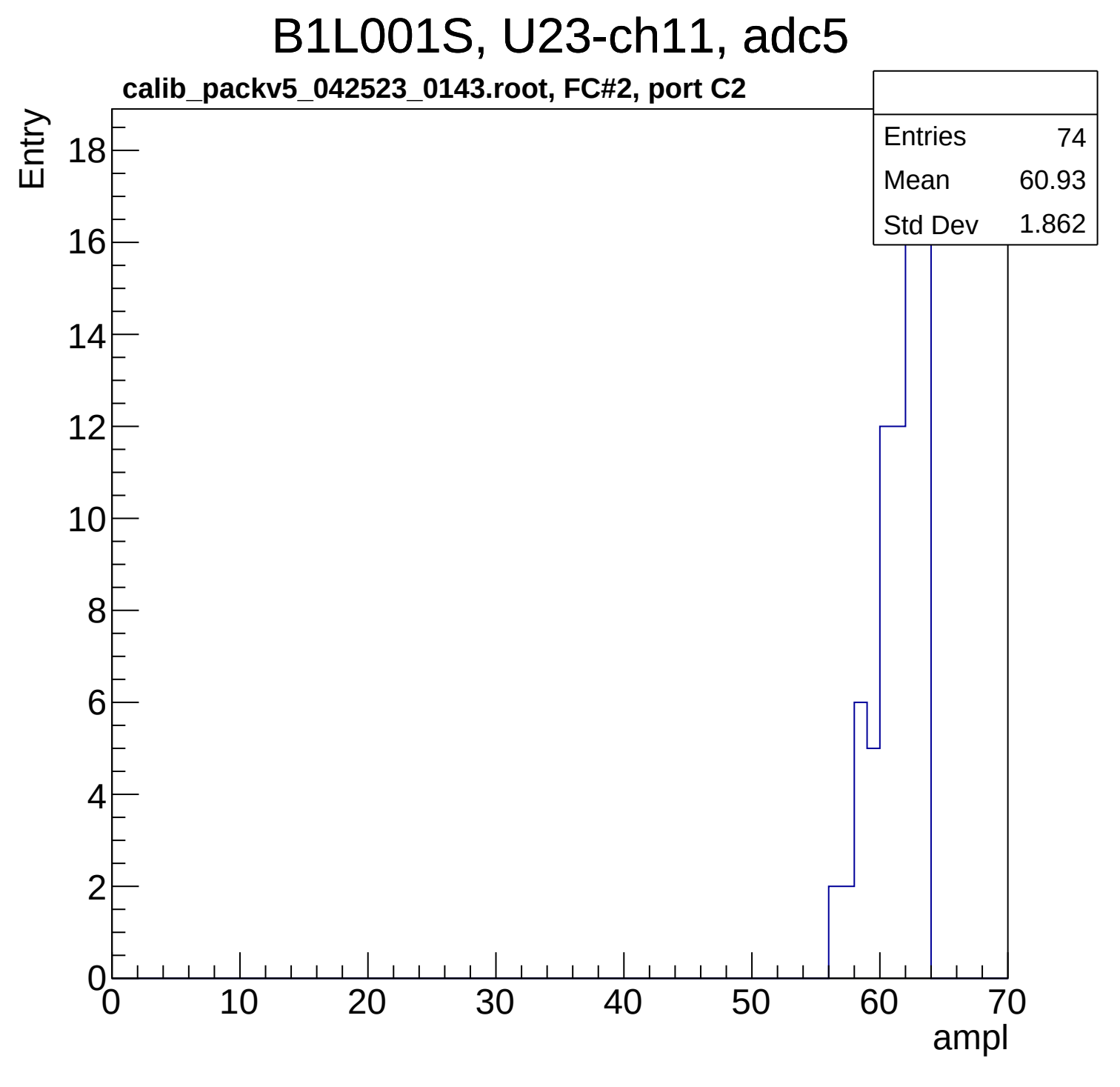
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch11, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 56.09
Std Dev 3.105
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch11, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 60.93
Std Dev 1.862
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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40
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60
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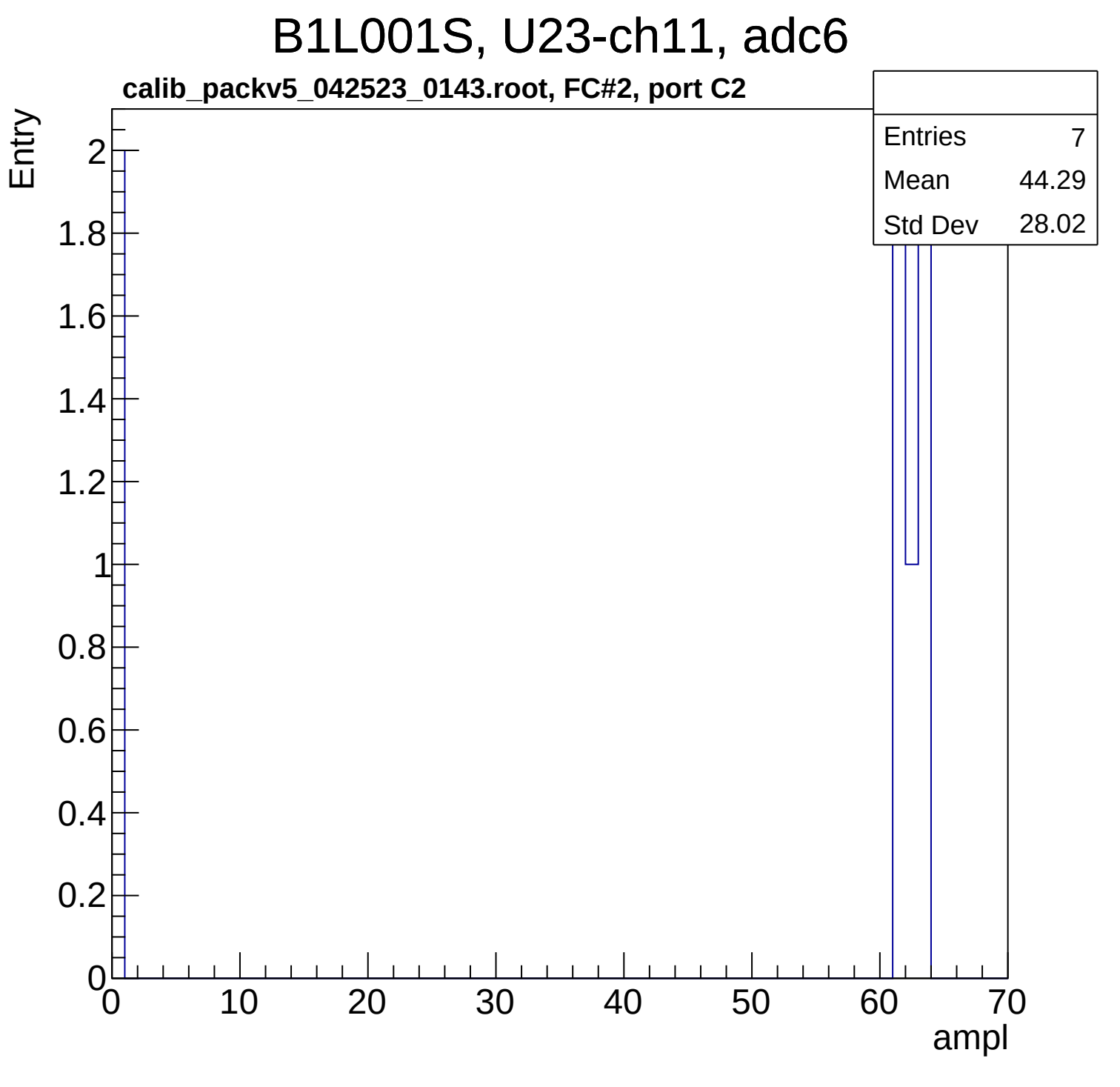
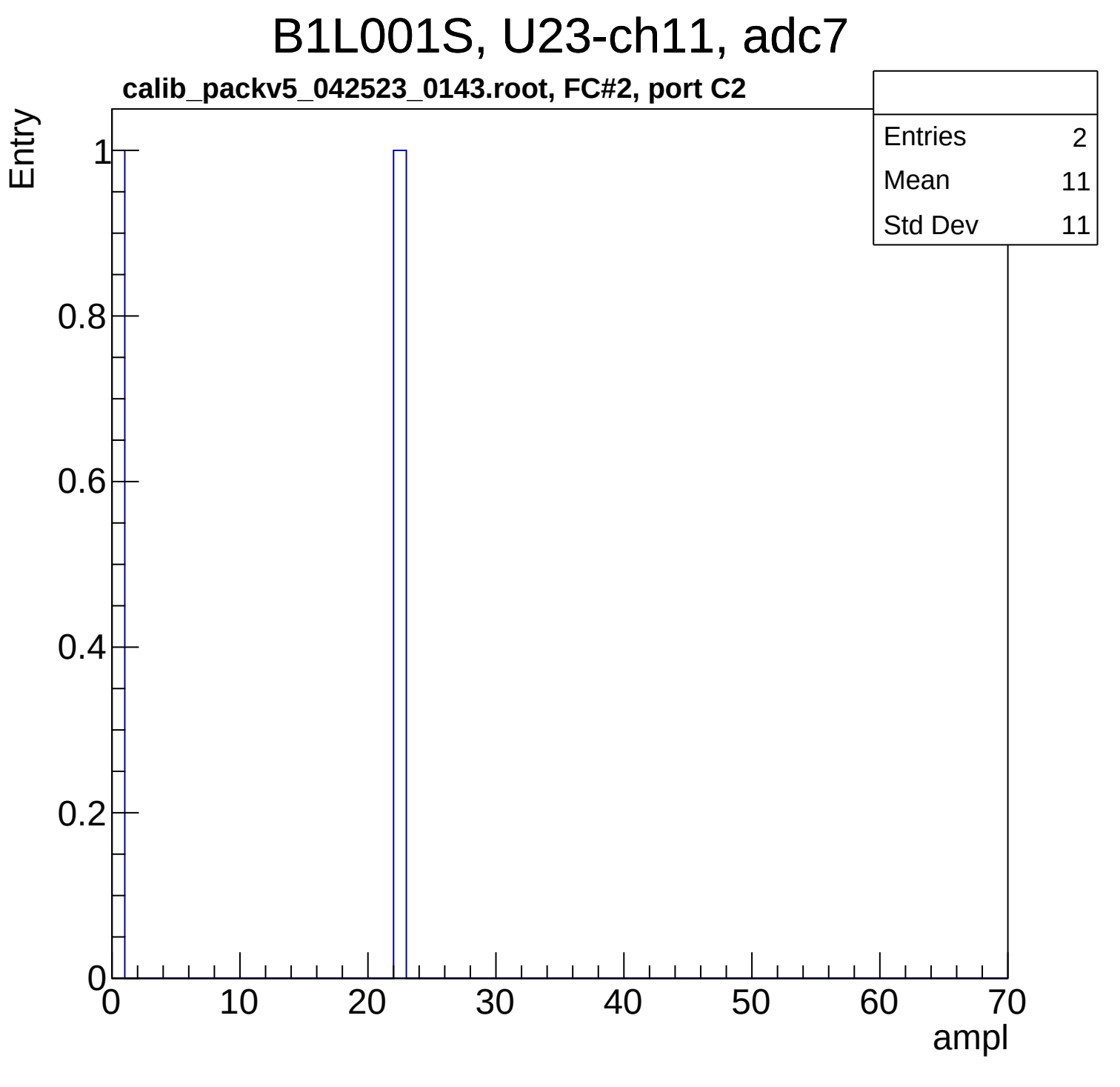
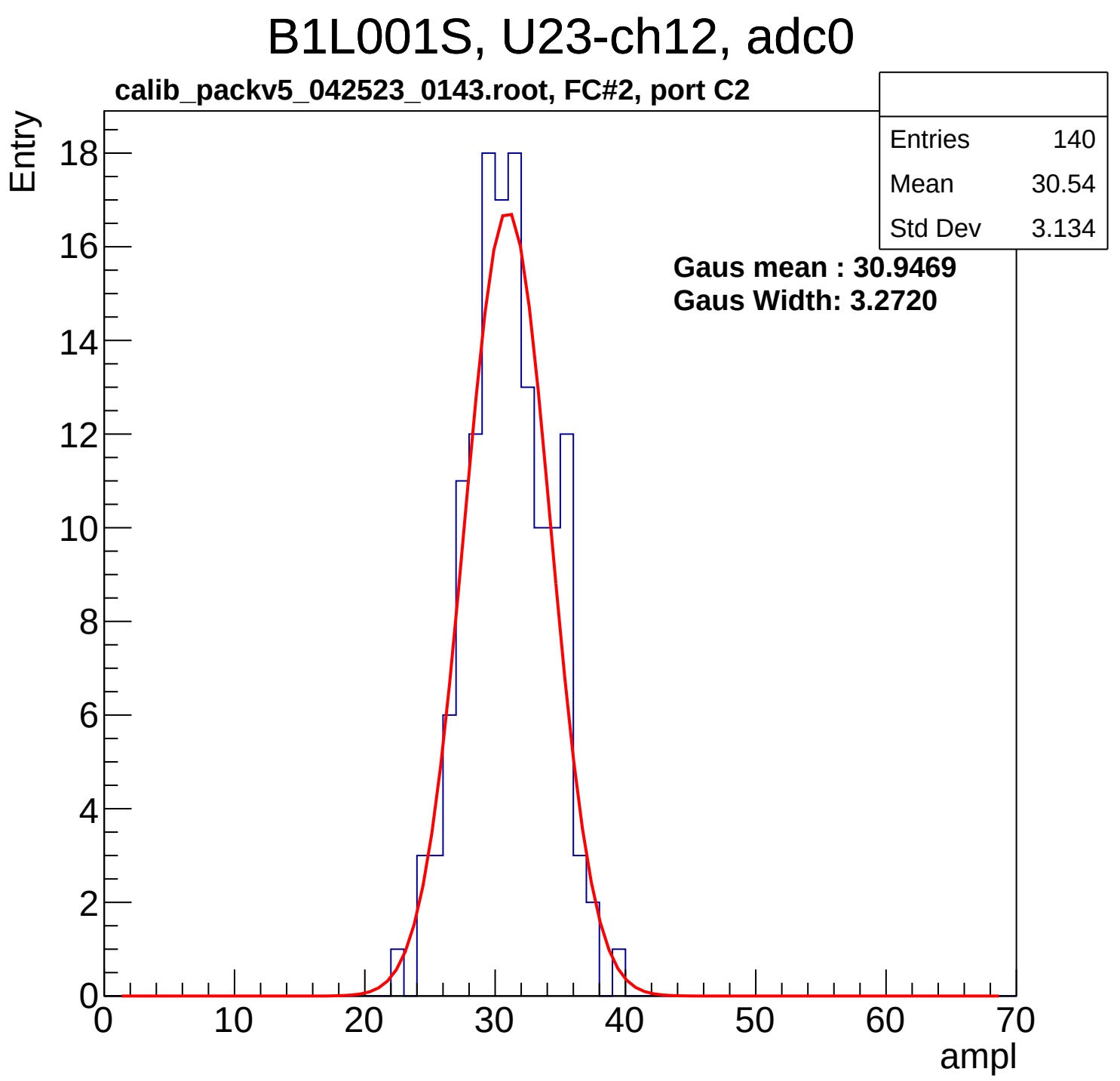

B1L001S, U23-ch11, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 44.29
Std Dev 28.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch11, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch12, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 30.54
Std Dev 3.134
Gaus mean : 30.9469
Gaus Width: 3.2720
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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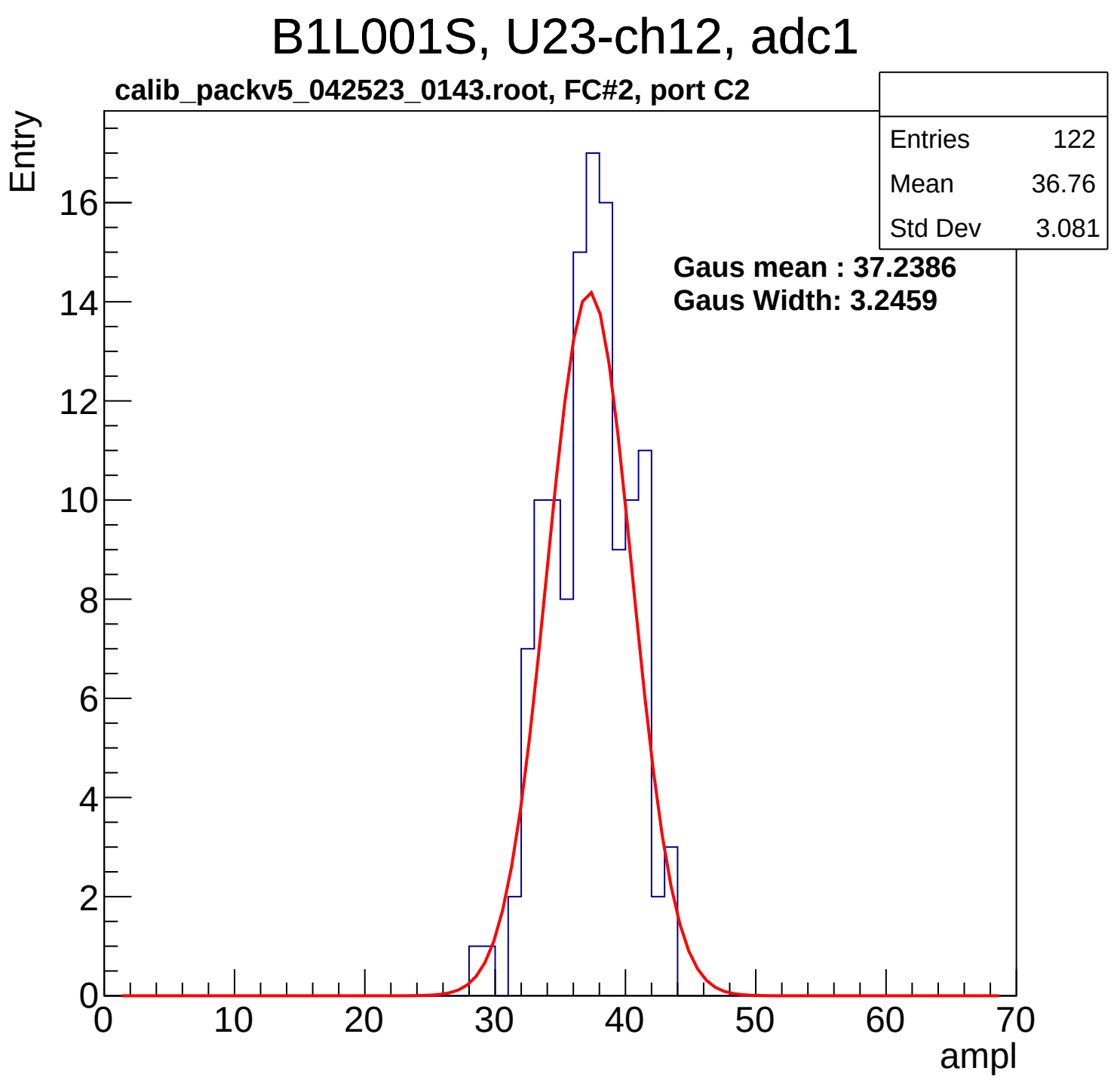
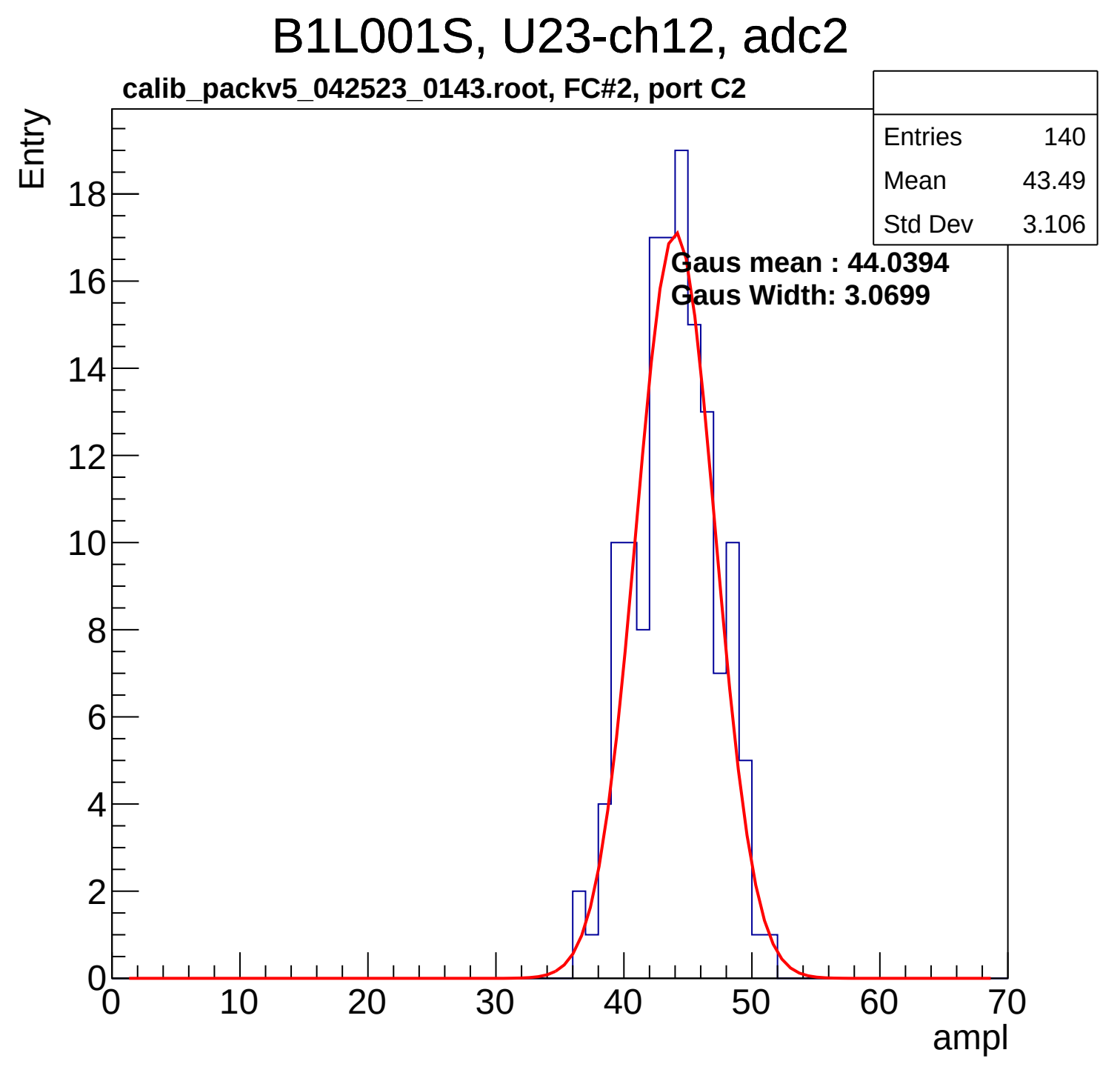

B1L001S, U23-ch12, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 36.76
Std Dev 3.081
Gaus mean : 37.2386
Gaus Width: 3.2459
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch12, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 43.49
Std Dev 3.106
Gaus mean : 44.0394
Gaus Width: 3.0699
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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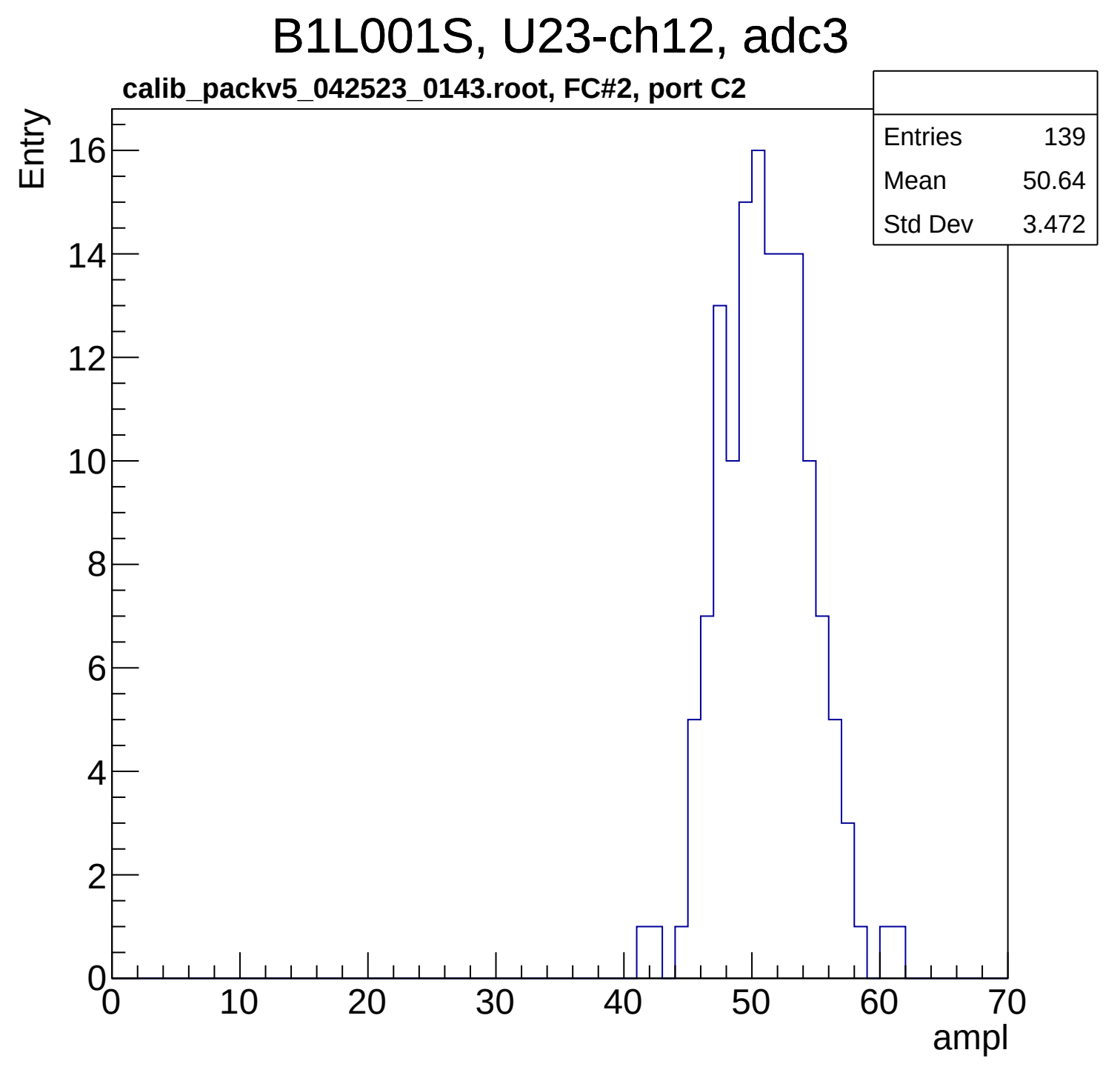
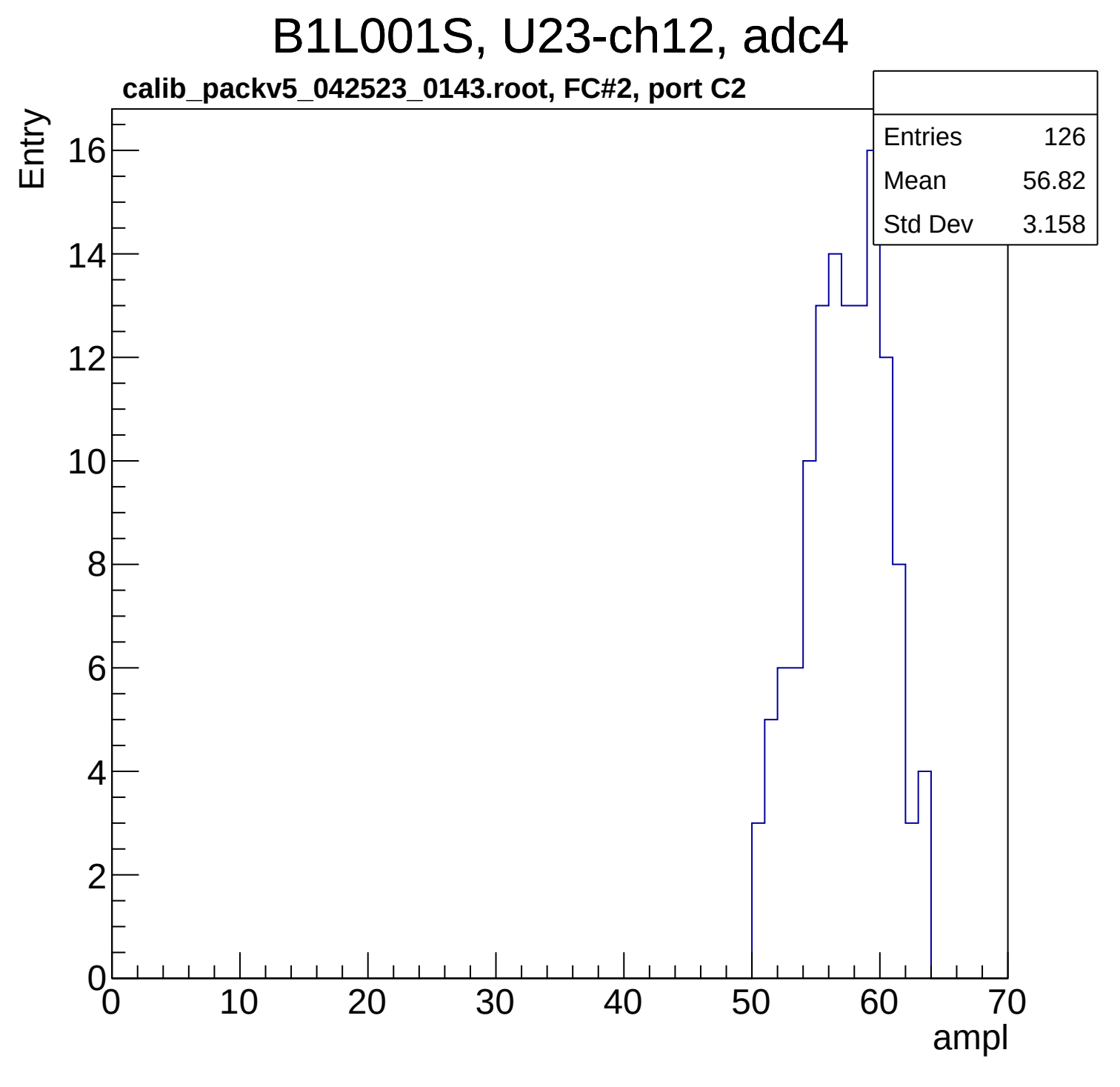

B1L001S, U23-ch12, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 50.64
Std Dev 3.472
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch12, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 56.82
Std Dev 3.158
Entry
ampl
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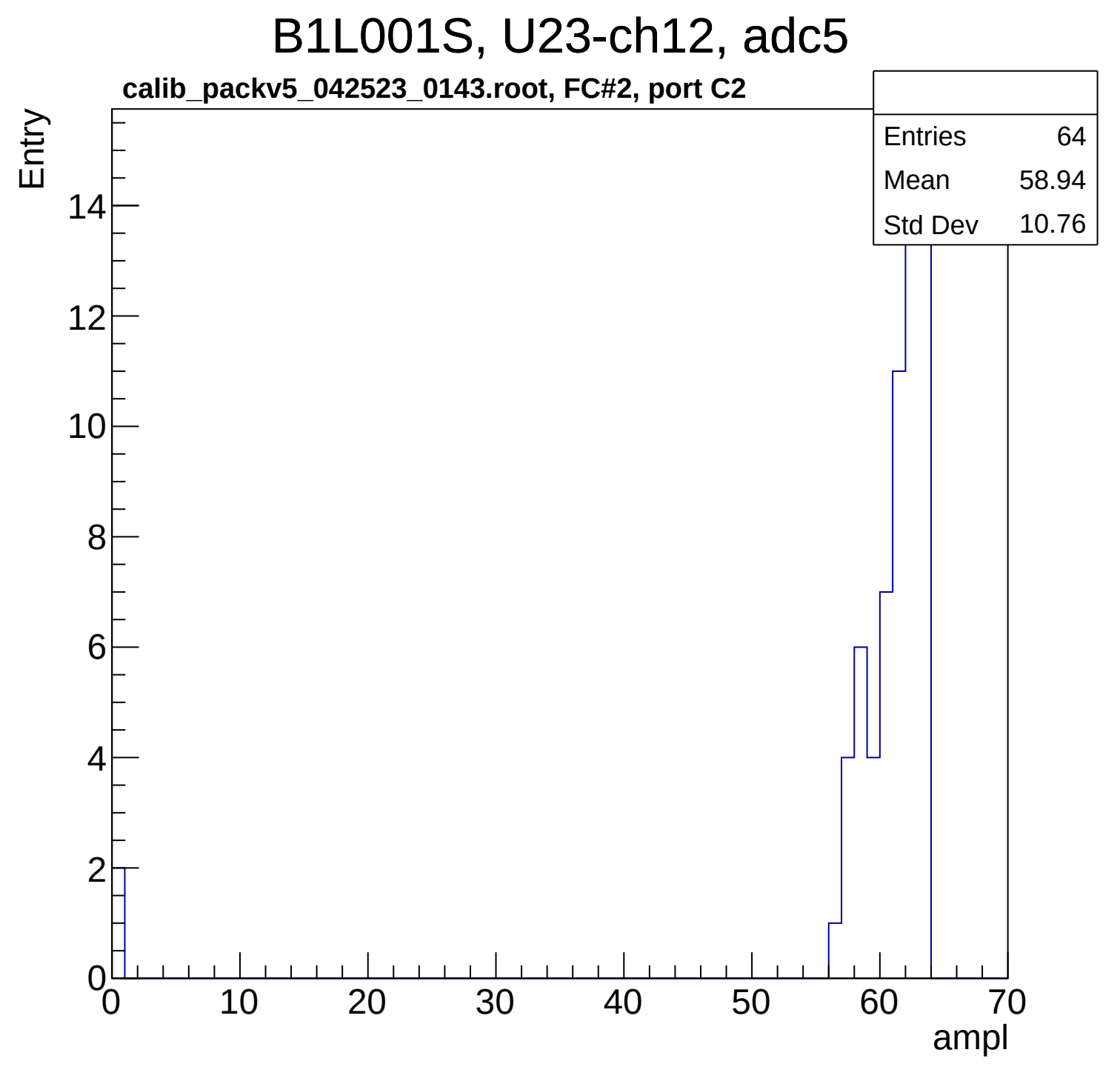
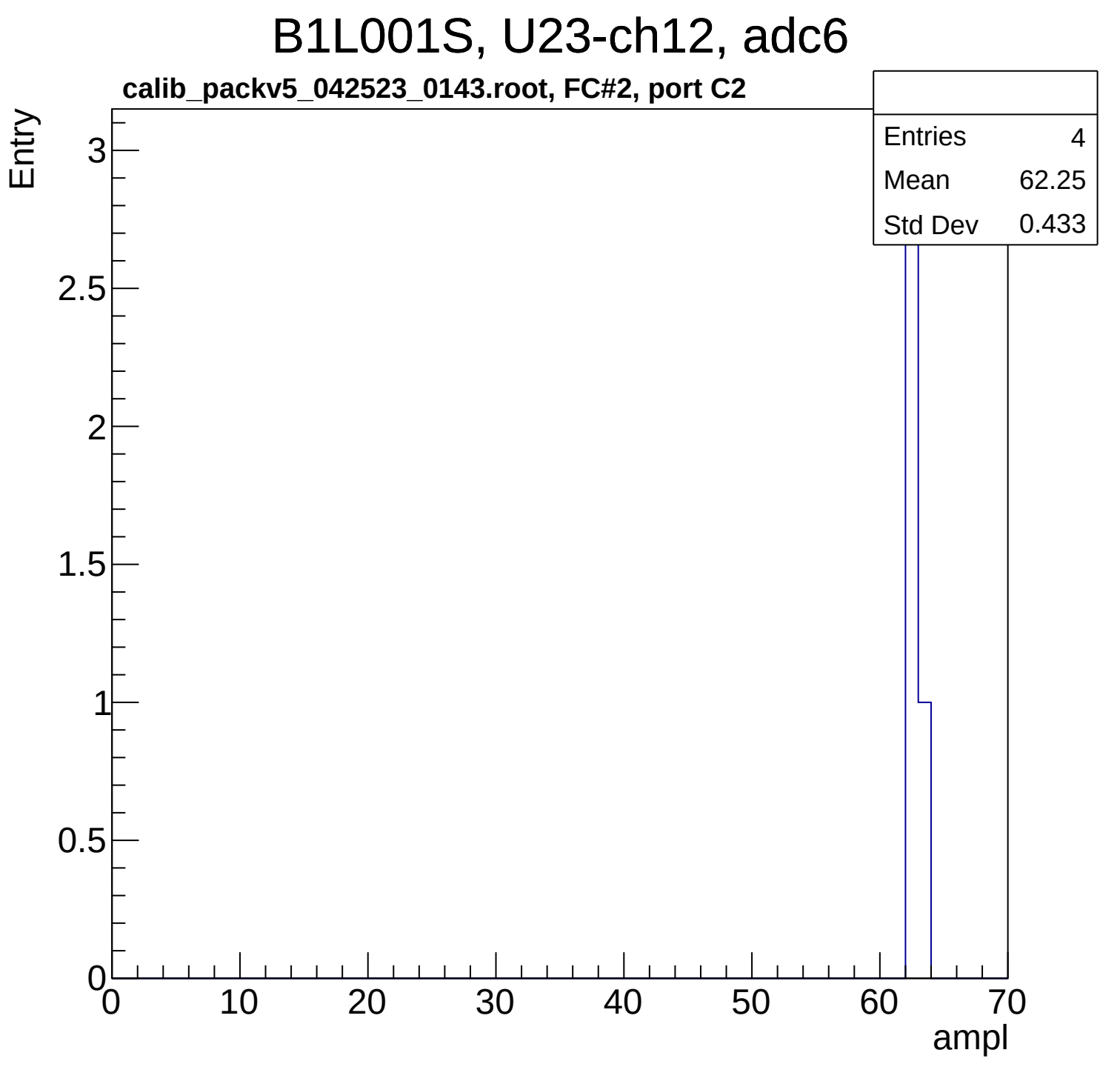
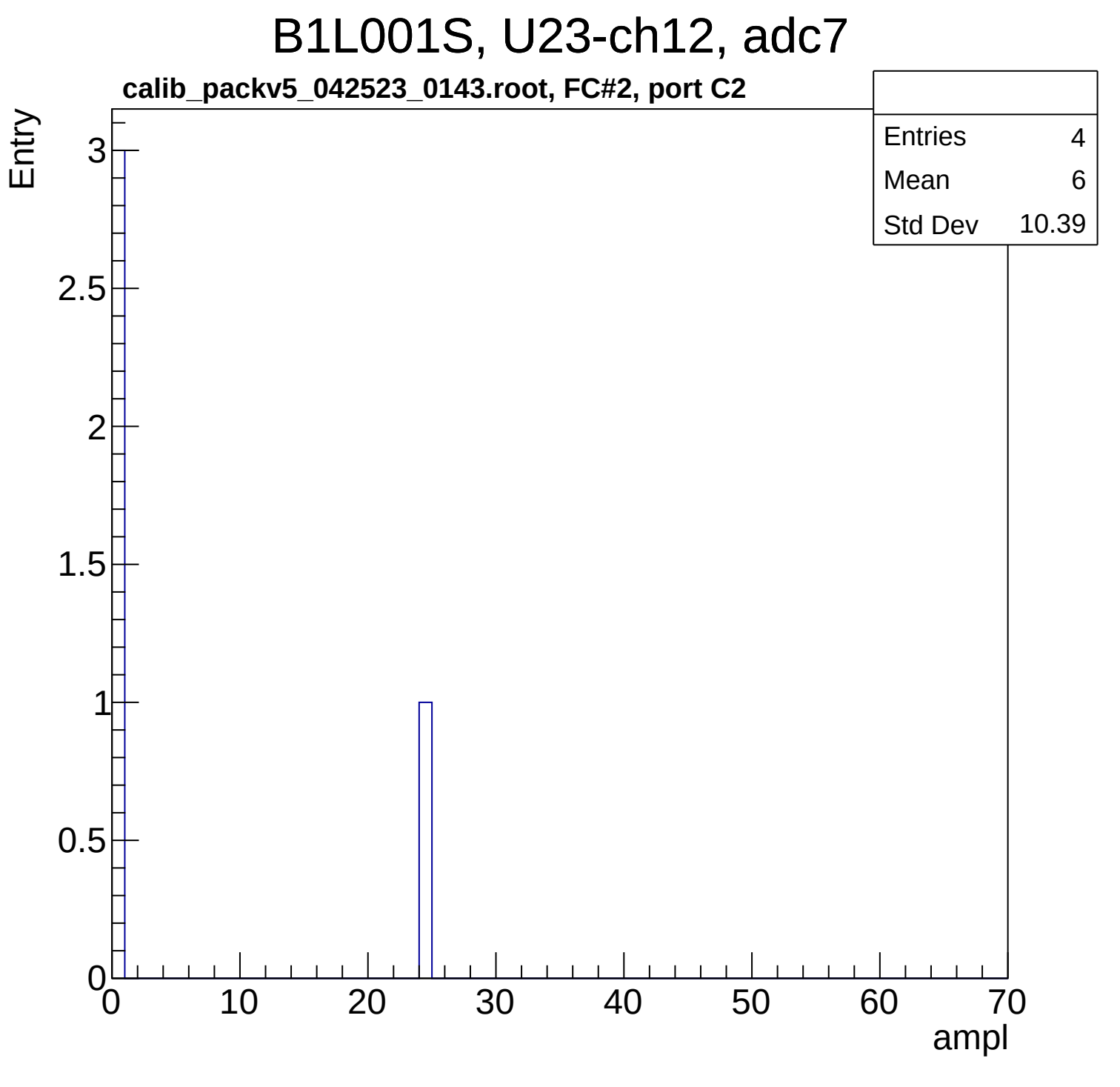
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch12, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 64
Mean 58.94
Std Dev 10.76
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch12, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch12, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 6
Std Dev 10.39
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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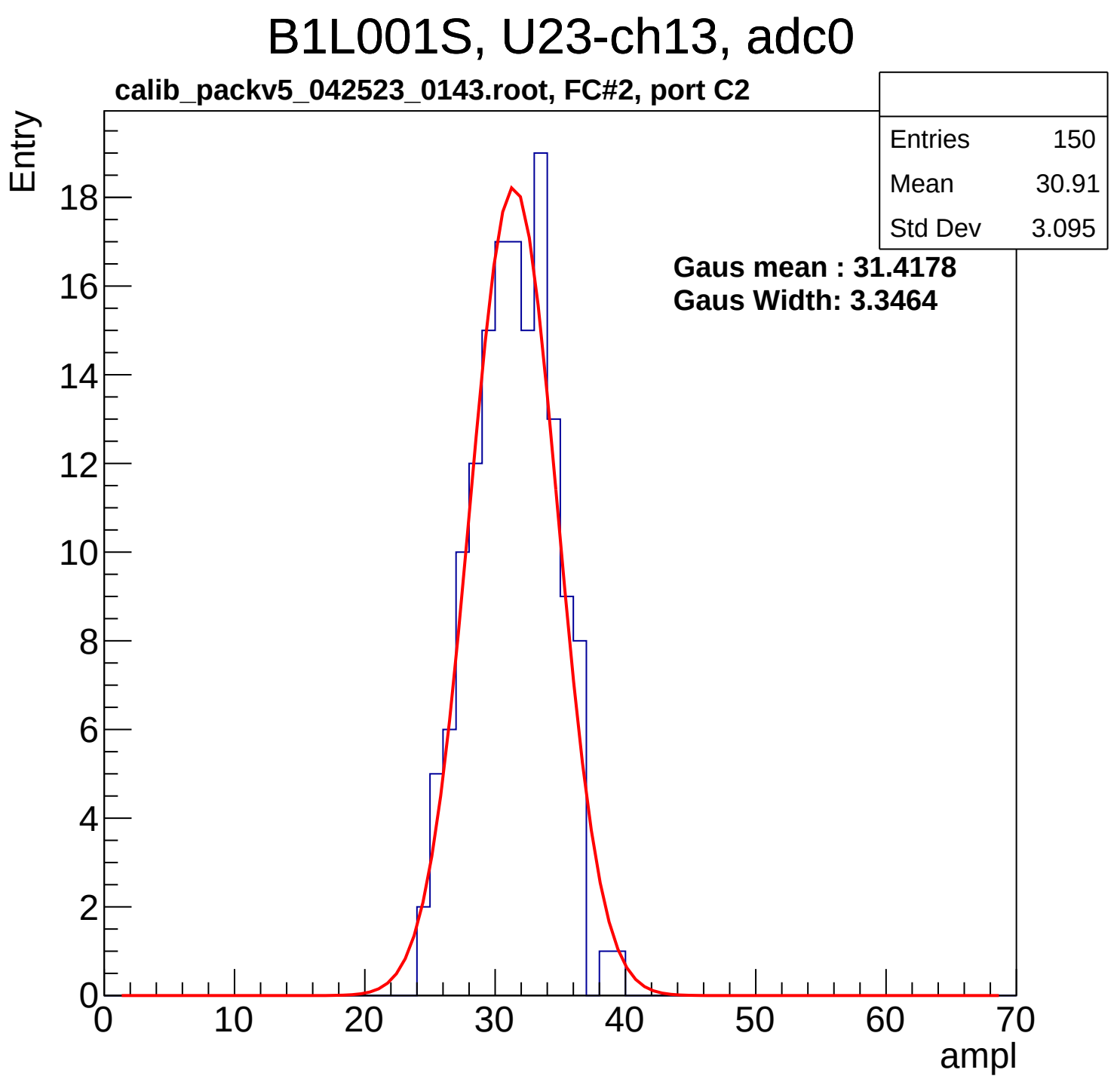
B1L001S, U23-ch13, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 30.91
Std Dev 3.095
Gaus mean : 31.4178
Gaus Width: 3.3464
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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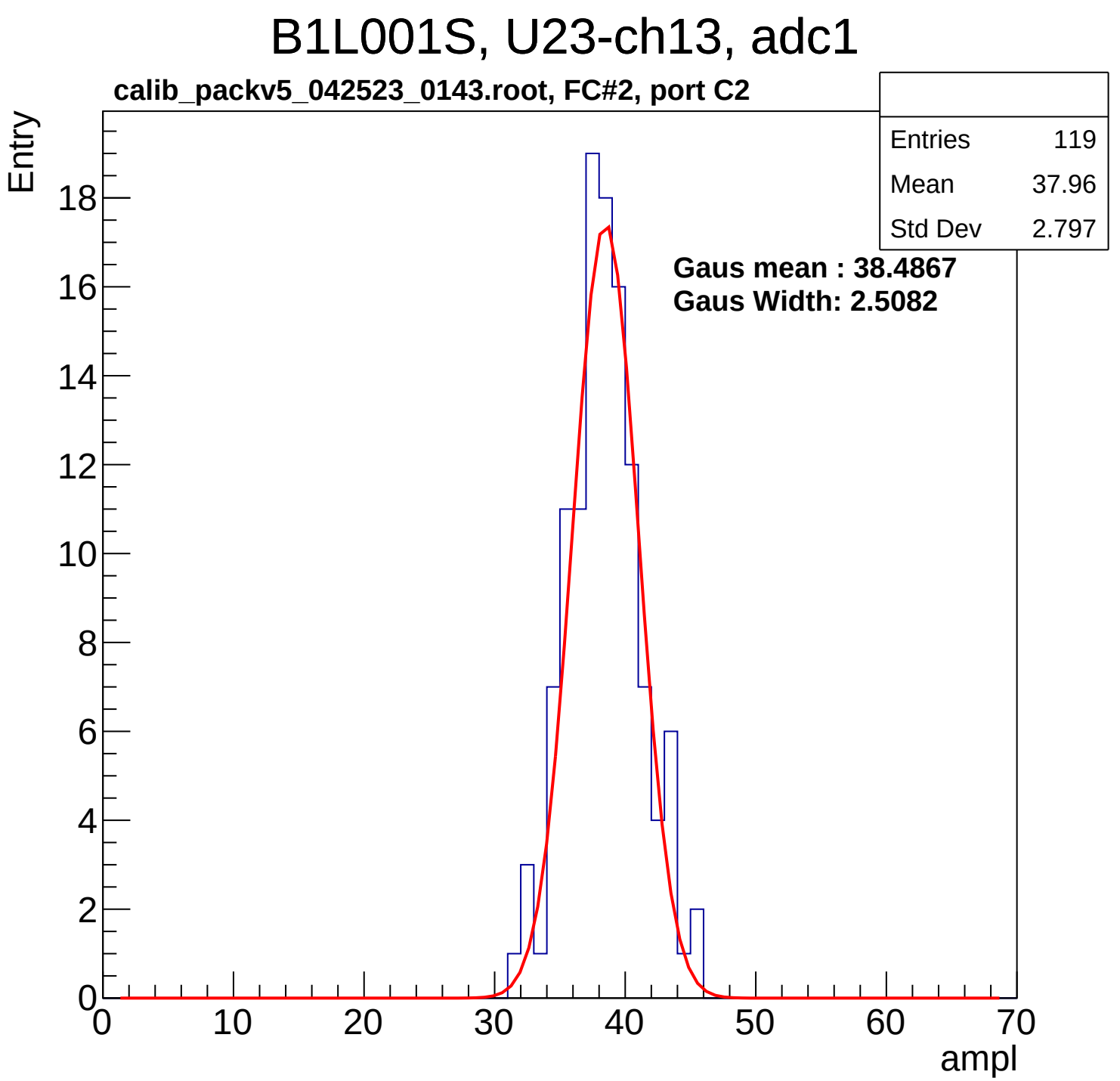
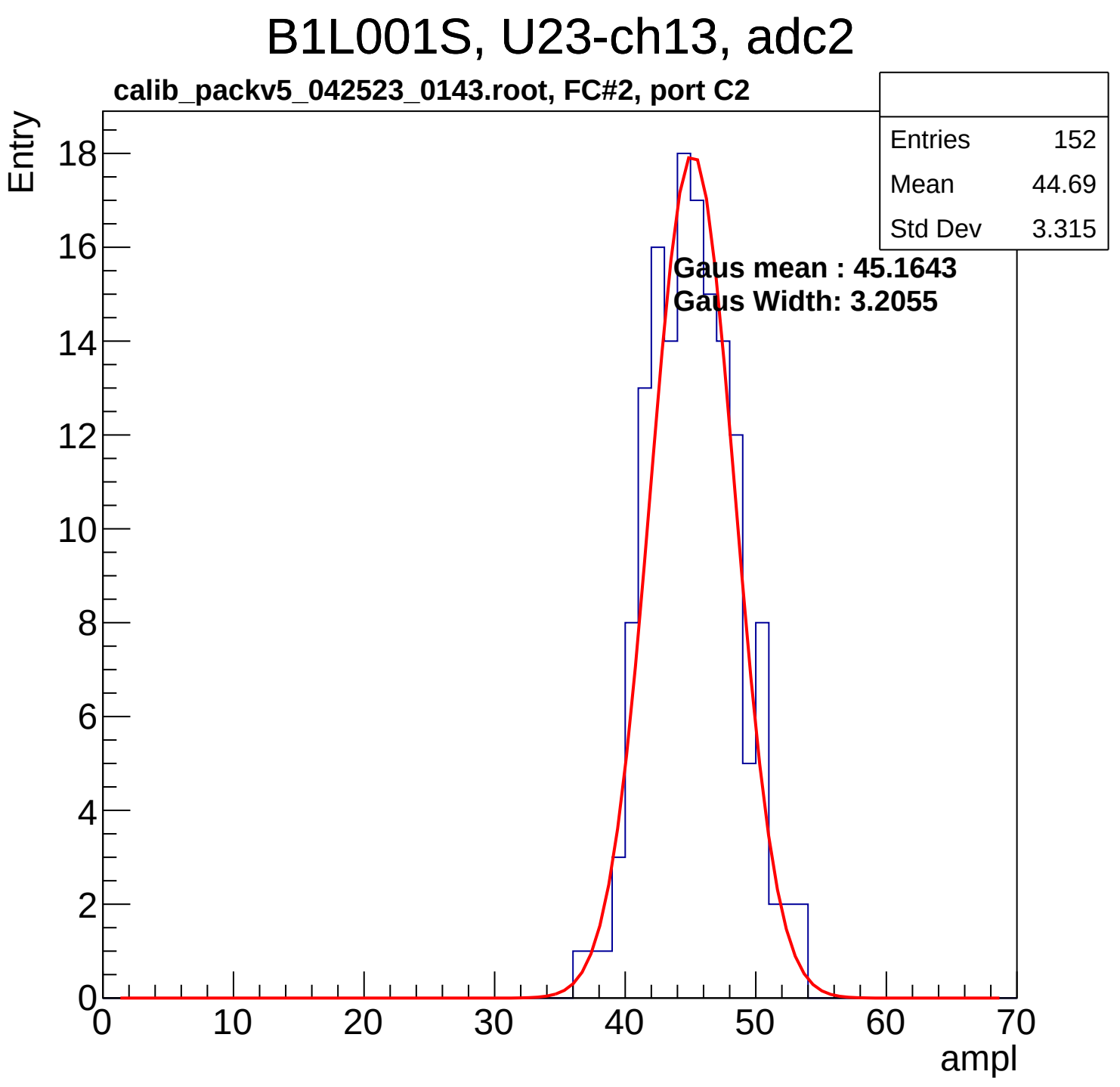

B1L001S, U23-ch13, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 37.96
Std Dev 2.797
Gaus mean : 38.4867
Gaus Width: 2.5082
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch13, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 44.69
Std Dev 3.315
Gaus mean : 45.1643
Gaus Width: 3.2055
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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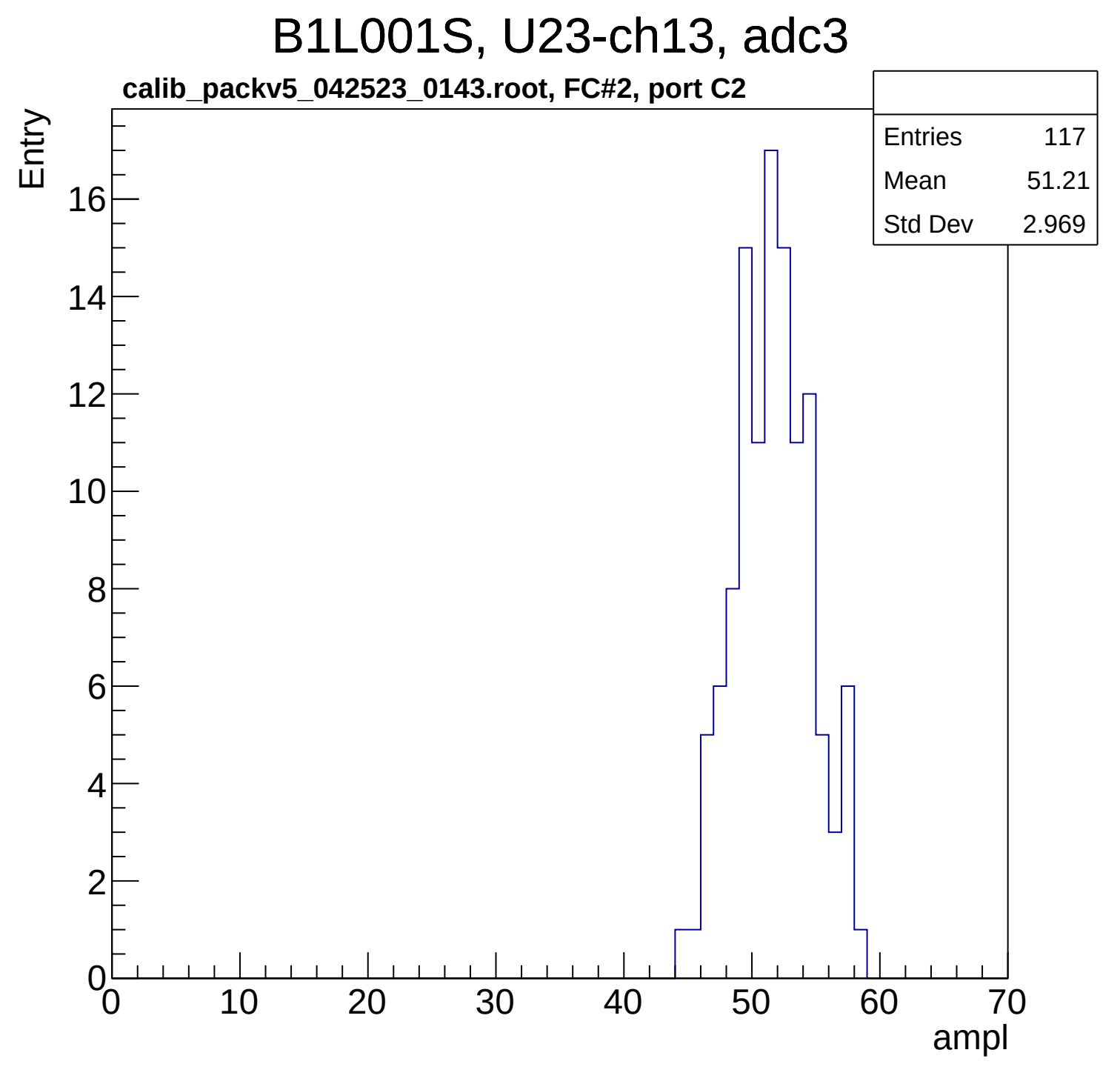
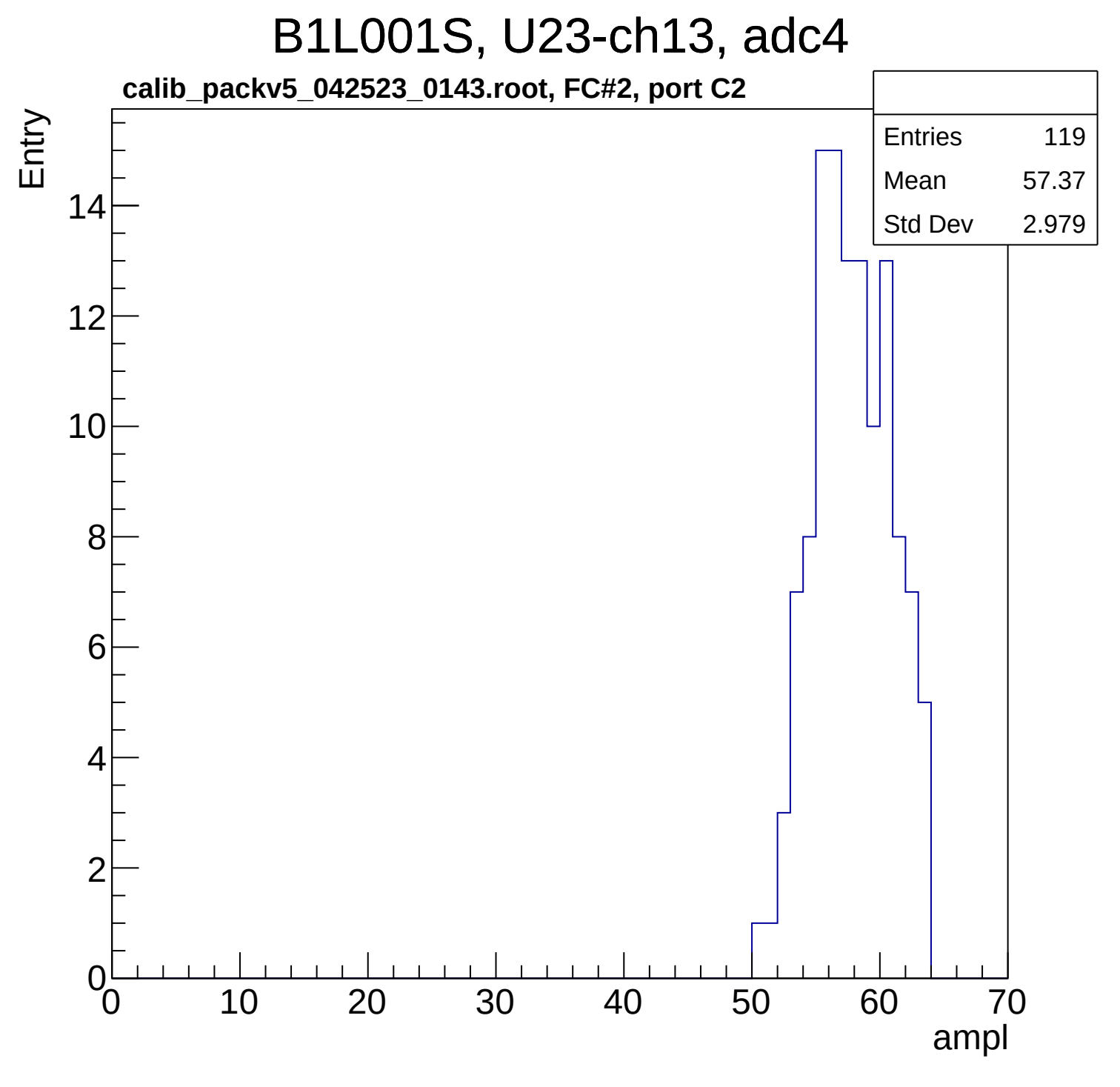
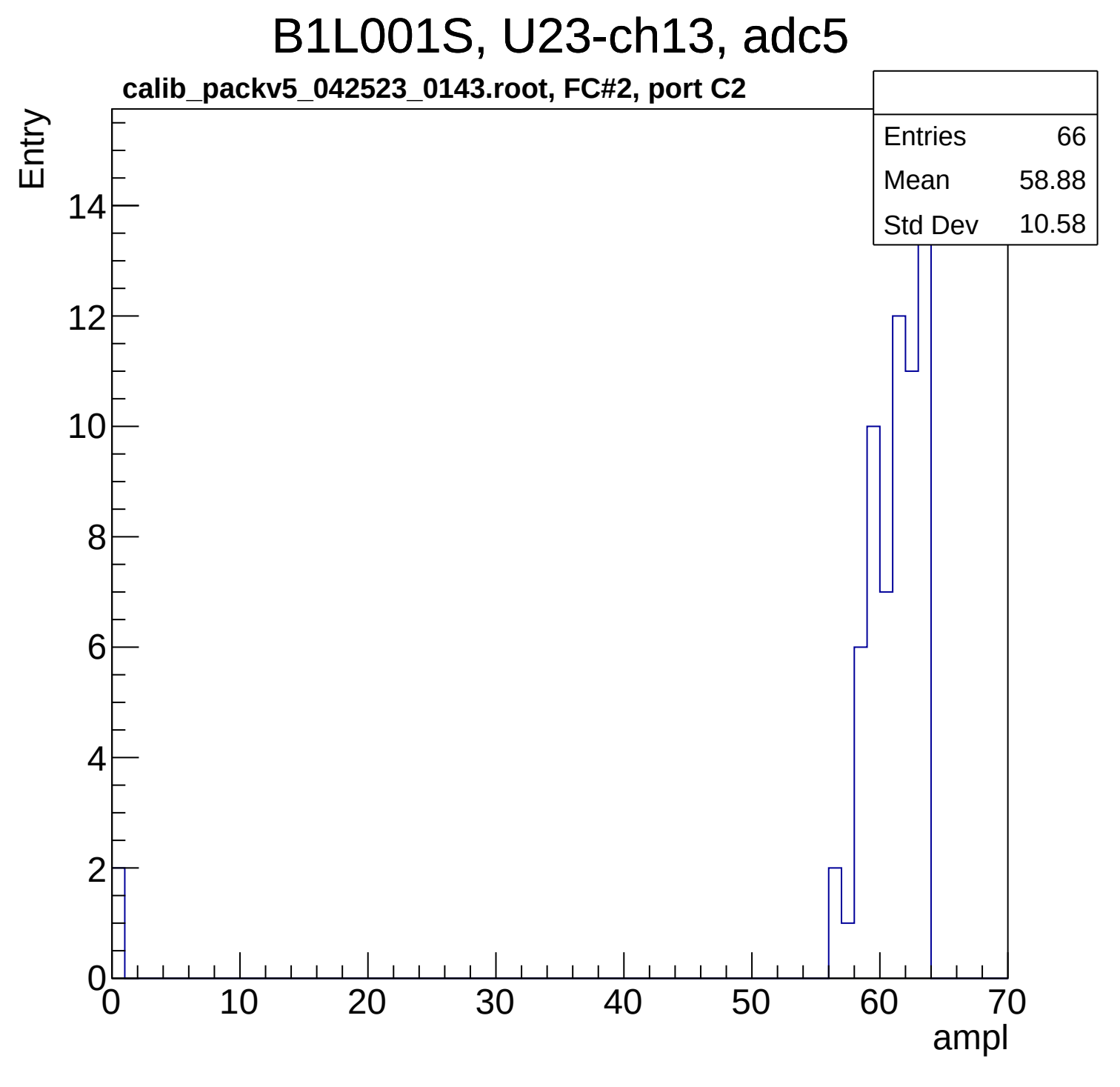
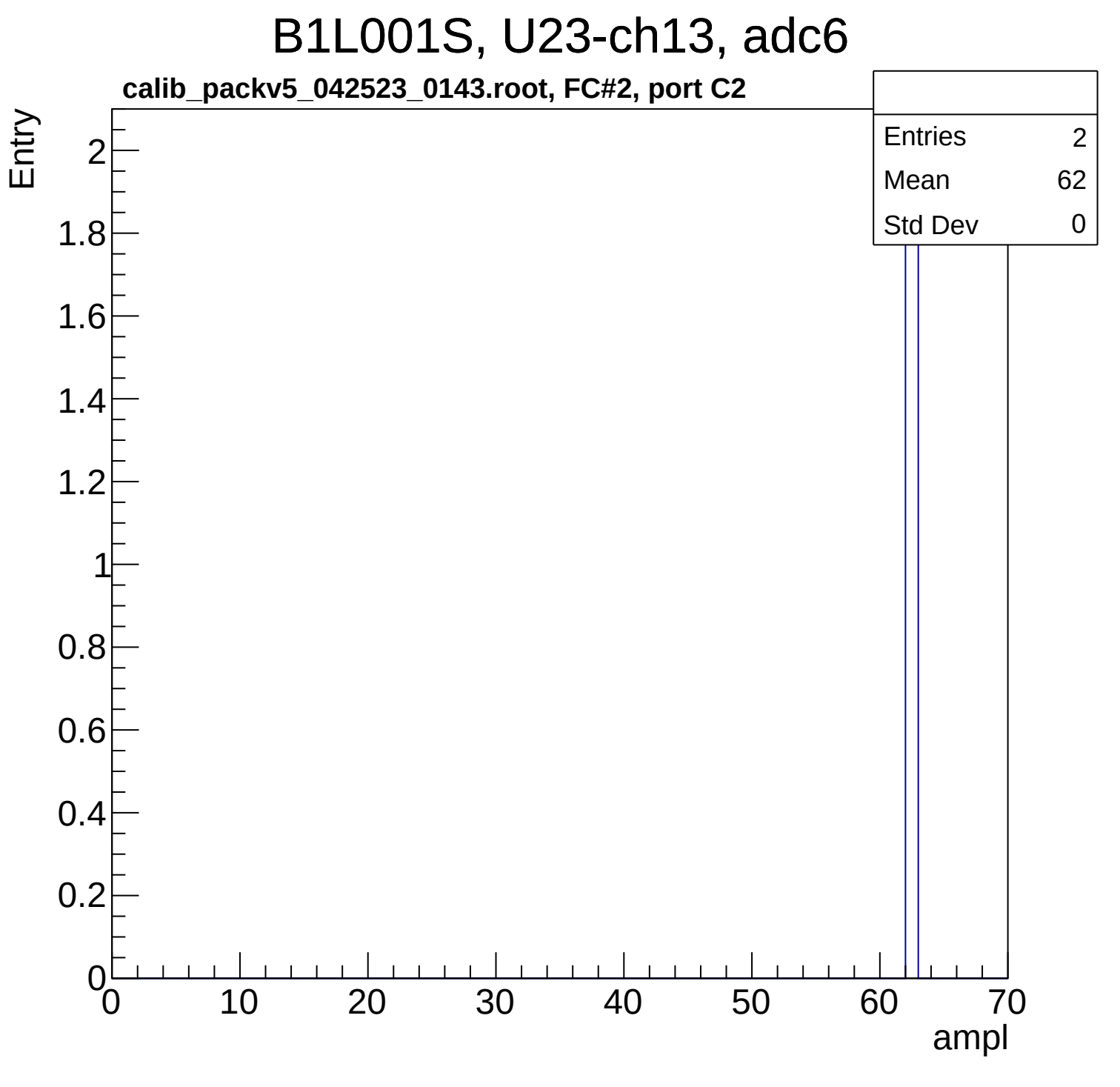
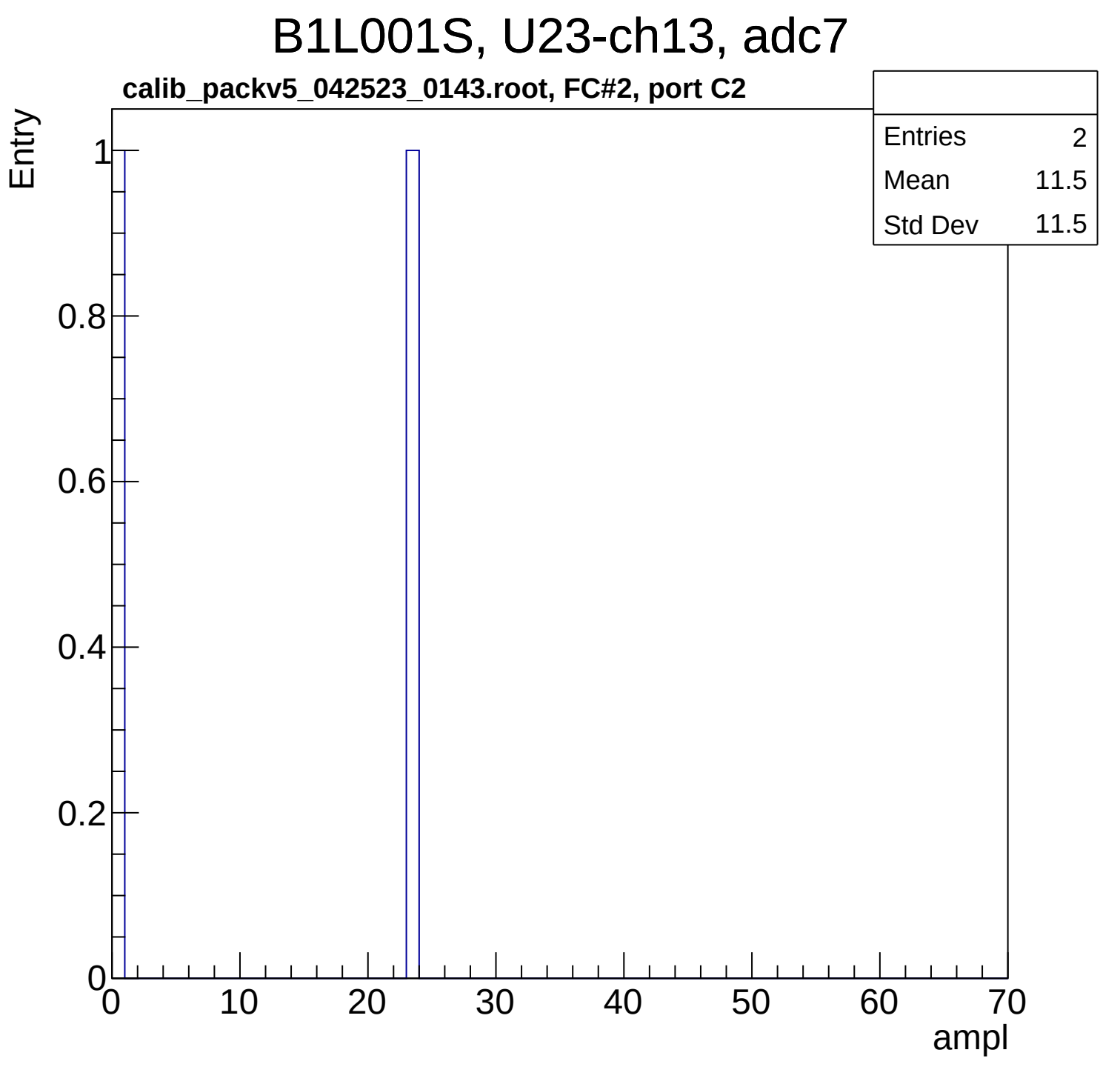
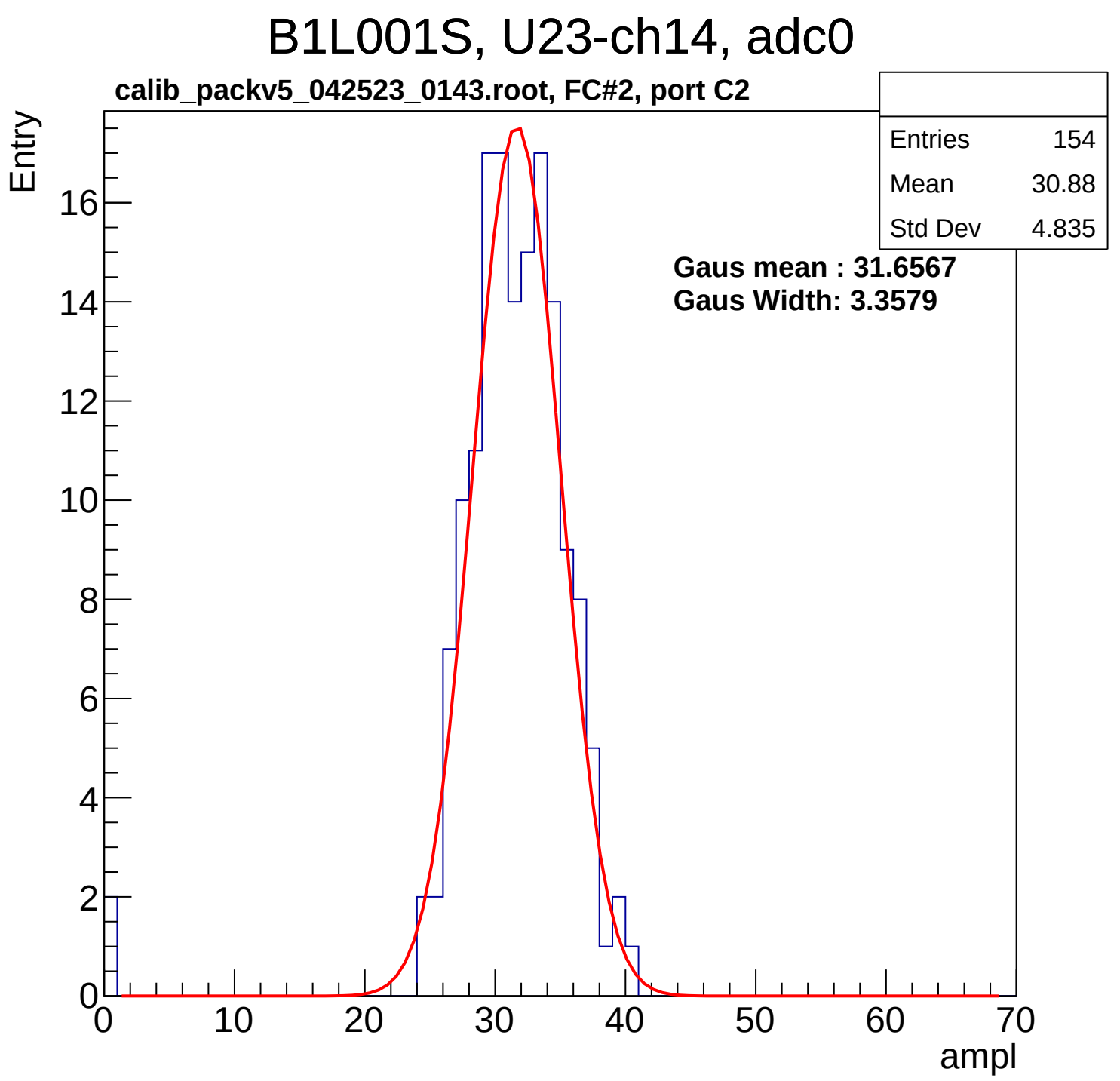

B1L001S, U23-ch13, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 51.21
Std Dev 2.969
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch13, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 57.37
Std Dev 2.979
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch13, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 66
Mean 58.88
Std Dev 10.58
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch13, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch13, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch14, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 30.88
Std Dev 4.835
Gaus mean : 31.6567
Gaus Width: 3.3579
Entry
ampl
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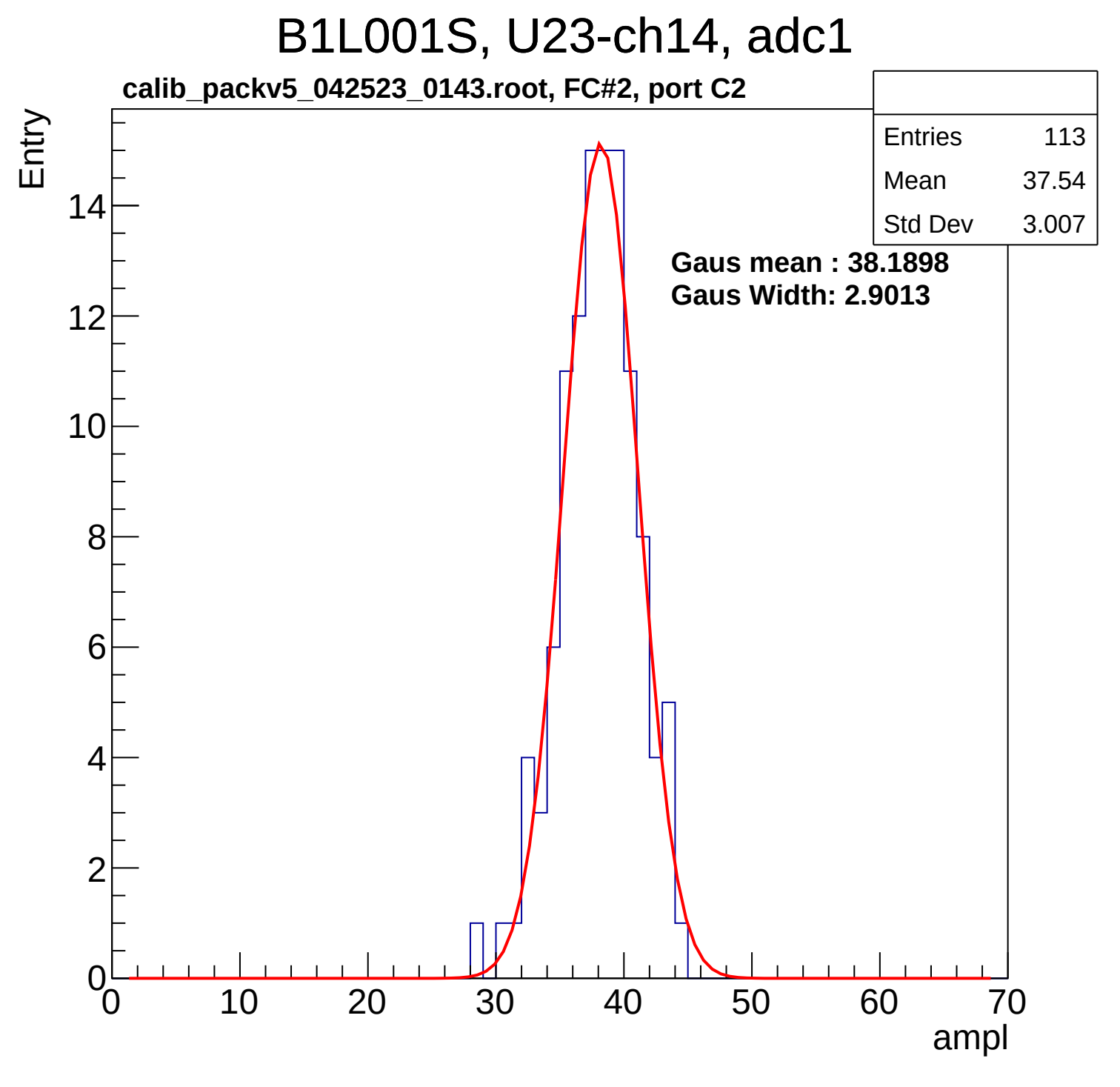
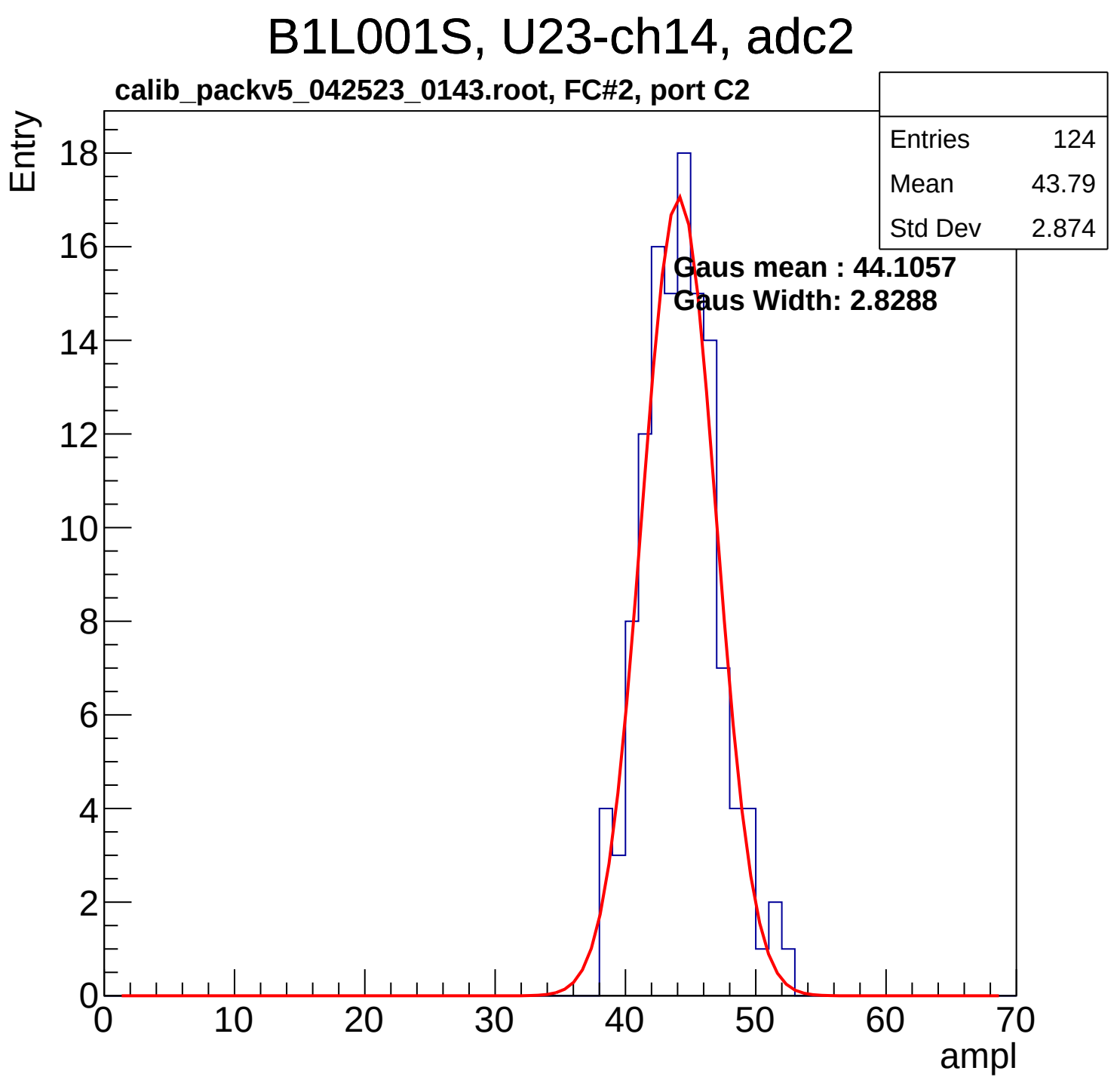
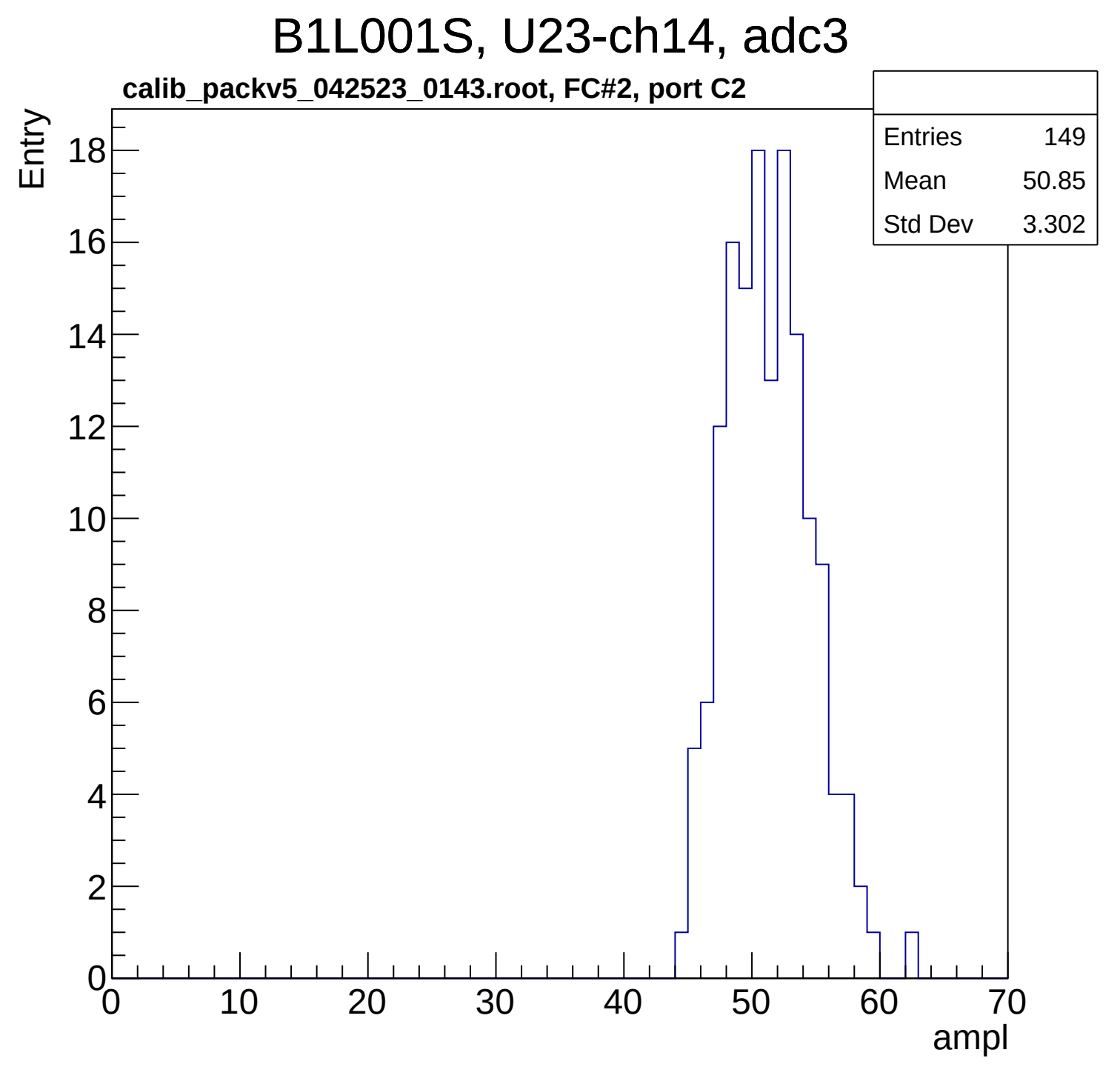
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch14, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 37.54
Std Dev 3.007
Gaus mean : 38.1898
Gaus Width: 2.9013
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch14, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 43.79
Std Dev 2.874
Gaus mean : 44.1057
Gaus Width: 2.8288
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch14, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 50.85
Std Dev 3.302
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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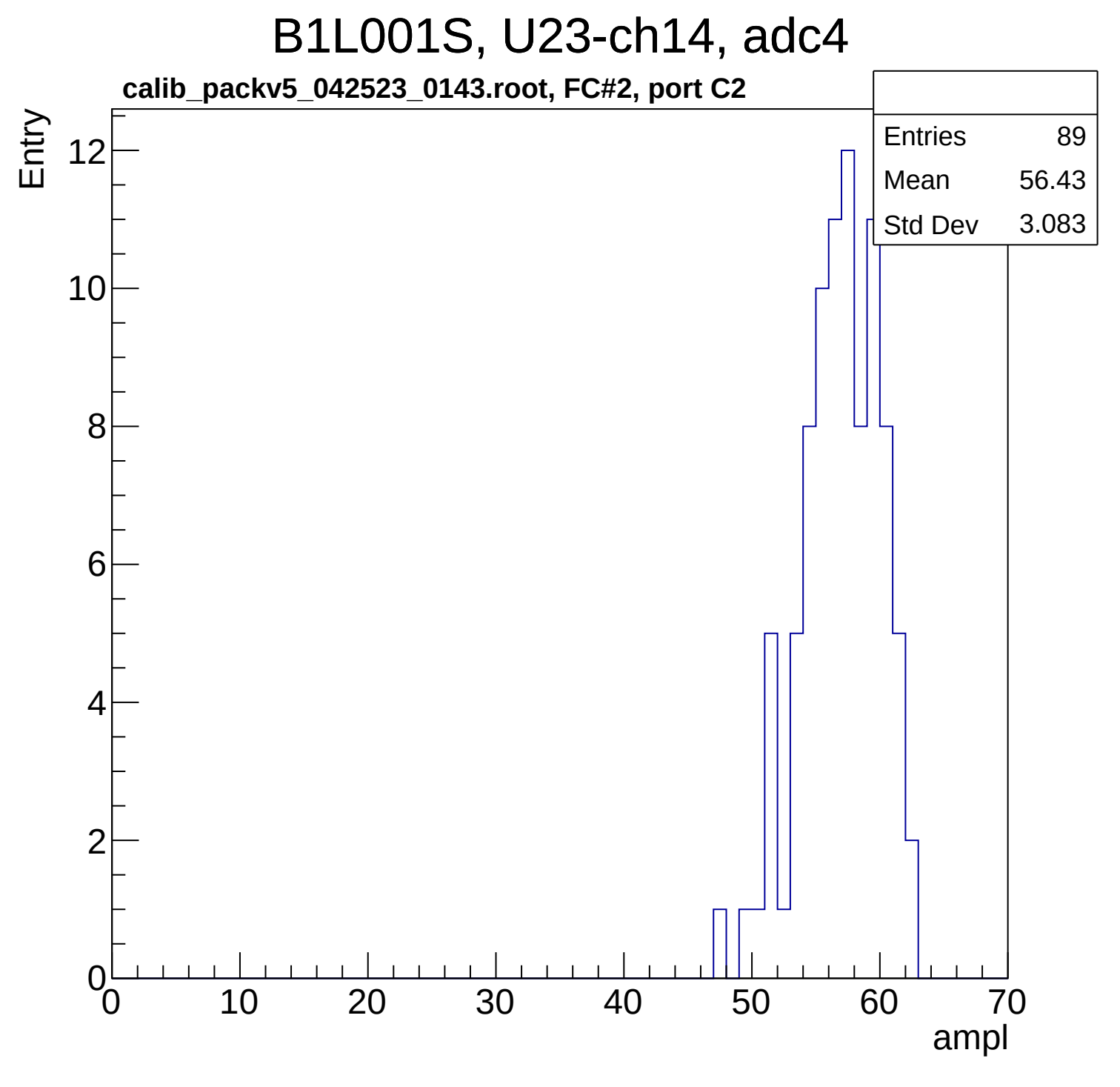
40
50
60
70


B1L001S, U23-ch14, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 56.43
Std Dev 3.083
Entry
ampl
0
2
4
6
8
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0
10
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40
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60
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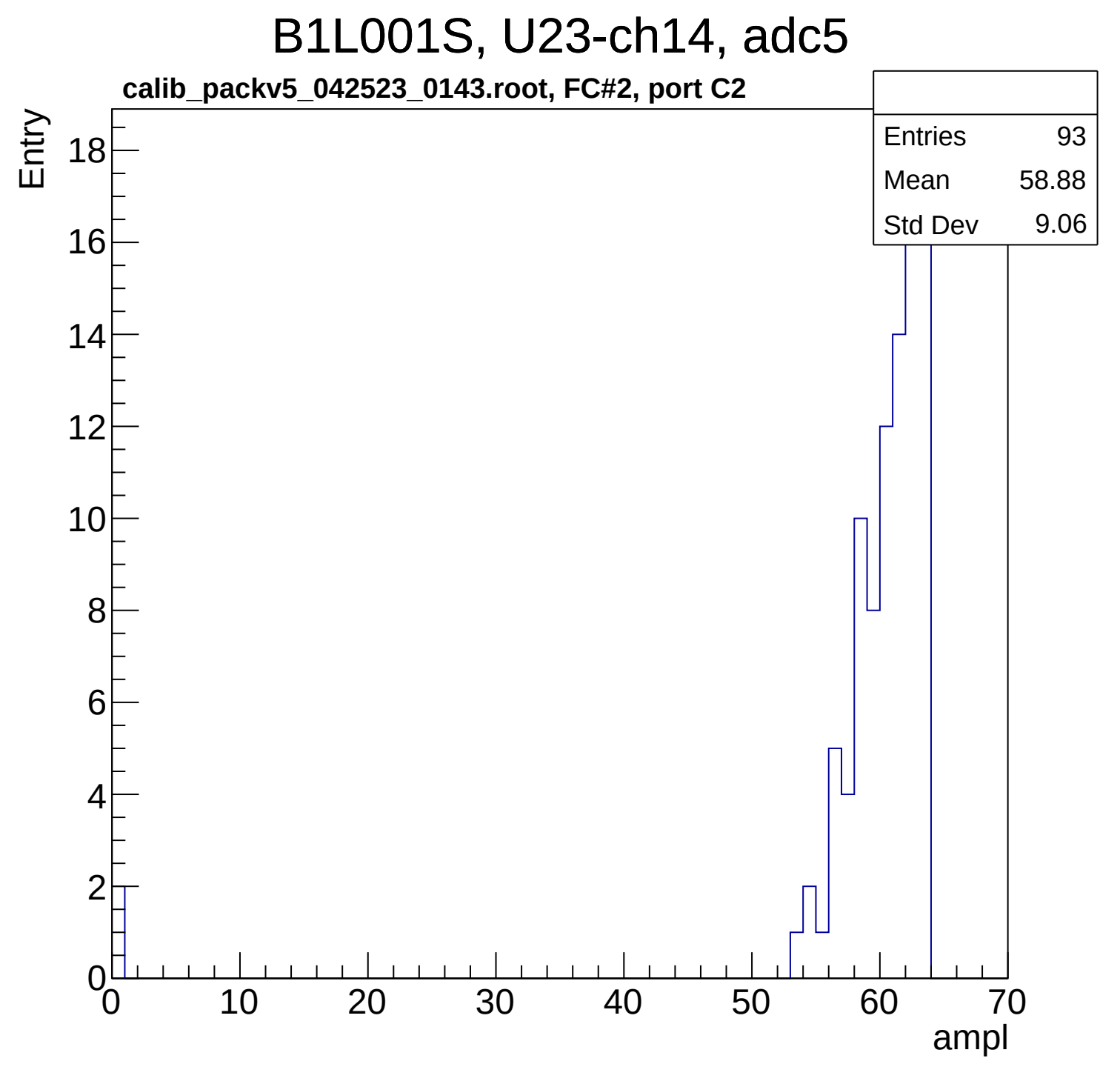
B1L001S, U23-ch14, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 58.88
Std Dev 9.06
Entry
ampl
0
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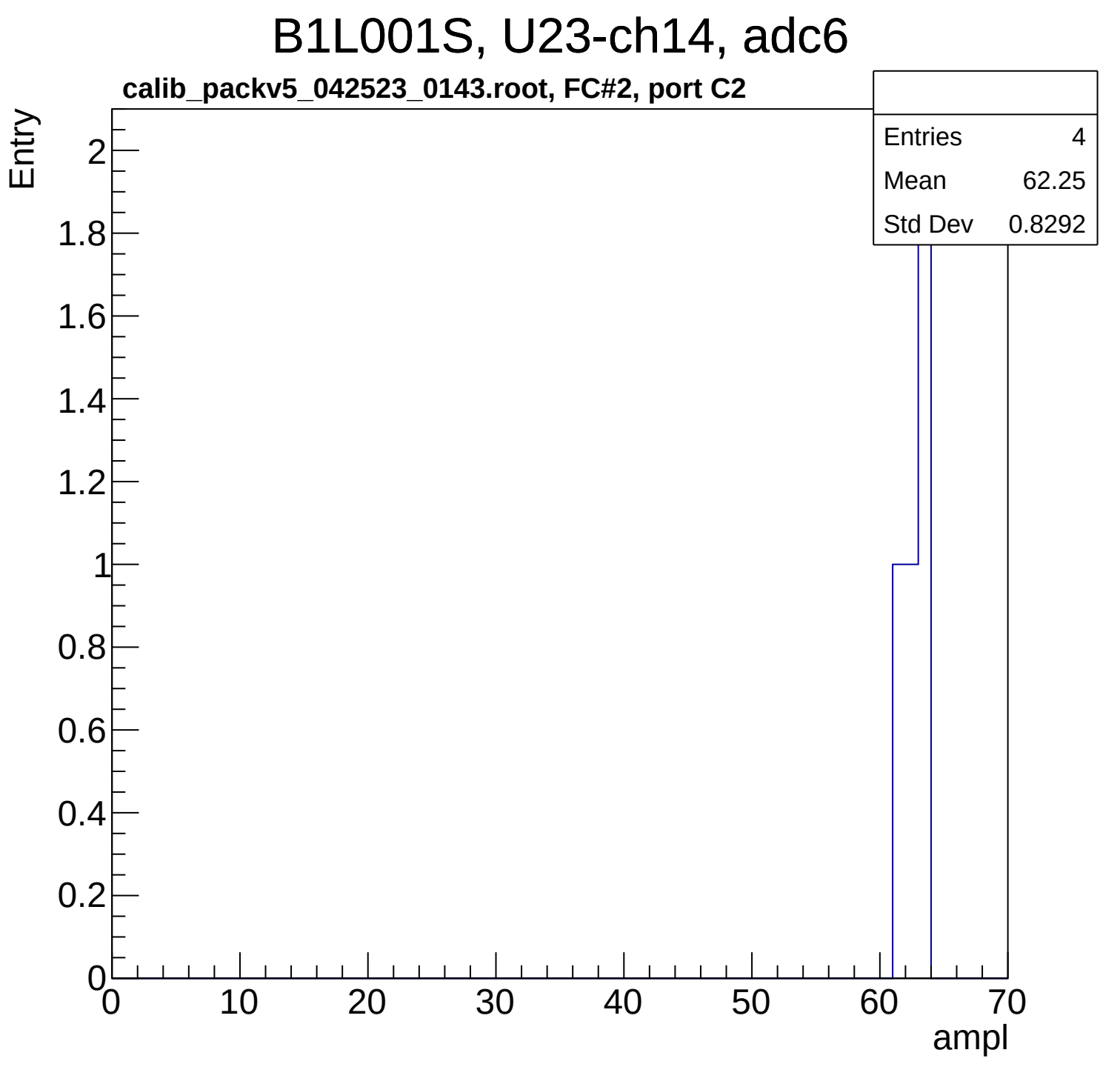
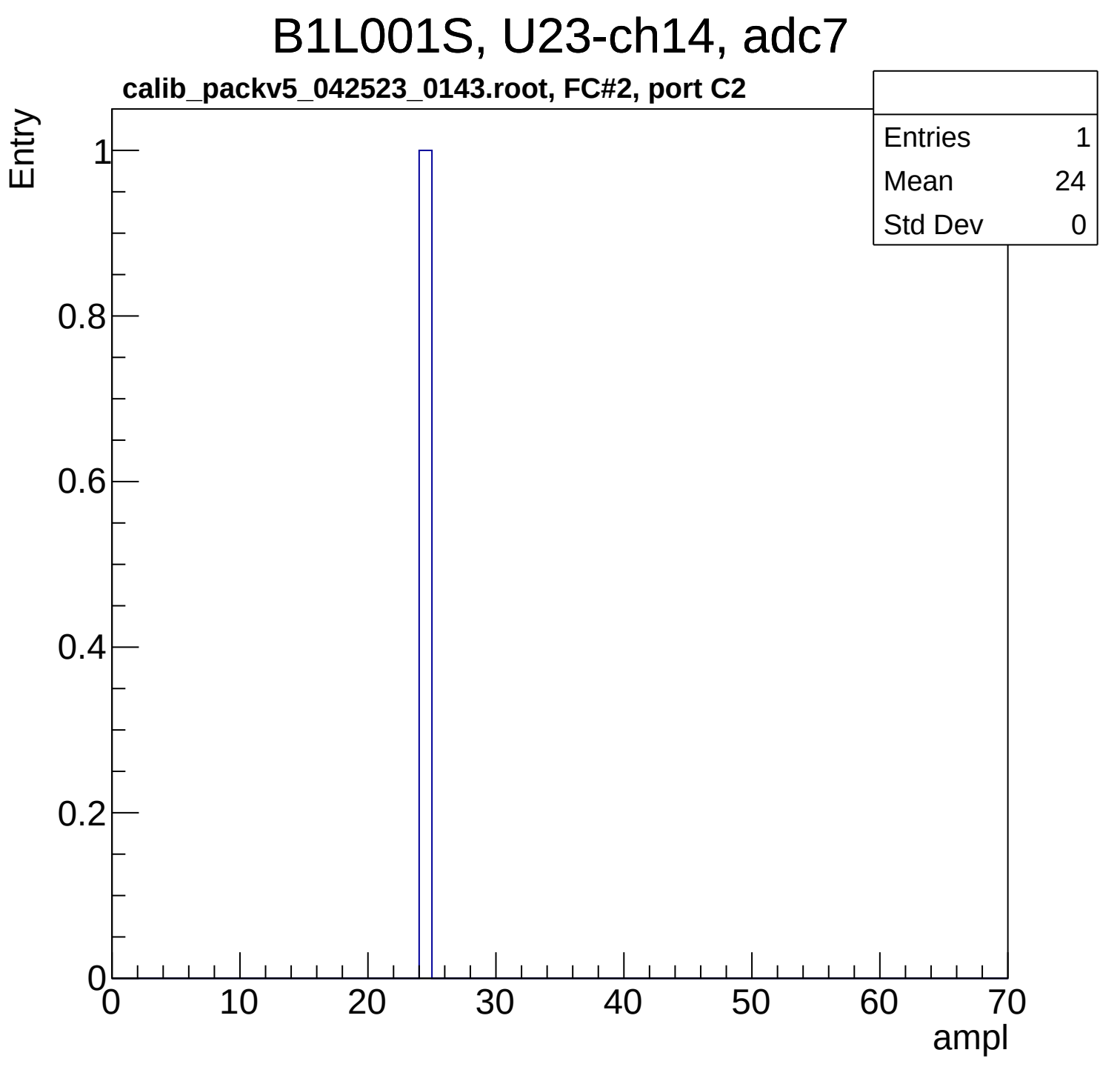
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch14, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch14, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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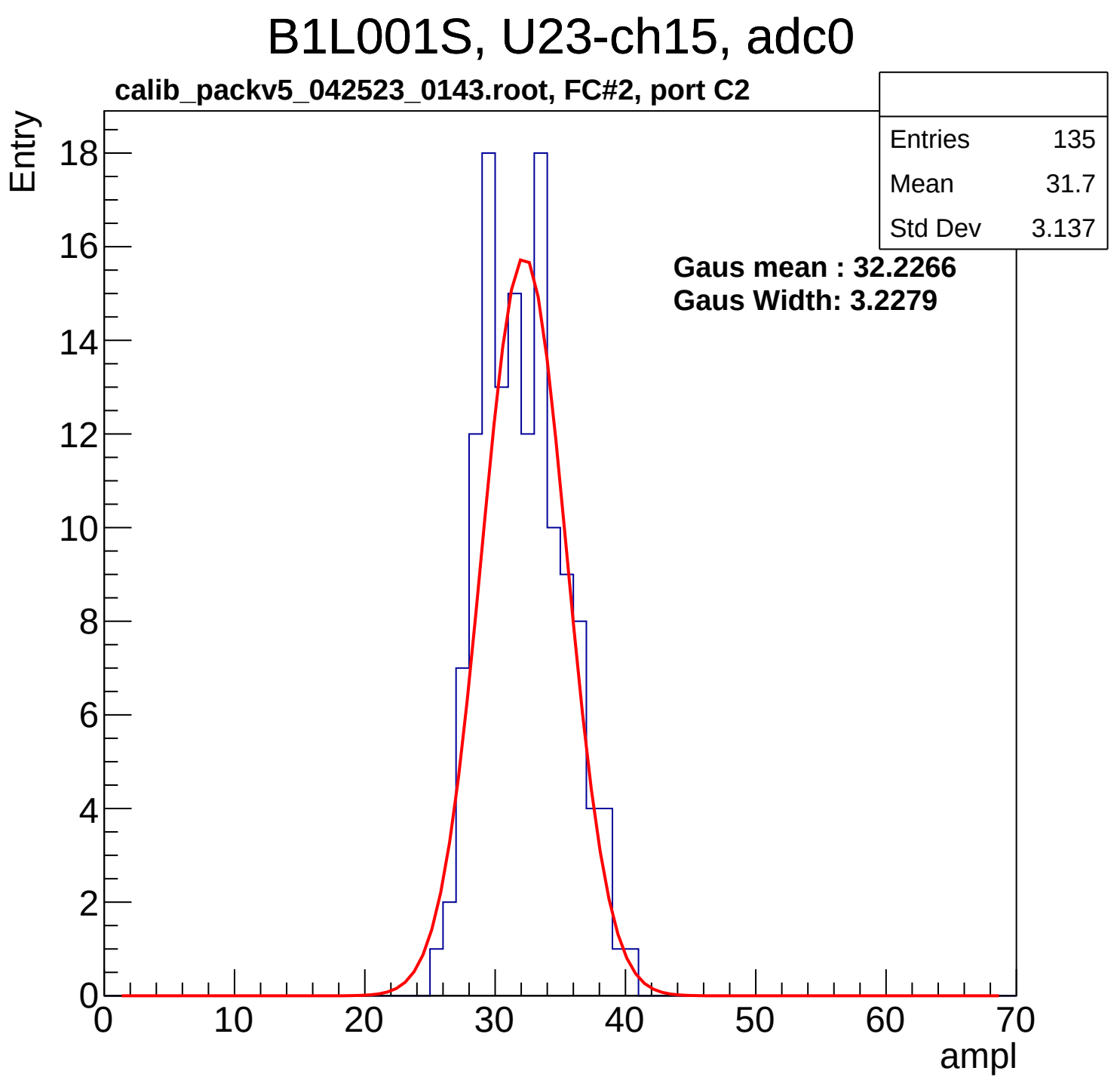
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch15, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 31.7
Std Dev 3.137
Gaus mean : 32.2266
Gaus Width: 3.2279
Entry
ampl
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4
6
8
10
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14
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0
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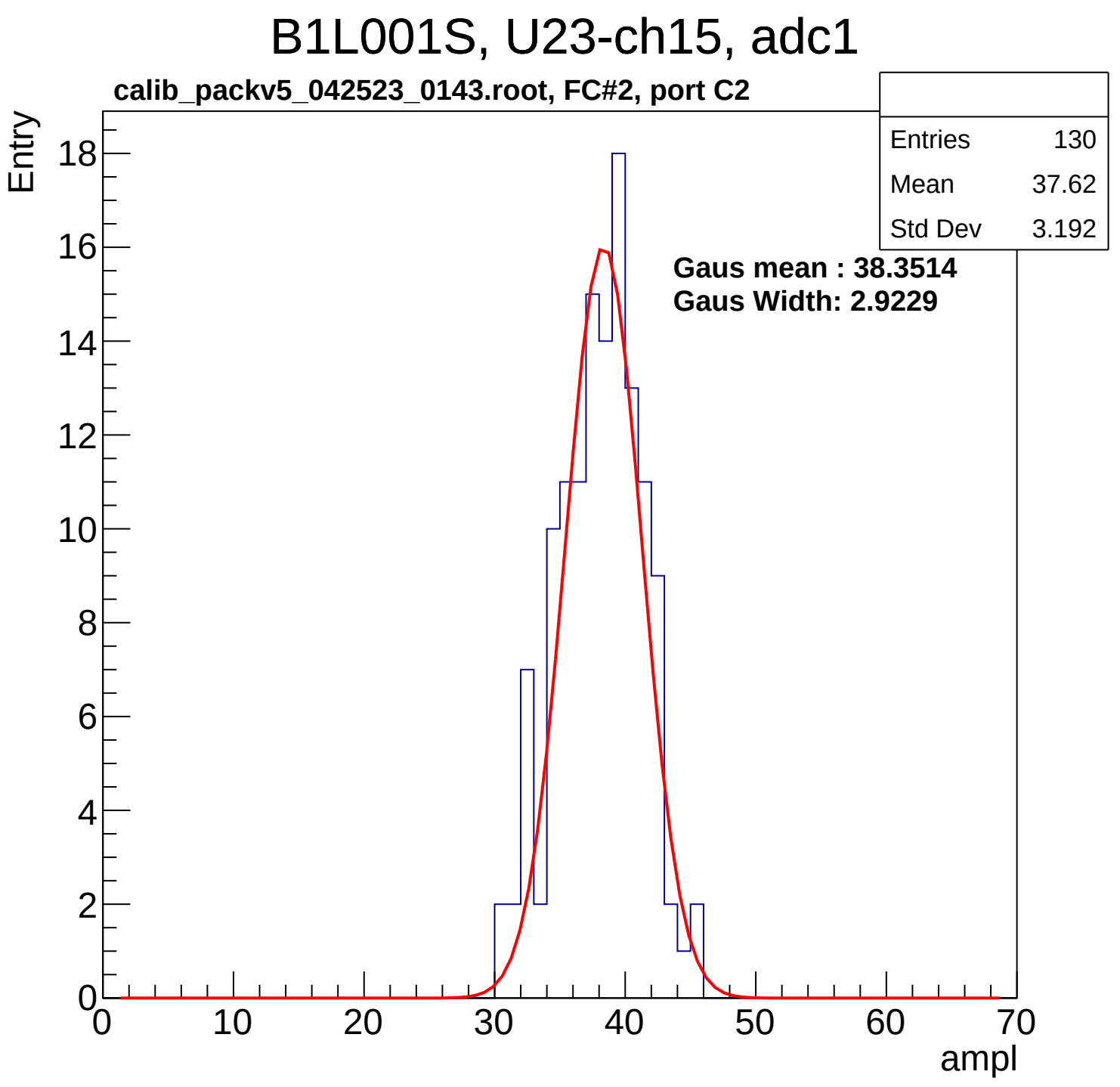
B1L001S, U23-ch15, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 37.62
Std Dev 3.192
Gaus mean : 38.3514
Gaus Width: 2.9229
Entry
ampl
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4
6
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10
12
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0
10
20
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40
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60
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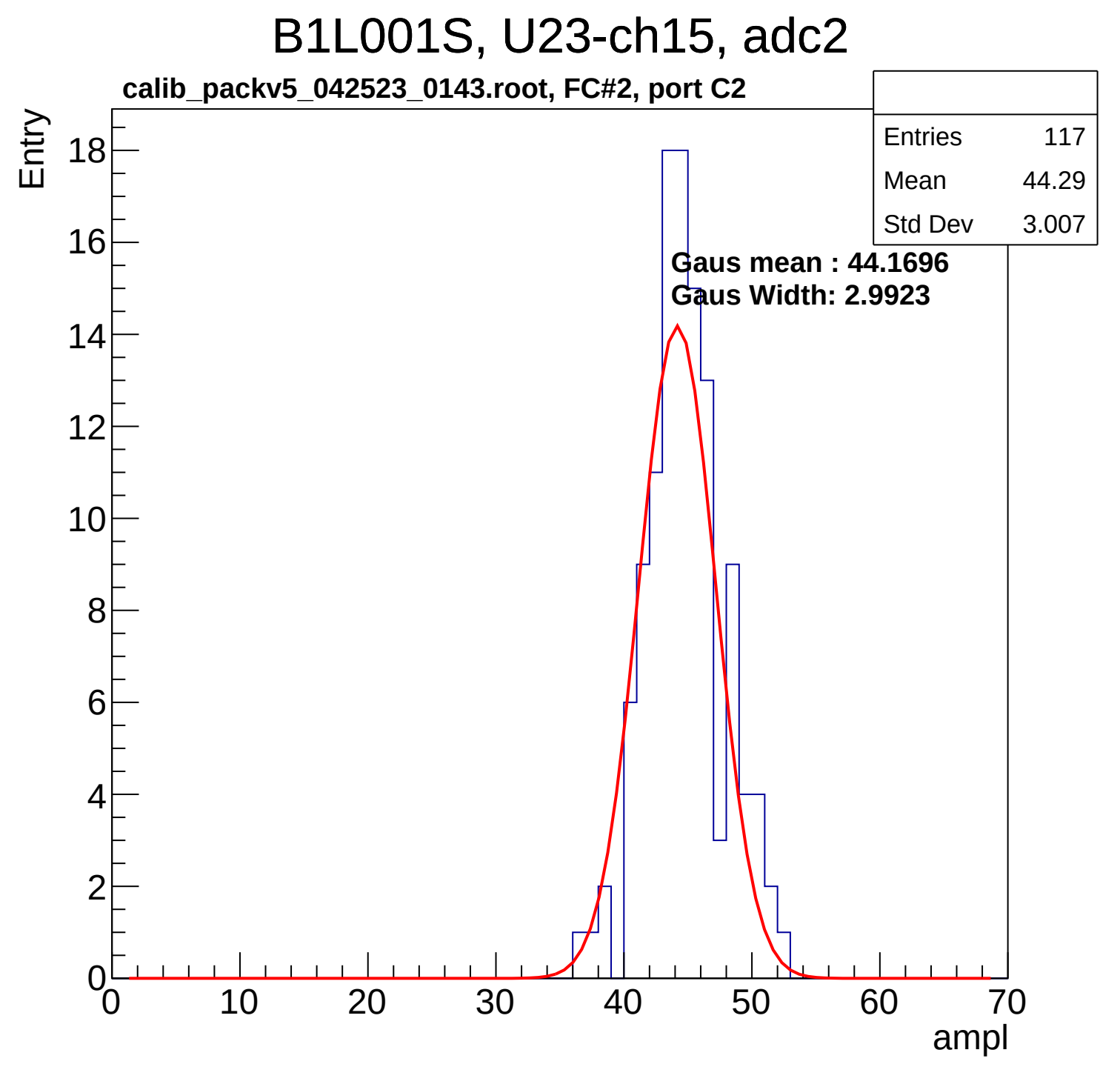
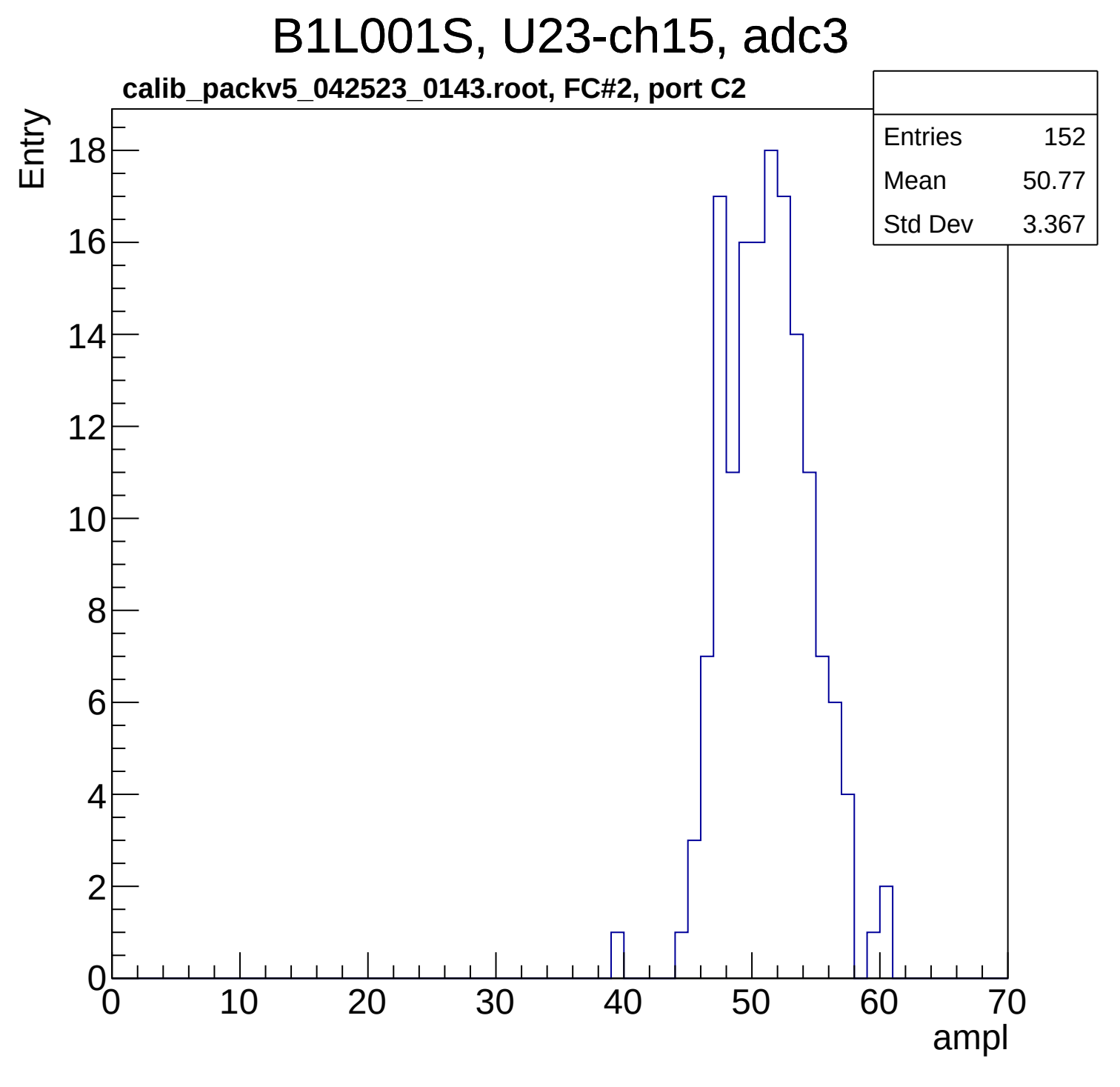

B1L001S, U23-ch15, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 44.29
Std Dev 3.007
Gaus mean : 44.1696
Gaus Width: 2.9923
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch15, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 50.77
Std Dev 3.367
Entry
ampl
0
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6
8
10
12
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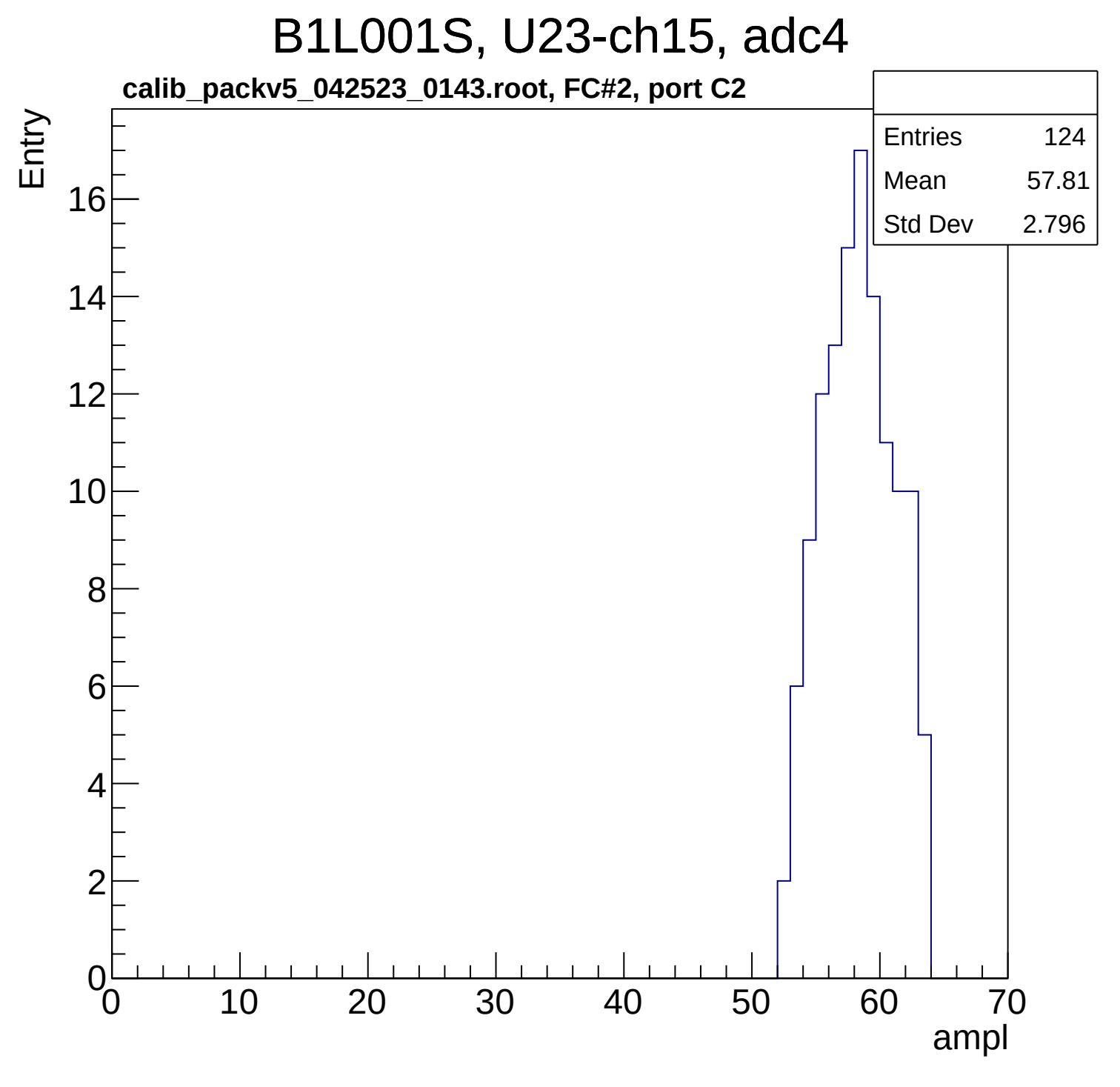
B1L001S, U23-ch15, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 57.81
Std Dev 2.796
Entry
ampl
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2
4
6
8
10
12
14
16
0
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40
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60
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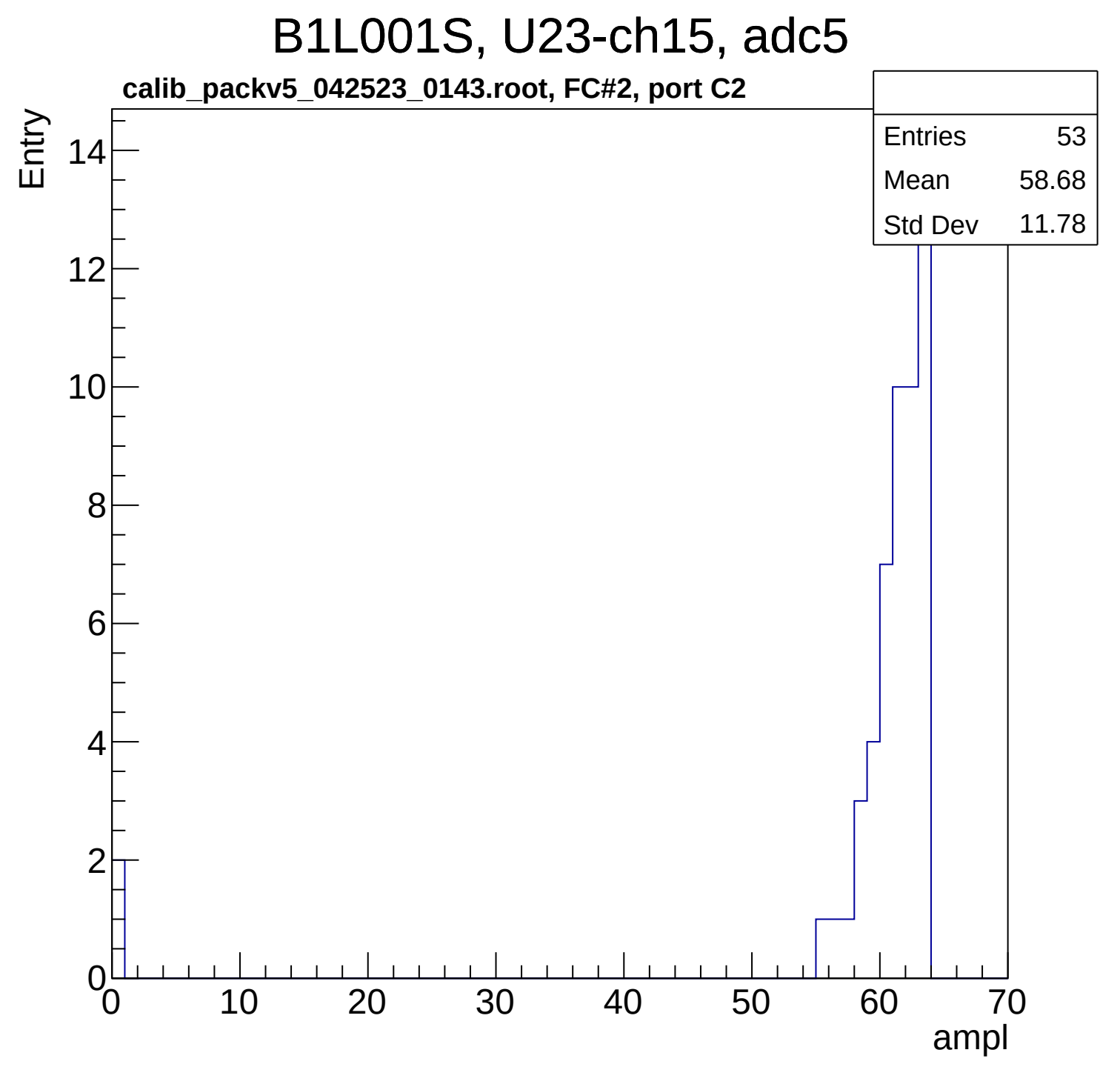
B1L001S, U23-ch15, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 53
Mean 58.68
Std Dev 11.78
Entry
0
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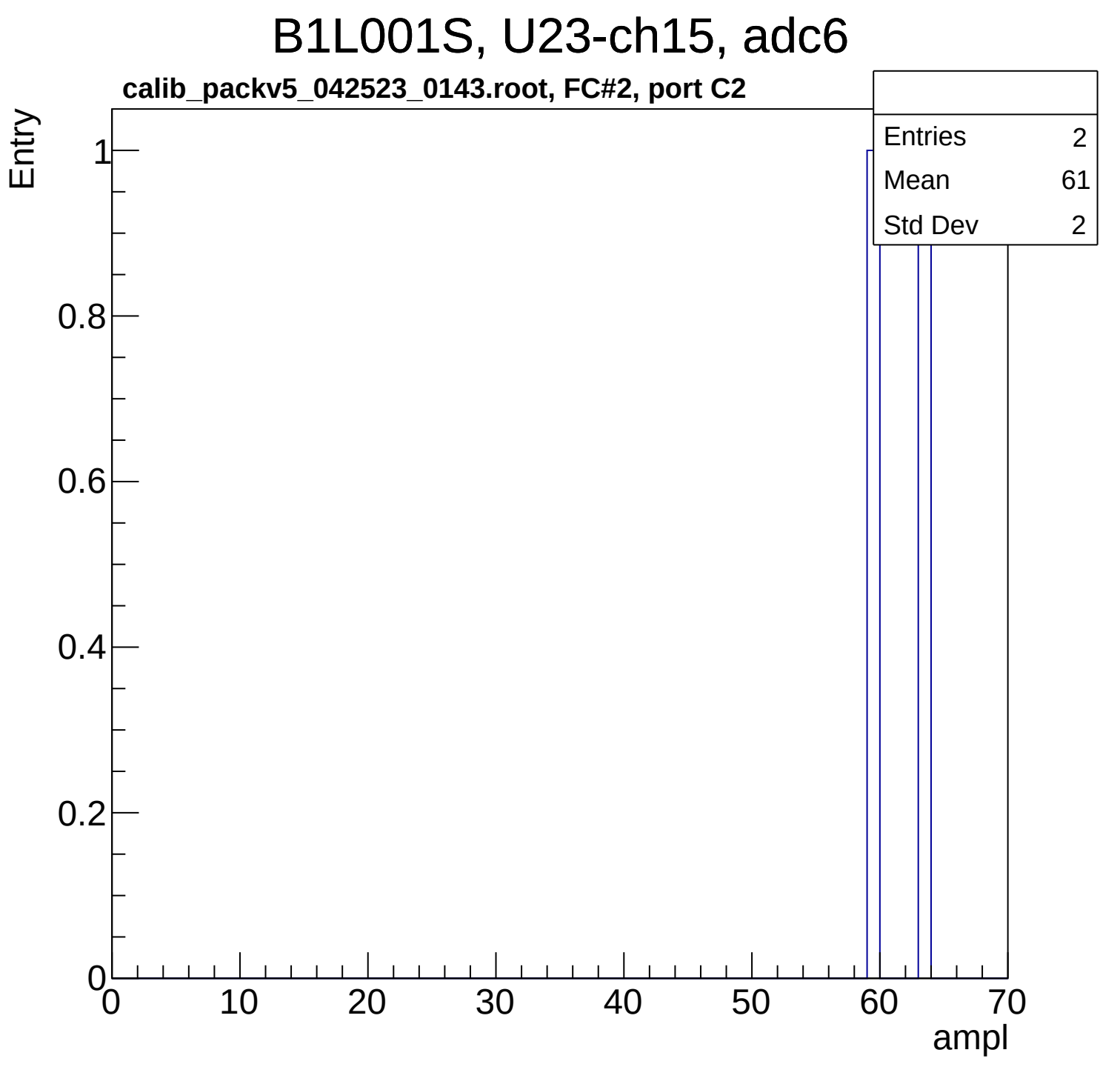
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch15, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 61
Std Dev 2
Entry
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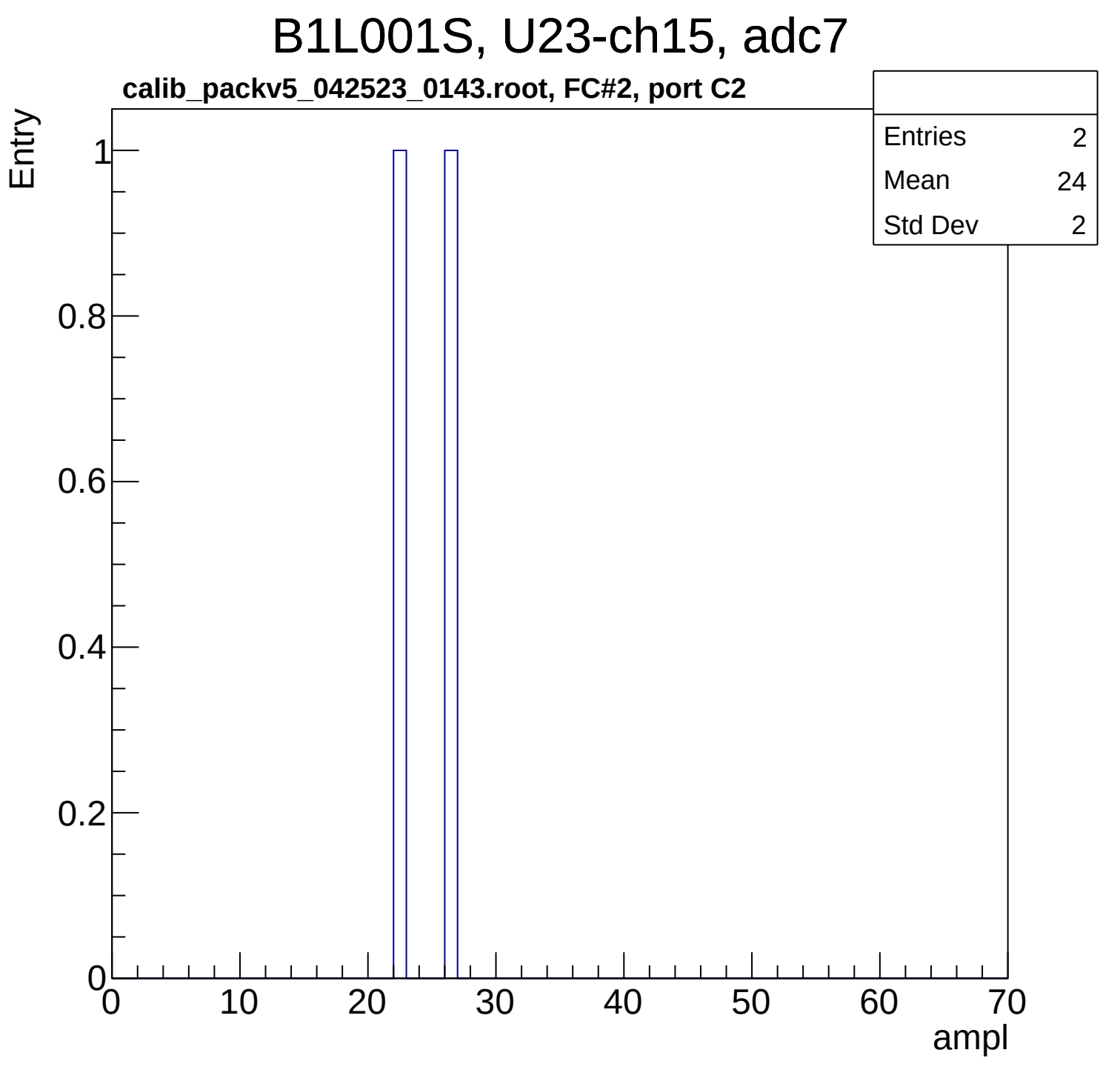
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch15, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 24
Std Dev 2
Entry
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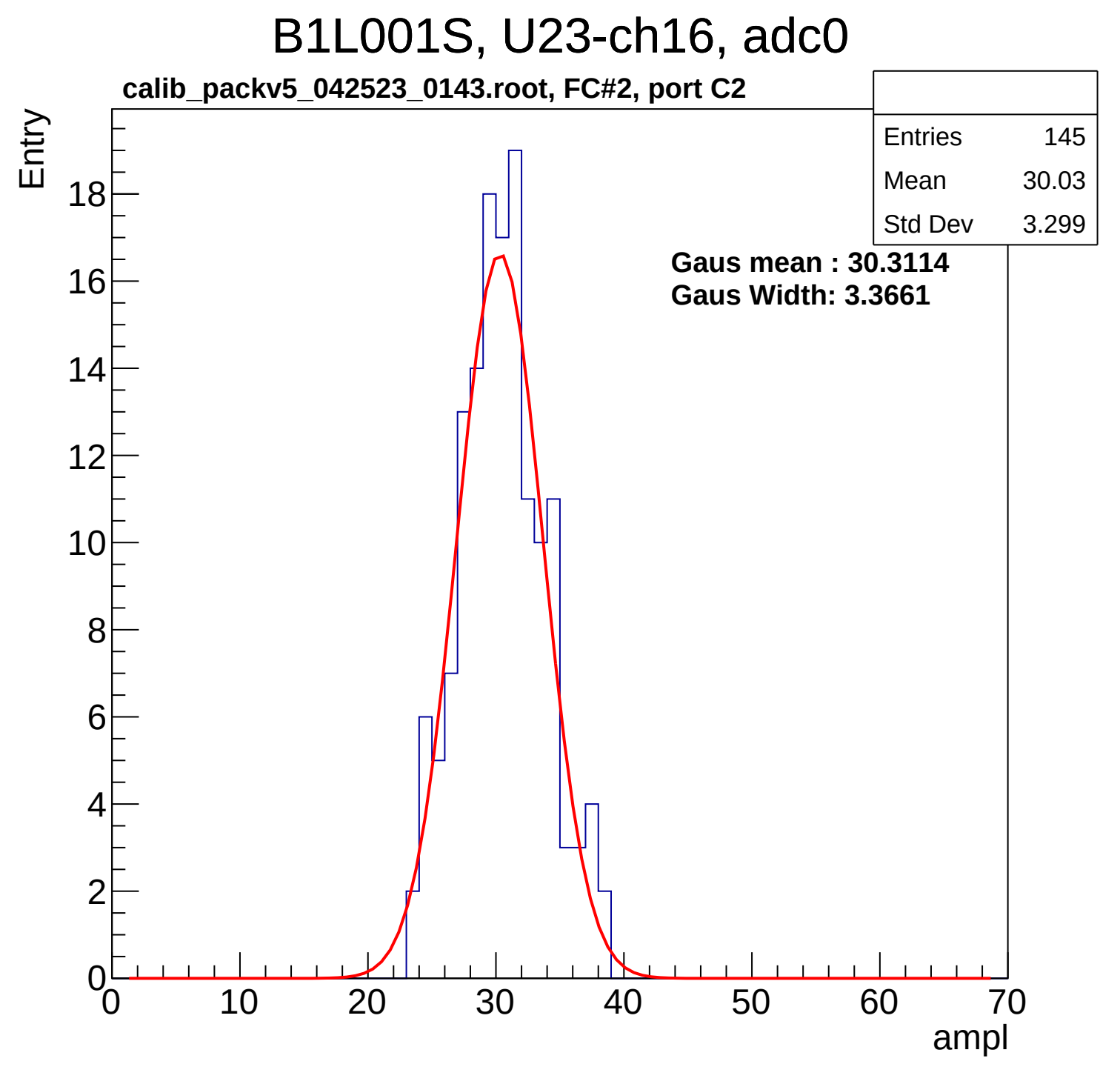
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch16, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 30.03
Std Dev 3.299
Gaus mean : 30.3114
Gaus Width: 3.3661
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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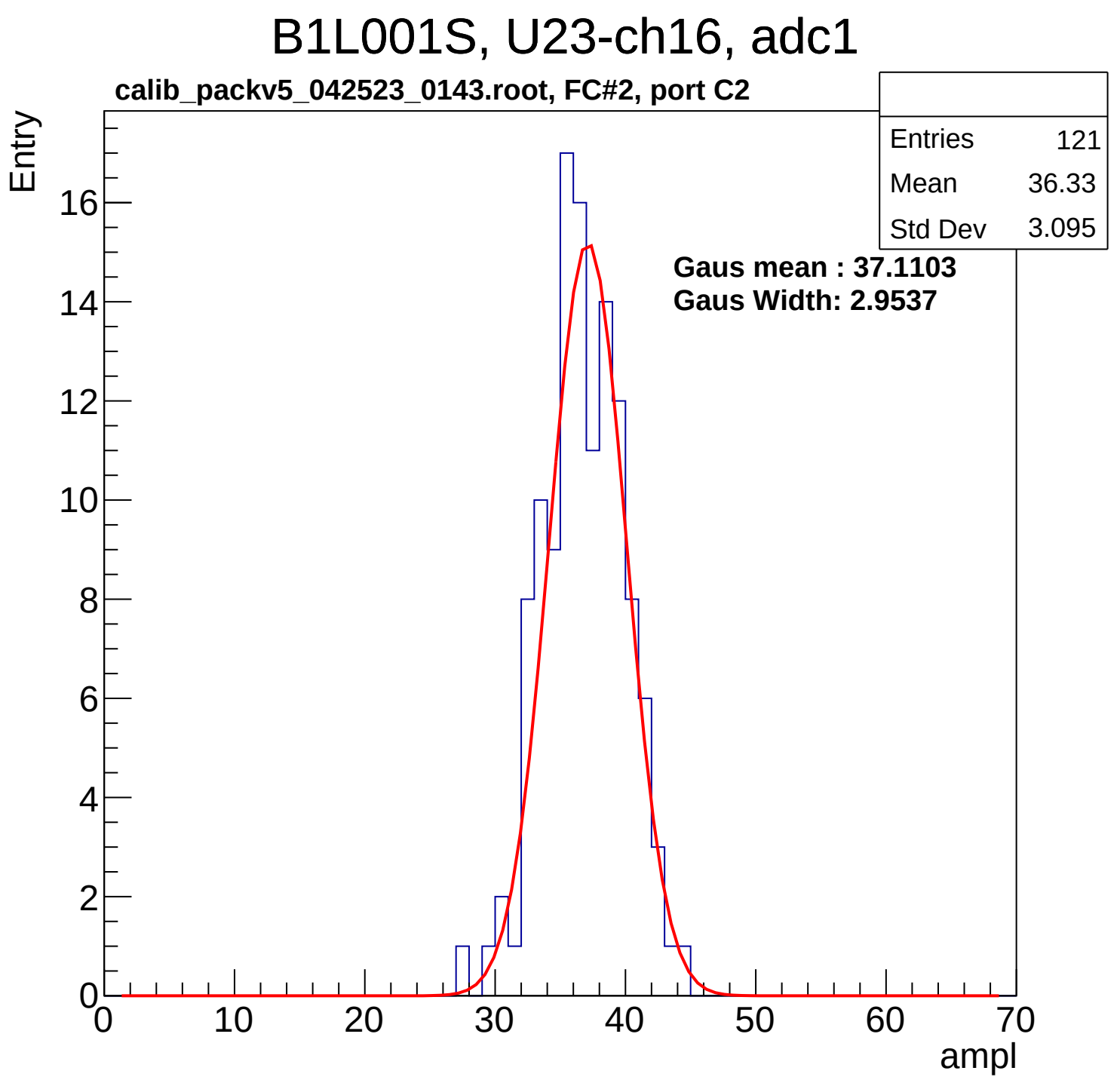
B1L001S, U23-ch16, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 36.33
Std Dev 3.095
Gaus mean : 37.1103
Gaus Width: 2.9537
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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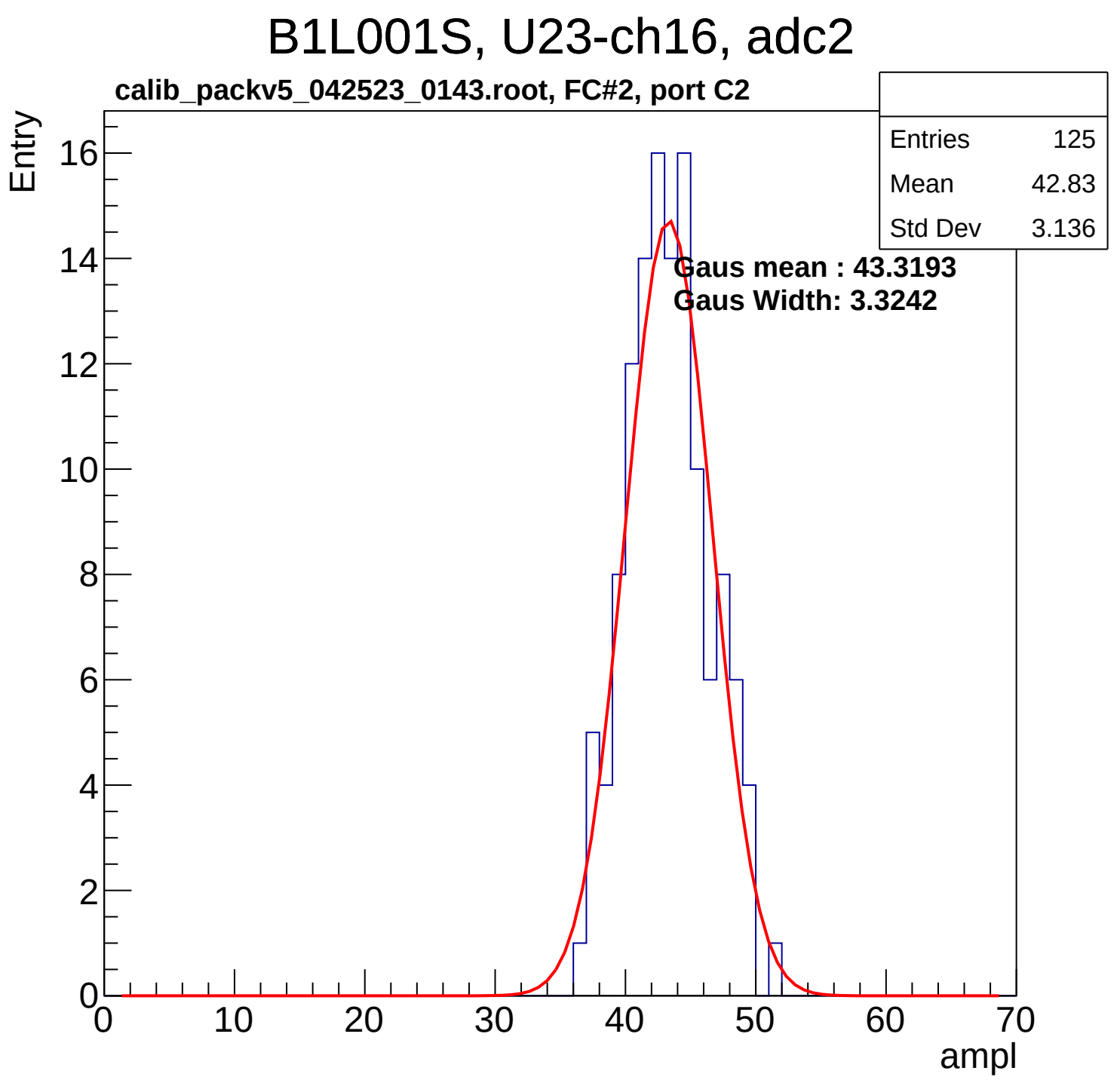
B1L001S, U23-ch16, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 42.83
Std Dev 3.136
Gaus mean : 43.3193
Gaus Width: 3.3242
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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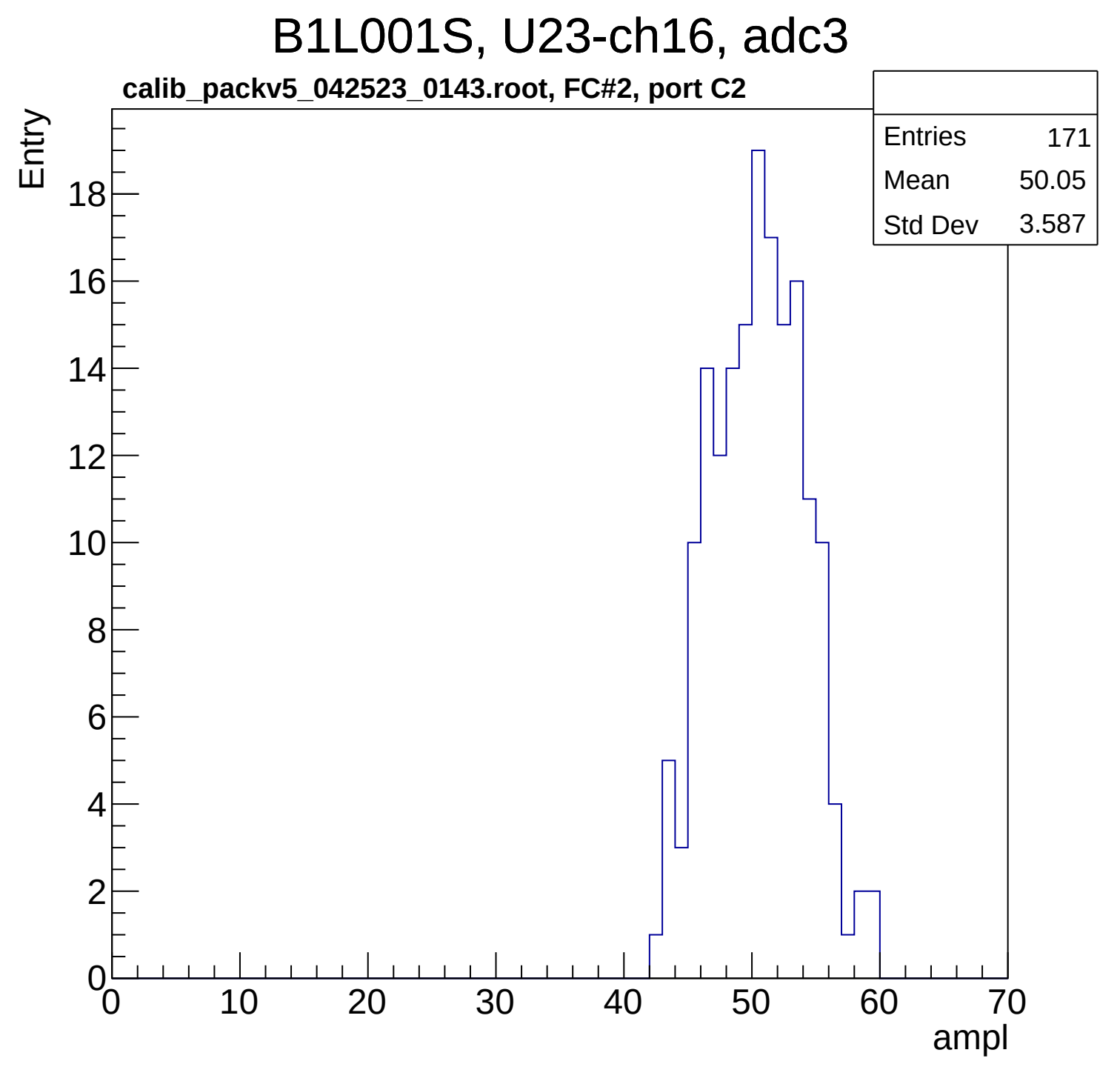
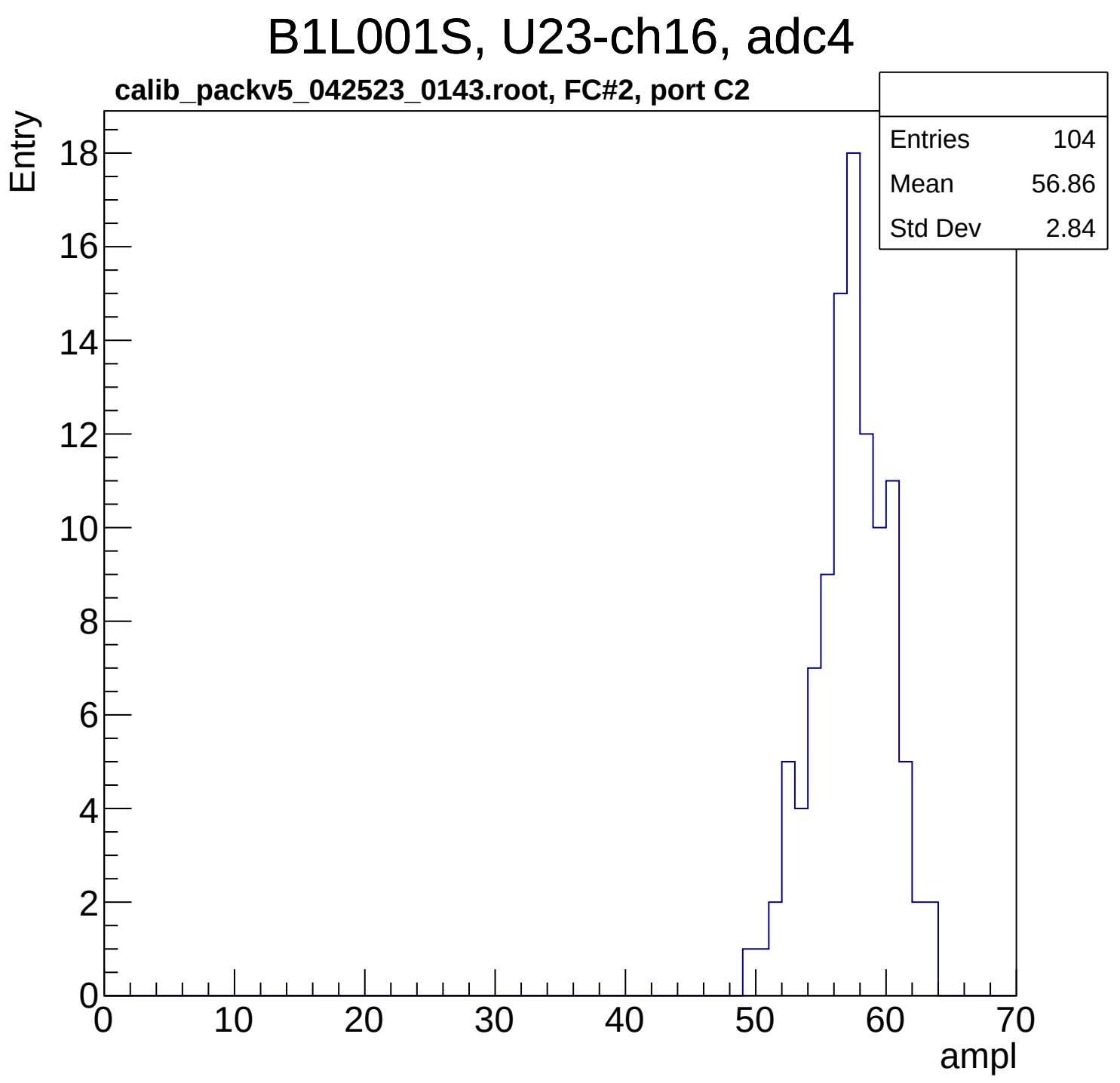
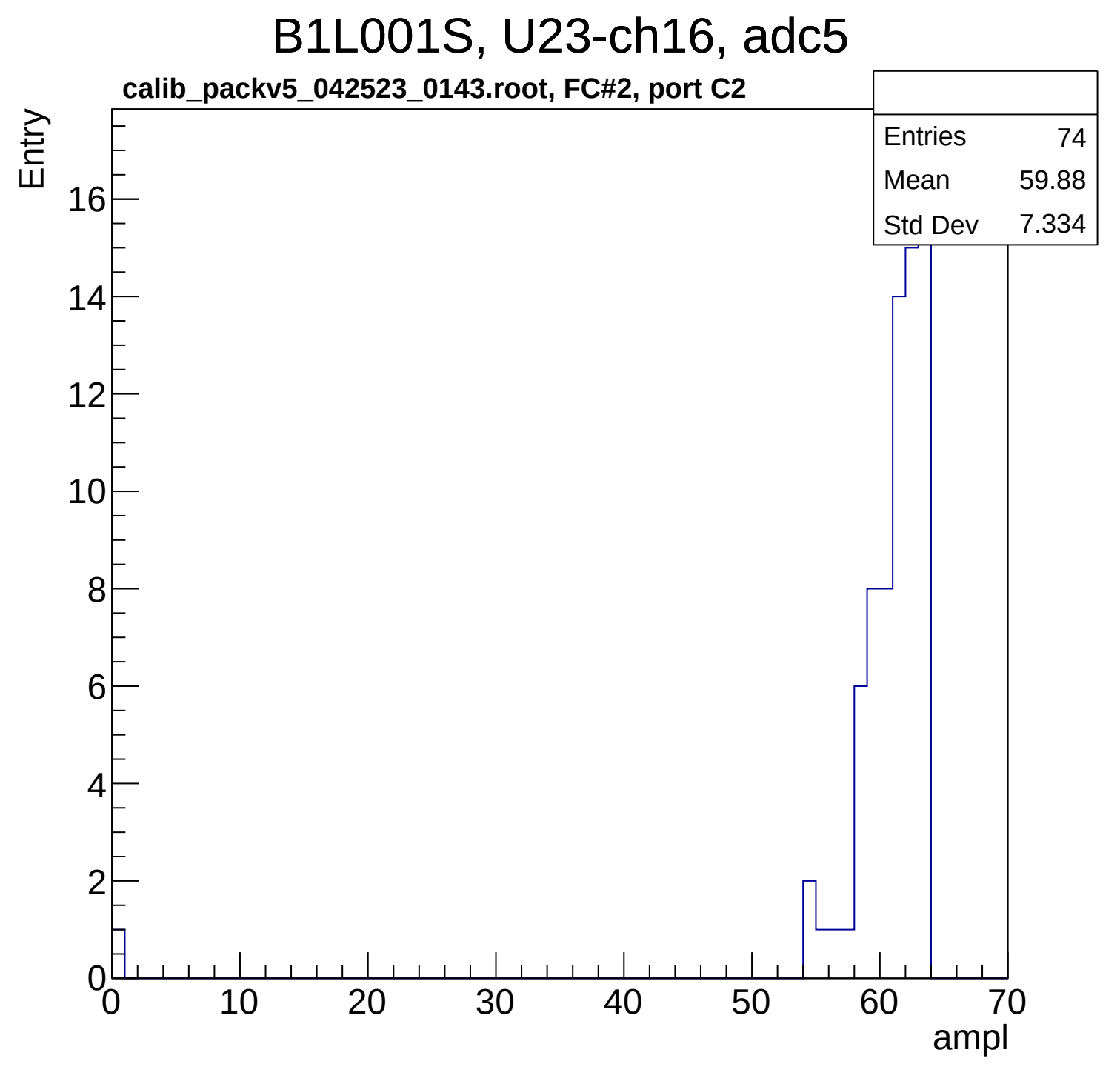

B1L001S, U23-ch16, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 171
Mean 50.05
Std Dev 3.587
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch16, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 56.86
Std Dev 2.84
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch16, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 59.88
Std Dev 7.334
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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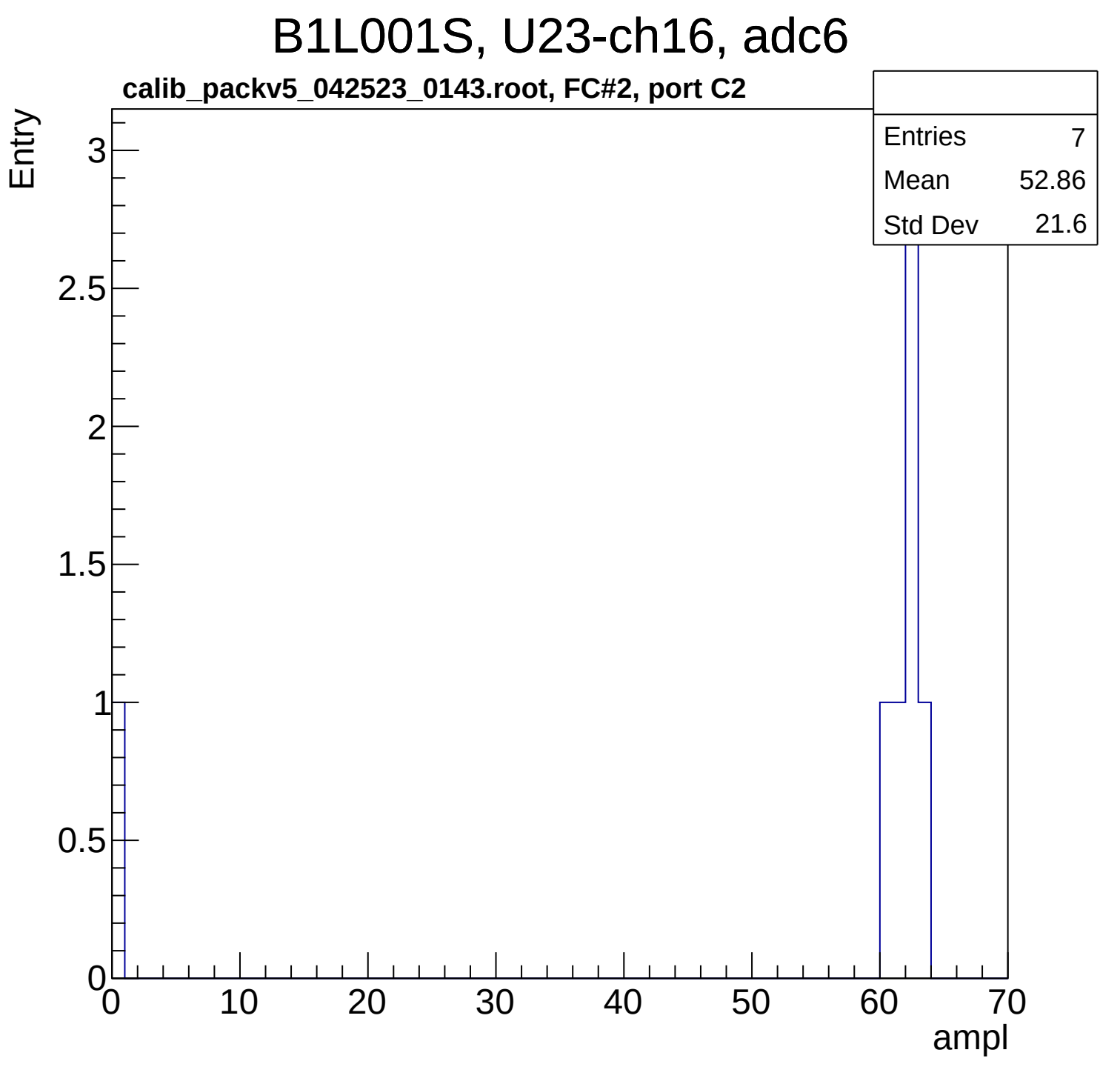
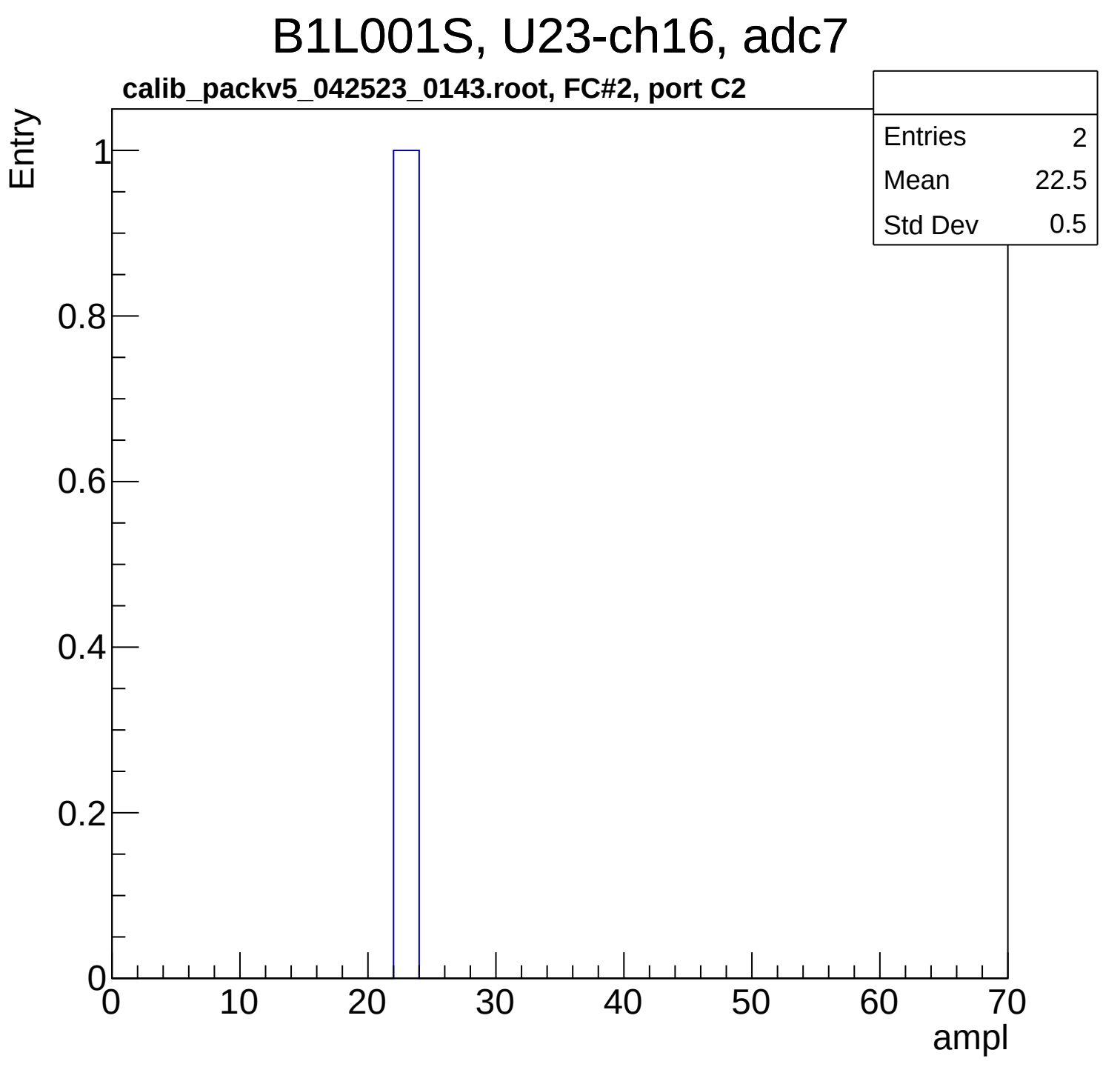
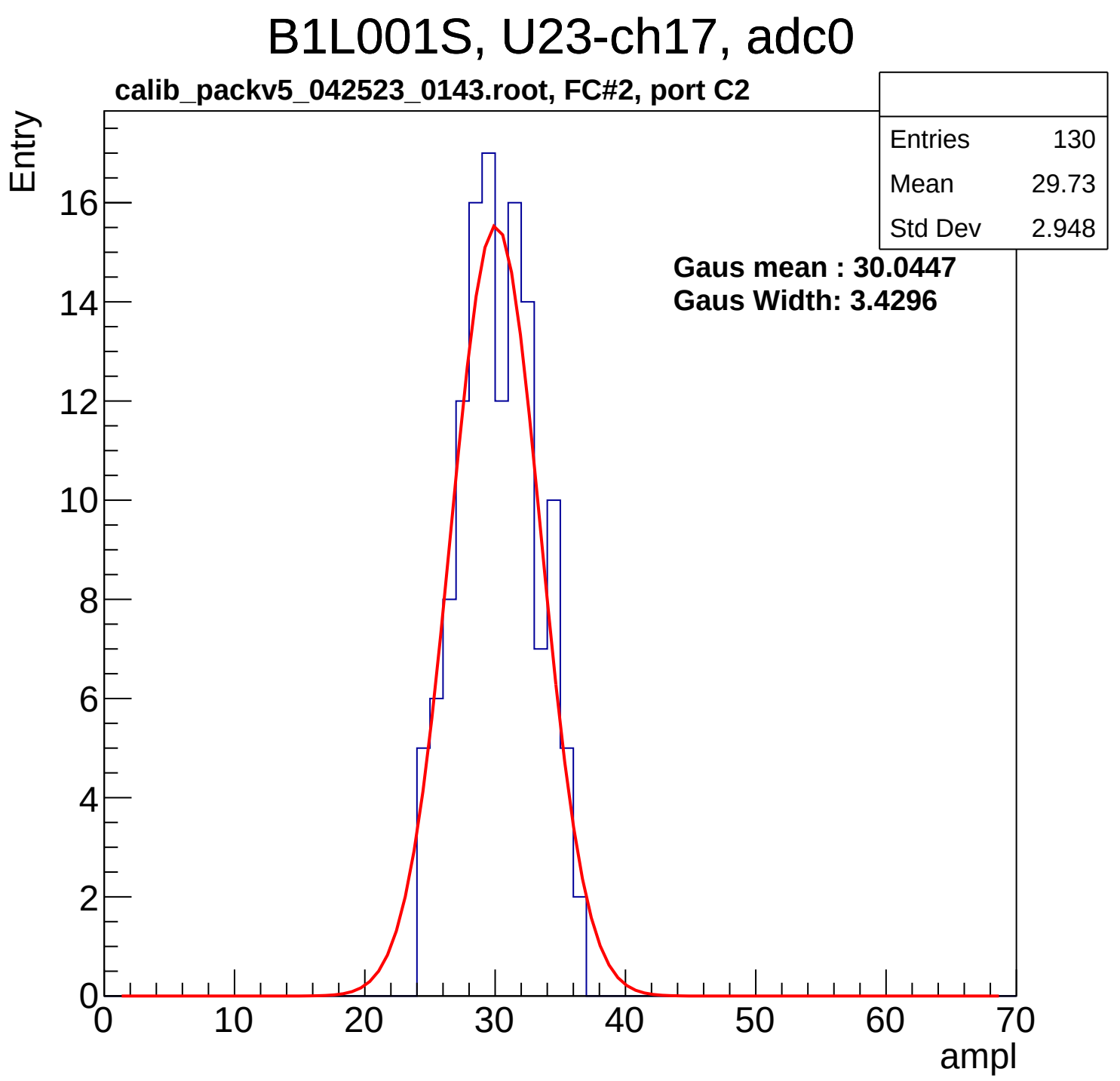
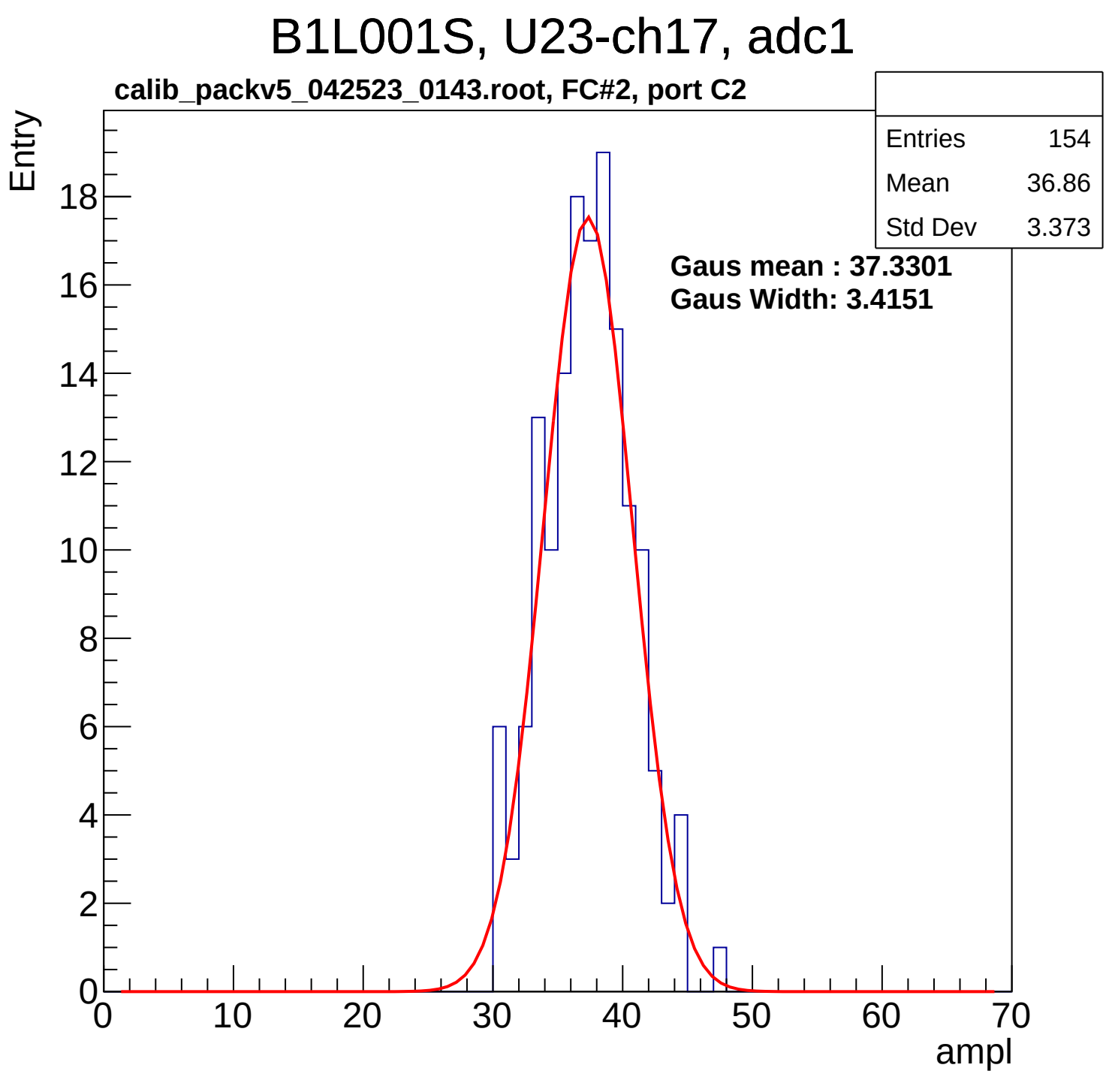
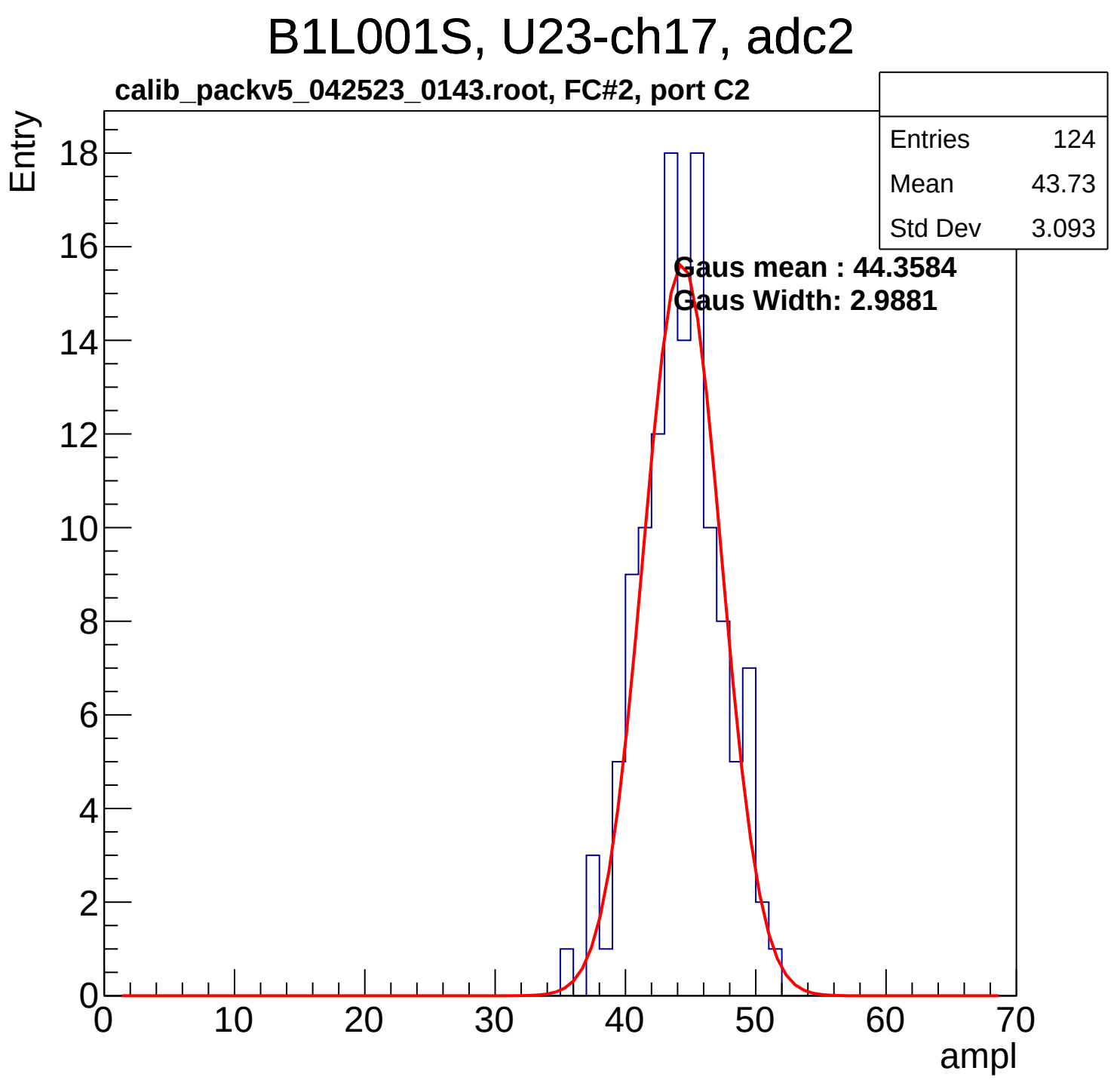

B1L001S, U23-ch16, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 52.86
Std Dev 21.6
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch16, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 22.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 29.73
Std Dev 2.948
Gaus mean : 30.0447
Gaus Width: 3.4296
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 36.86
Std Dev 3.373
Gaus mean : 37.3301
Gaus Width: 3.4151
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 43.73
Std Dev 3.093
Gaus mean : 44.3584
Gaus Width: 2.9881
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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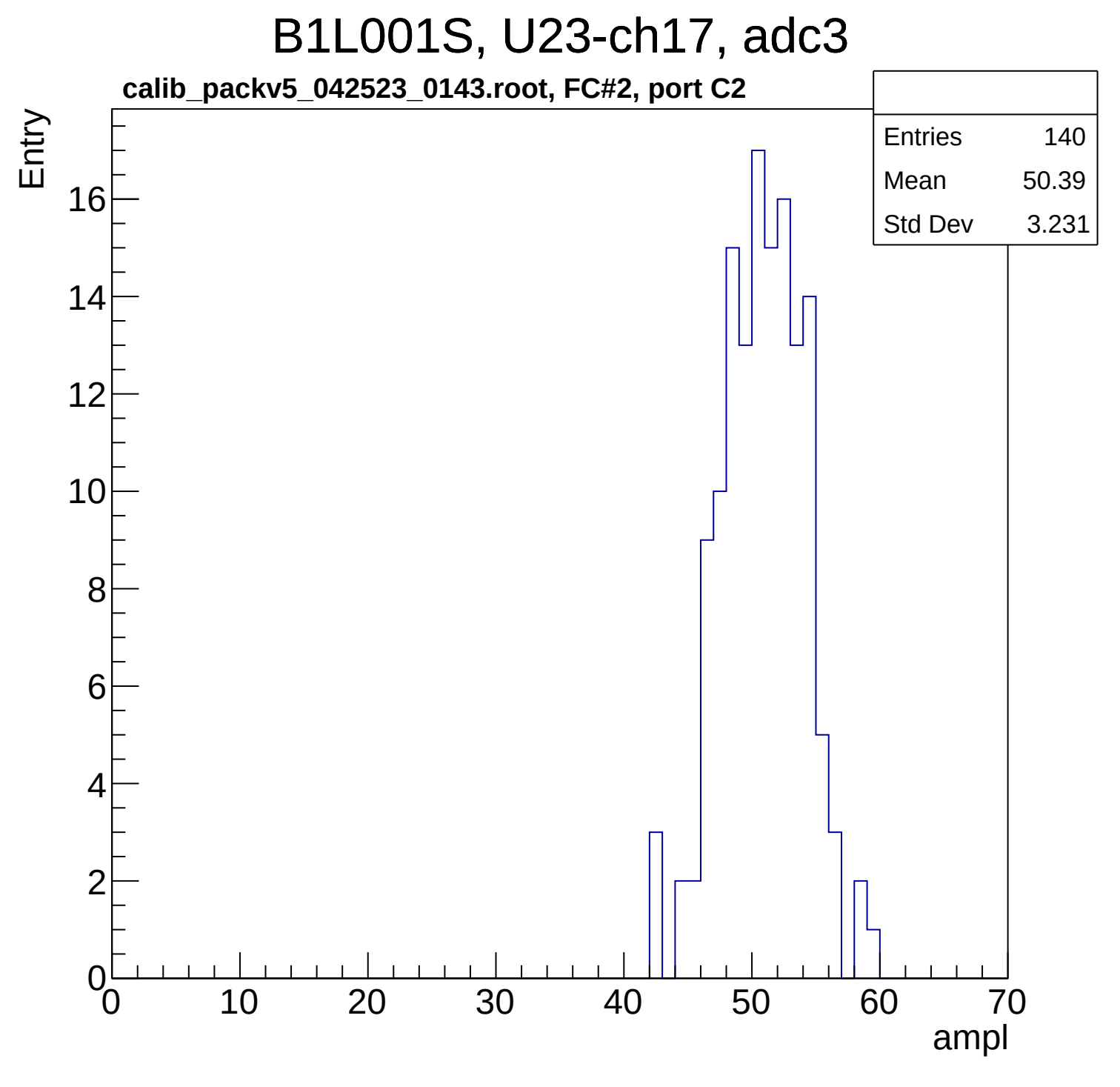
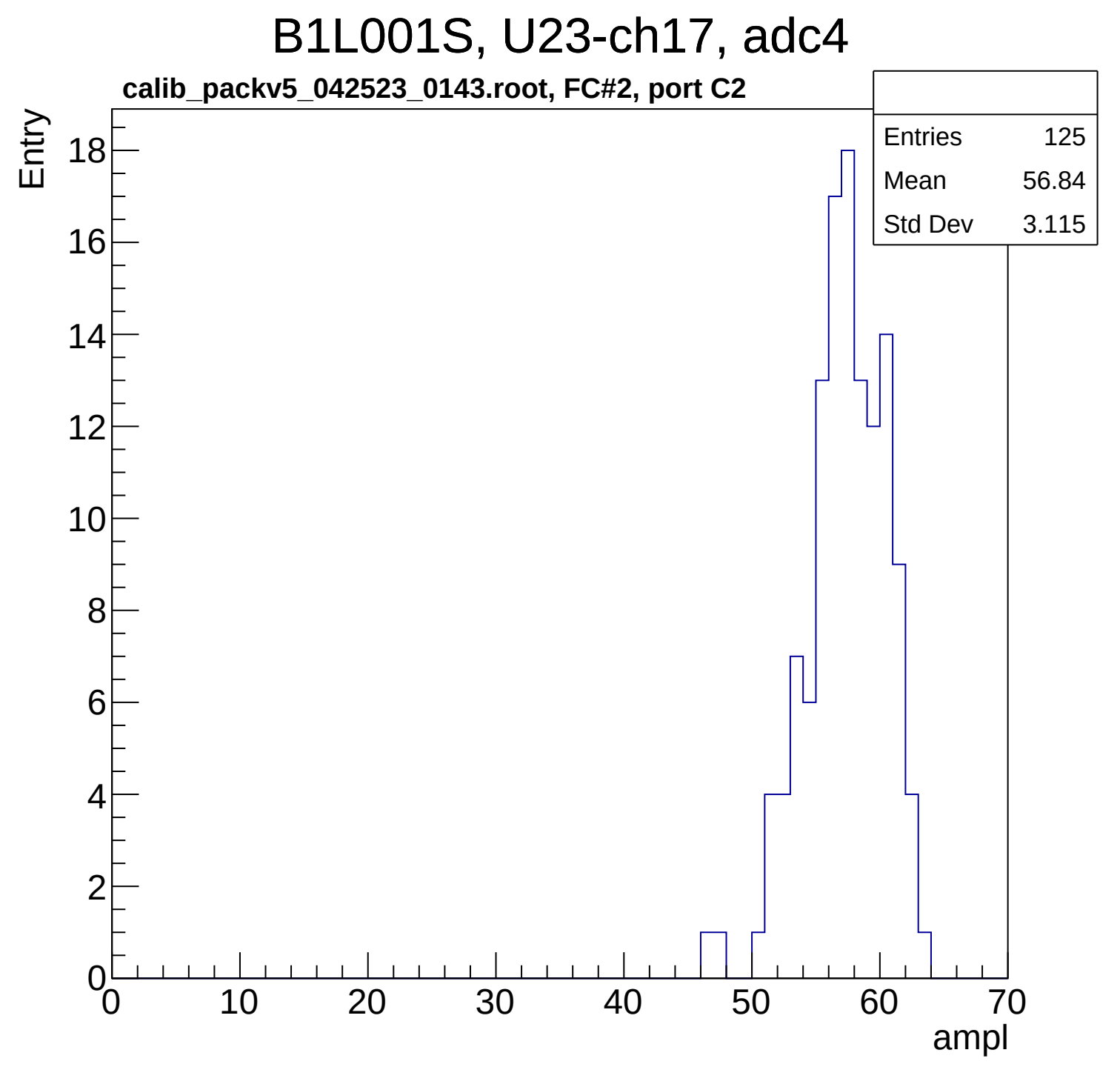
60
70


B1L001S, U23-ch17, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 50.39
Std Dev 3.231
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 56.84
Std Dev 3.115
Entry
ampl
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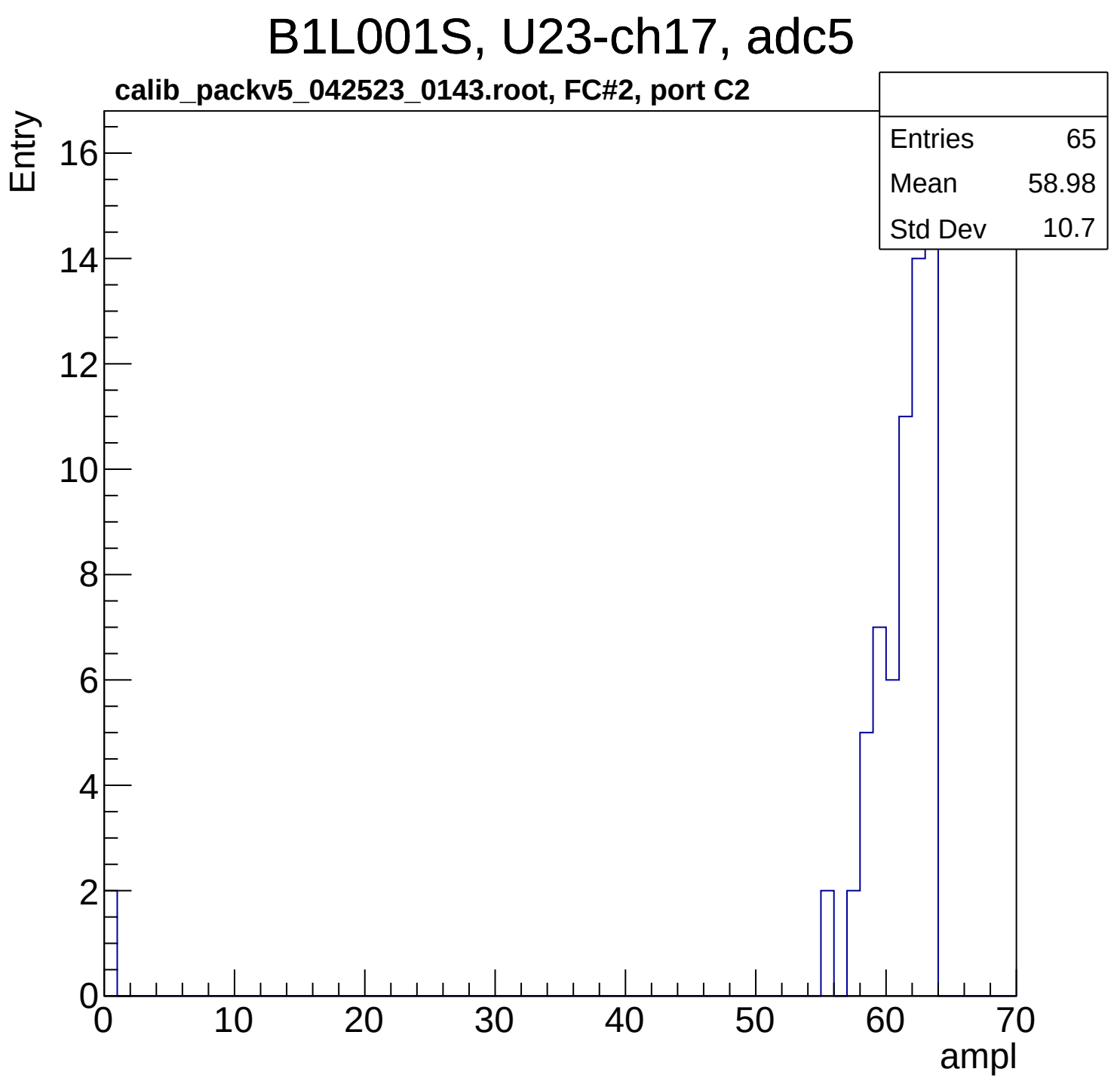
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 65
Mean 58.98
Std Dev 10.7
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
30
40
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60
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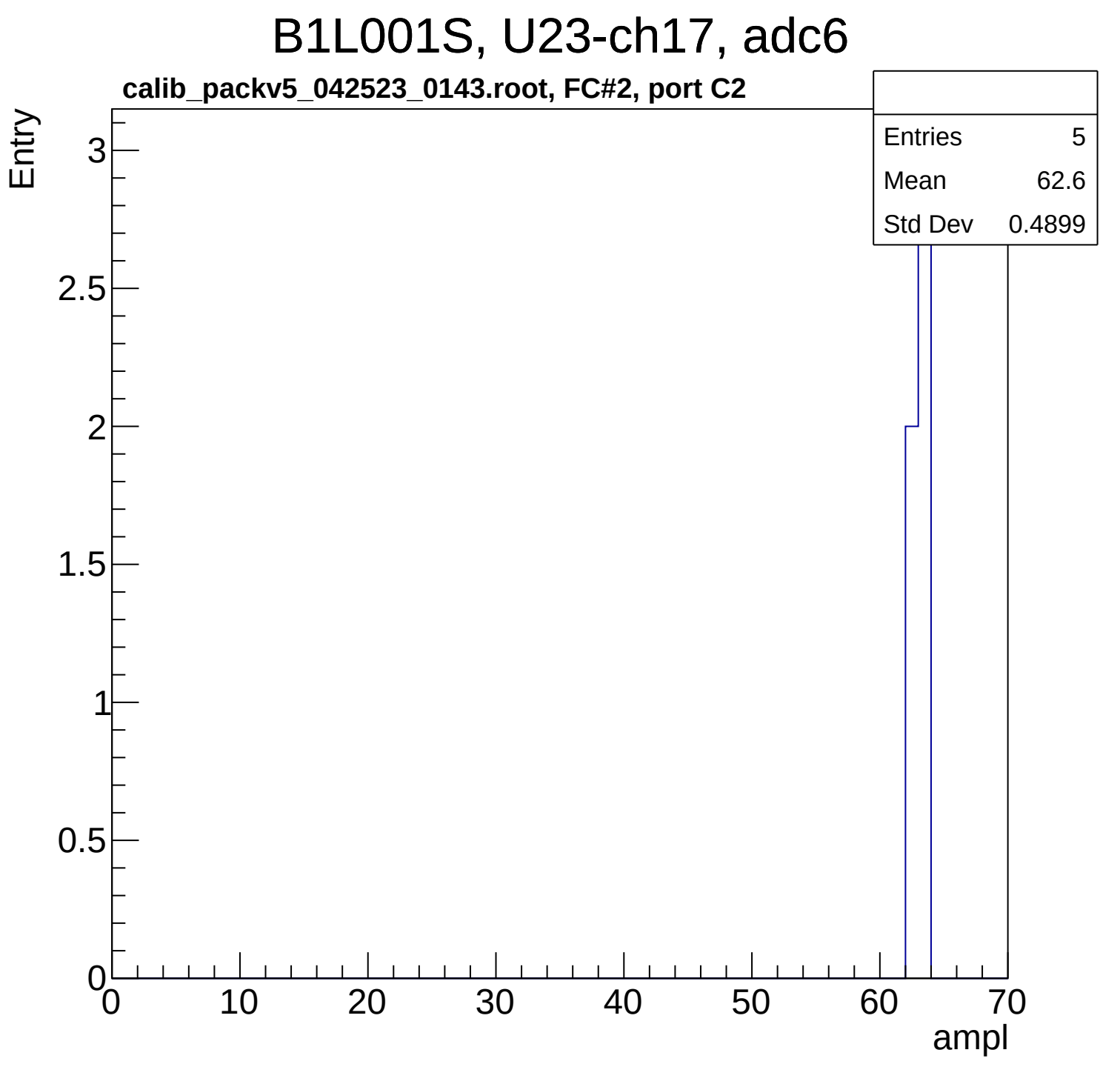
B1L001S, U23-ch17, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.6
Std Dev 0.4899
Entry
ampl
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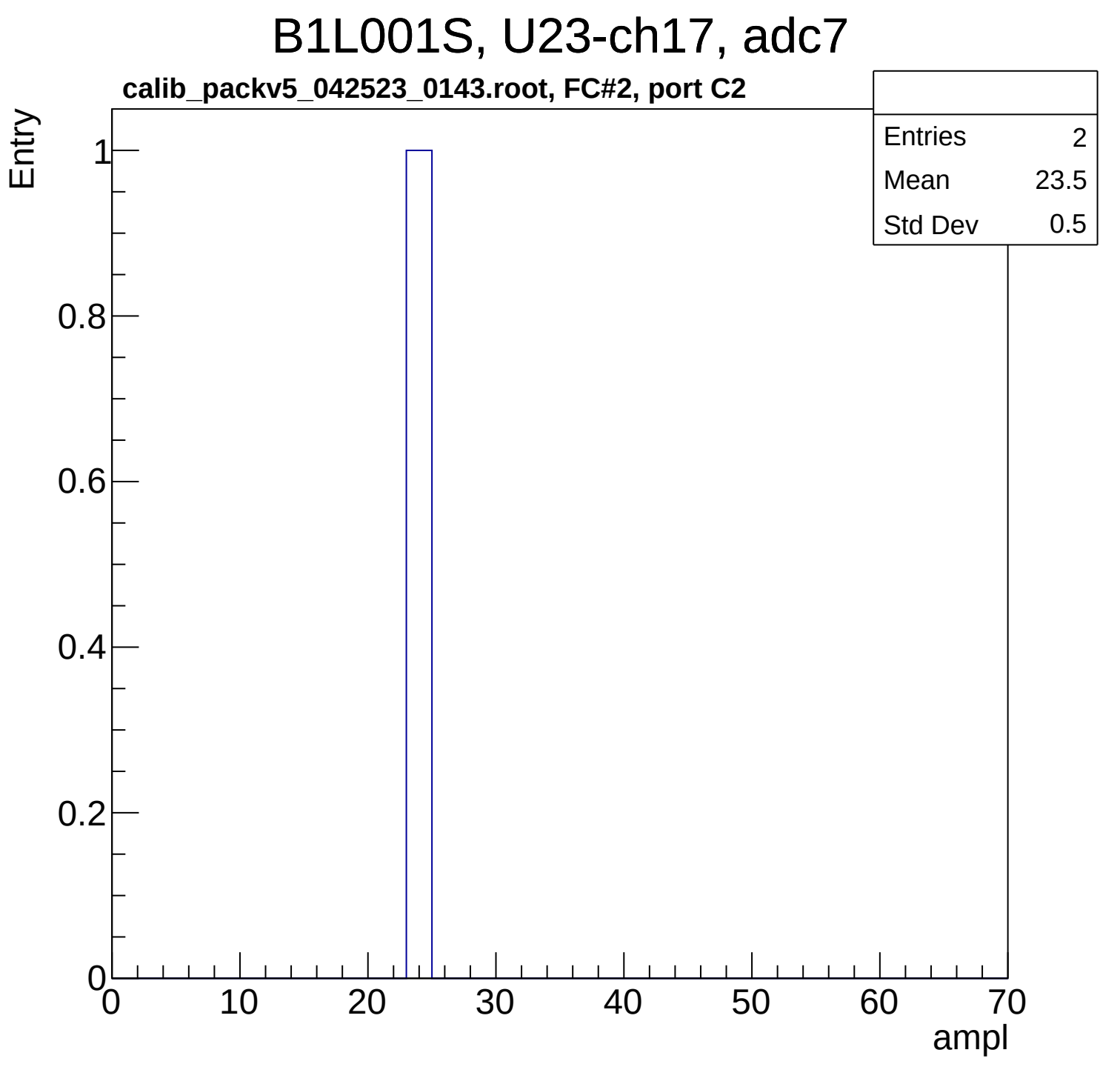
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch17, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 23.5
Std Dev 0.5
Entry
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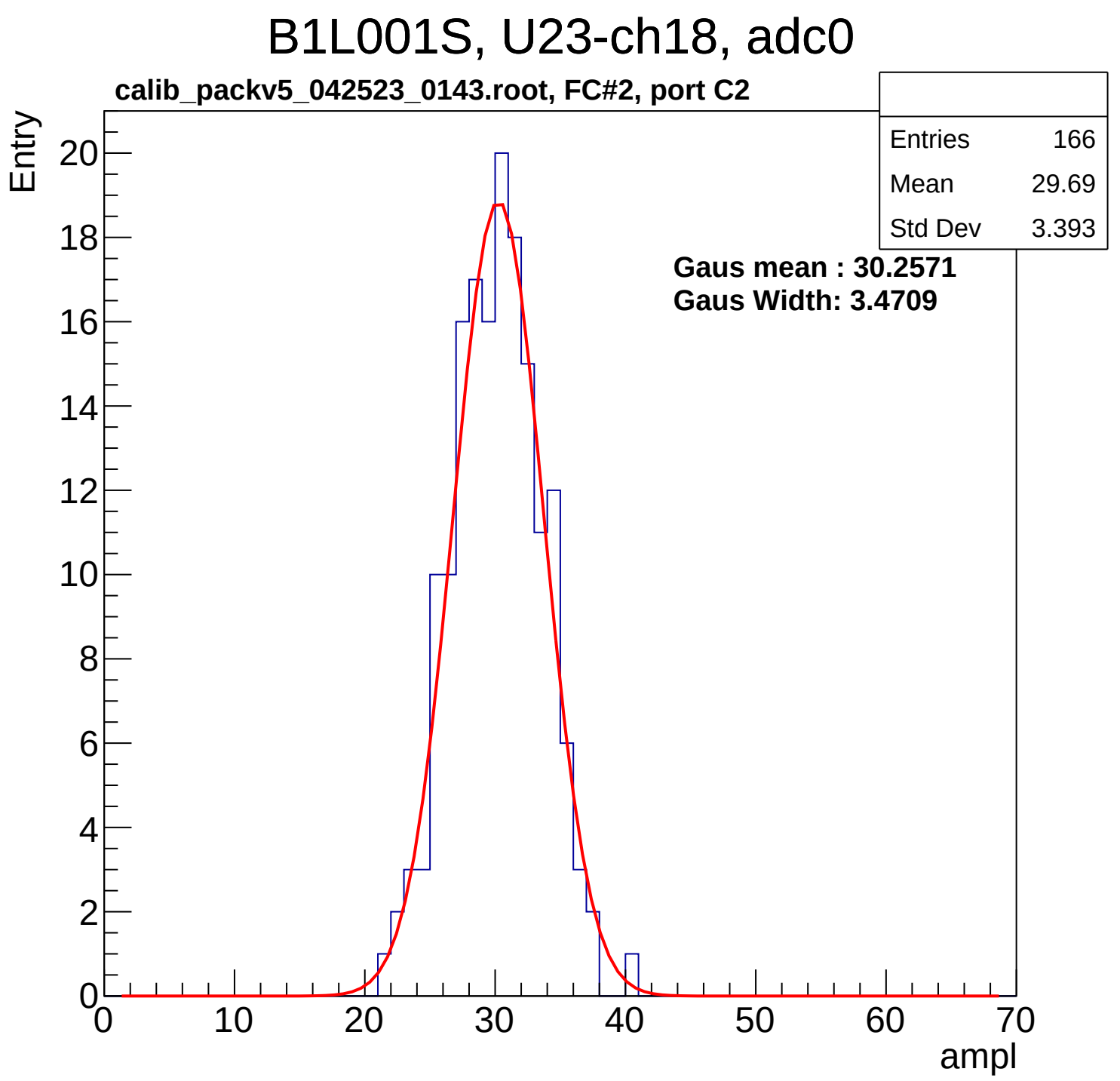
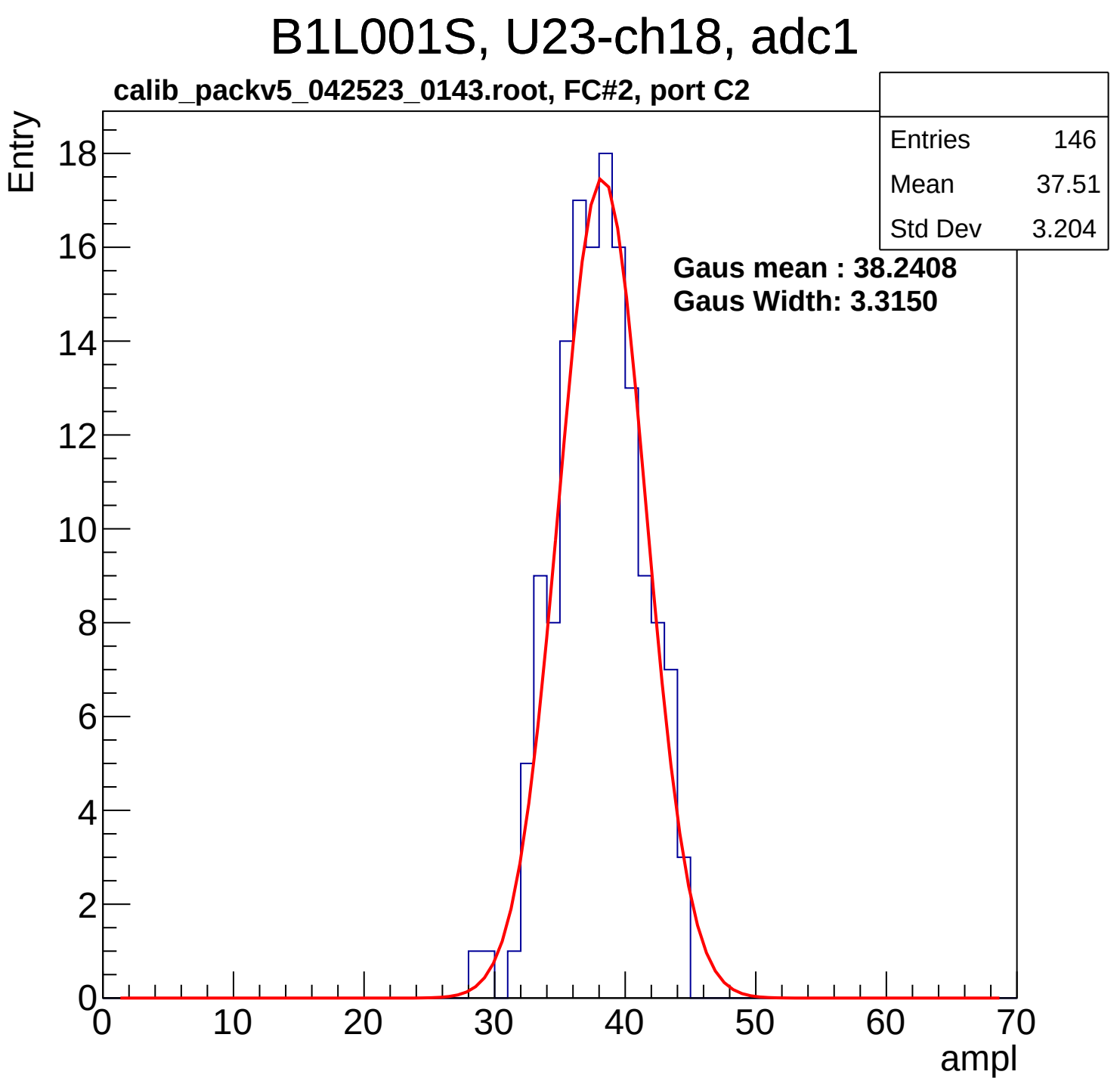
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch18, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 166
Mean 29.69
Std Dev 3.393
Gaus mean : 30.2571
Gaus Width: 3.4709
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch18, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 37.51
Std Dev 3.204
Gaus mean : 38.2408
Gaus Width: 3.3150
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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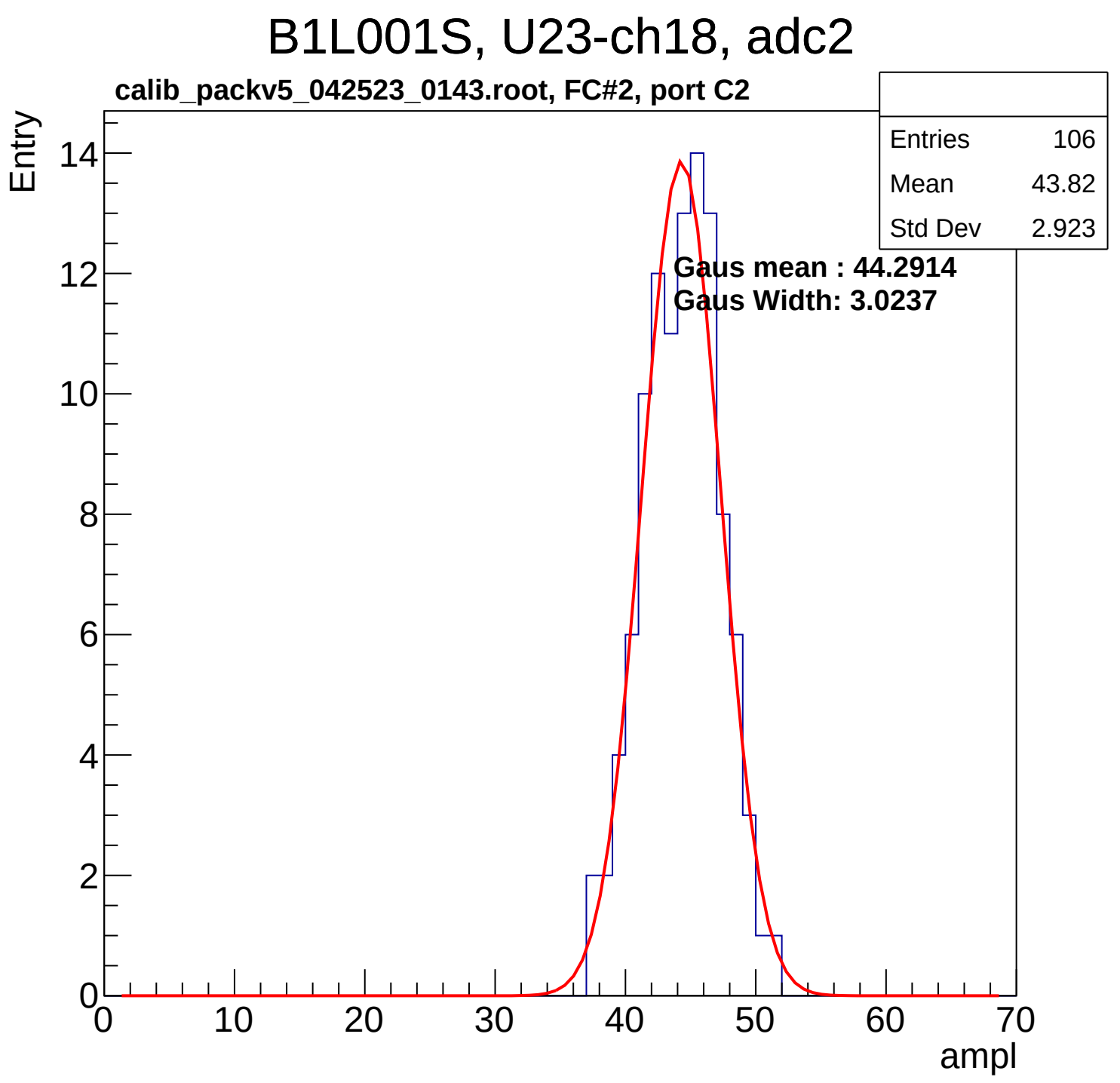
B1L001S, U23-ch18, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 43.82
Std Dev 2.923
Gaus mean : 44.2914
Gaus Width: 3.0237
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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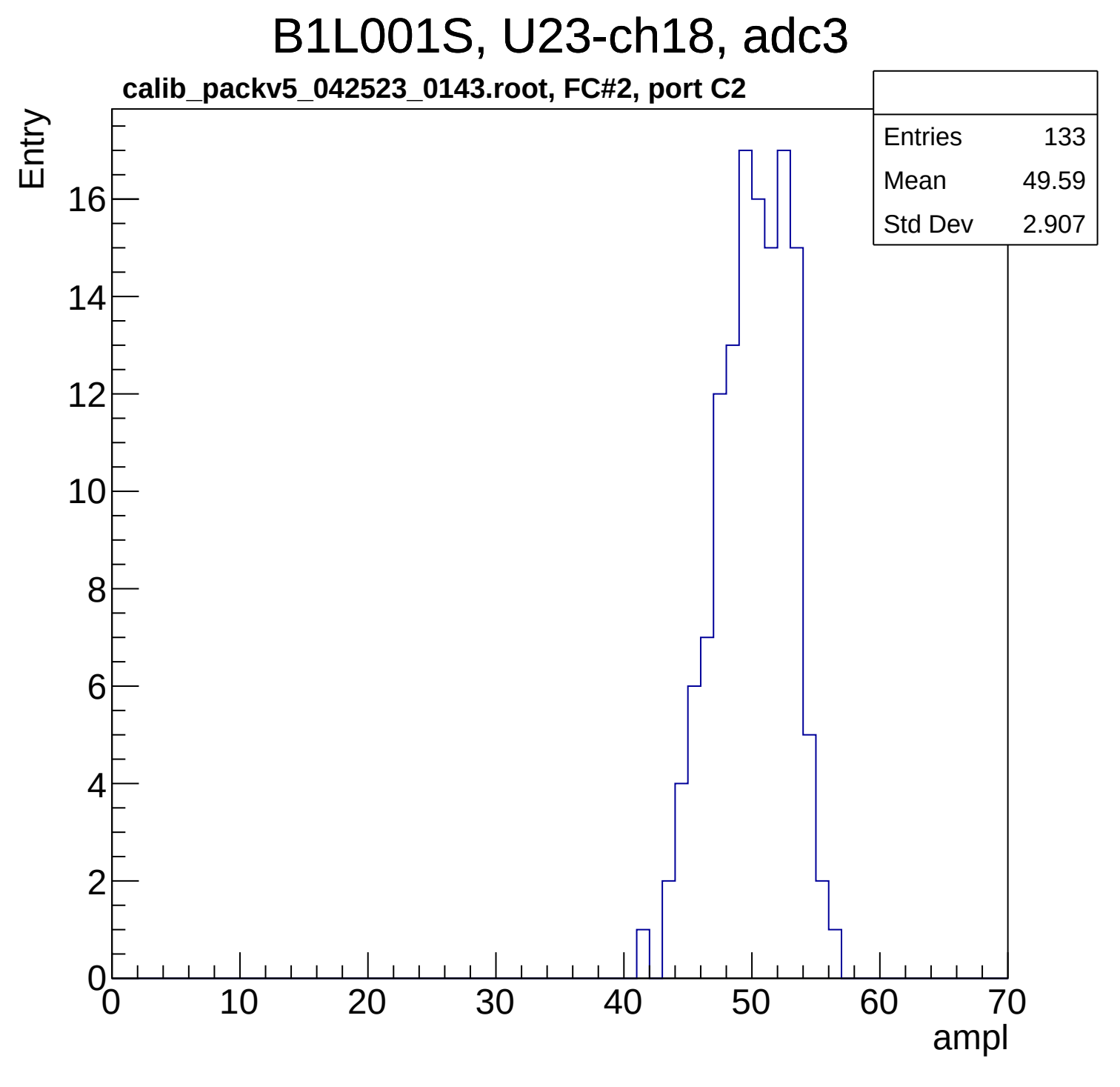
B1L001S, U23-ch18, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 49.59
Std Dev 2.907
Entry
ampl
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6
8
10
12
14
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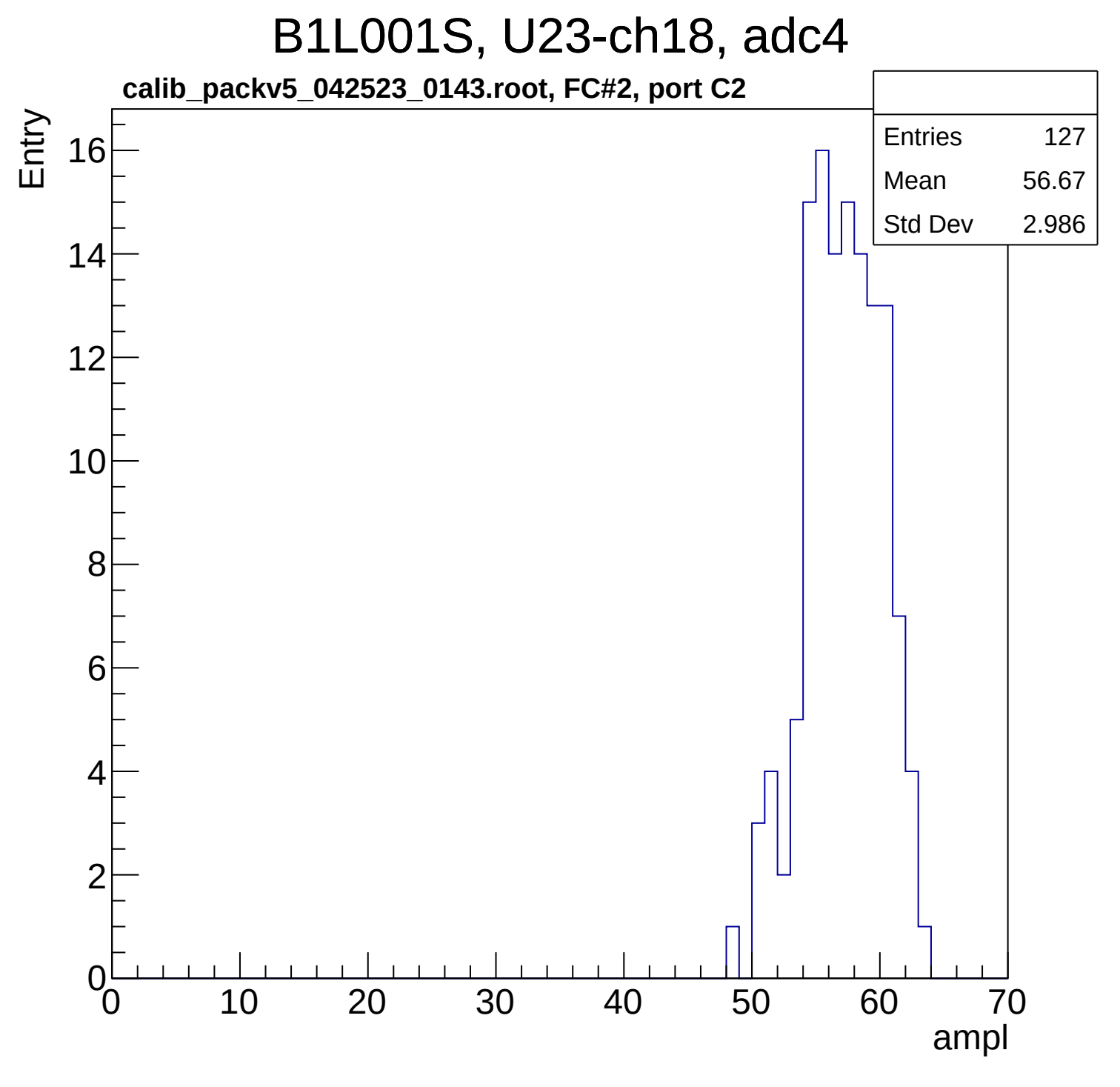
0
10
20
30
40
50
60
70


B1L001S, U23-ch18, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 56.67
Std Dev 2.986
Entry
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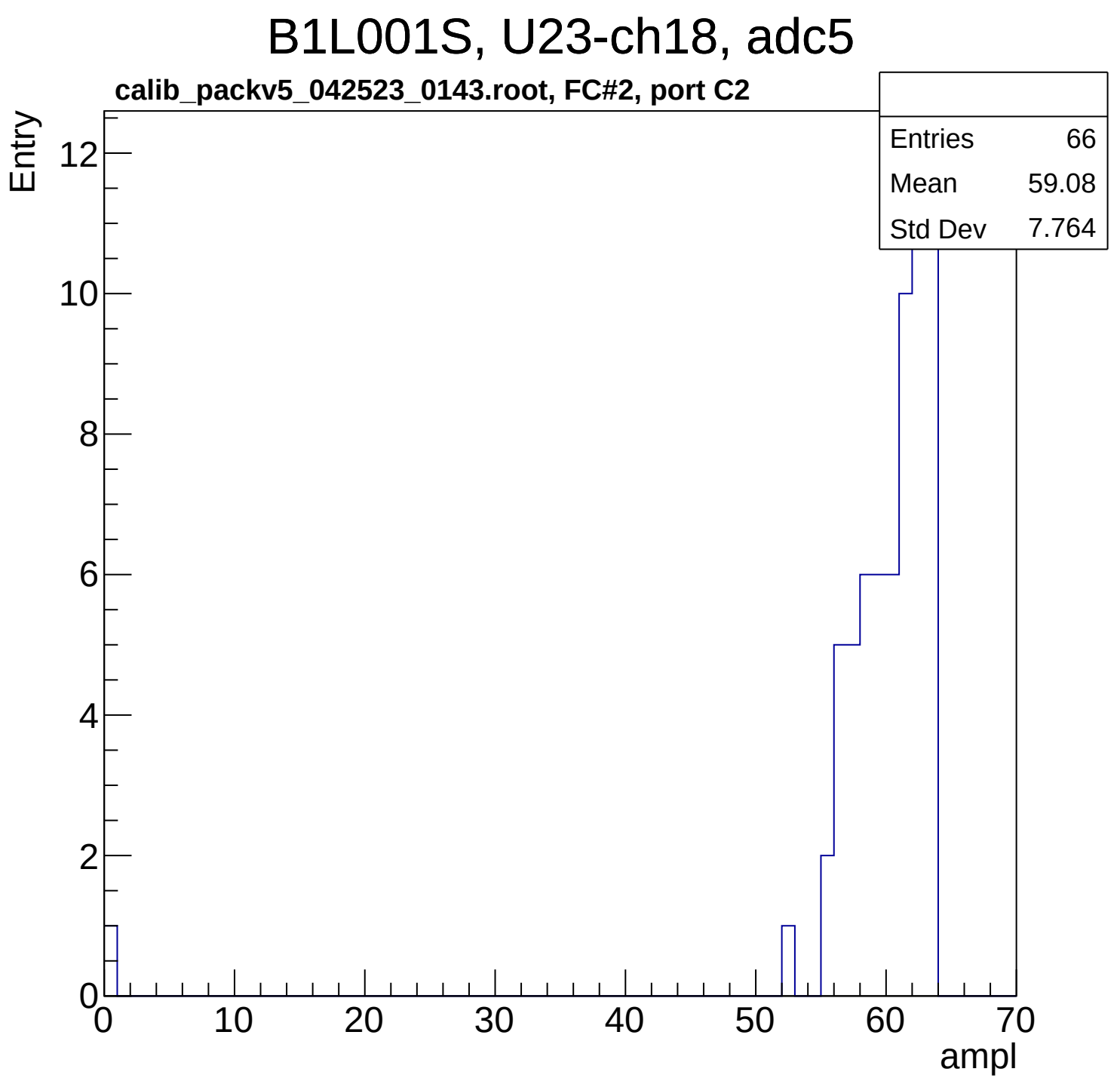
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch18, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 66
Mean 59.08
Std Dev 7.764
Entry
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4
6
8
10
12
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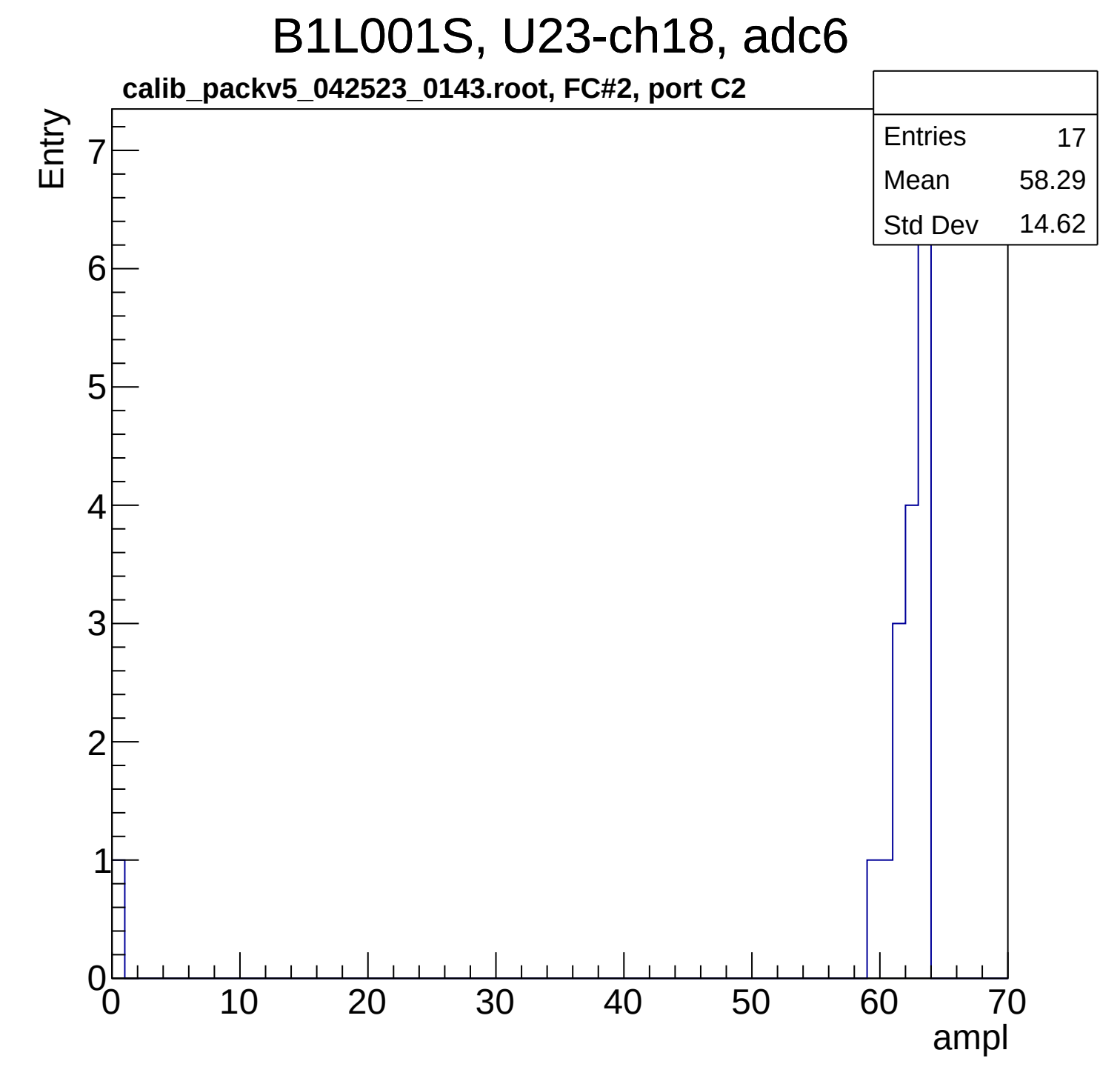
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch18, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 17
Mean 58.29
Std Dev 14.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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40
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60
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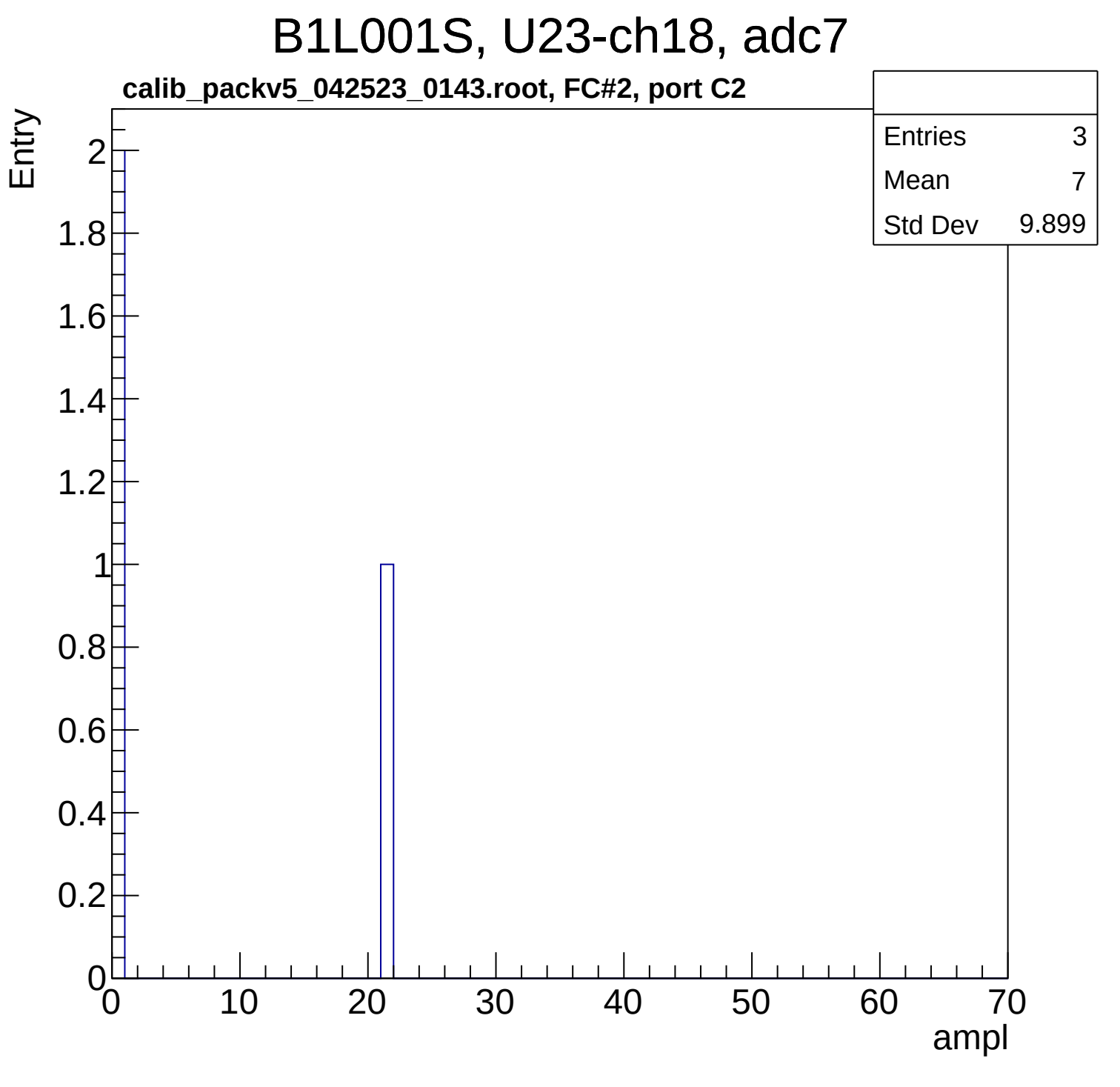
B1L001S, U23-ch18, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
0
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60
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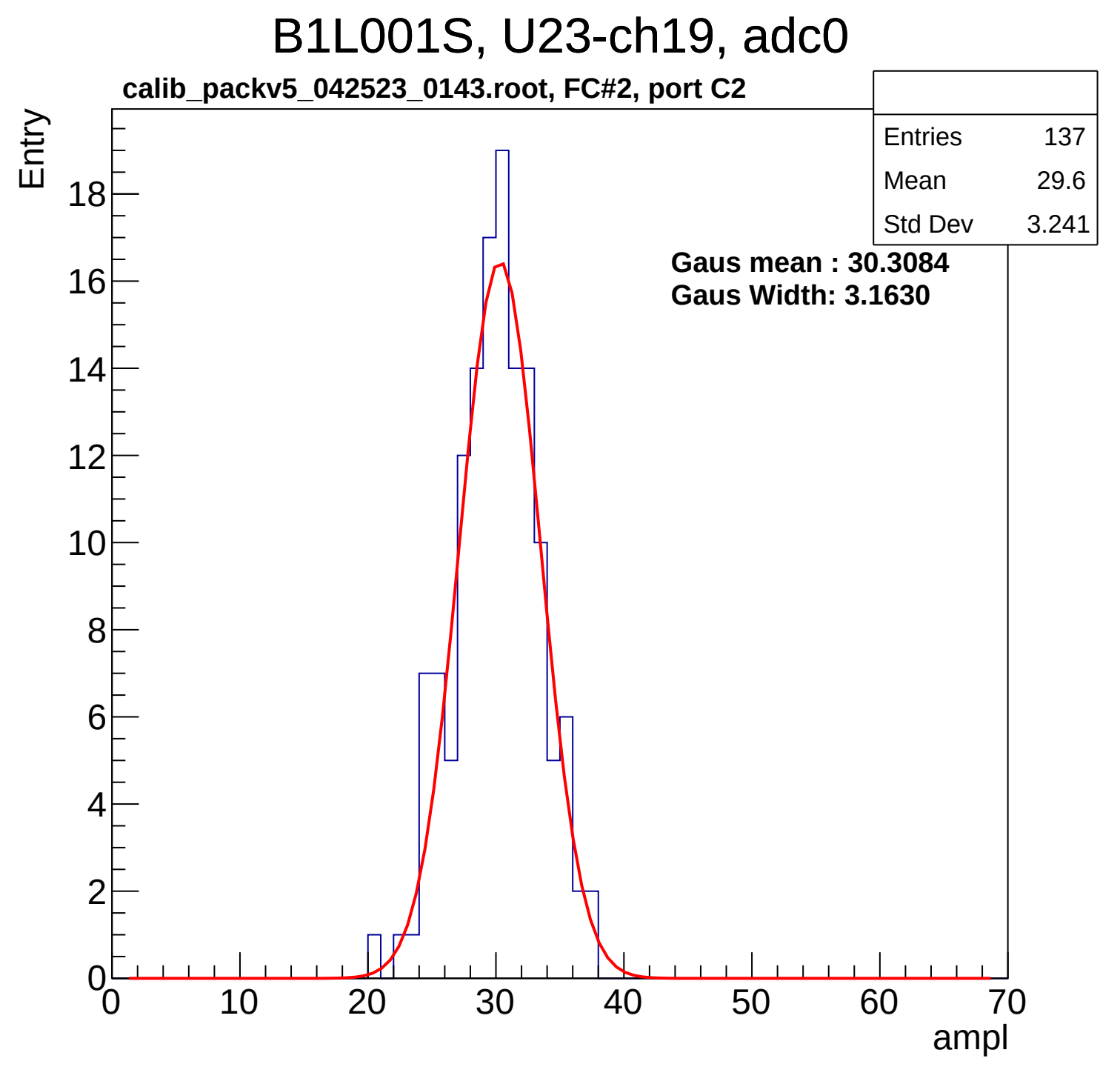
B1L001S, U23-ch19, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 29.6
Std Dev 3.241
Gaus mean : 30.3084
Gaus Width: 3.1630
Entry
ampl
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4
6
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10
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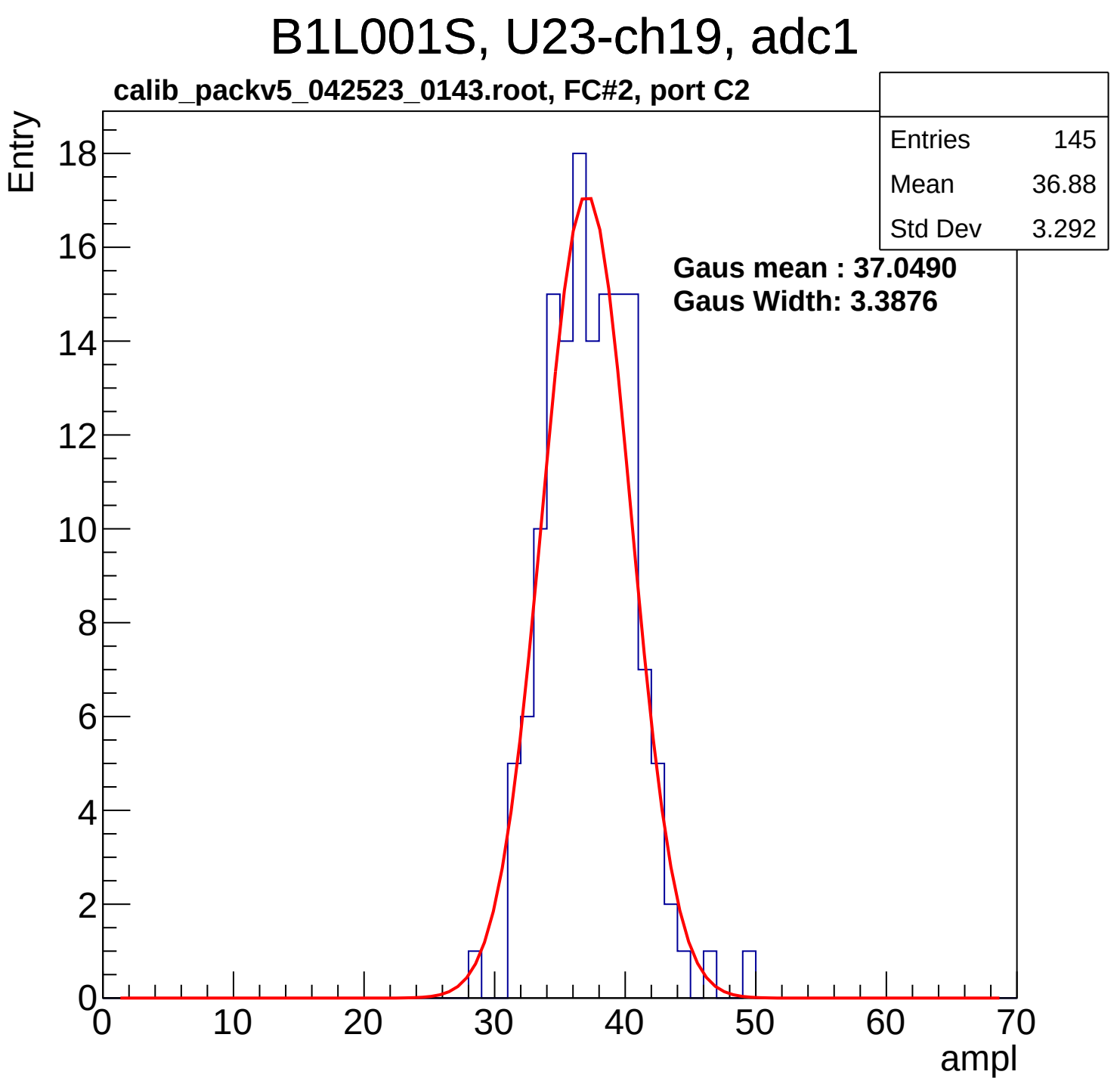
B1L001S, U23-ch19, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 36.88
Std Dev 3.292
Gaus mean : 37.0490
Gaus Width: 3.3876
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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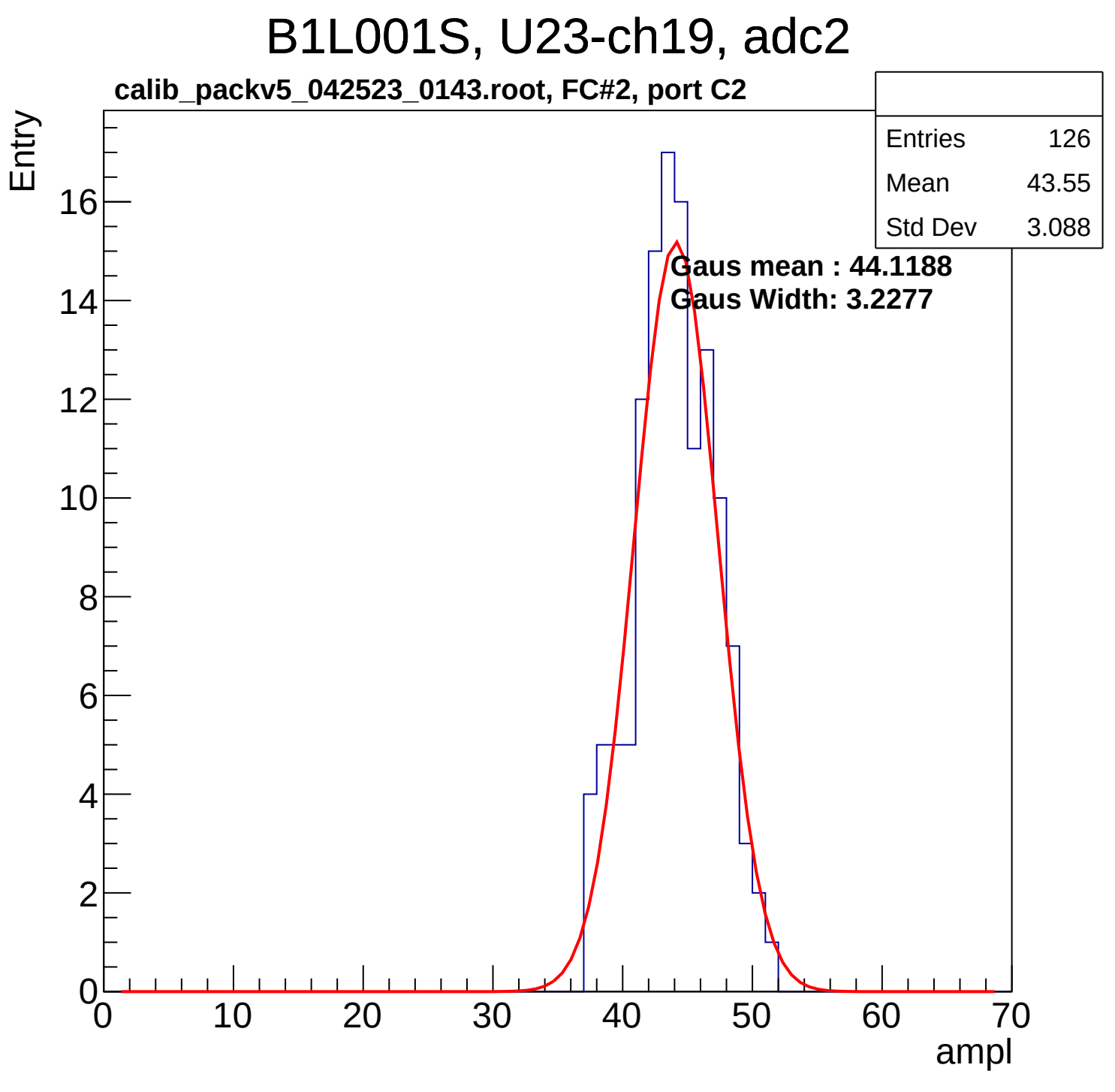
40
50
60
70


B1L001S, U23-ch19, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 43.55
Std Dev 3.088
Gaus mean : 44.1188
Gaus Width: 3.2277
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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20
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40
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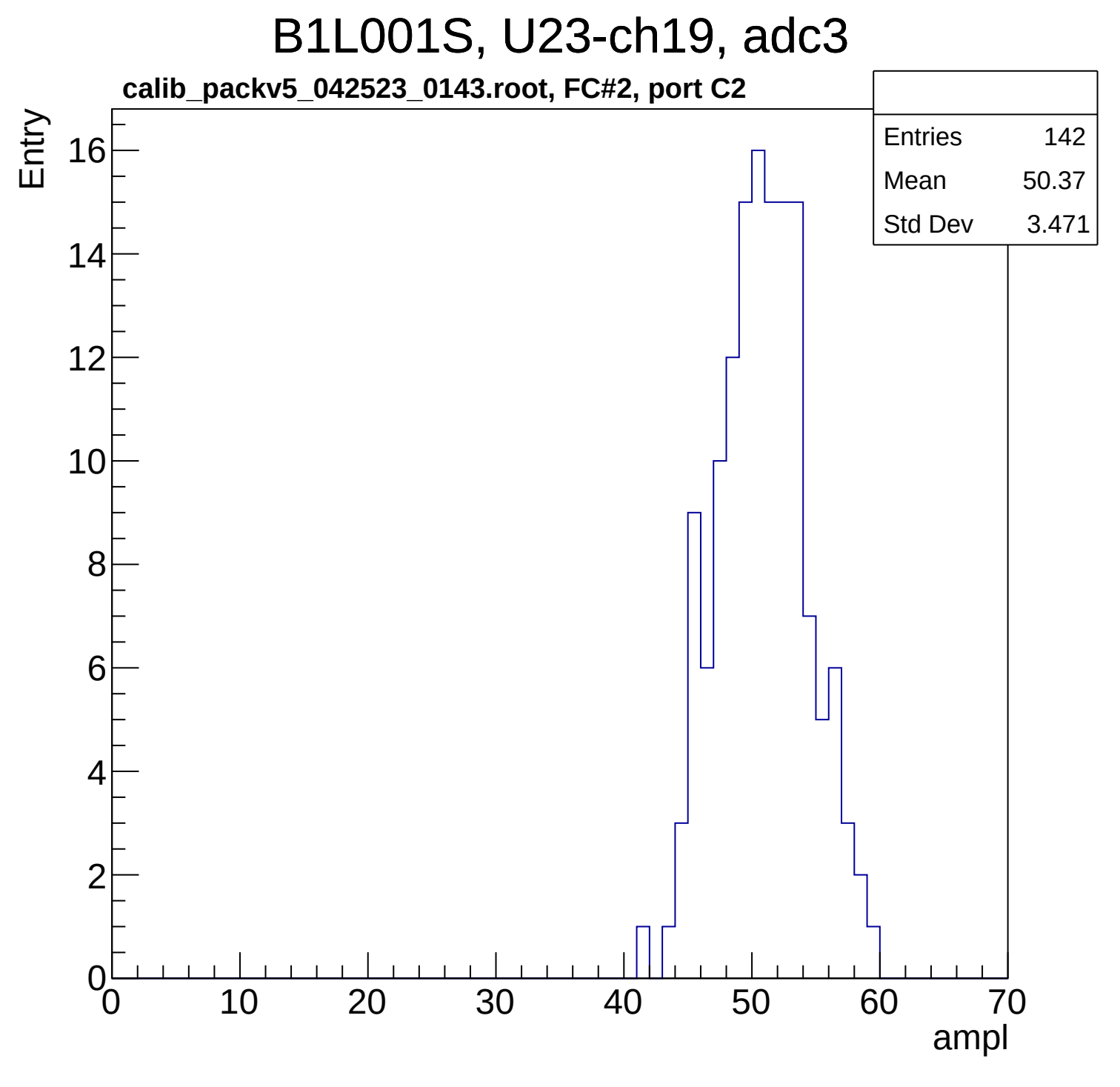
B1L001S, U23-ch19, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 50.37
Std Dev 3.471
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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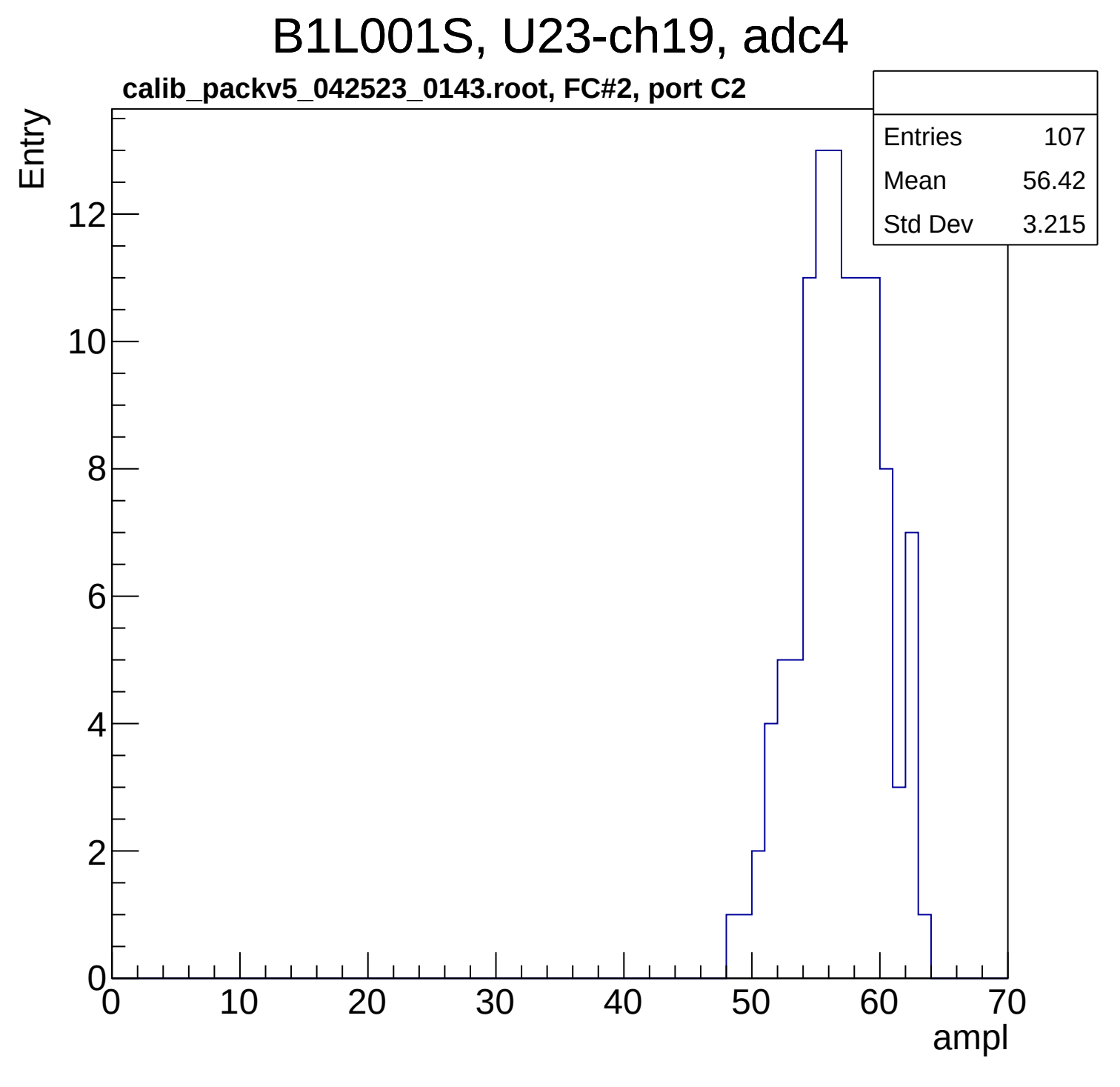
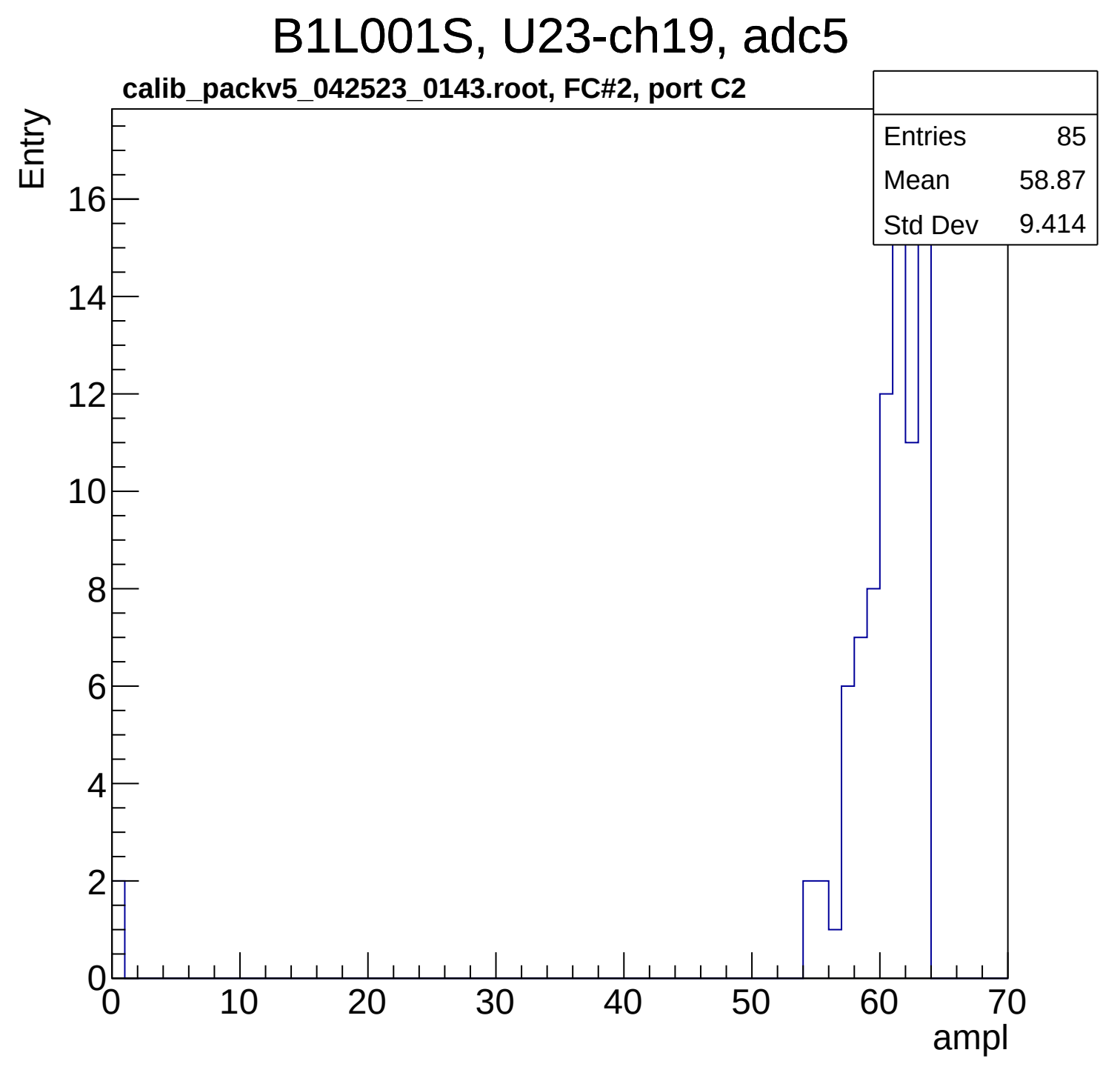
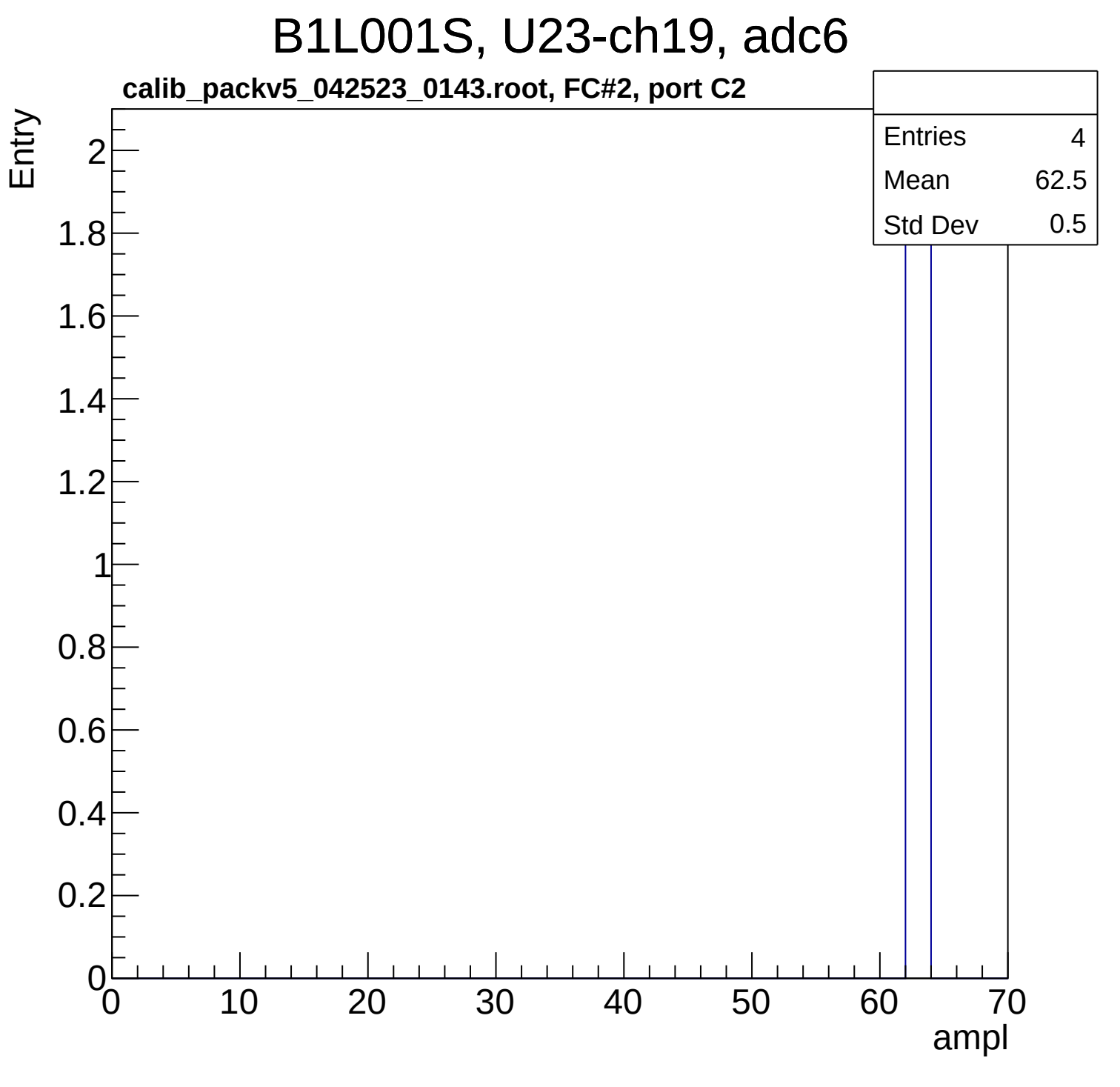
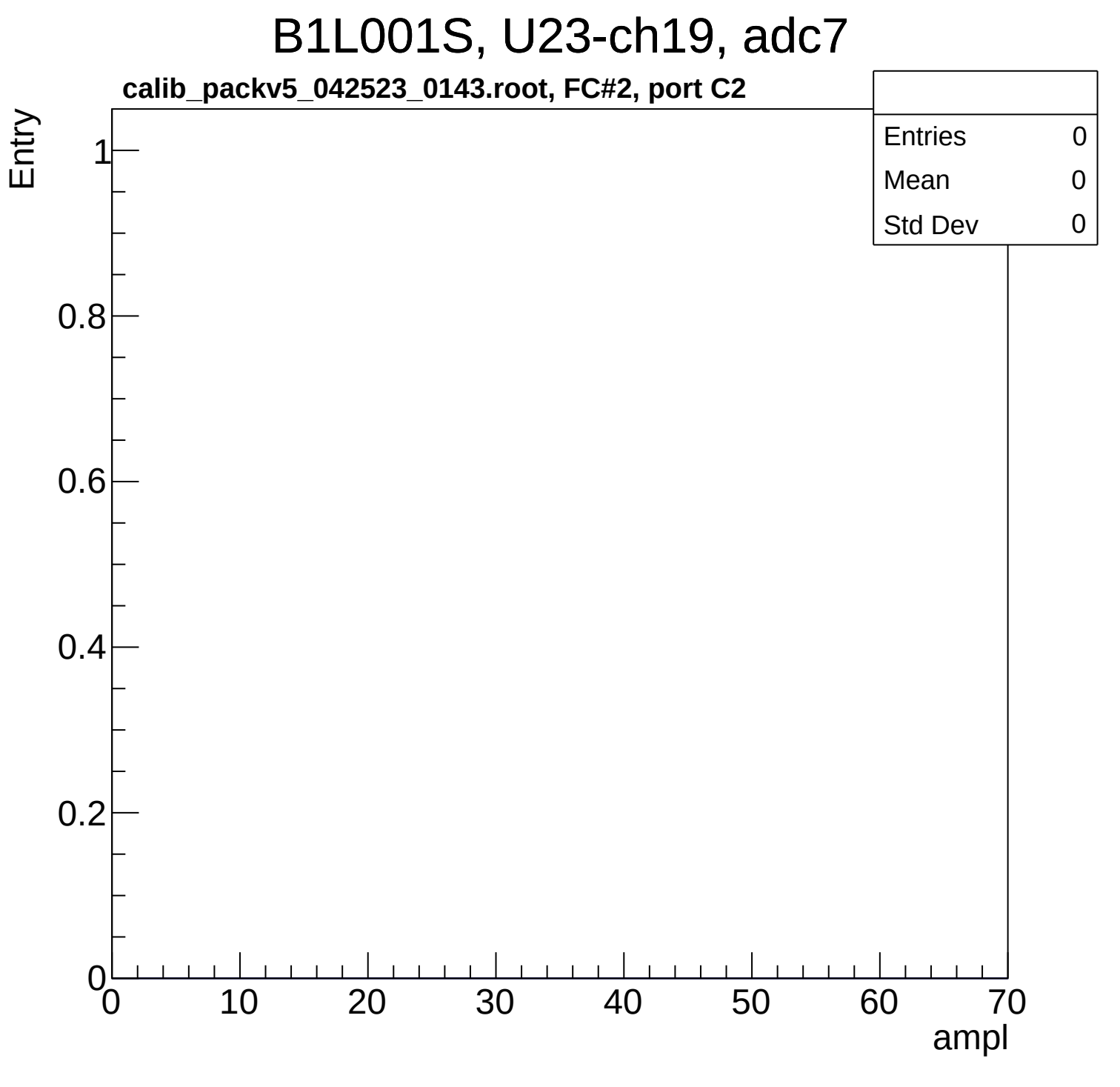
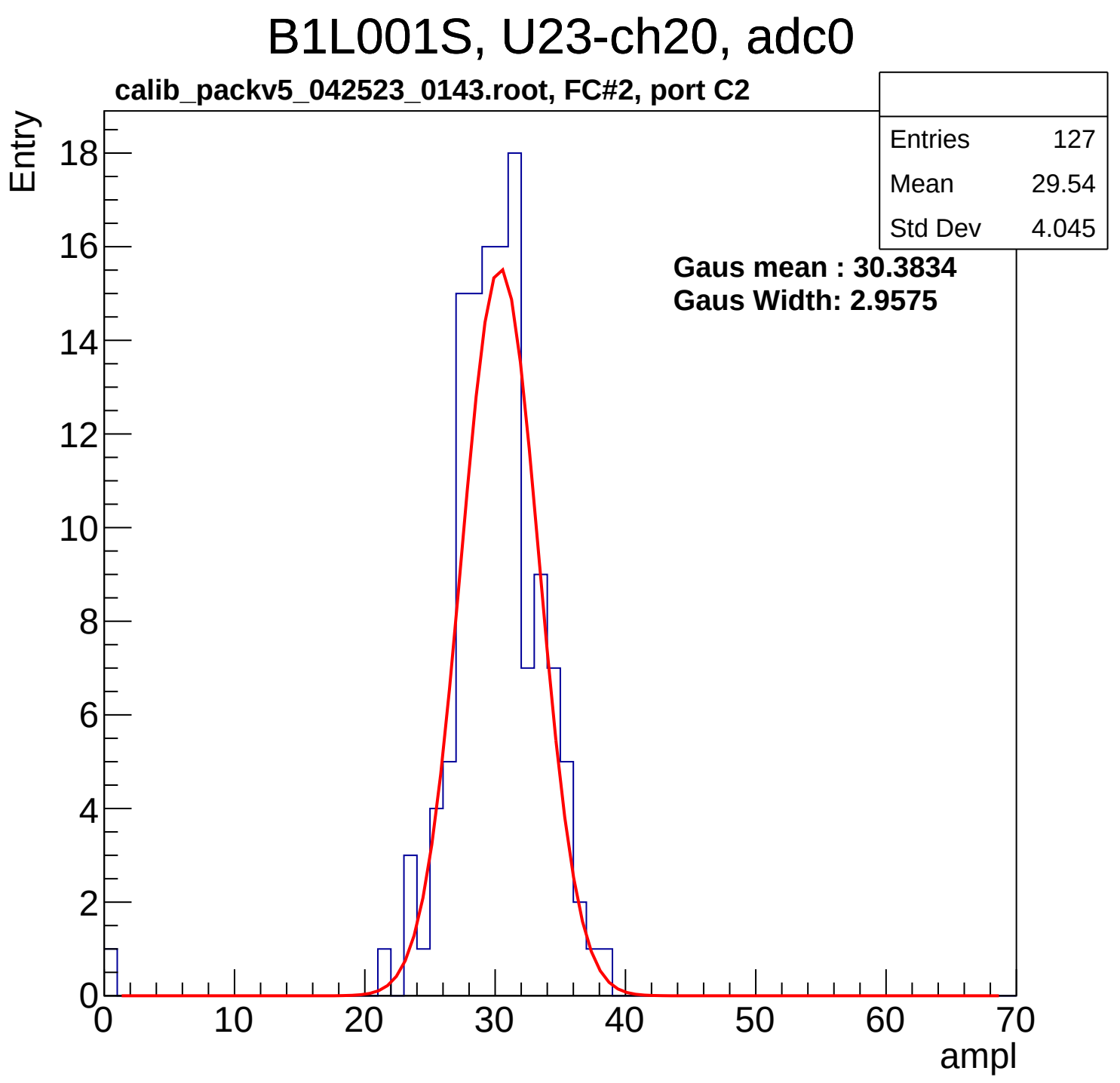
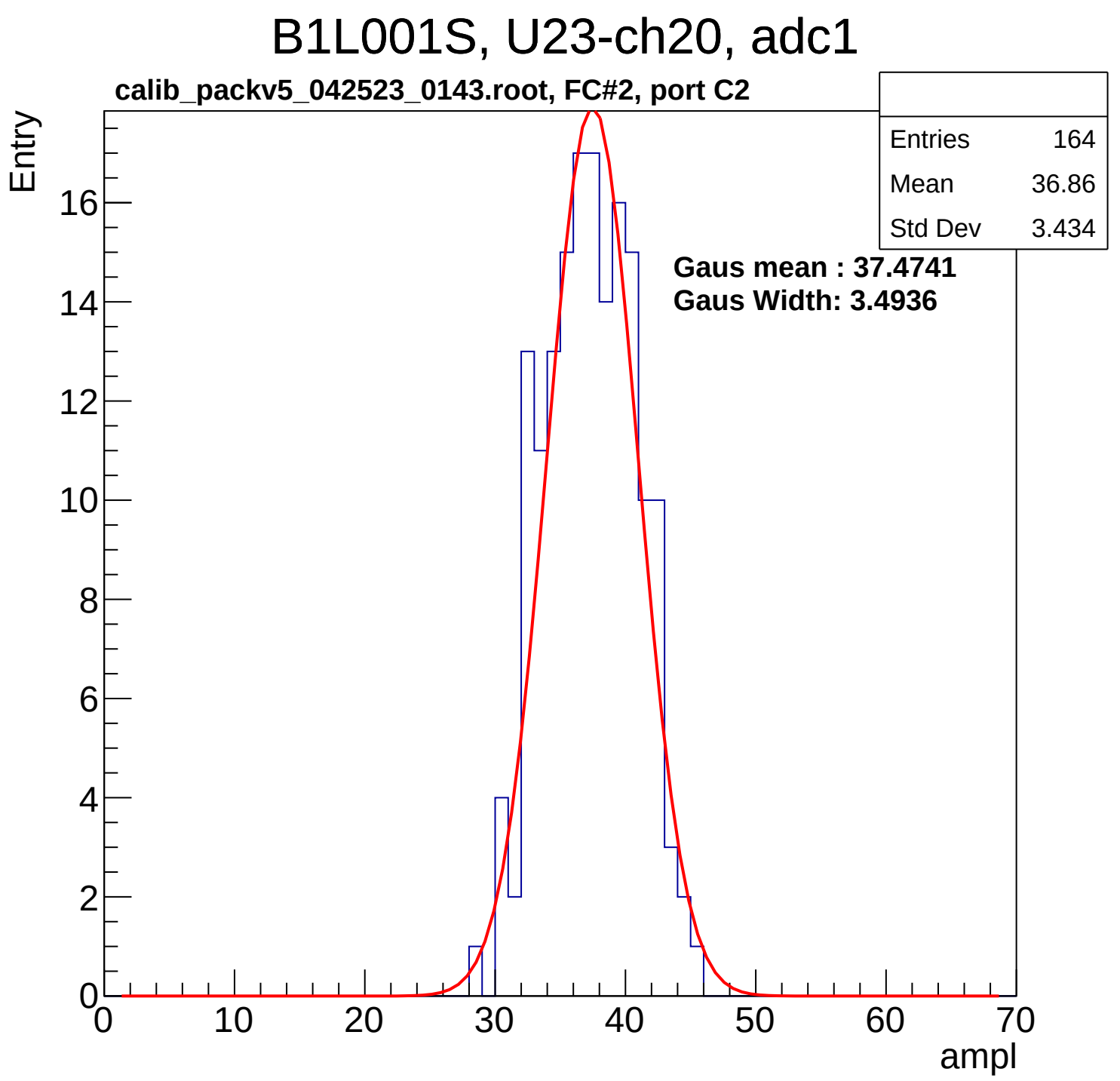

B1L001S, U23-ch19, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 56.42
Std Dev 3.215
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl

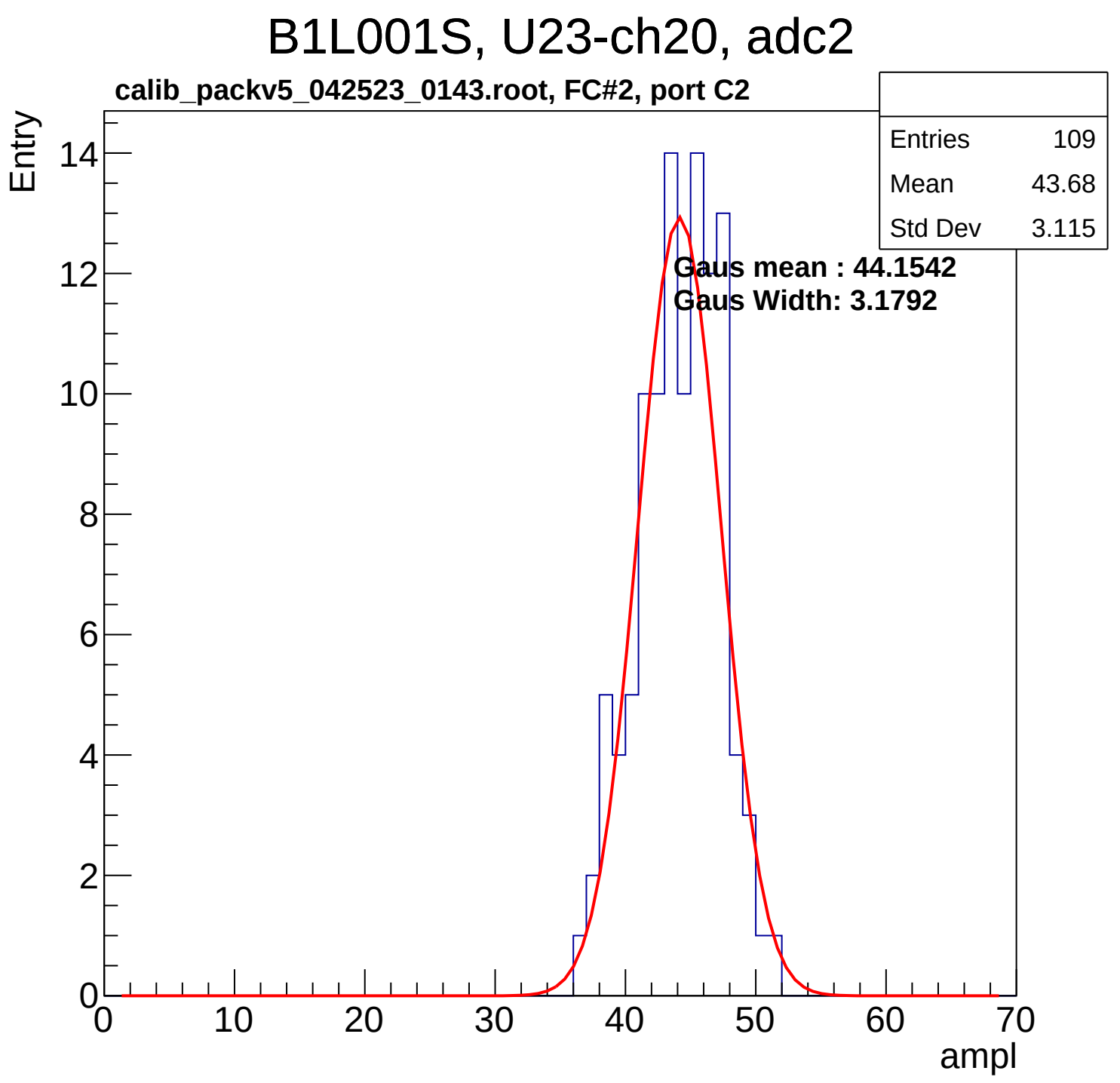

B1L001S, U23-ch19, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 58.87
Std Dev 9.414
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

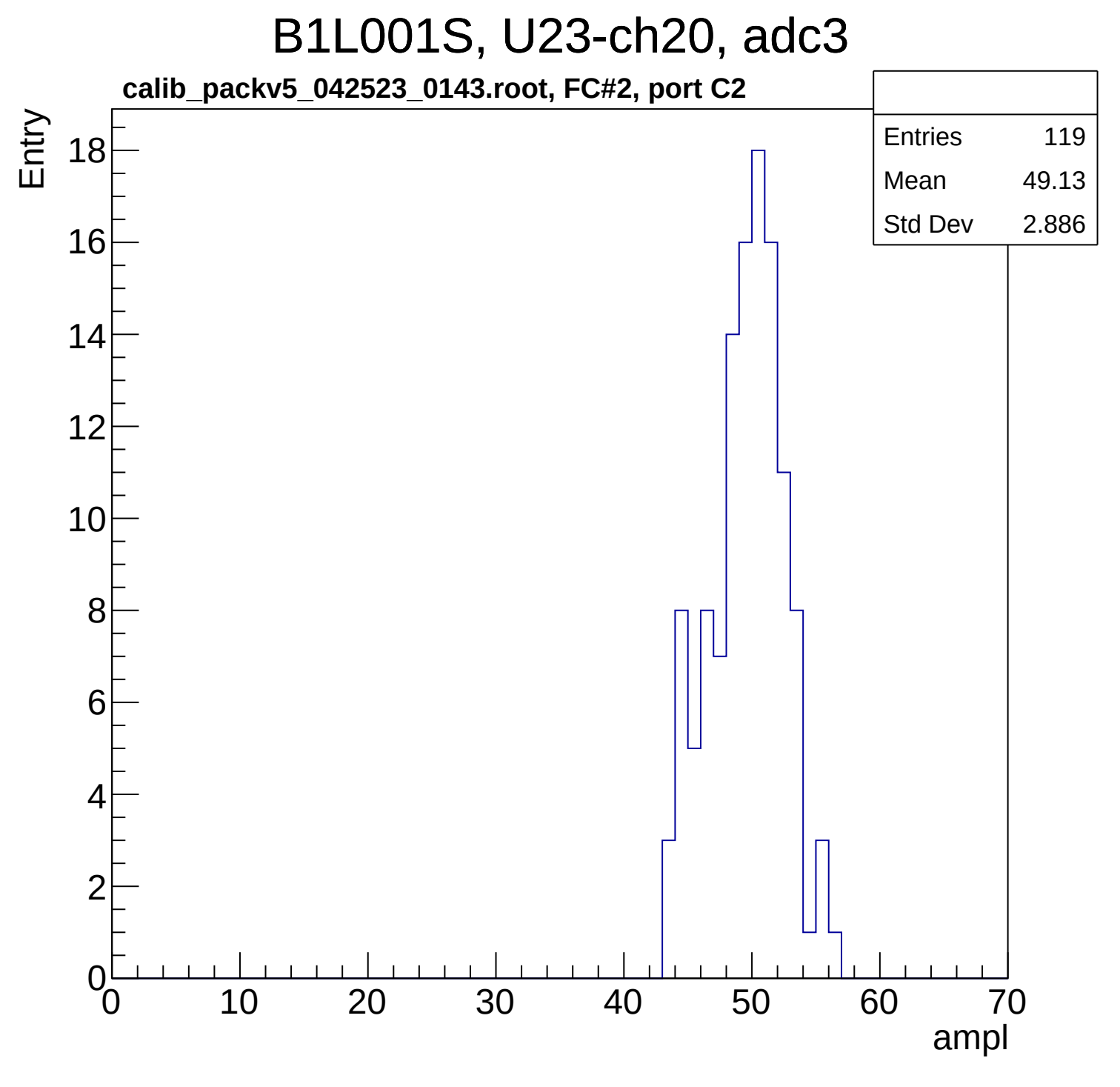

B1L001S, U23-ch19, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

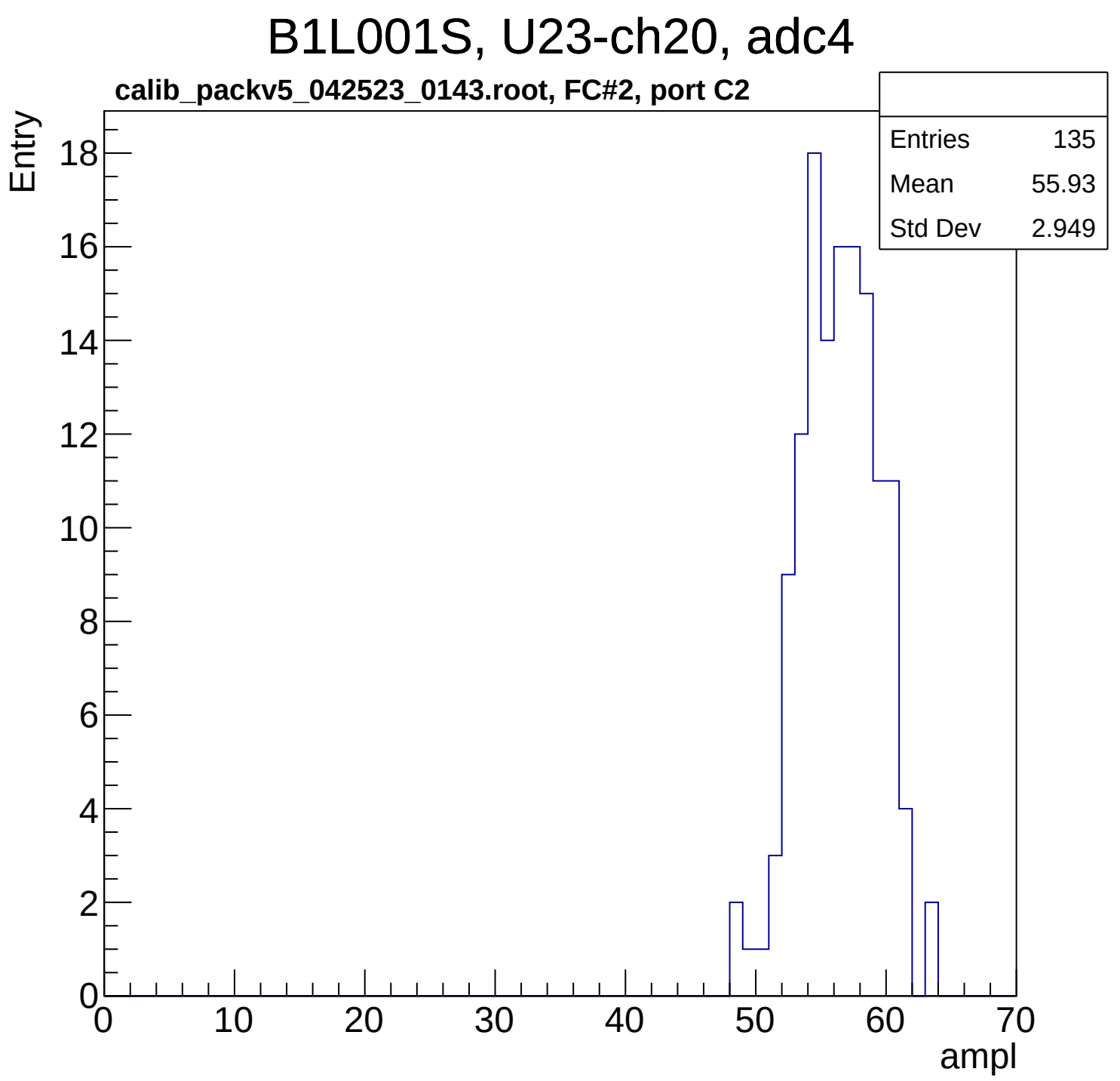

B1L001S, U23-ch19, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

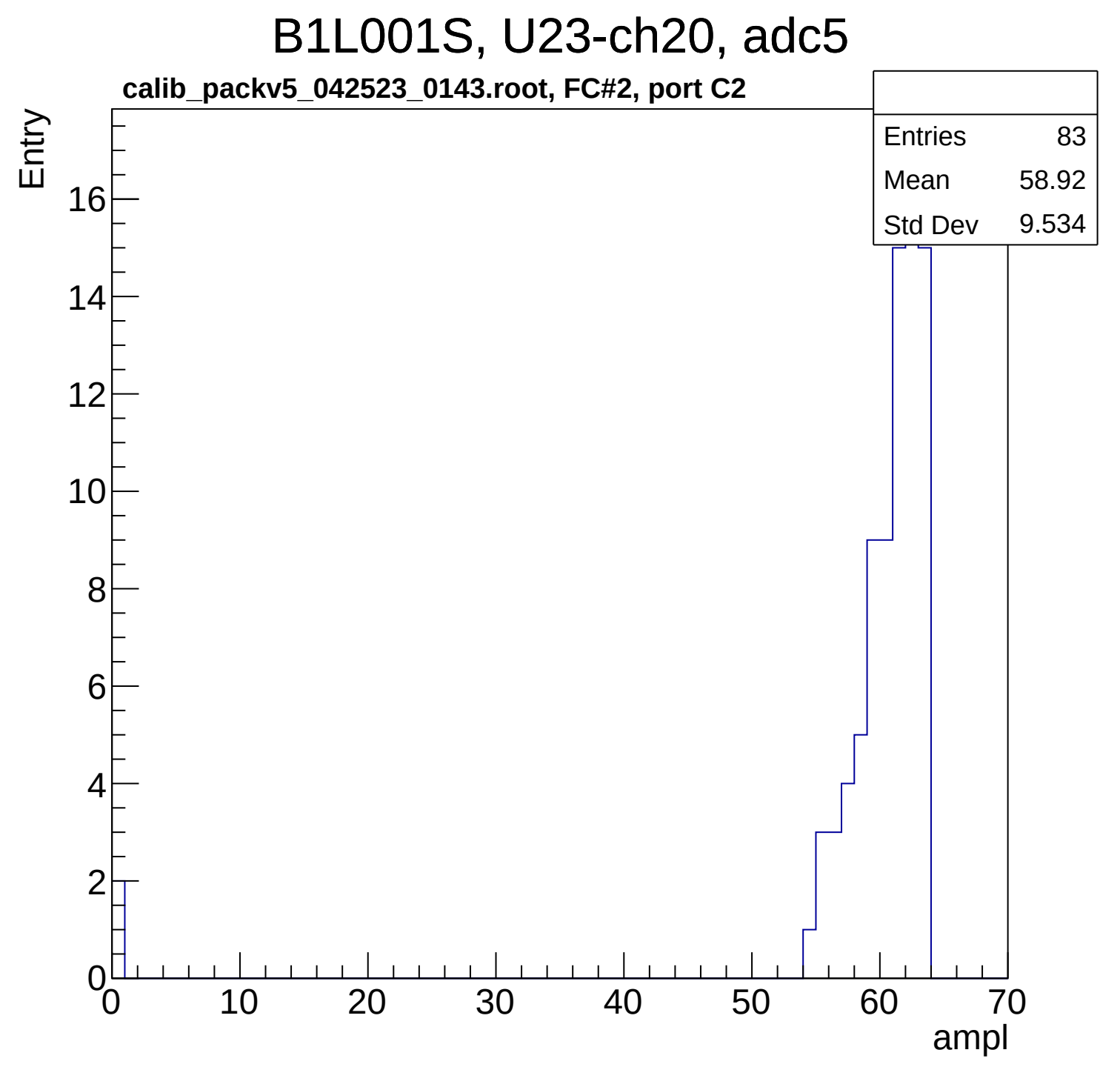

B1L001S, U23-ch20, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 29.54
Std Dev 4.045
Gaus mean : 30.3834
Gaus Width: 2.9575
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 164
Mean 36.86
Std Dev 3.434
Gaus mean : 37.4741
Gaus Width: 3.4936
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 43.68
Std Dev 3.115
Gaus mean : 44.1542
Gaus Width: 3.1792
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 49.13
Std Dev 2.886
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch20, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 55.93
Std Dev 2.949
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 58.92
Std Dev 9.534
Entry
ampl
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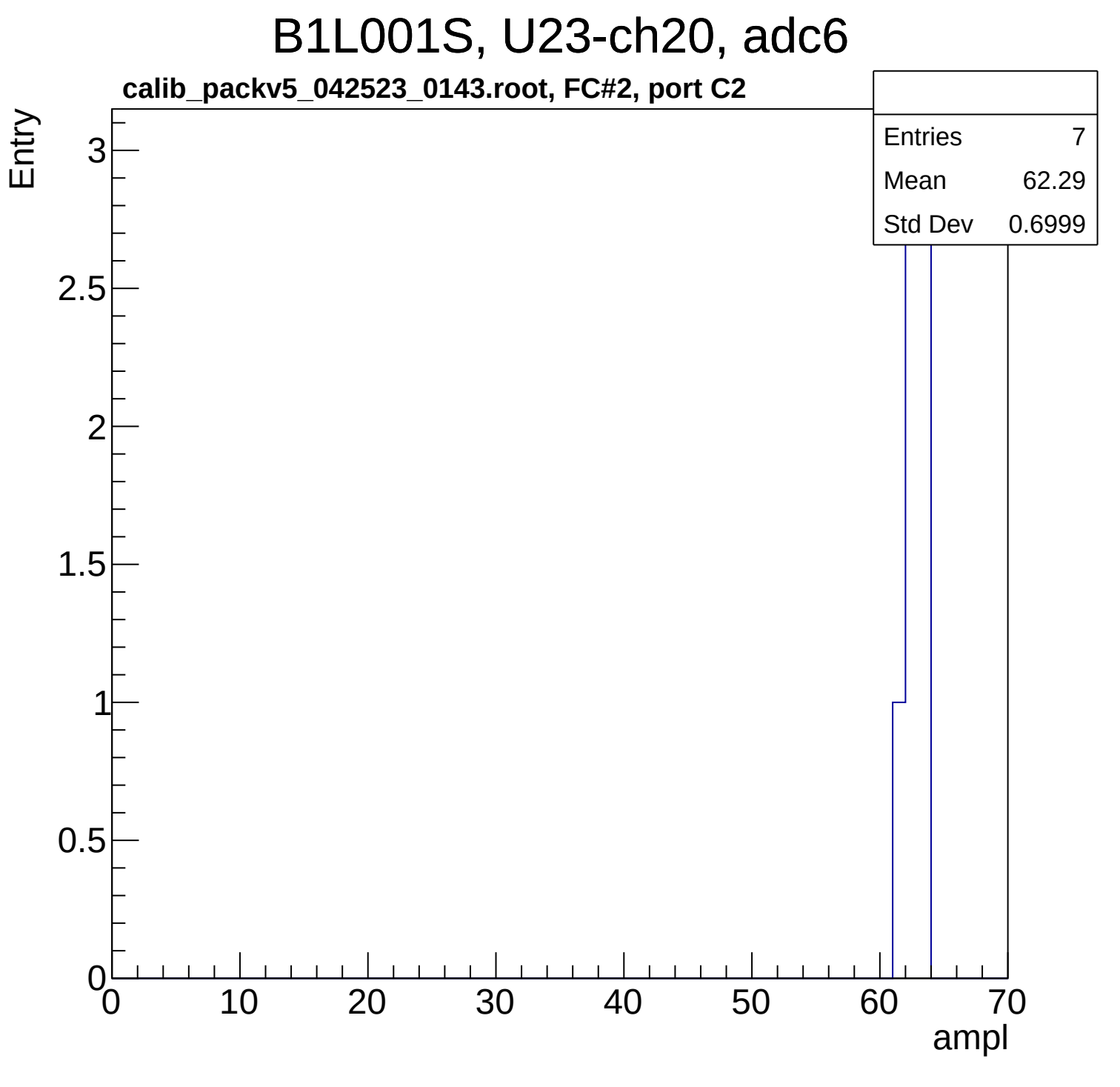
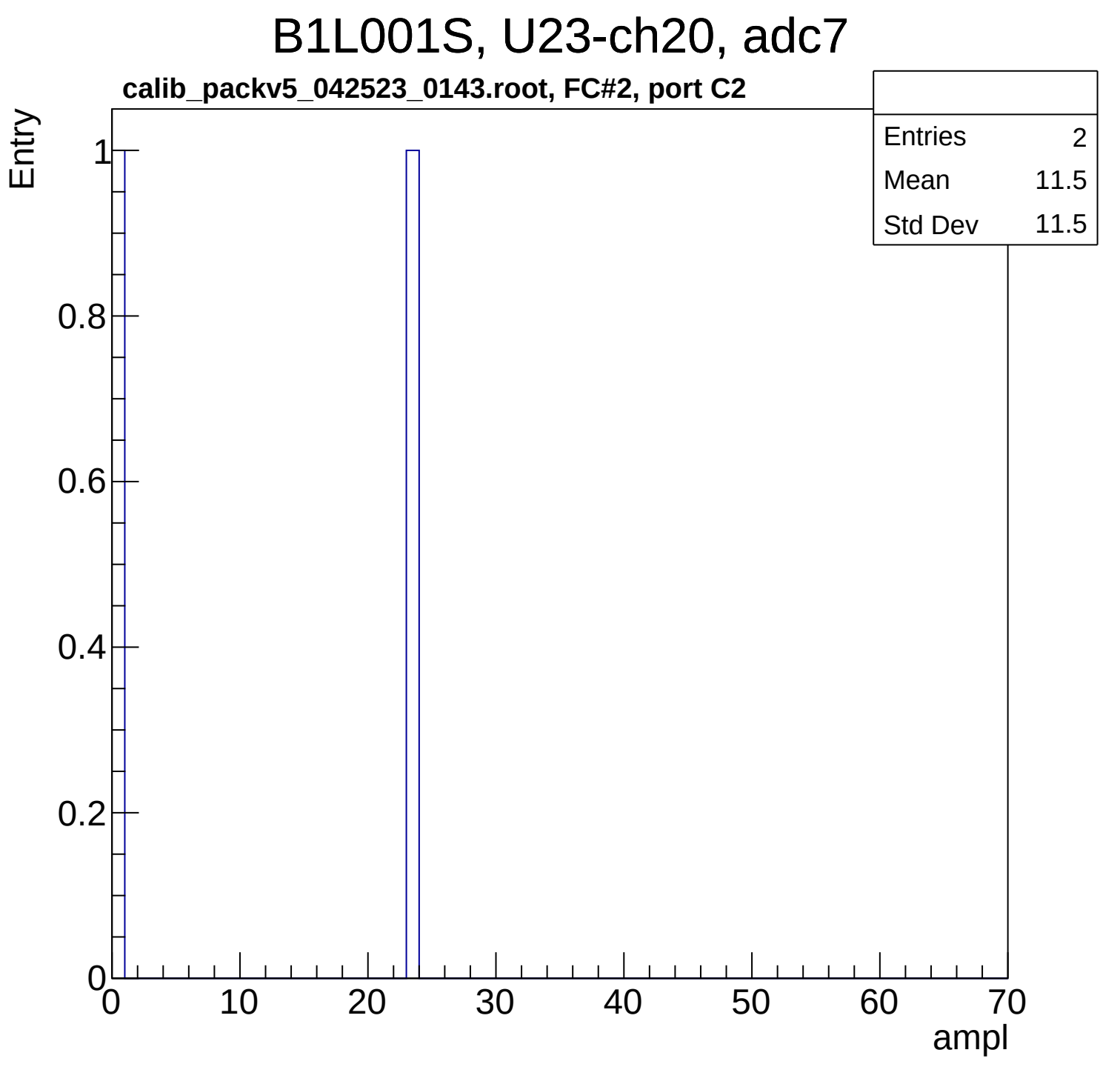
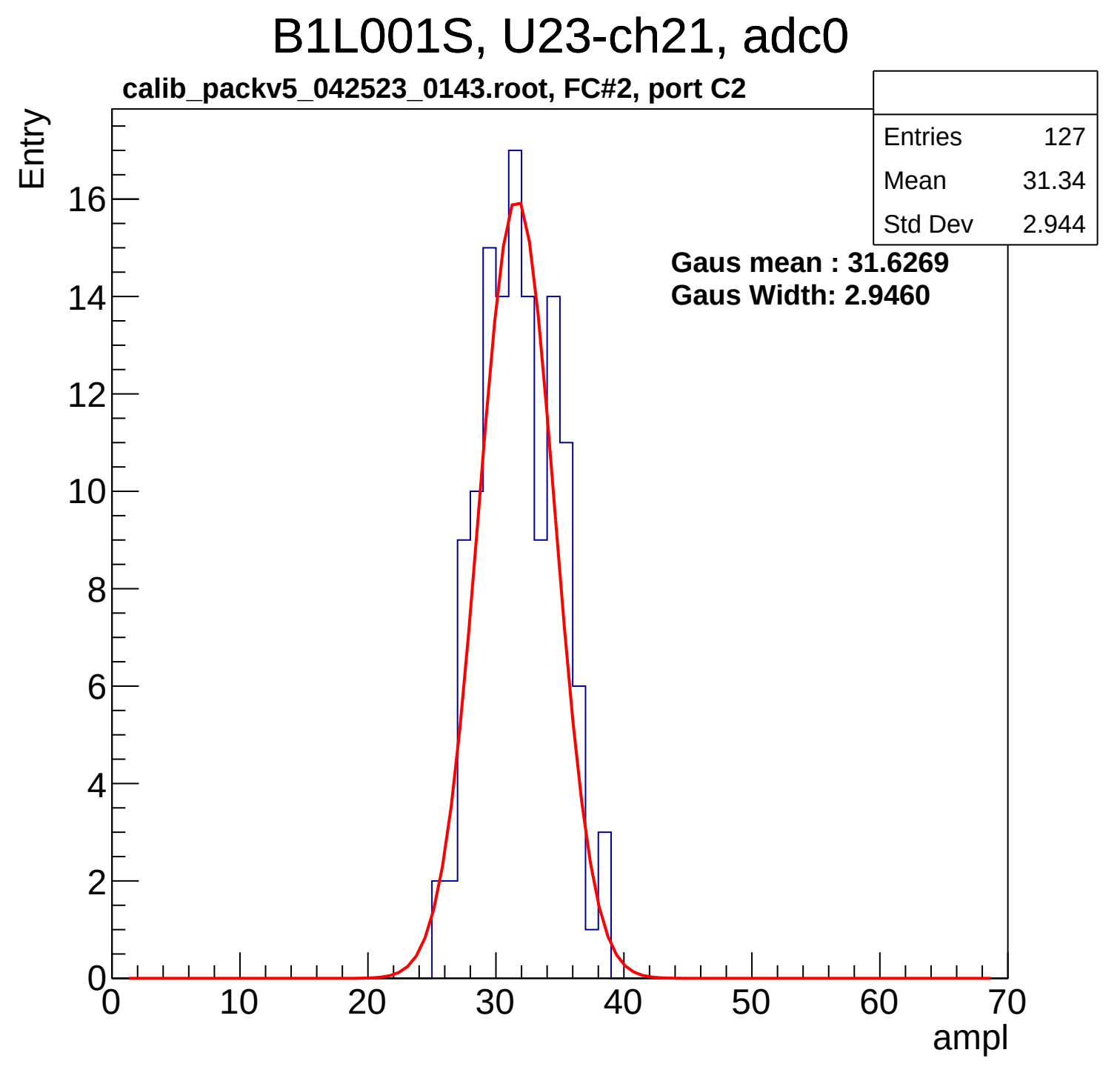
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 62.29
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch20, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch21, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 31.34
Std Dev 2.944
Gaus mean : 31.6269
Gaus Width: 2.9460
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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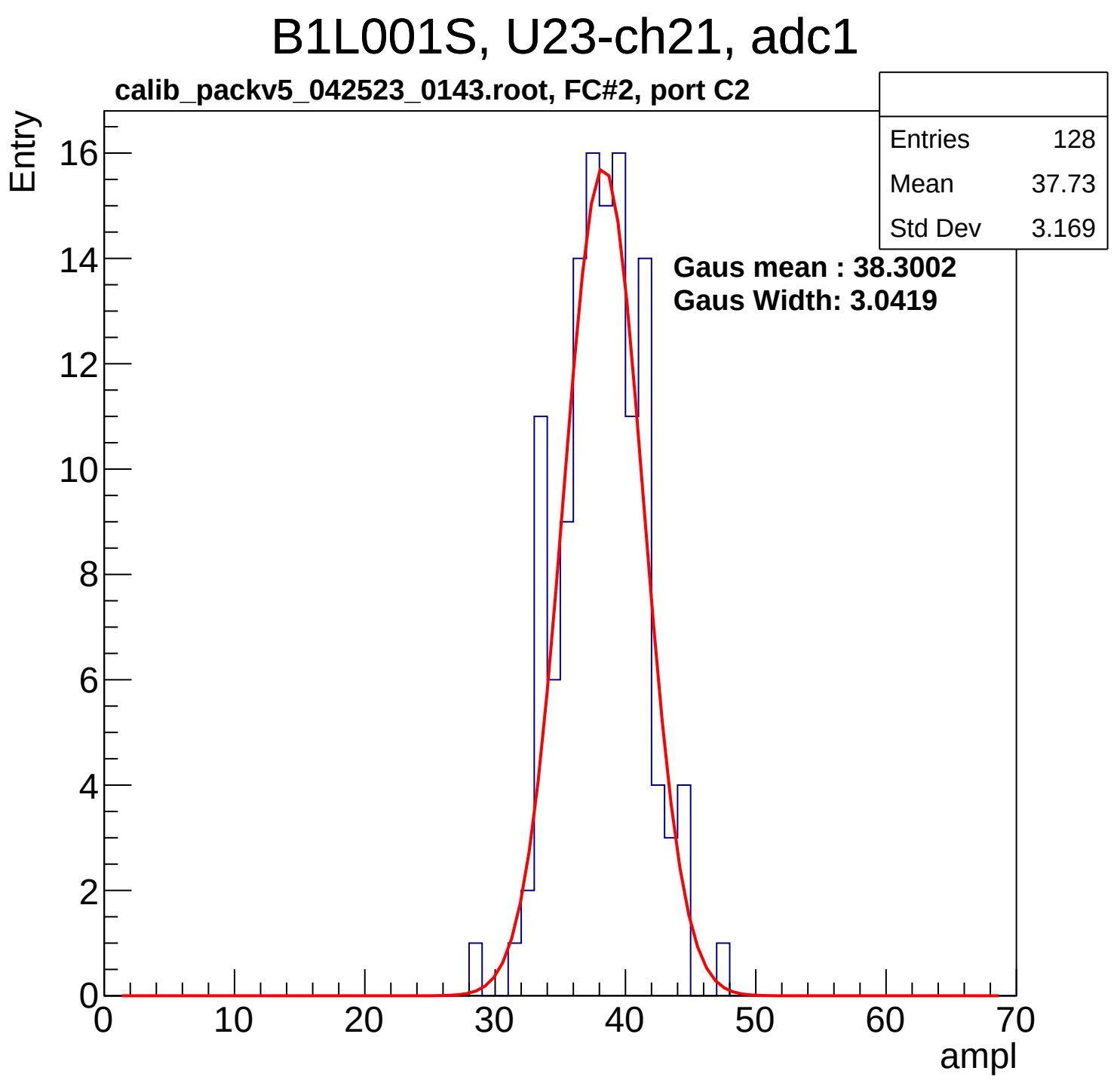
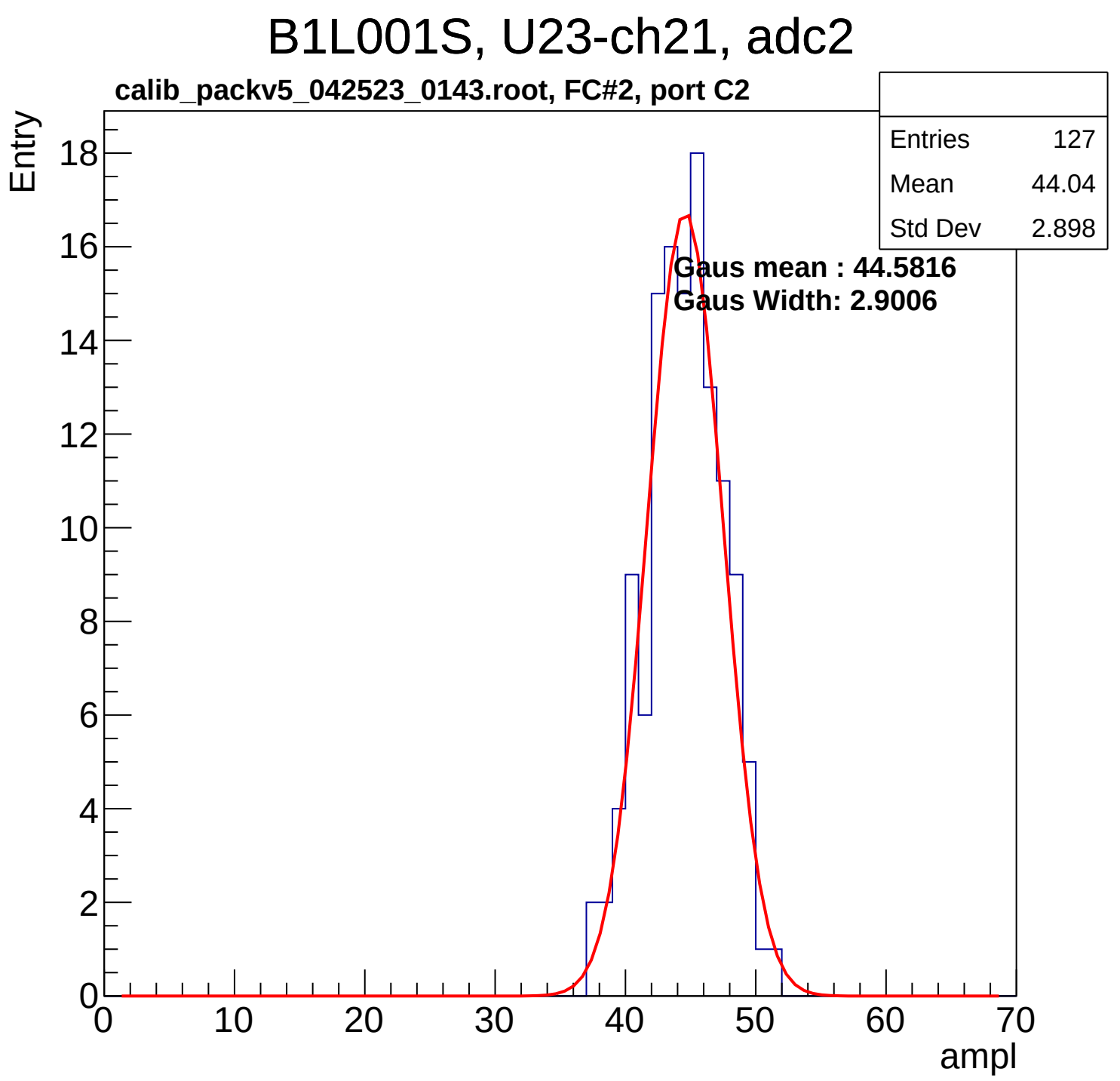
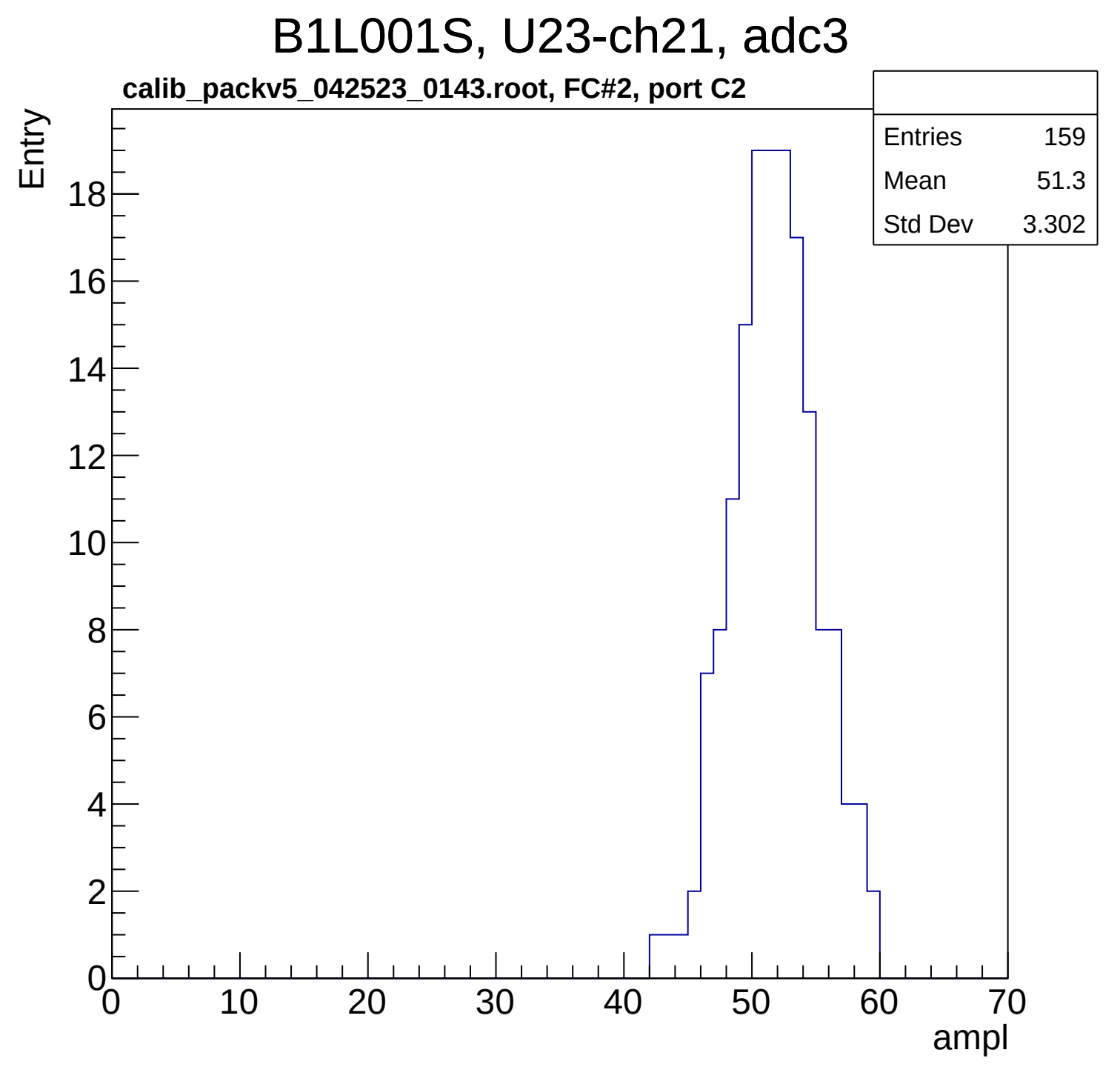
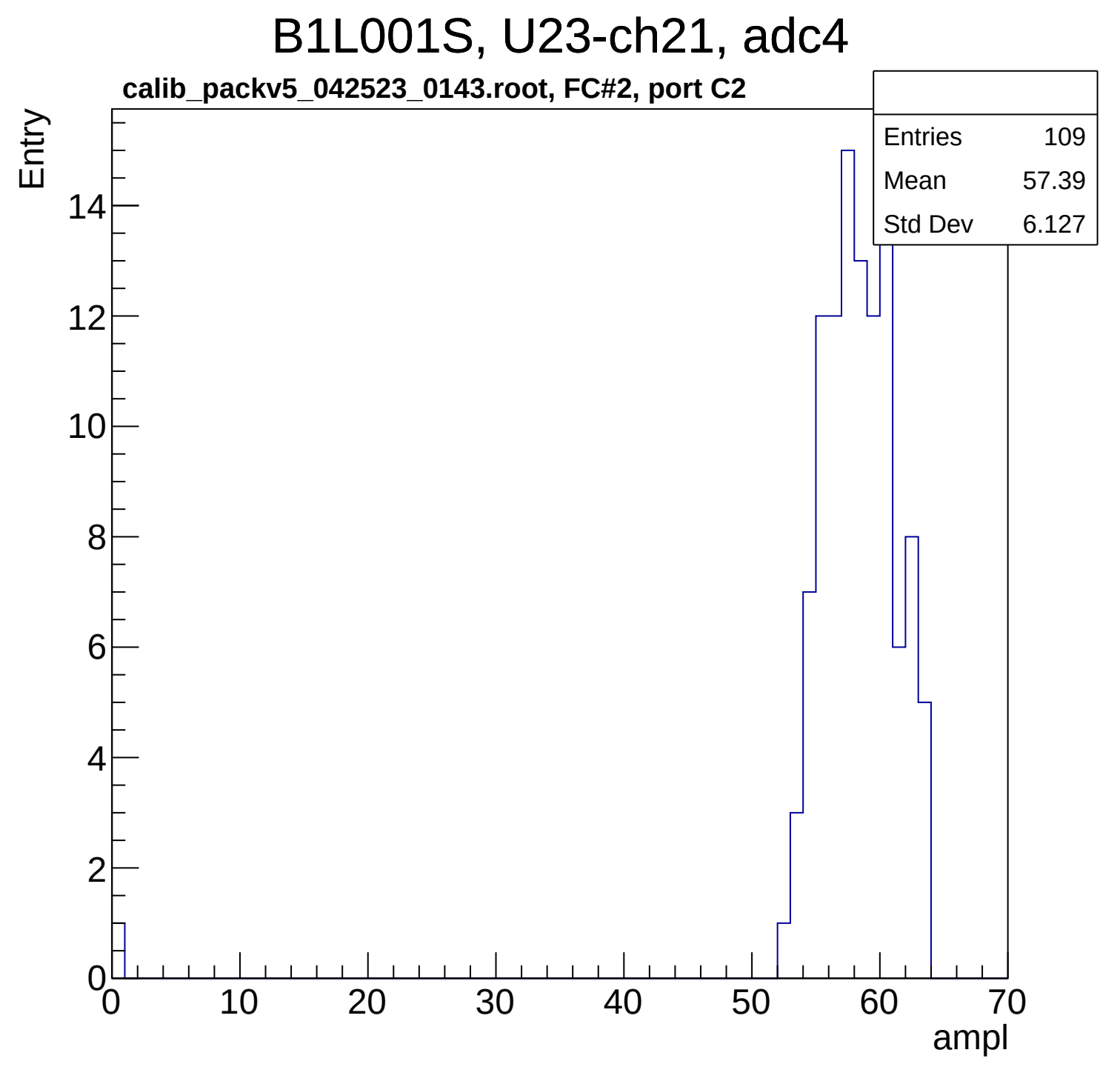
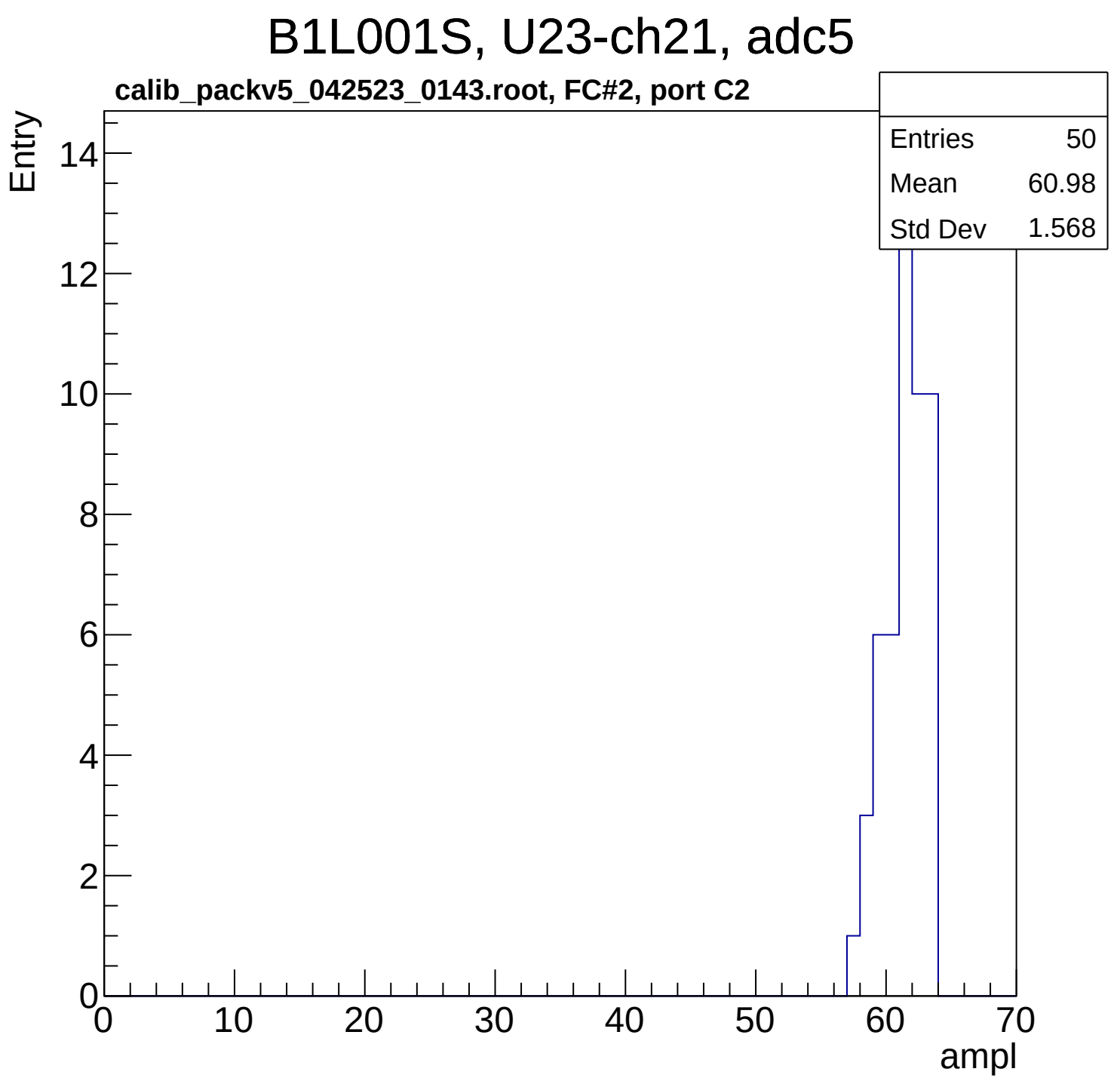

B1L001S, U23-ch21, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 37.73
Std Dev 3.169
Gaus mean : 38.3002
Gaus Width: 3.0419
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch21, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 44.04
Std Dev 2.898
Gaus mean : 44.5816
Gaus Width: 2.9006
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch21, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 51.3
Std Dev 3.302
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch21, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 57.39
Std Dev 6.127
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch21, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 50
Mean 60.98
Std Dev 1.568
Entry
ampl
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2
4
6
8
10
12
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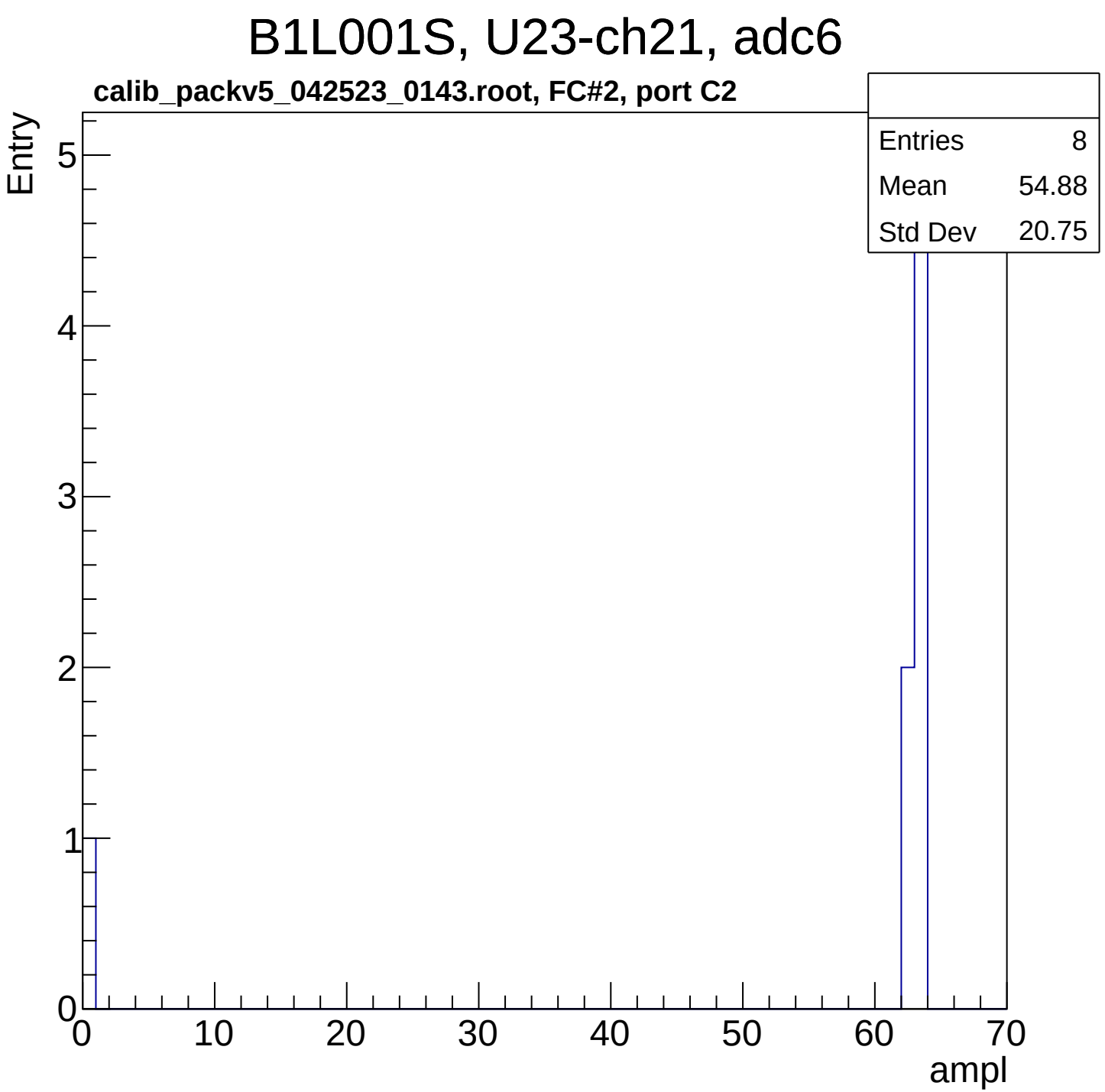
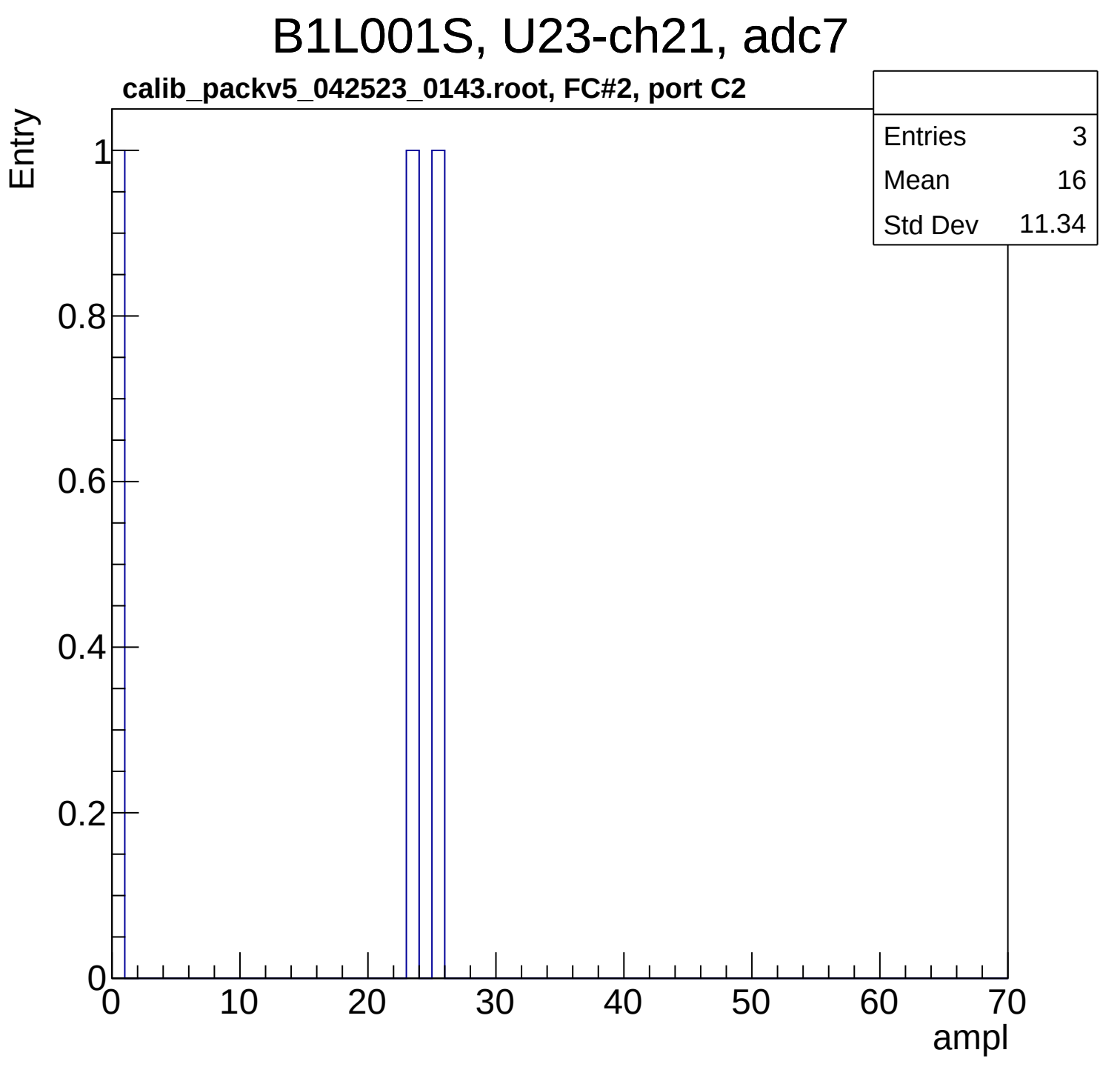
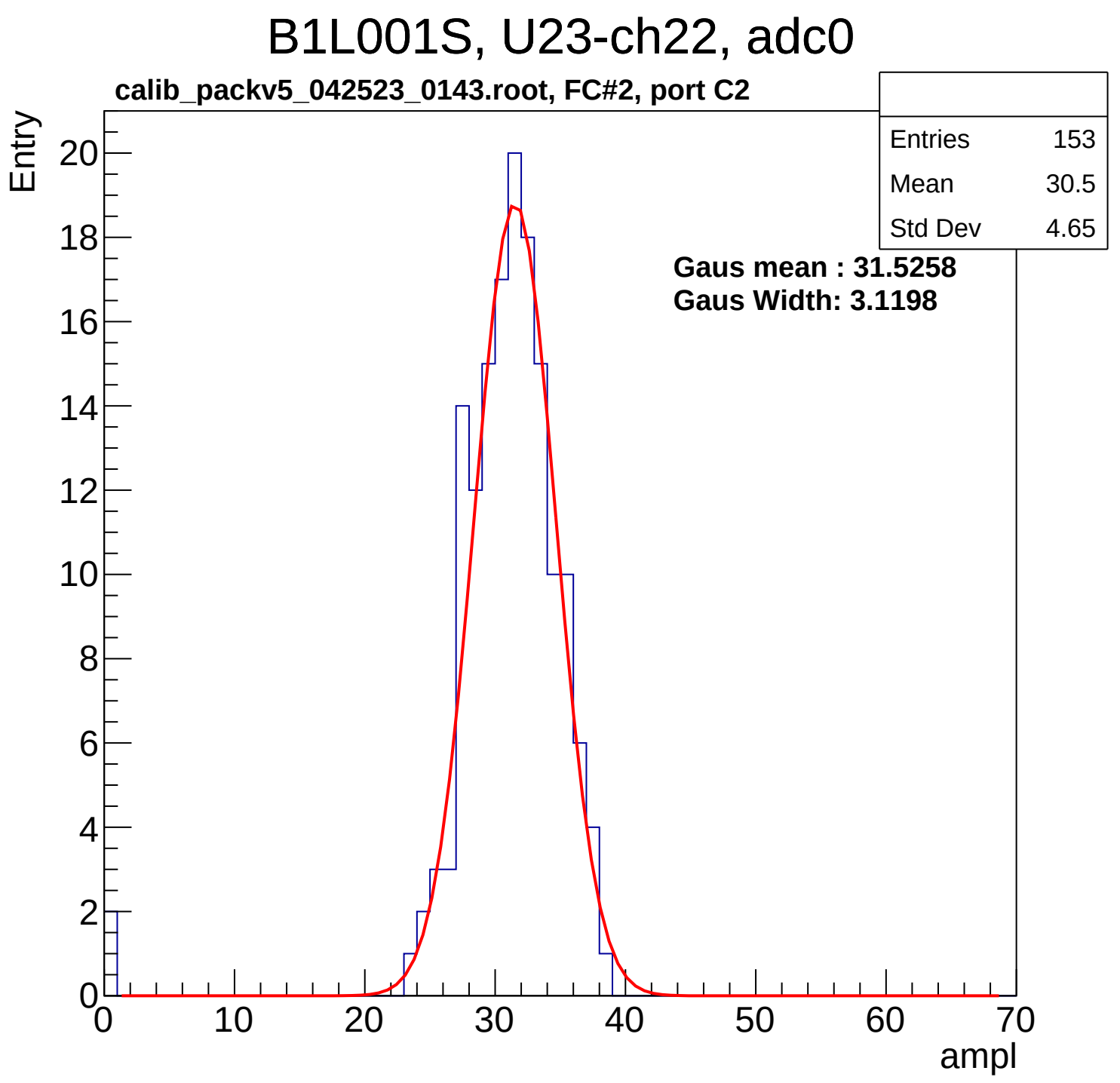
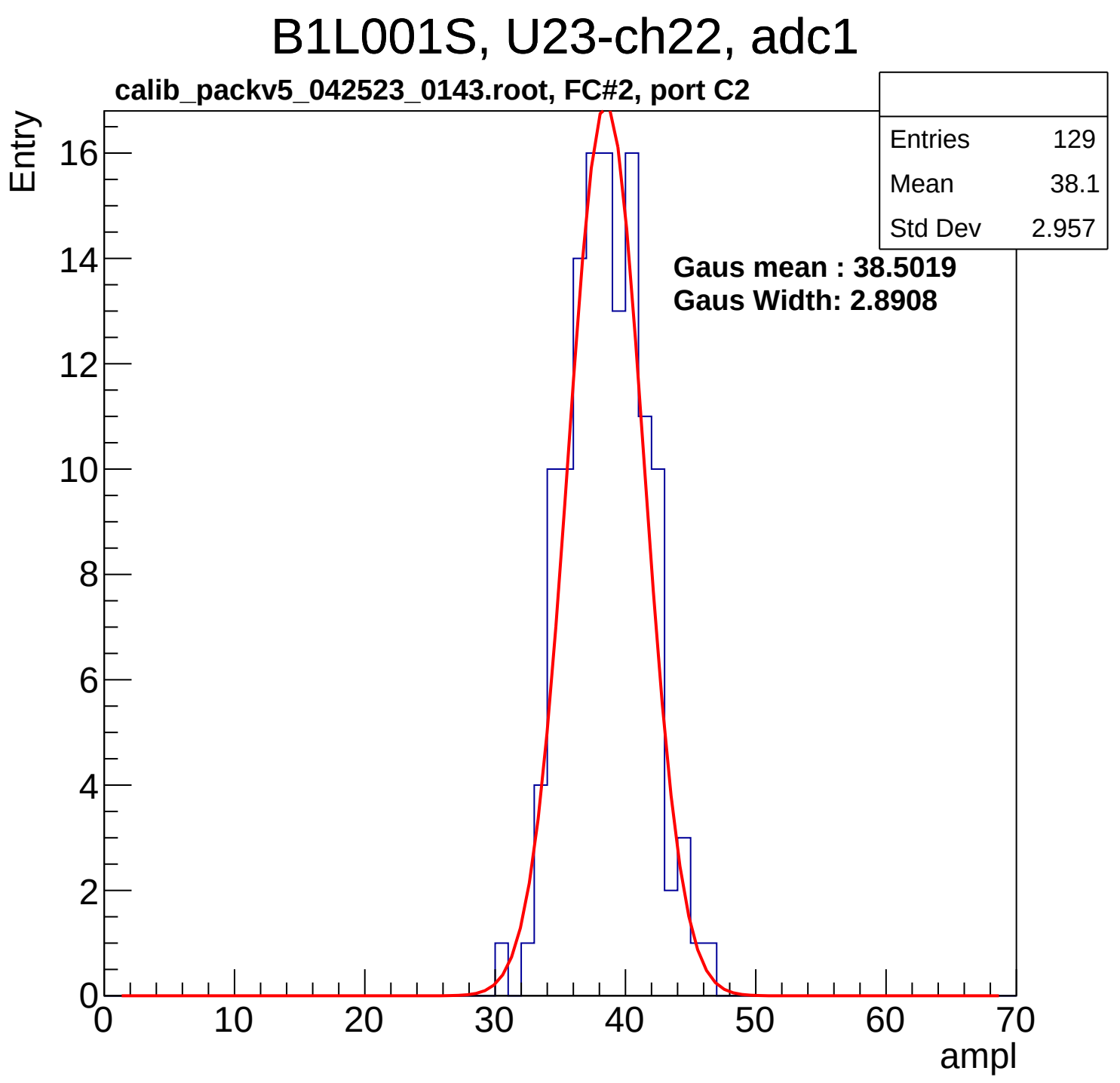
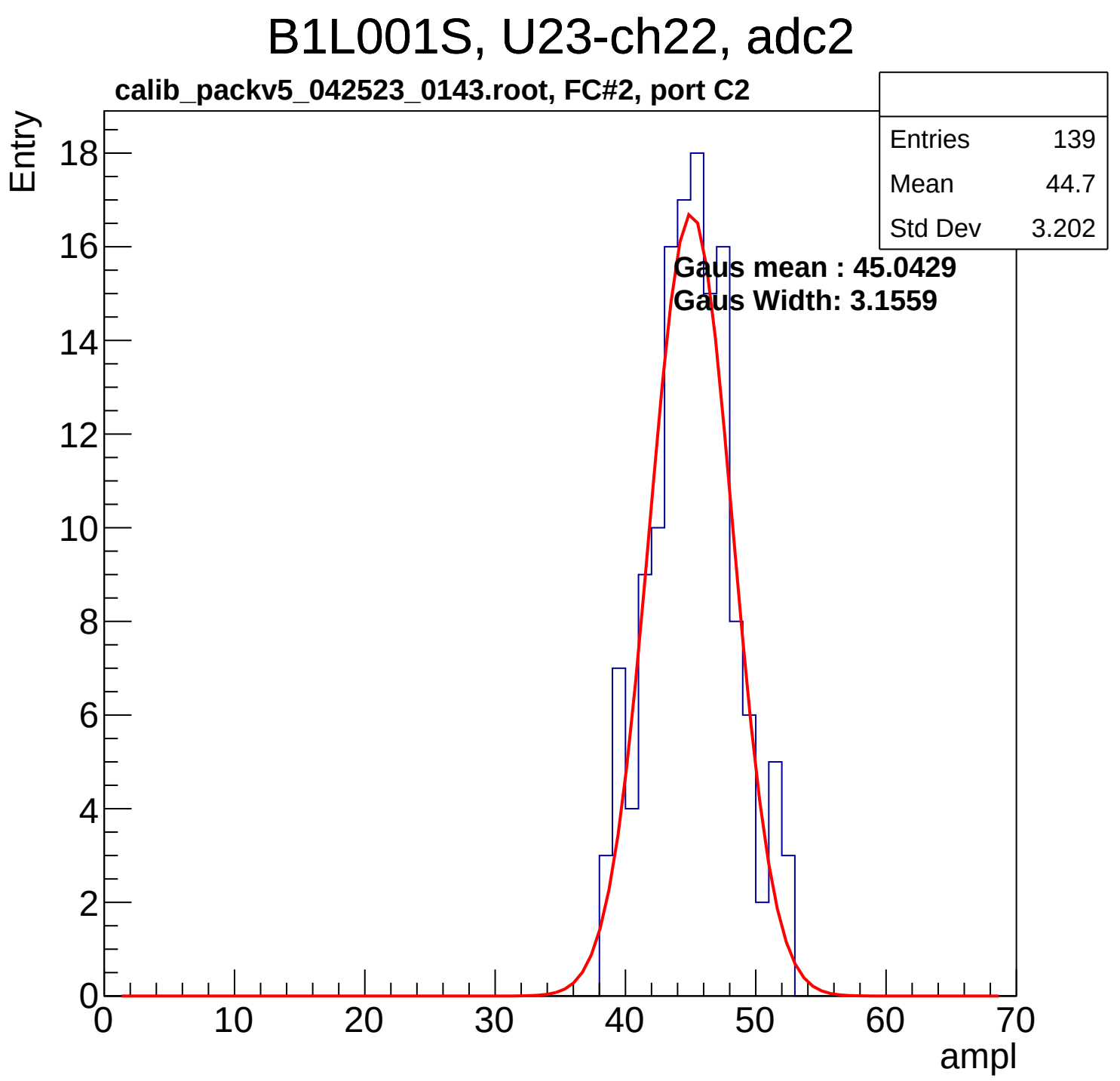
0
10
20
30
40
50
60
70

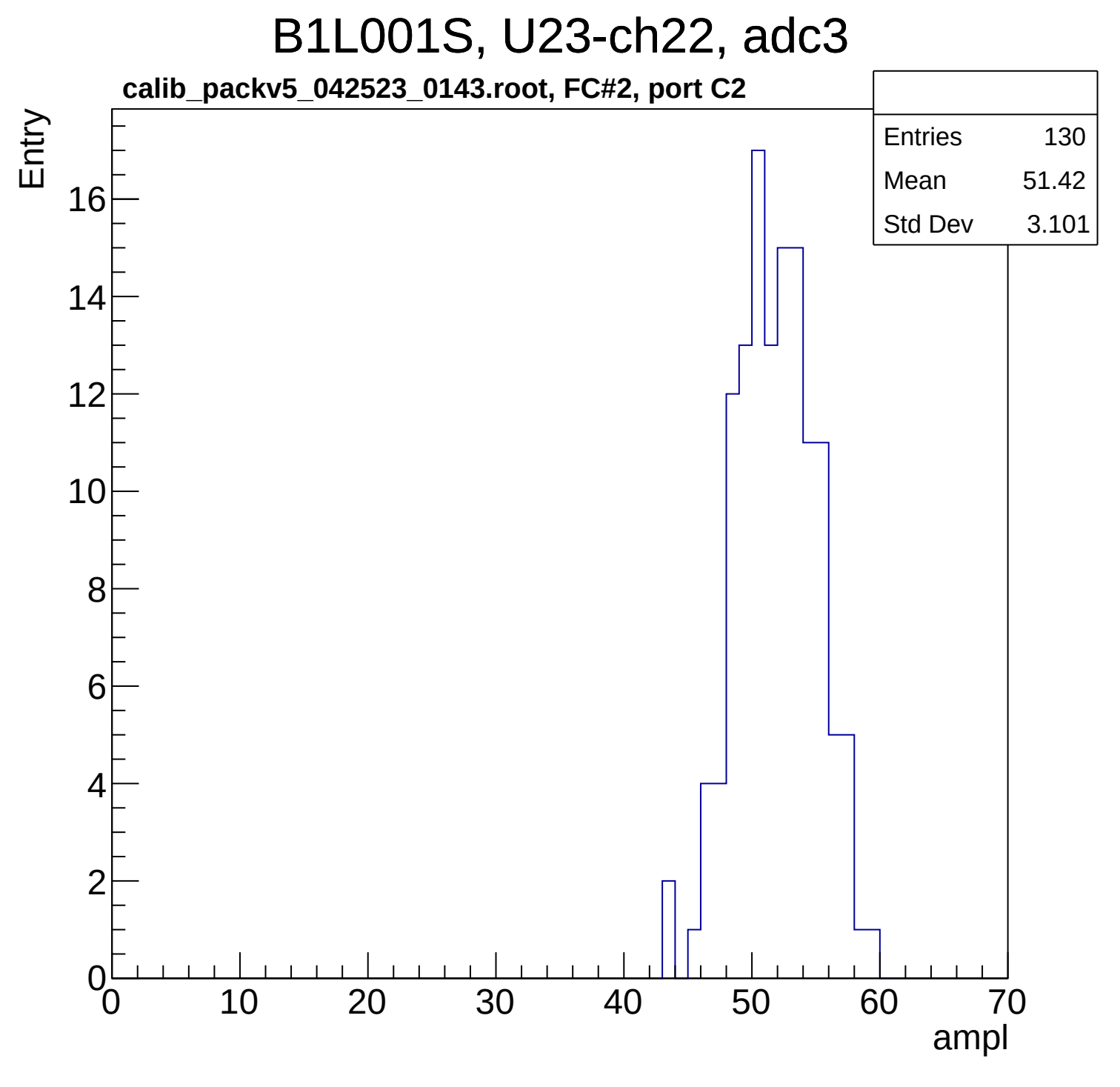

B1L001S, U23-ch21, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.88
Std Dev 20.75
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch21, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 16
Std Dev 11.34
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch22, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 30.5
Std Dev 4.65
Gaus mean : 31.5258
Gaus Width: 3.1198
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 38.1
Std Dev 2.957
Gaus mean : 38.5019
Gaus Width: 2.8908
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 44.7
Std Dev 3.202
Gaus mean : 45.0429
Gaus Width: 3.1559
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 51.42
Std Dev 3.101
Entry
ampl
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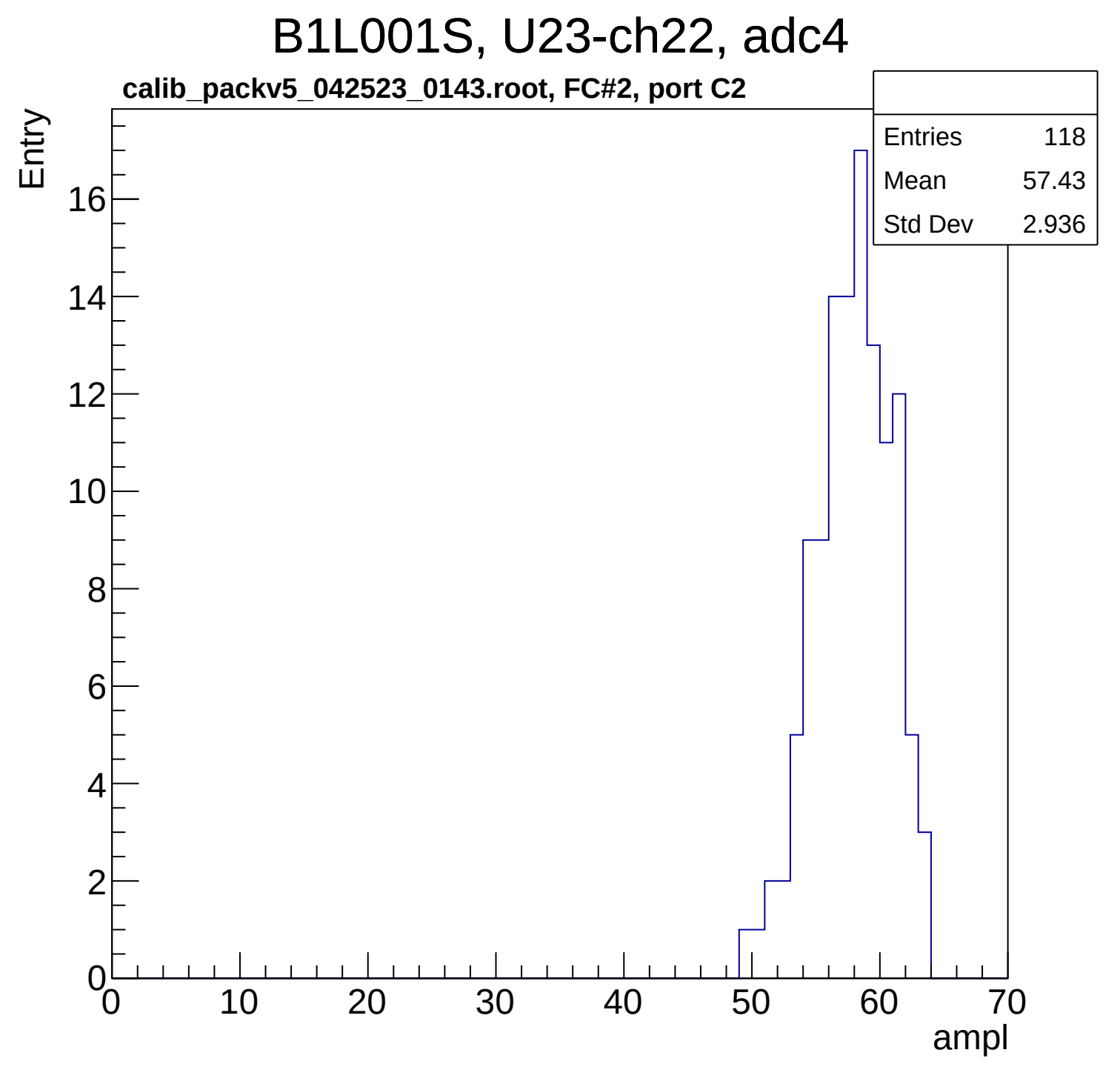
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 57.43
Std Dev 2.936
Entry
ampl
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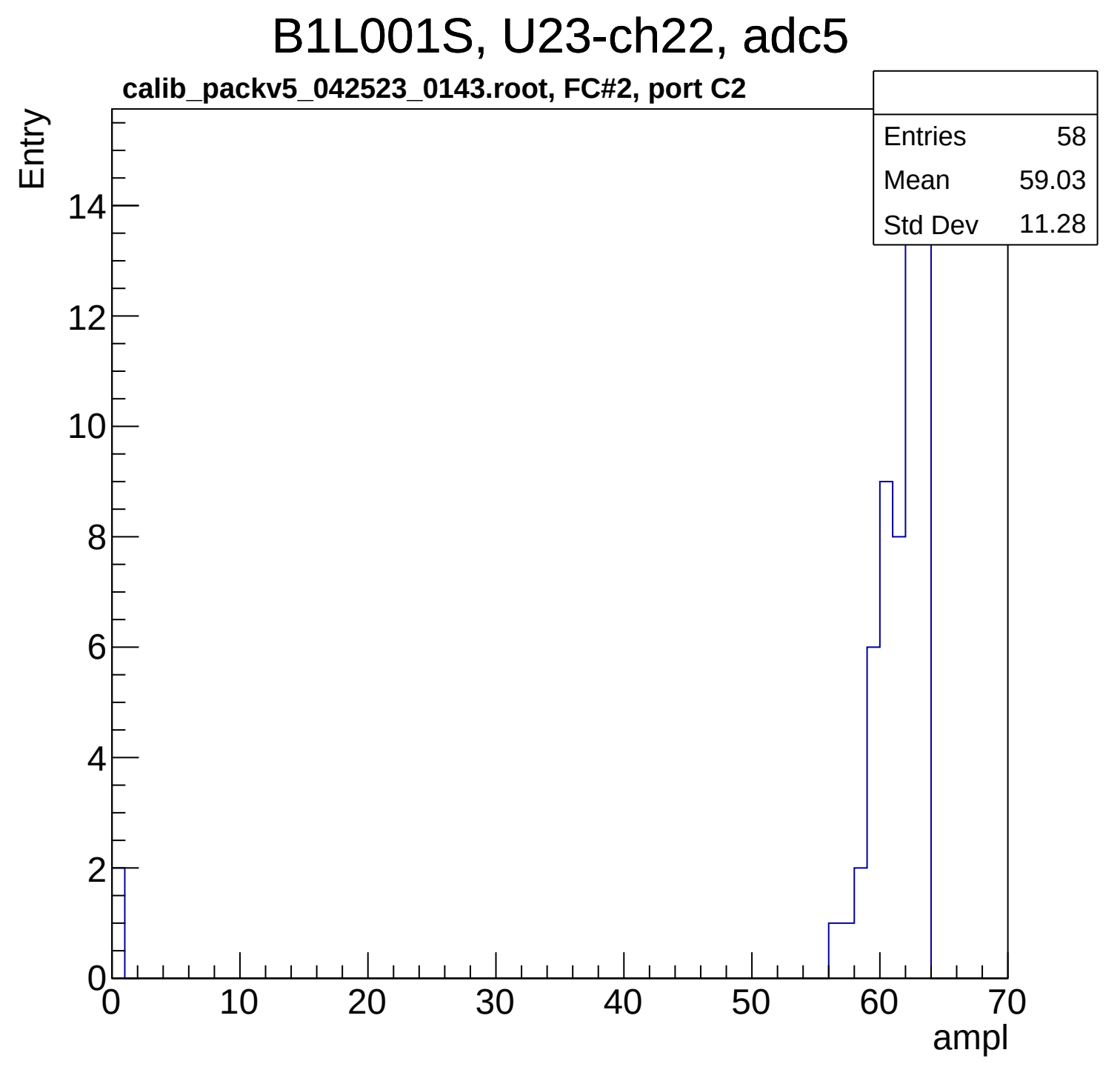
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 58
Mean 59.03
Std Dev 11.28
Entry
ampl
0
2
4
6
8
10
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14
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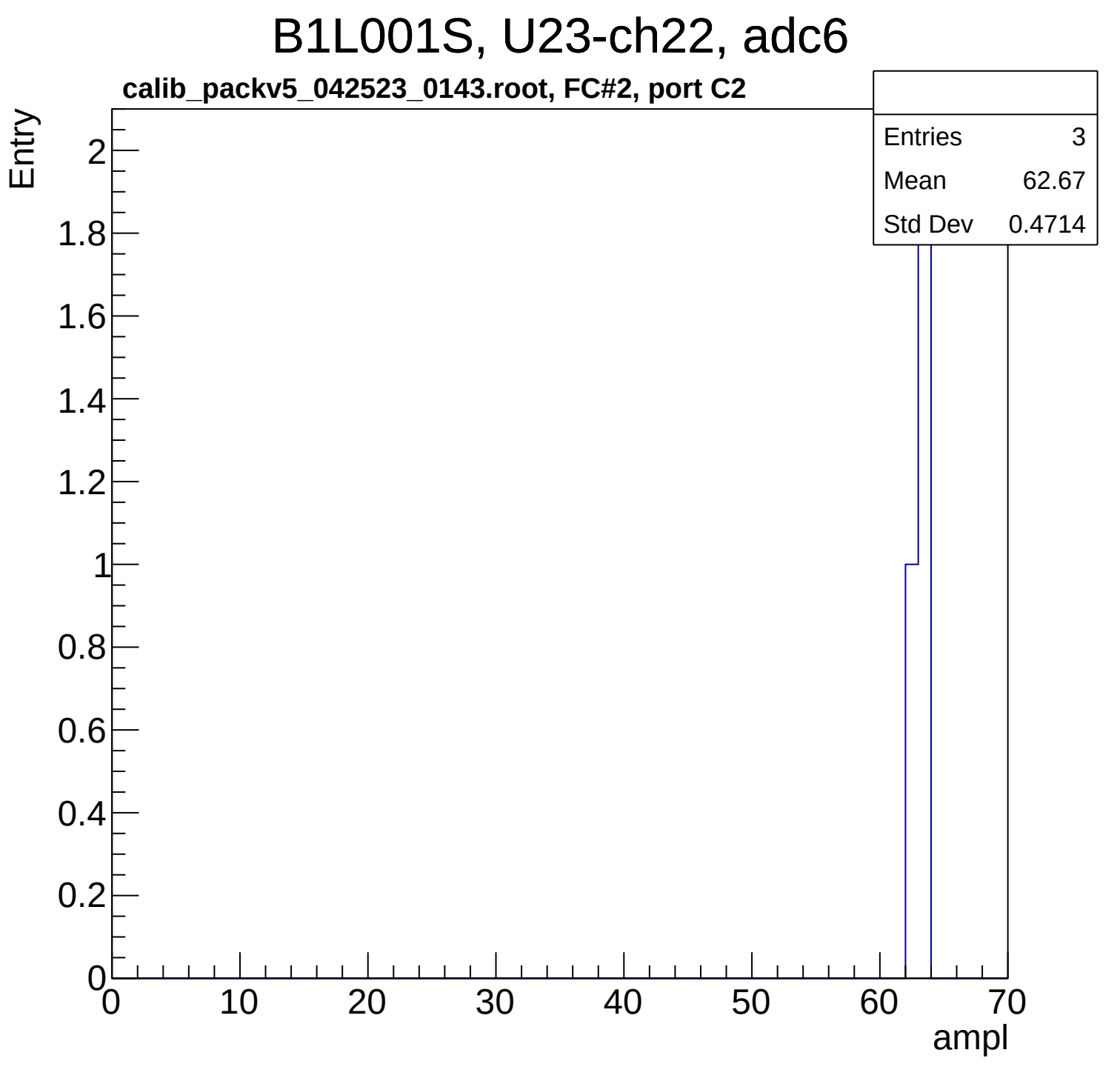
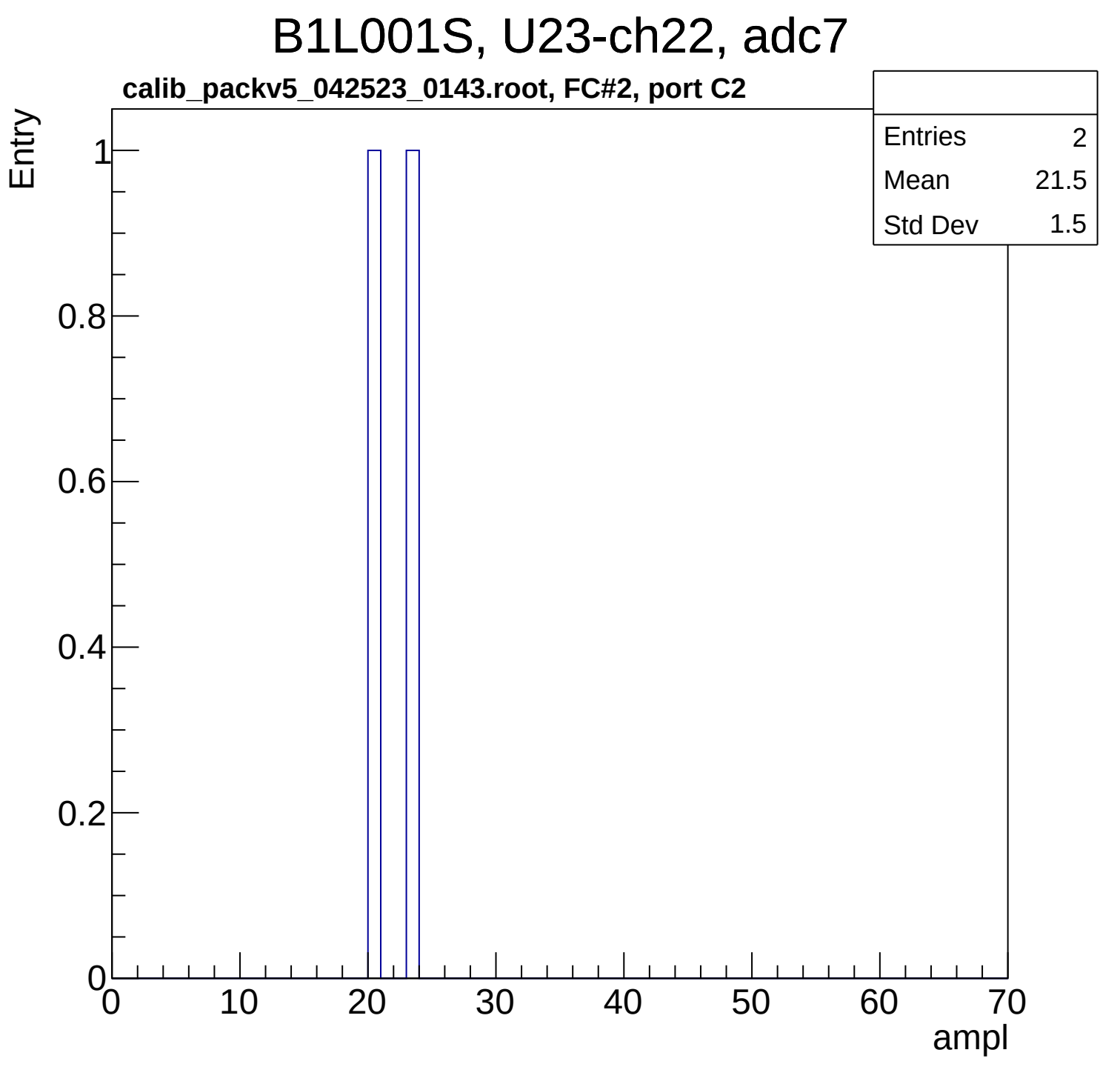

B1L001S, U23-ch22, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch22, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 21.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
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60
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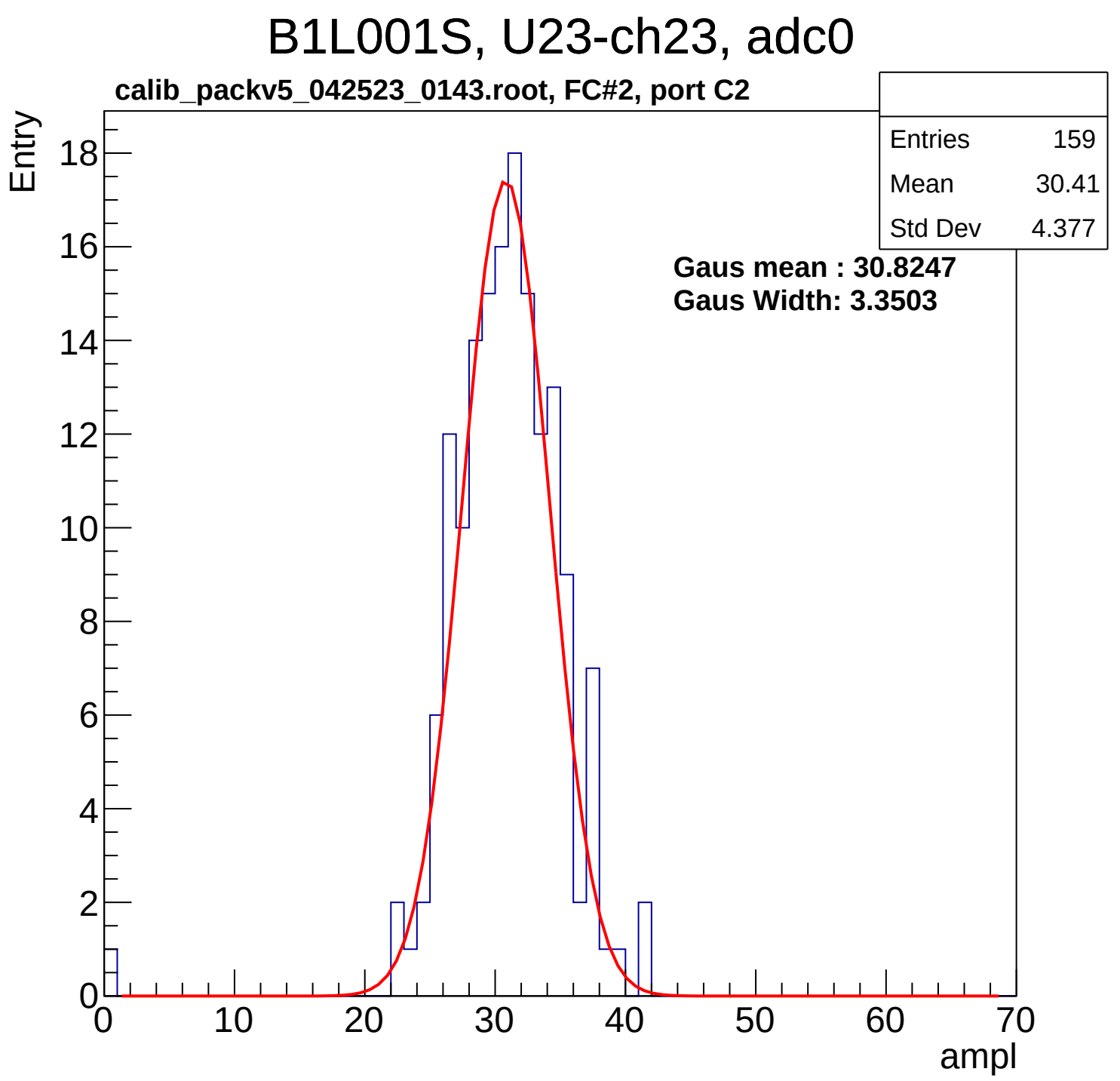
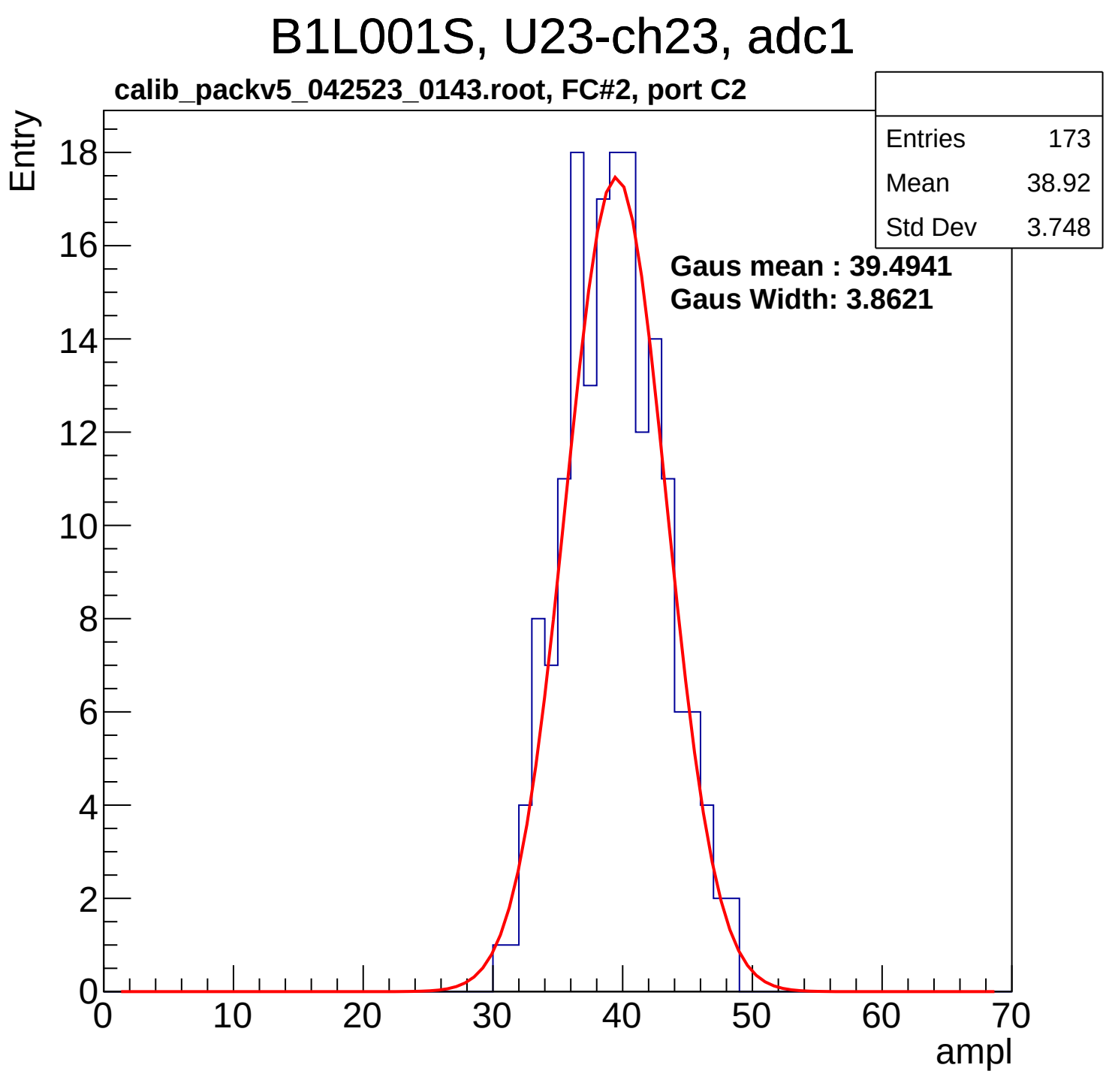

B1L001S, U23-ch23, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 30.41
Std Dev 4.377
Gaus mean : 30.8247
Gaus Width: 3.3503
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch23, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 173
Mean 38.92
Std Dev 3.748
Gaus mean : 39.4941
Gaus Width: 3.8621
Entry
ampl
0
2
4
6
8
10
12
14
16
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0
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20
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40
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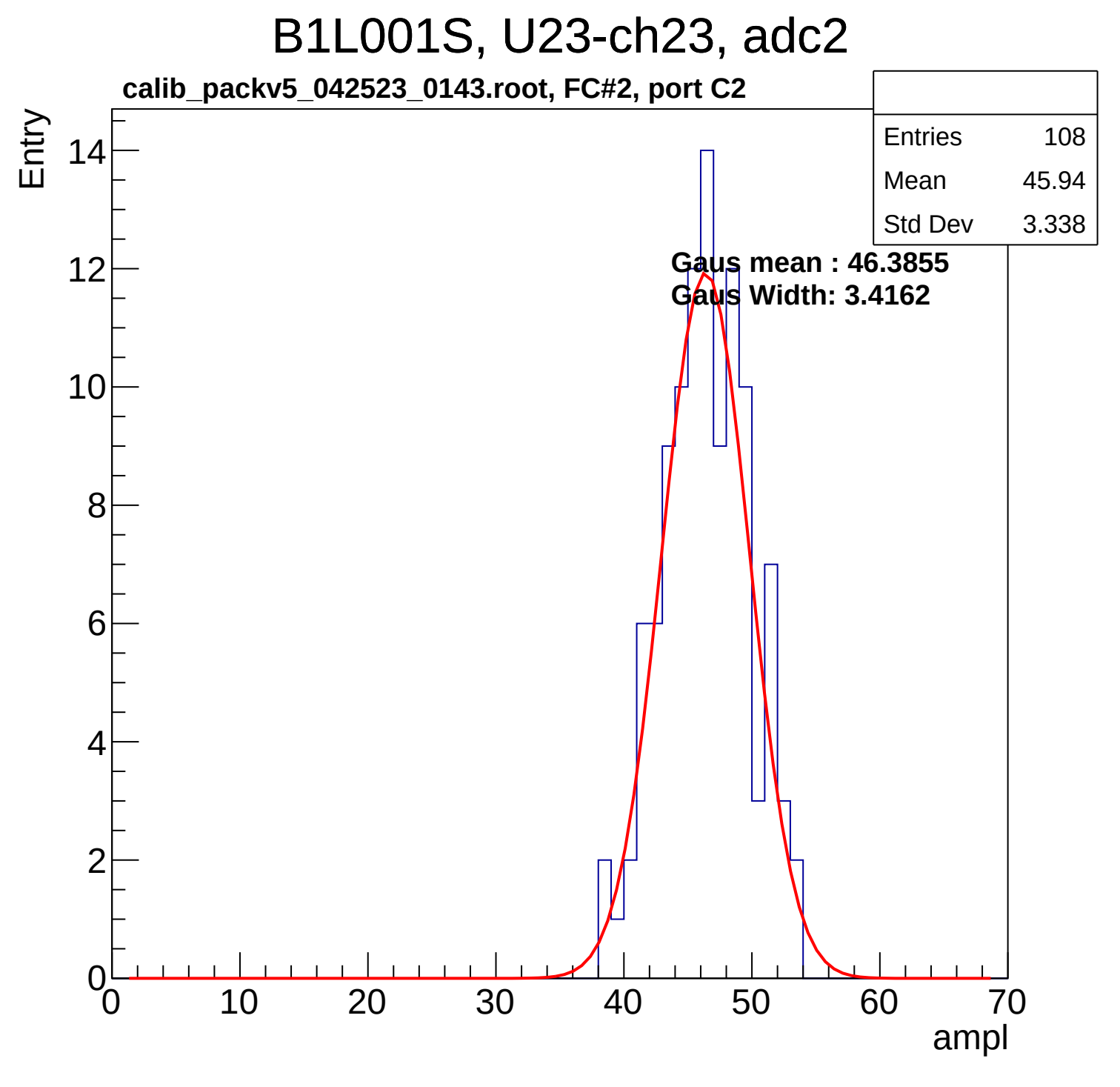
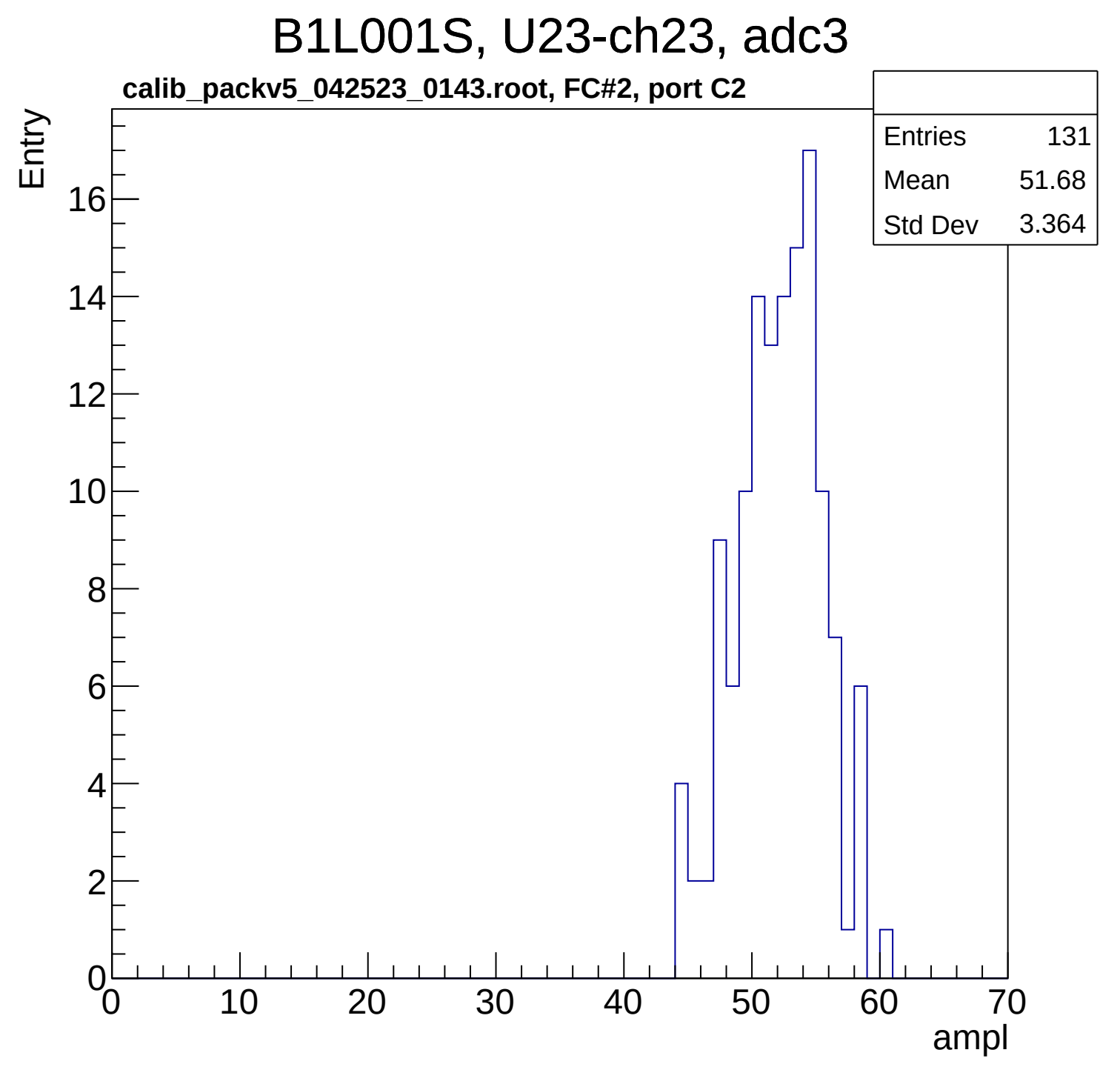
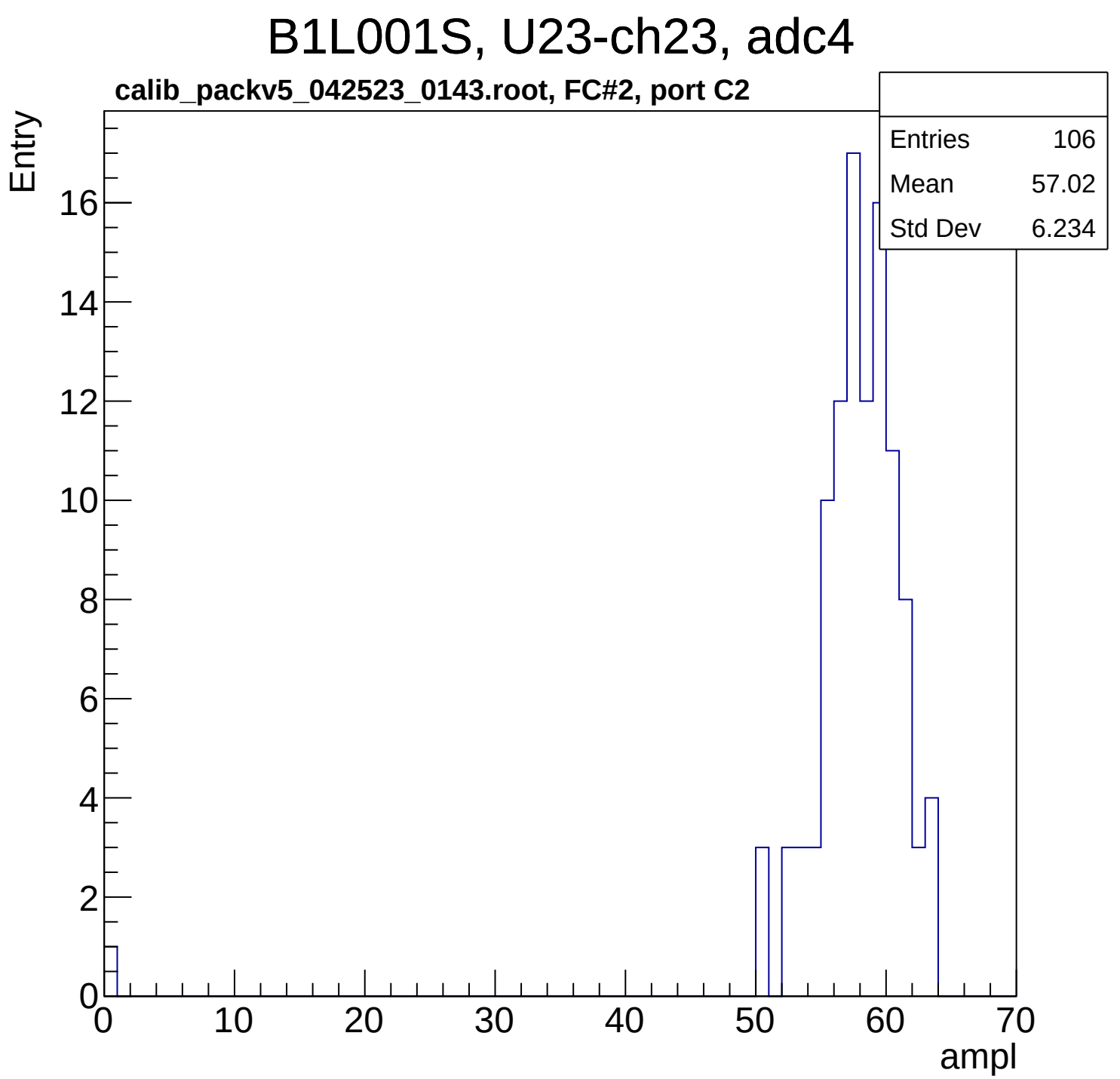
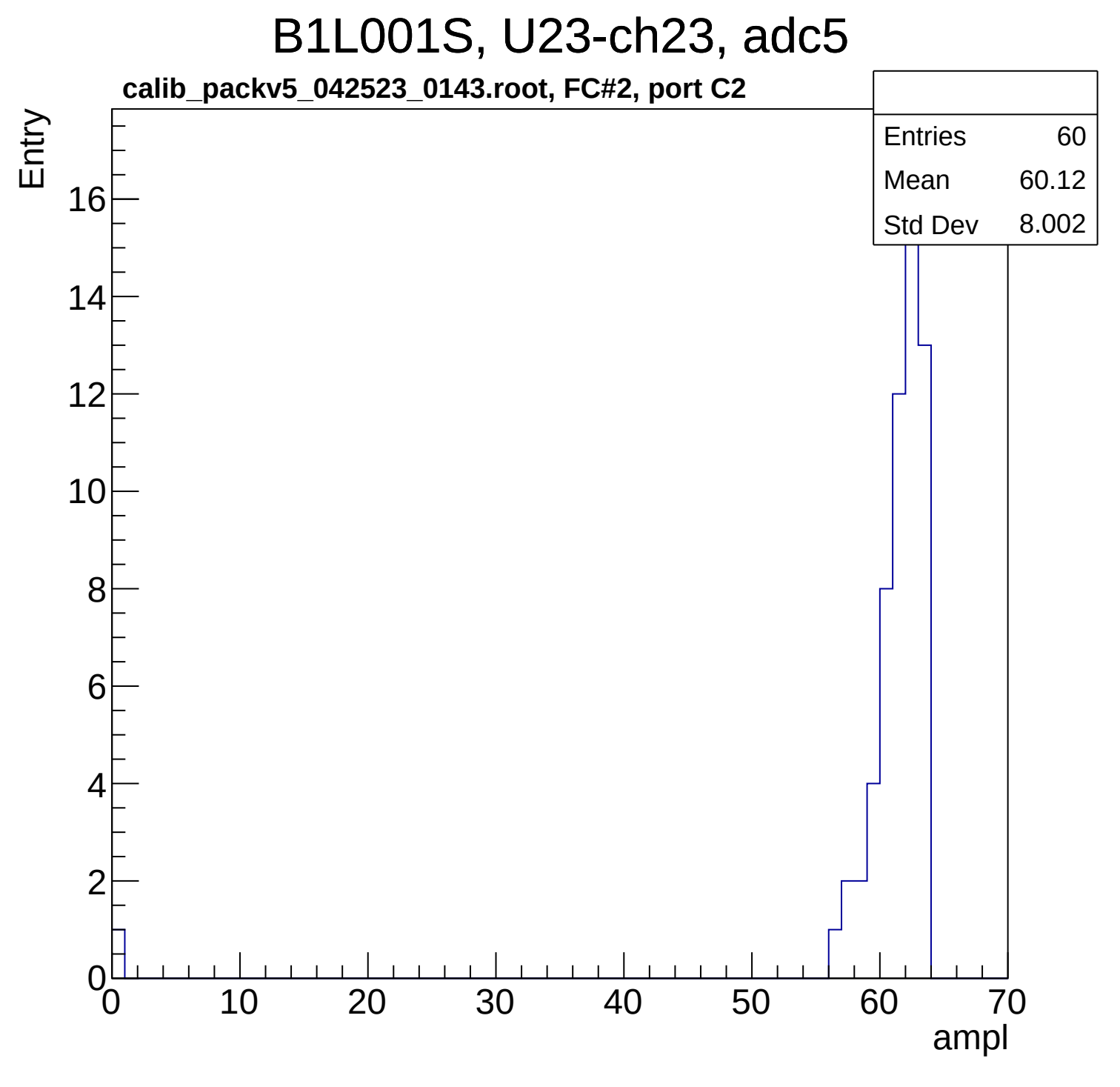
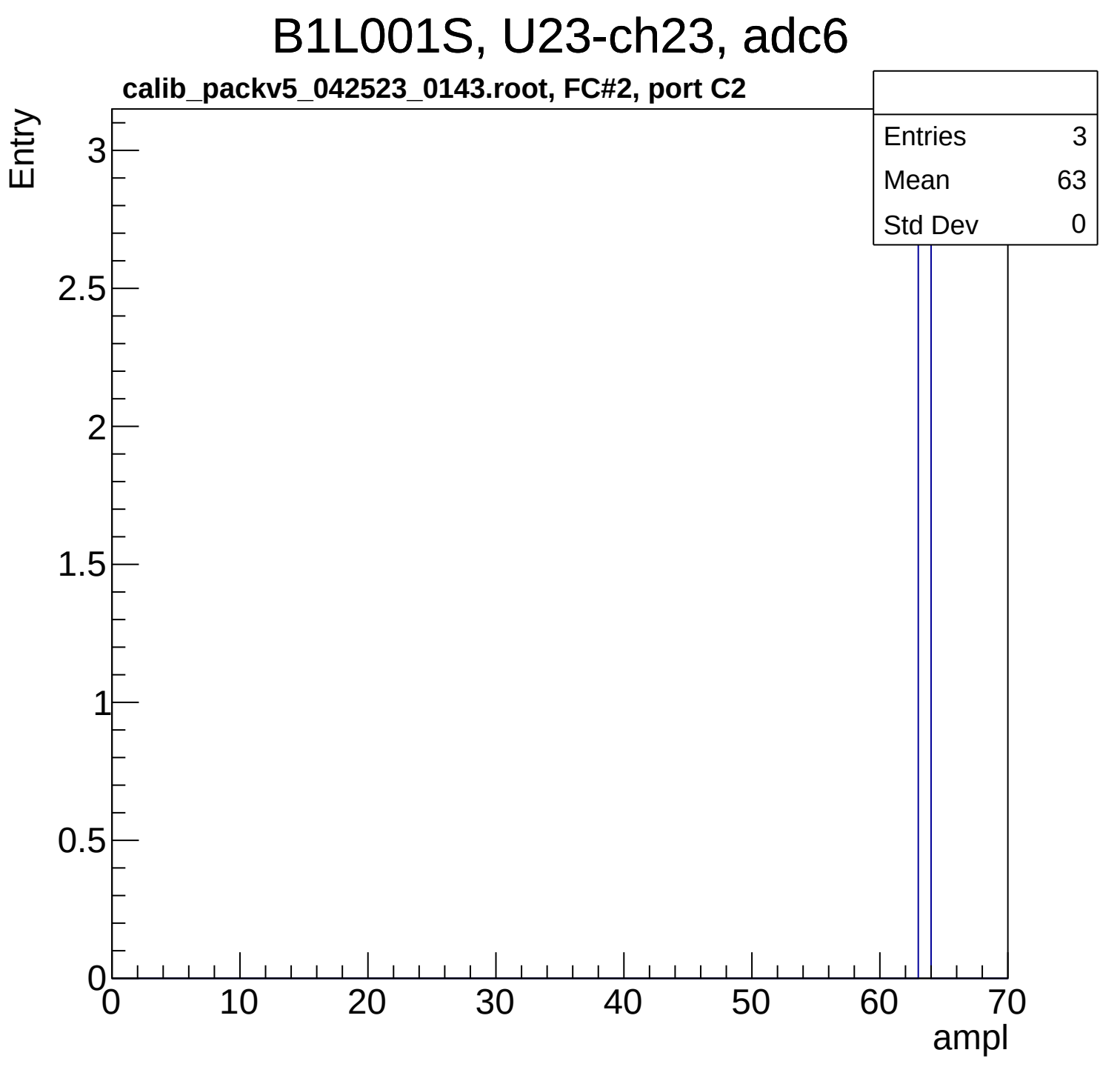
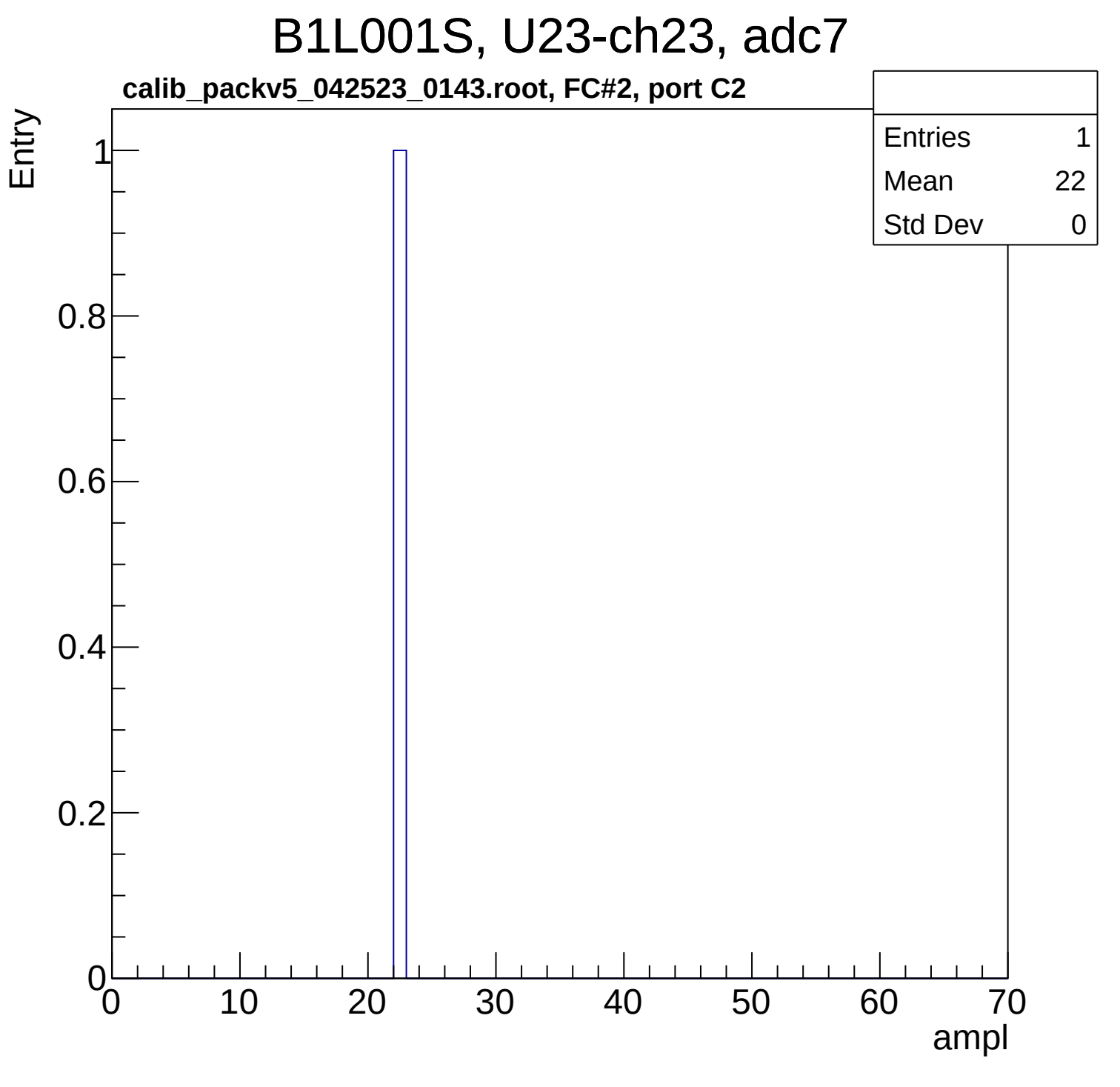

B1L001S, U23-ch23, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 45.94
Std Dev 3.338
Gaus mean : 46.3855
Gaus Width: 3.4162
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

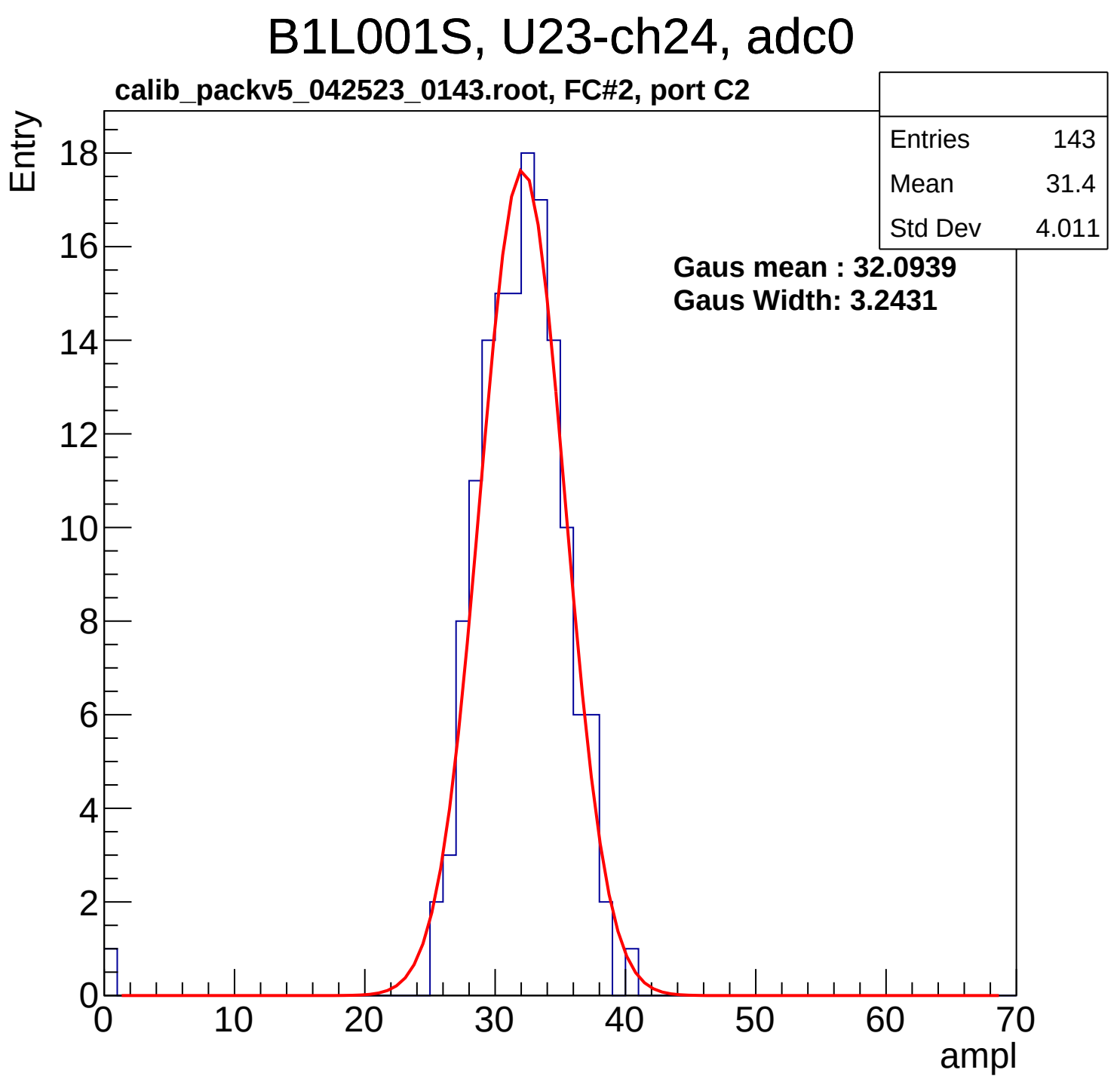

B1L001S, U23-ch23, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 51.68
Std Dev 3.364
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch23, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 57.02
Std Dev 6.234
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch23, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 60
Mean 60.12
Std Dev 8.002
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch23, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch23, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch24, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 31.4
Std Dev 4.011
Gaus mean : 32.0939
Gaus Width: 3.2431
Entry
ampl
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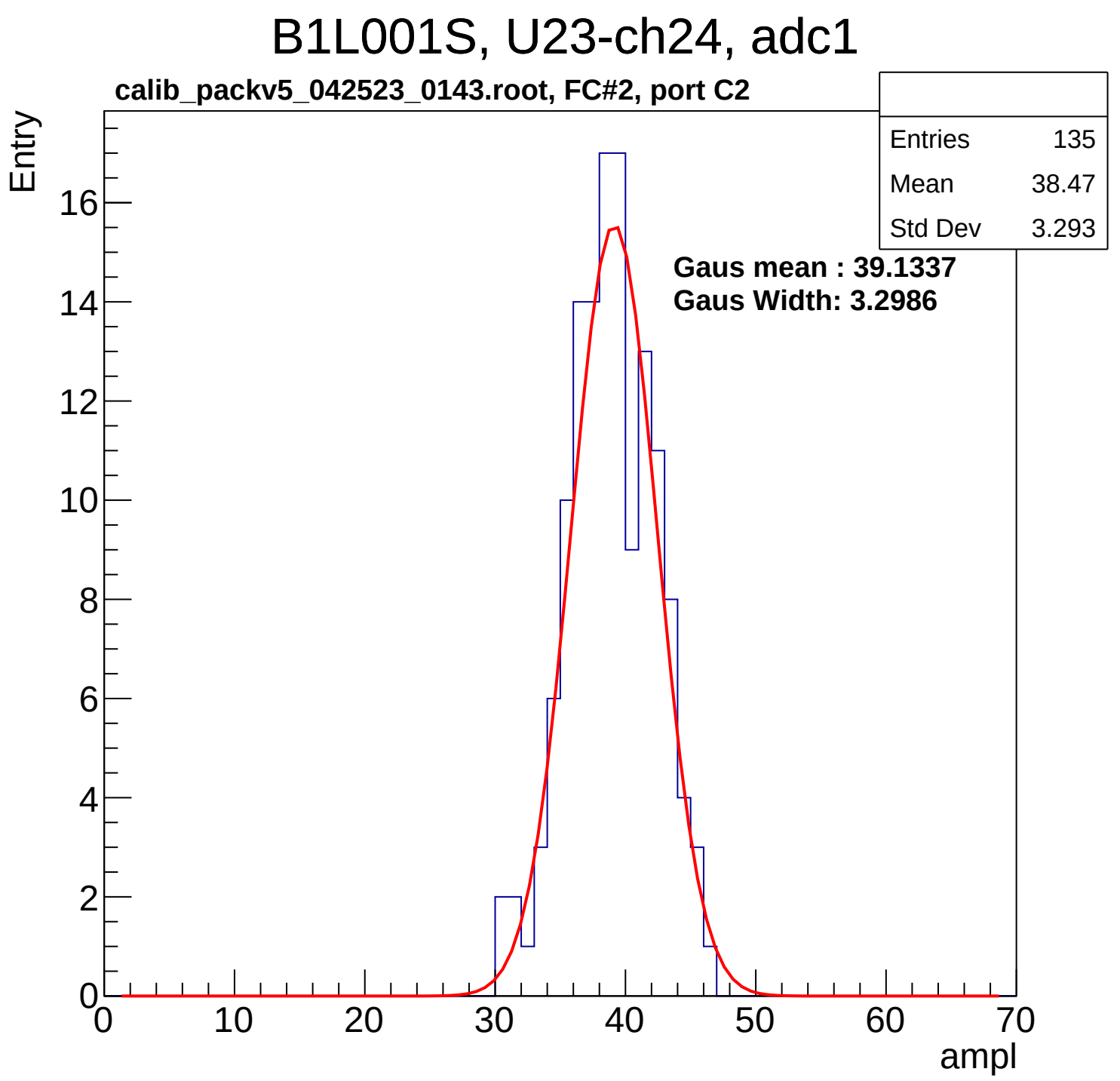
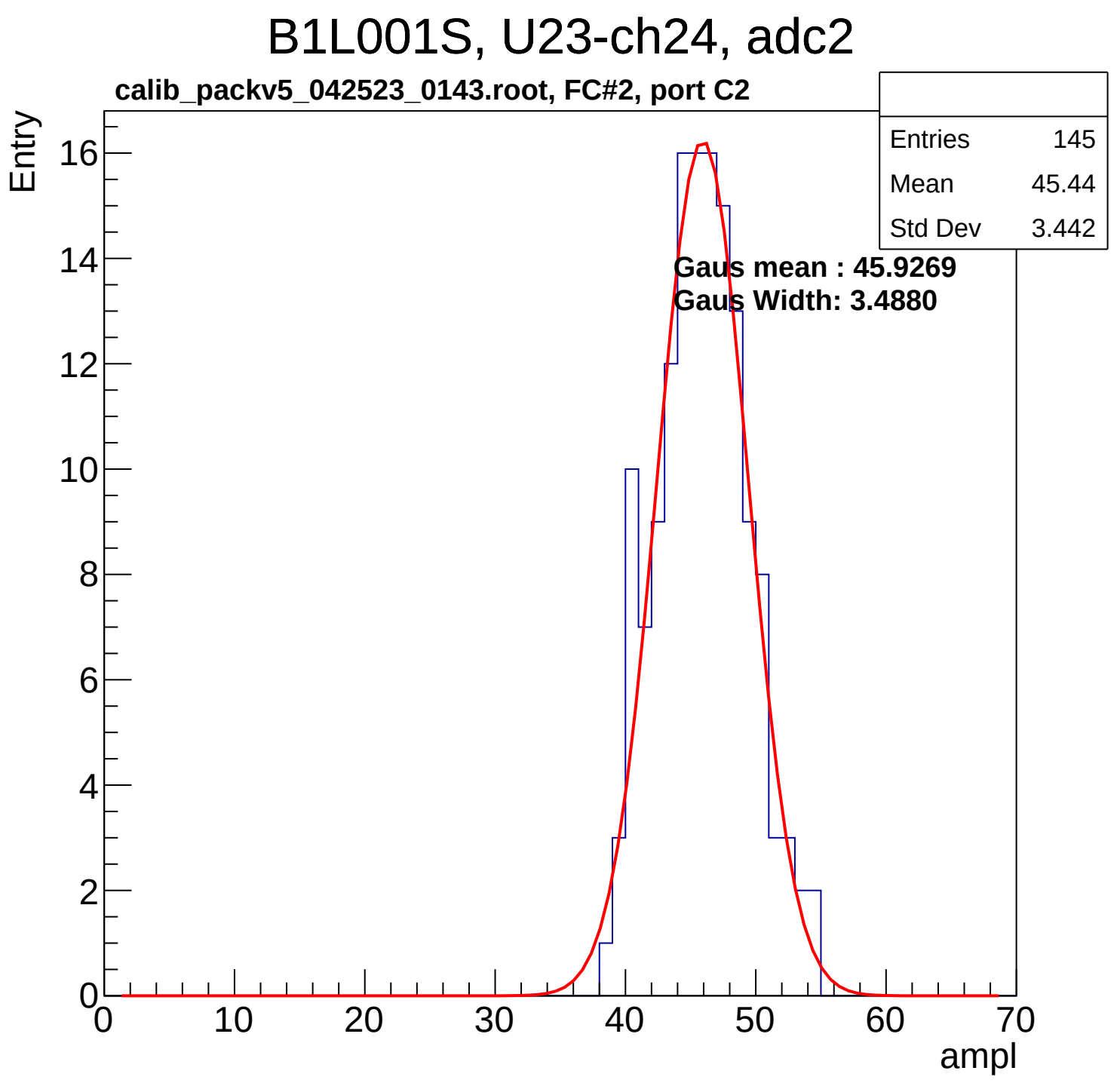
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch24, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 38.47
Std Dev 3.293
Gaus mean : 39.1337
Gaus Width: 3.2986
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch24, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 45.44
Std Dev 3.442
Gaus mean : 45.9269
Gaus Width: 3.4880
Entry
ampl
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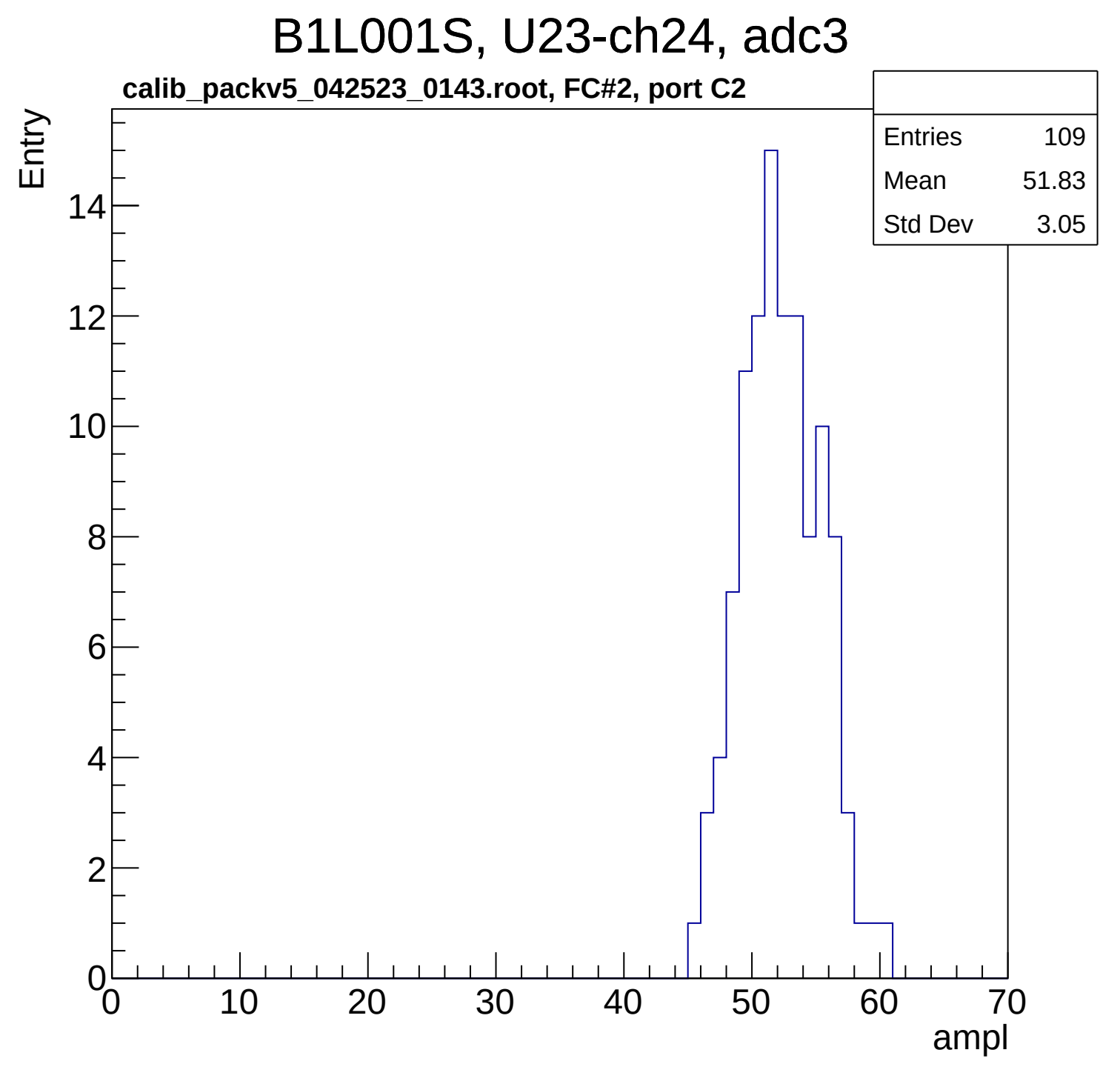
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch24, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 51.83
Std Dev 3.05
Entry
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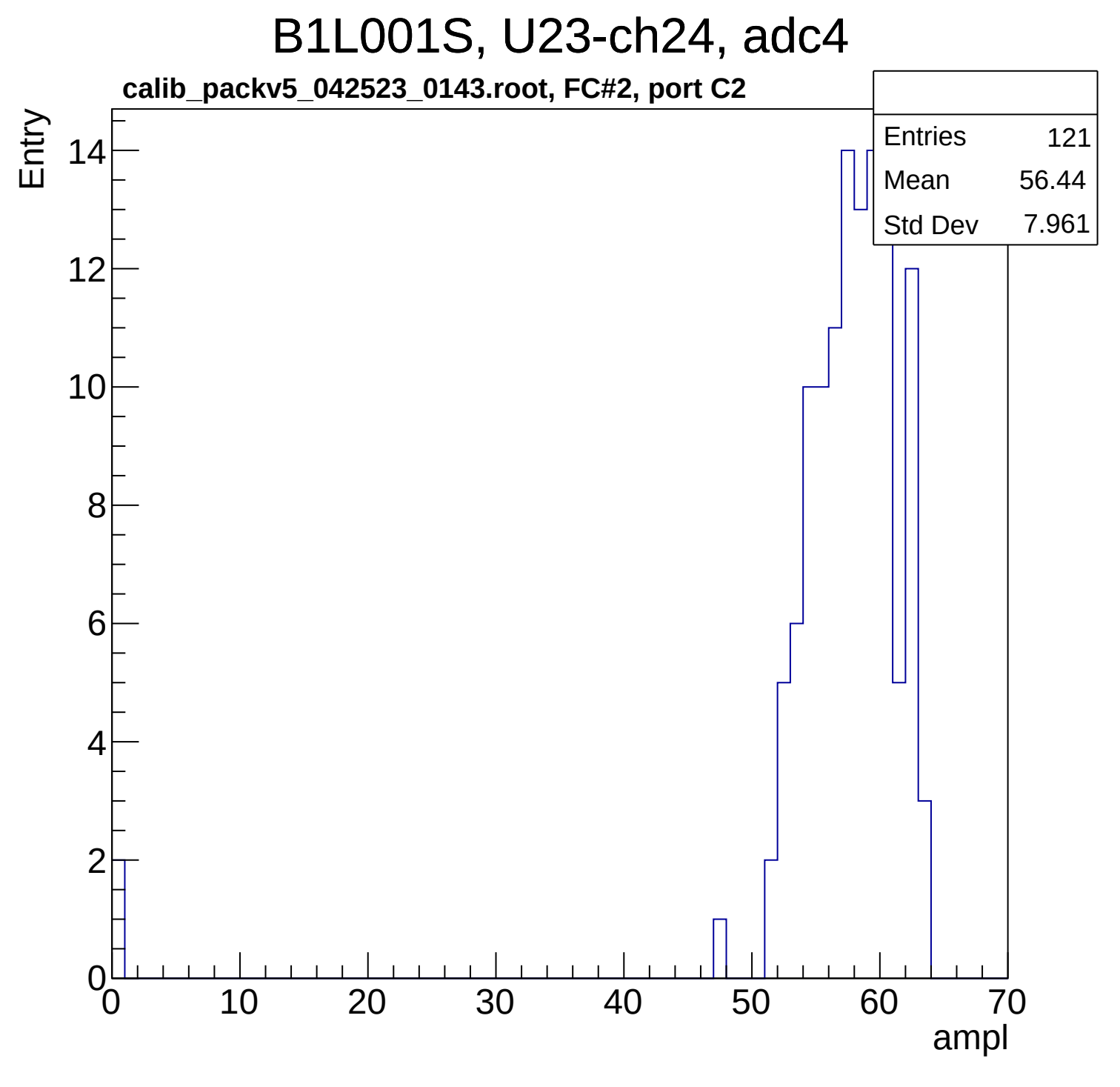
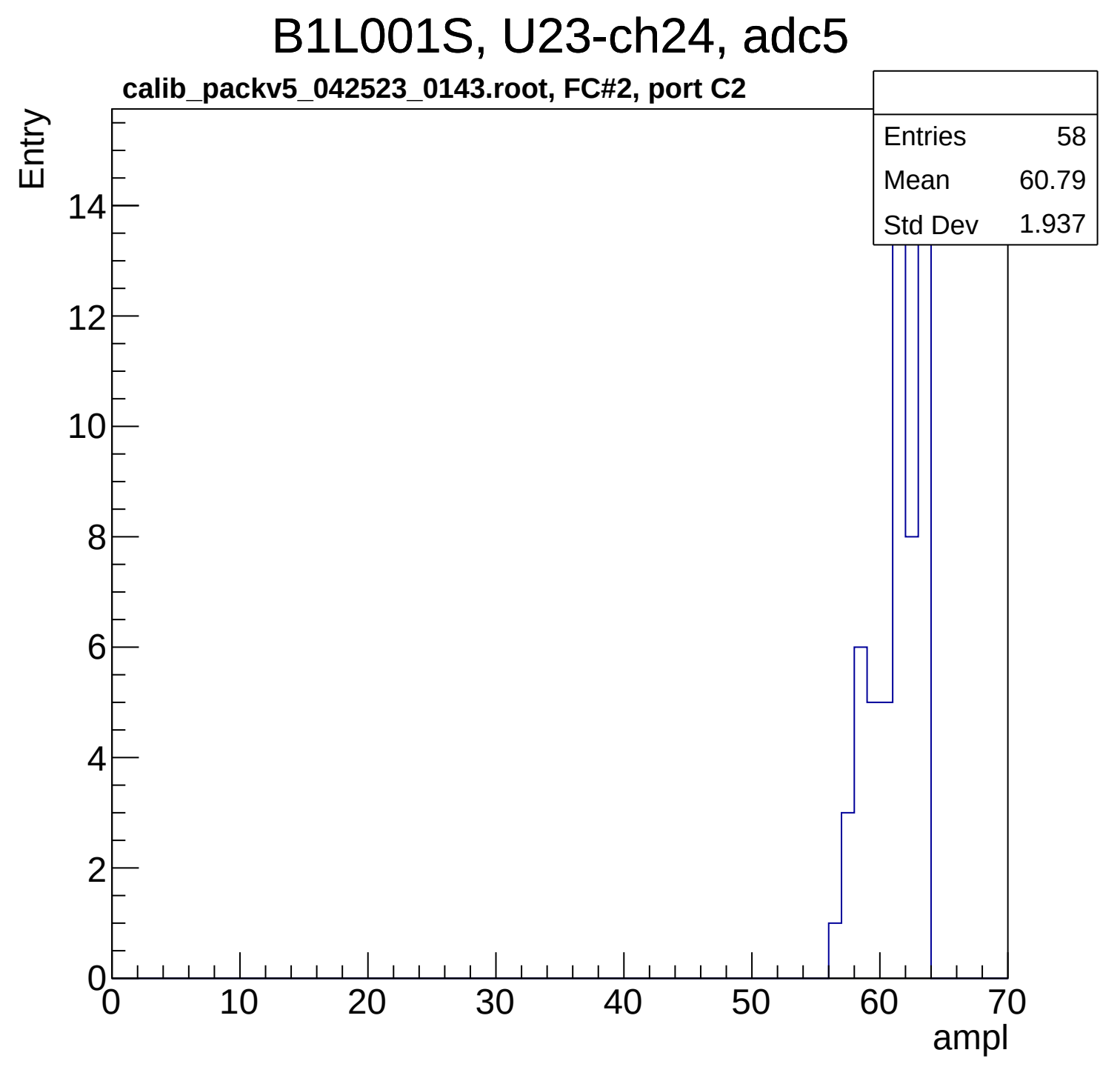
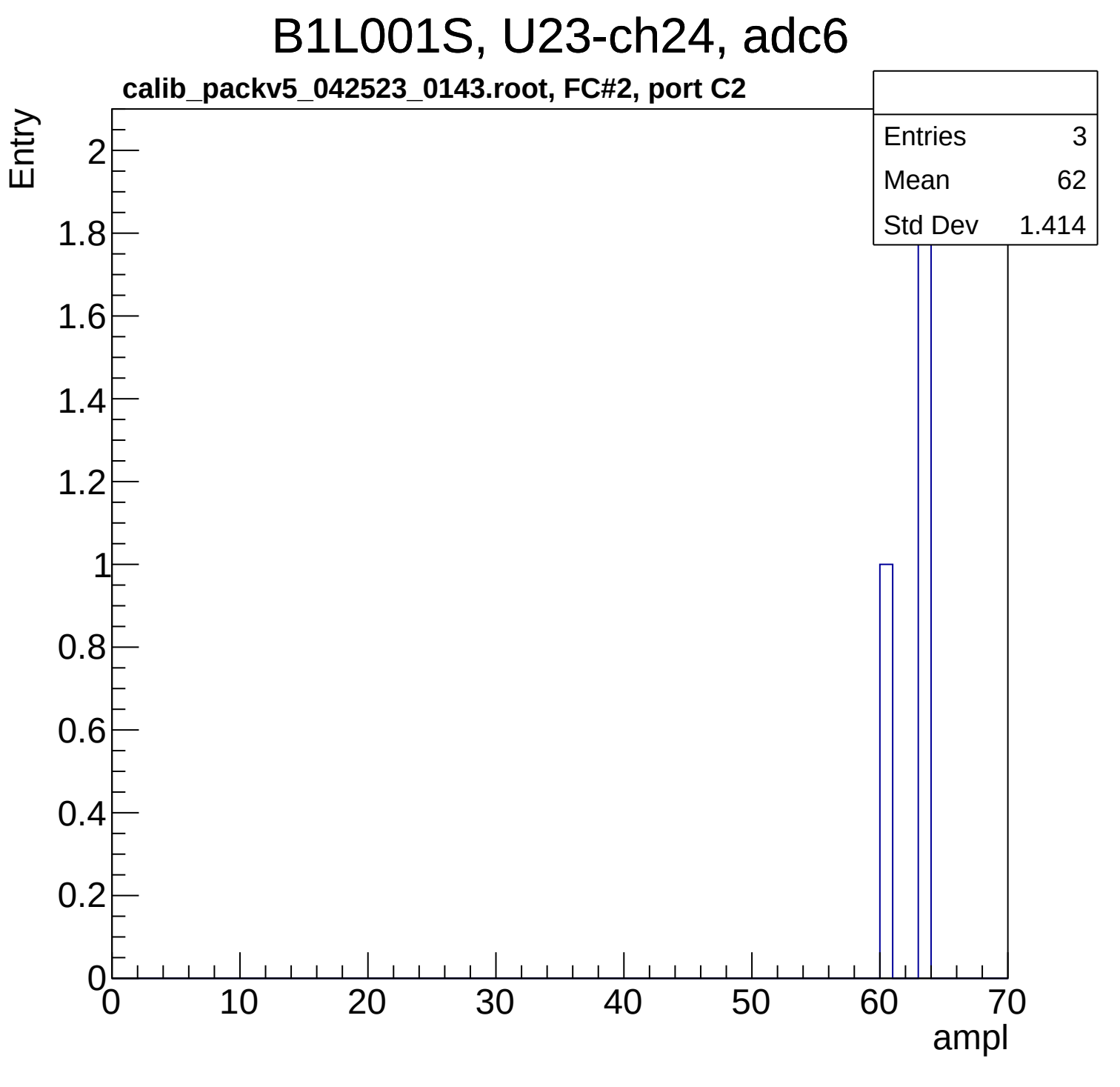
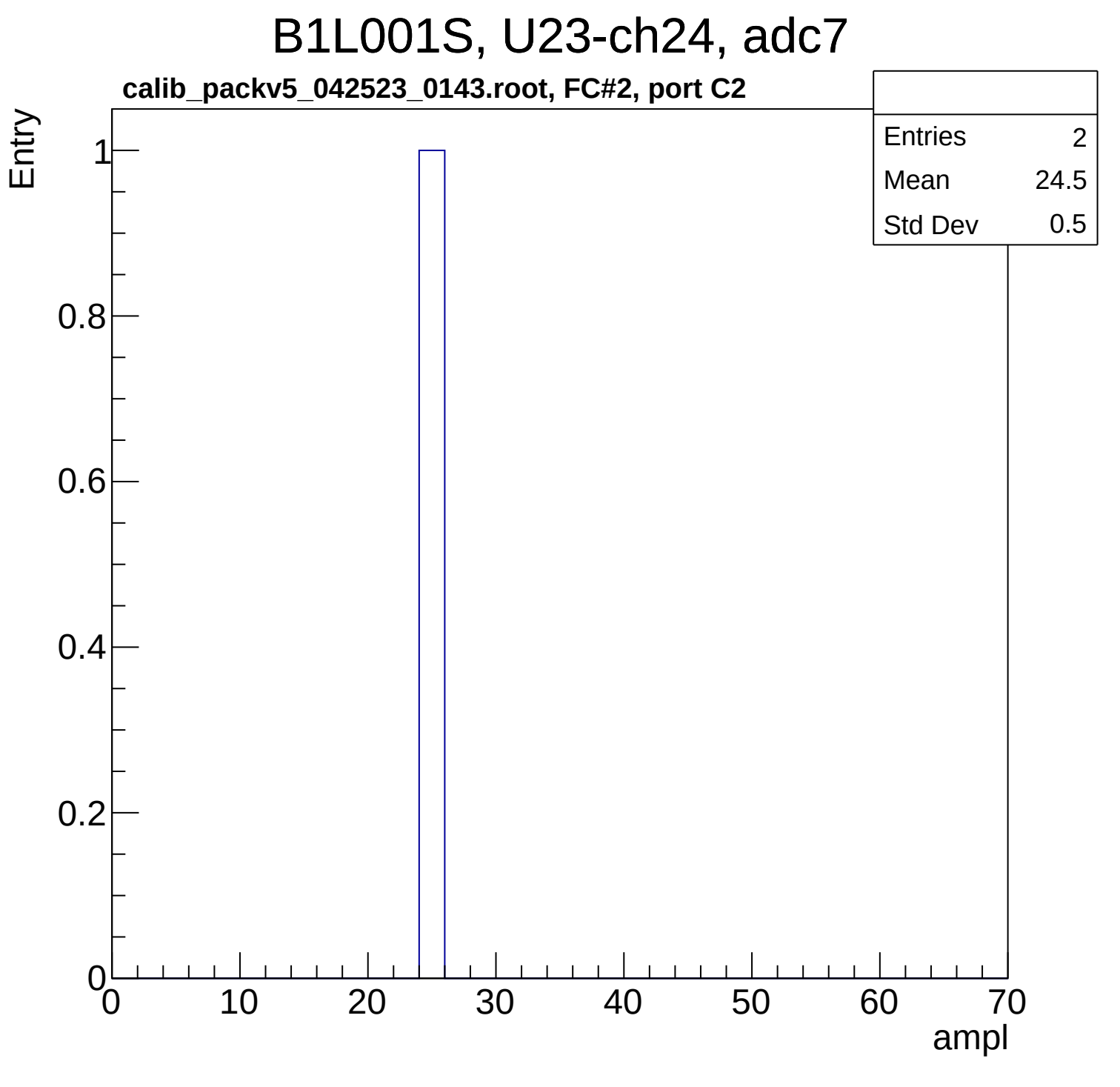
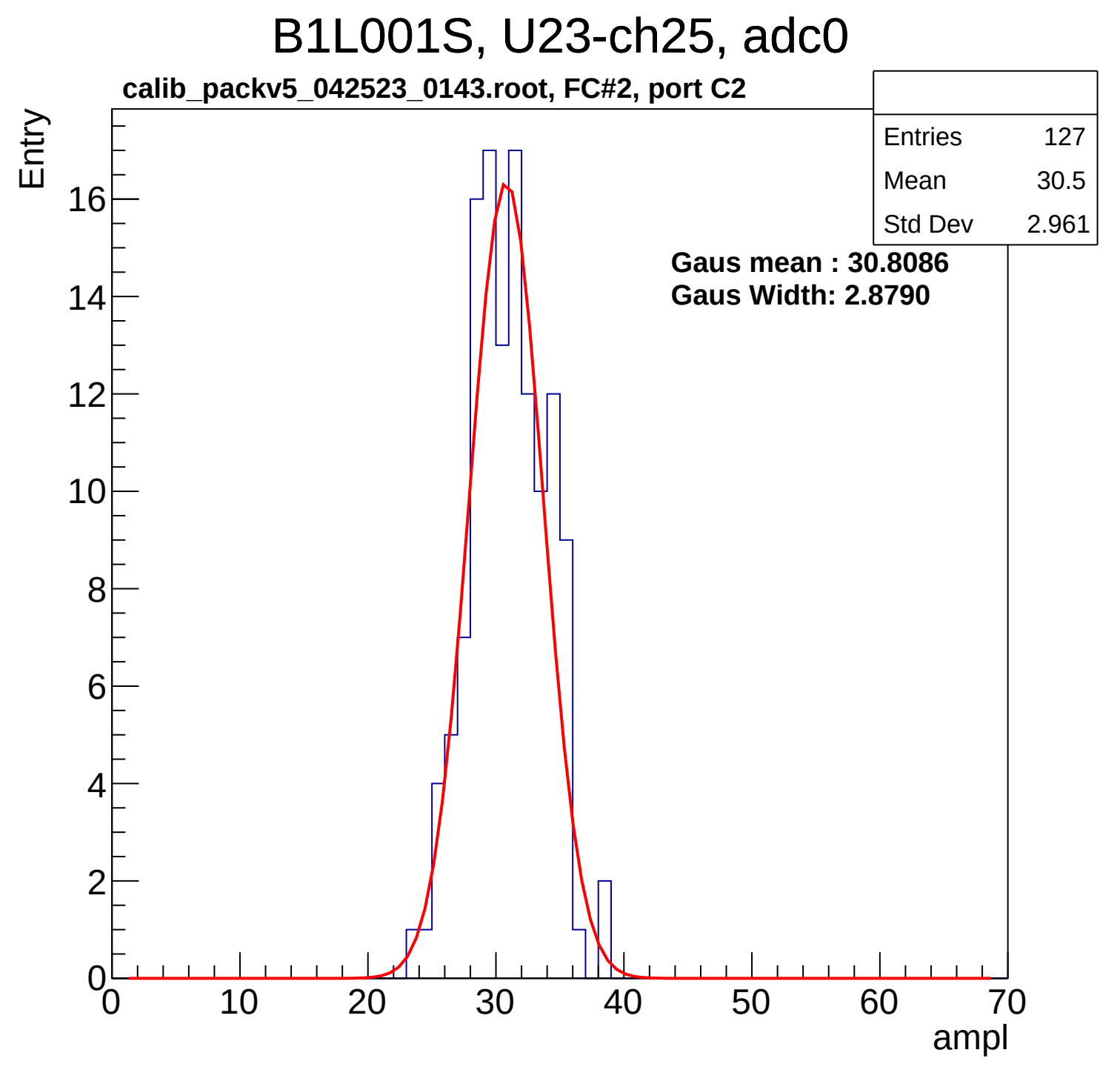
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

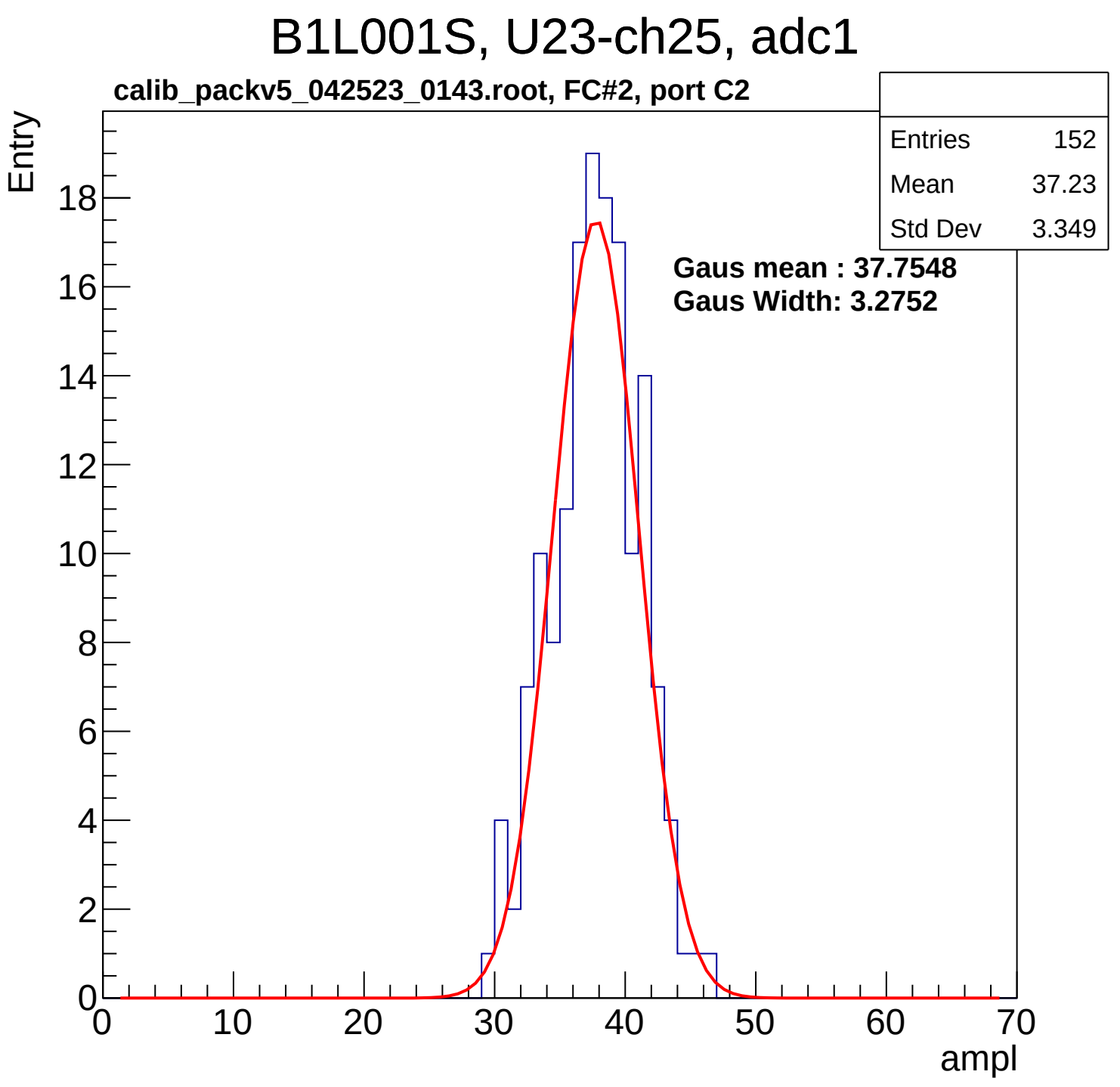

B1L001S, U23-ch24, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 56.44
Std Dev 7.961
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

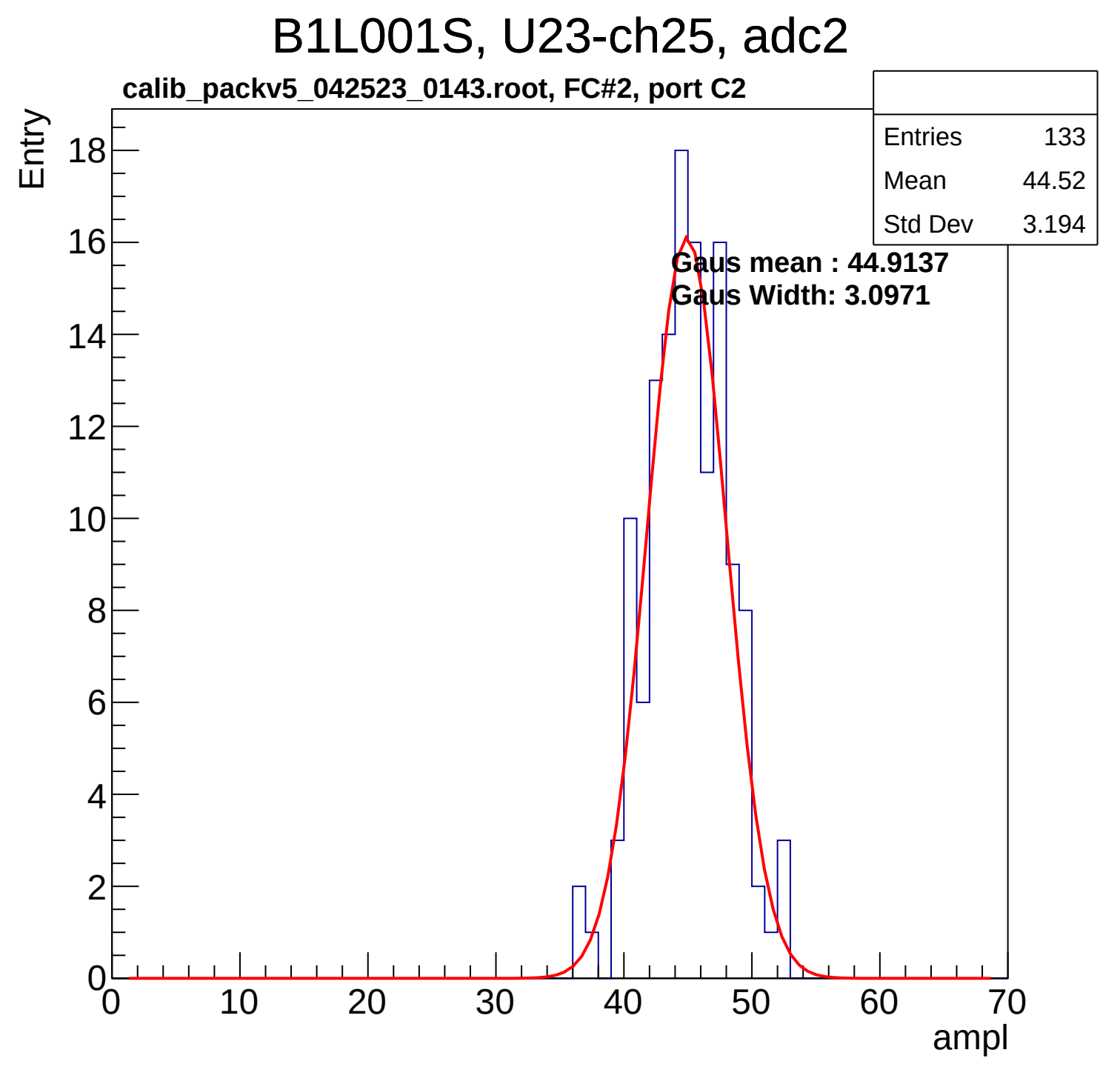

B1L001S, U23-ch24, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 58
Mean 60.79
Std Dev 1.937
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch24, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch24, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 24.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch25, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 30.5
Std Dev 2.961
Gaus mean : 30.8086
Gaus Width: 2.8790
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch25, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 37.23
Std Dev 3.349
Gaus mean : 37.7548
Gaus Width: 3.2752
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch25, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 44.52
Std Dev 3.194
Gaus mean : 44.9137
Gaus Width: 3.0971
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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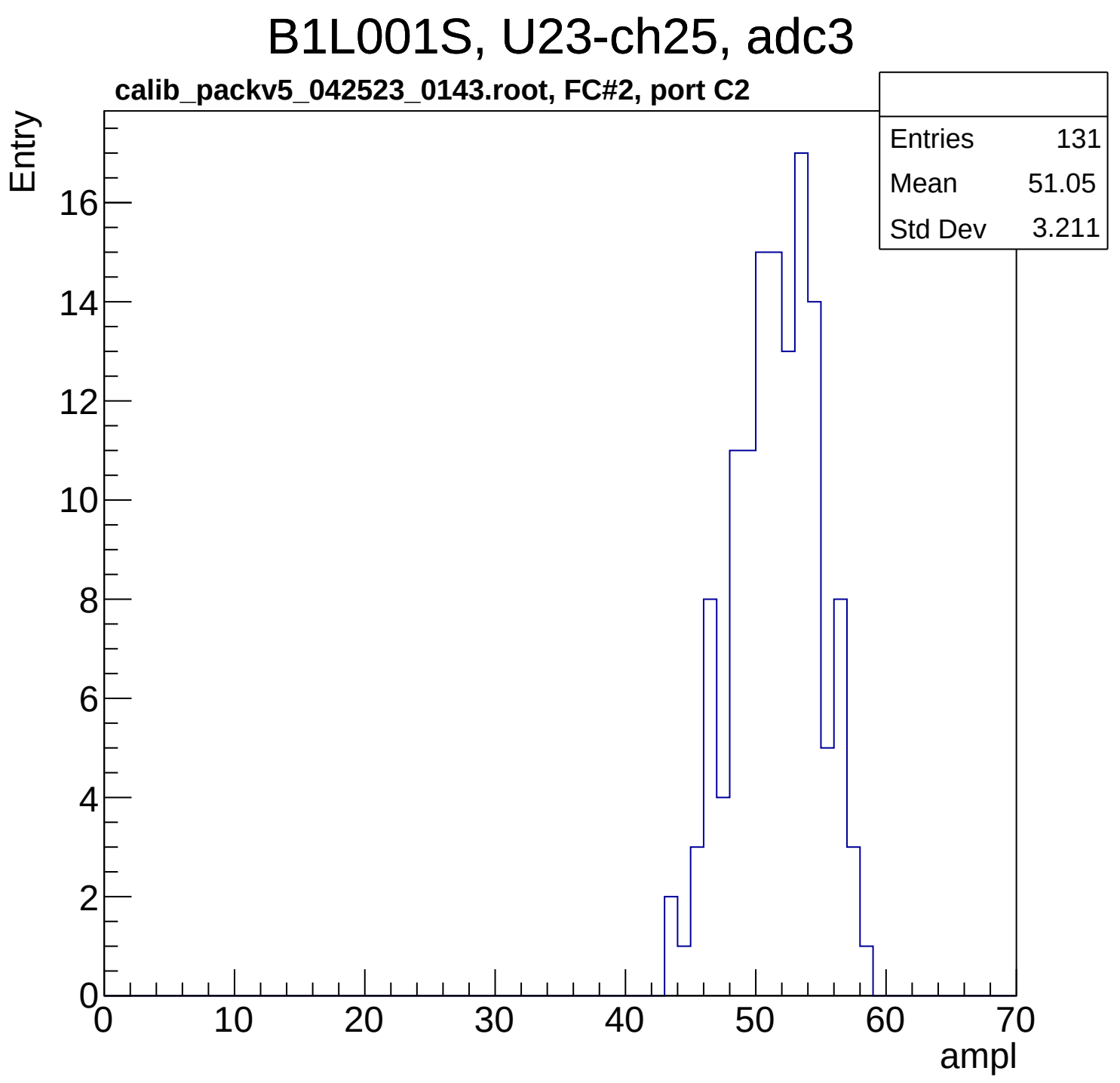
B1L001S, U23-ch25, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 51.05
Std Dev 3.211
Entry
ampl
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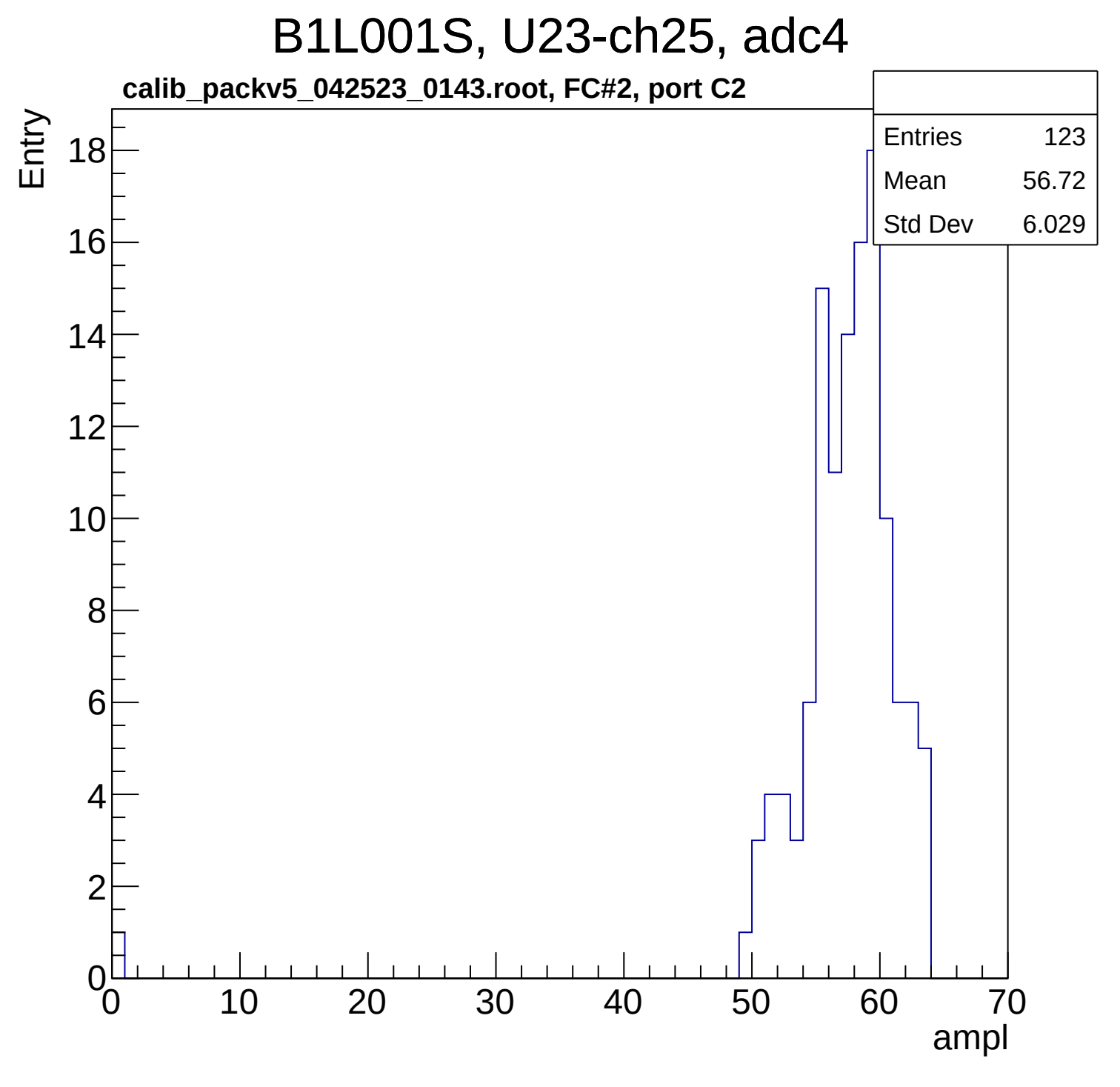
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch25, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 56.72
Std Dev 6.029
Entry
ampl
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4
6
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10
12
14
16
18
0
10
20
30
40
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60
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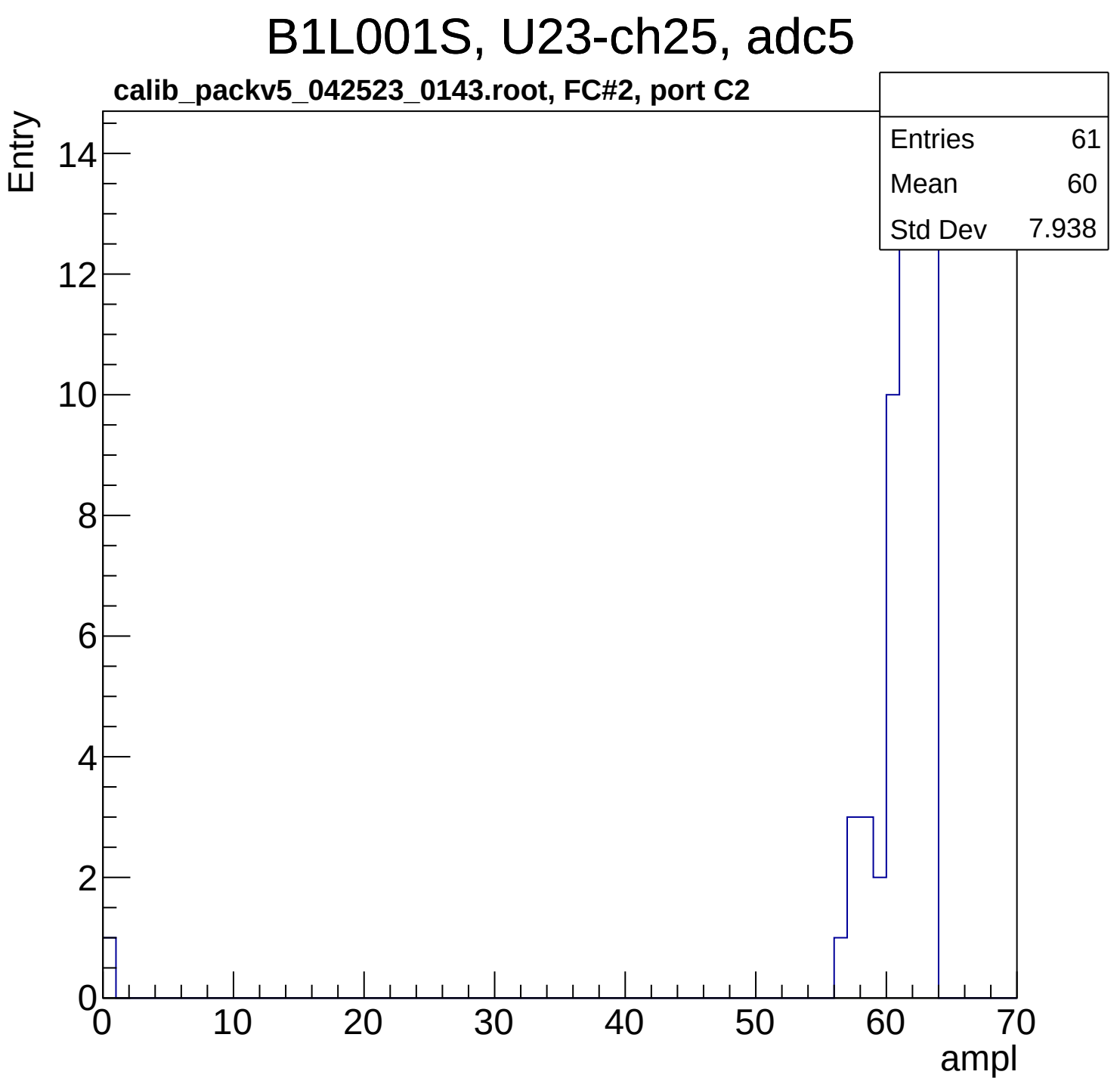
B1L001S, U23-ch25, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 60
Std Dev 7.938
Entry
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2
4
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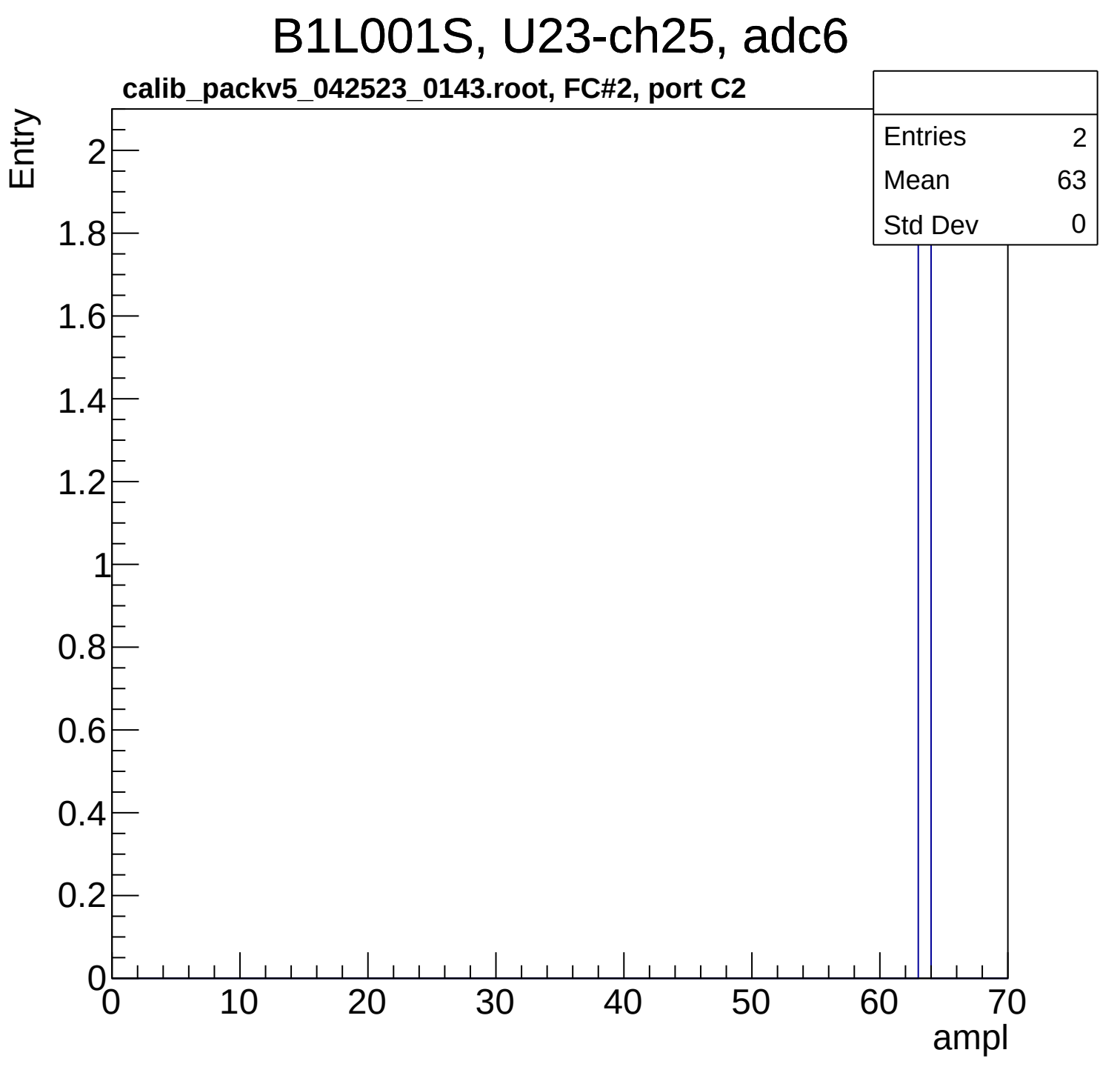
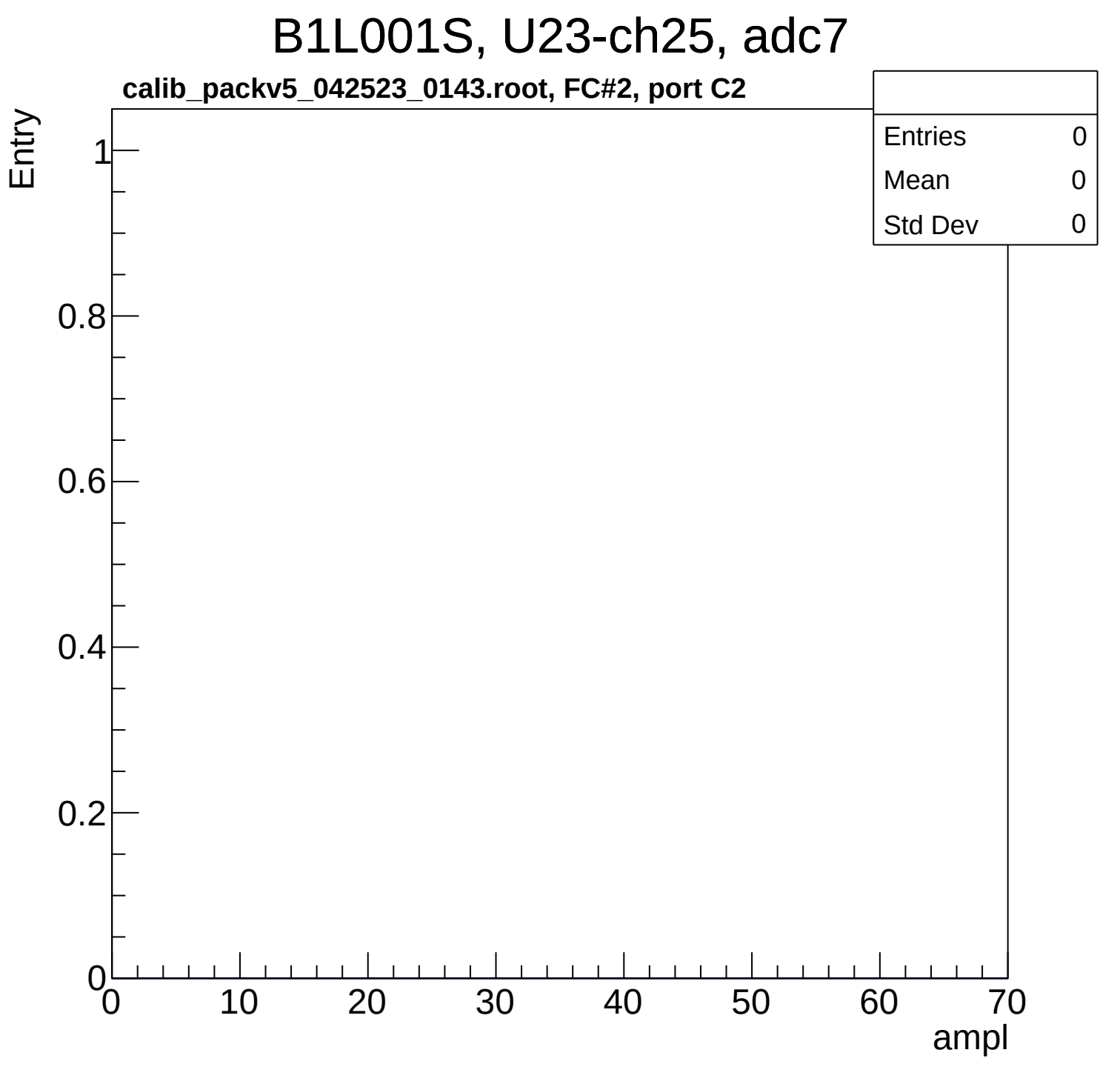
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch25, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch25, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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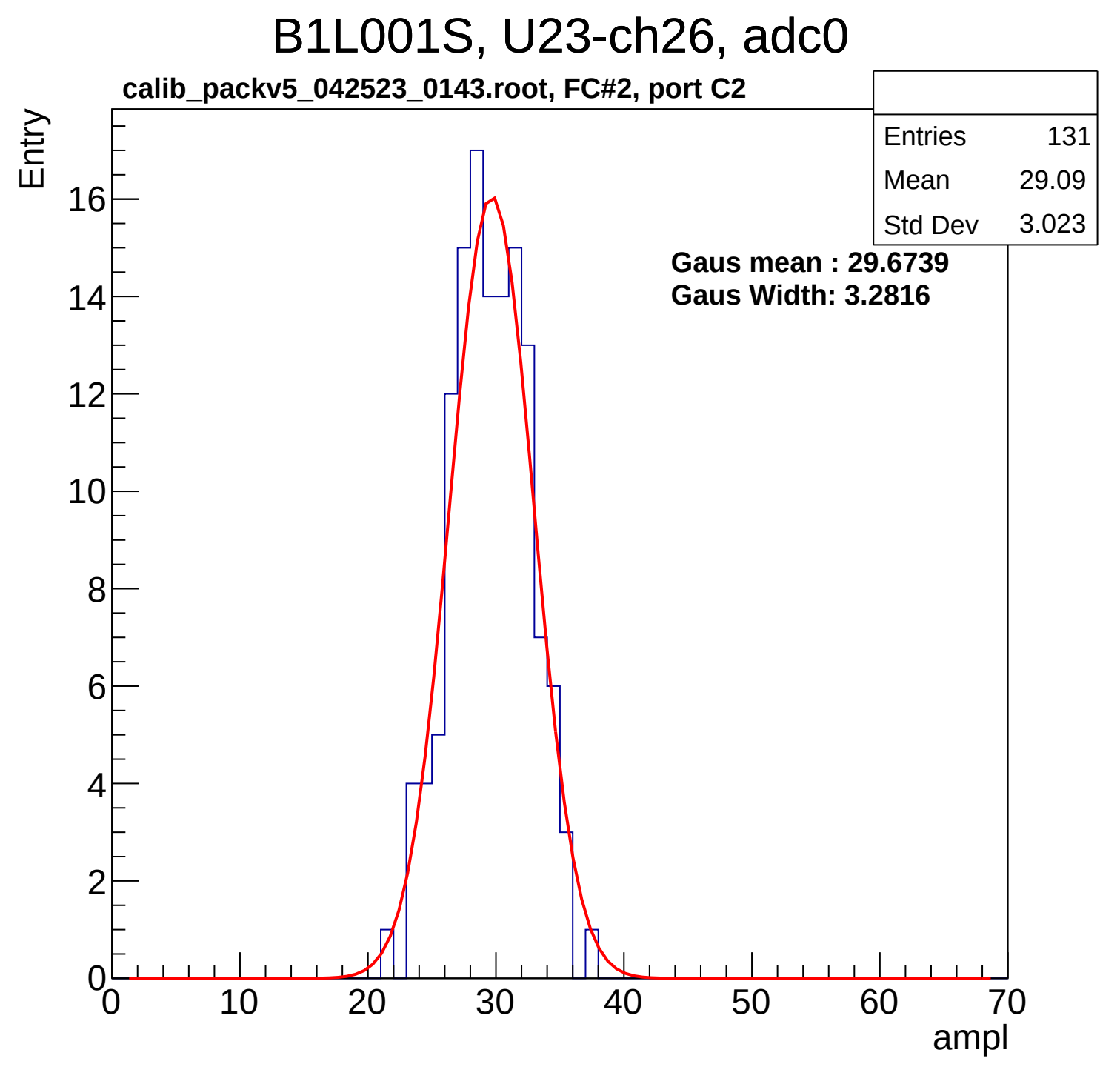
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch26, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 29.09
Std Dev 3.023
Gaus mean : 29.6739
Gaus Width: 3.2816
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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40
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60
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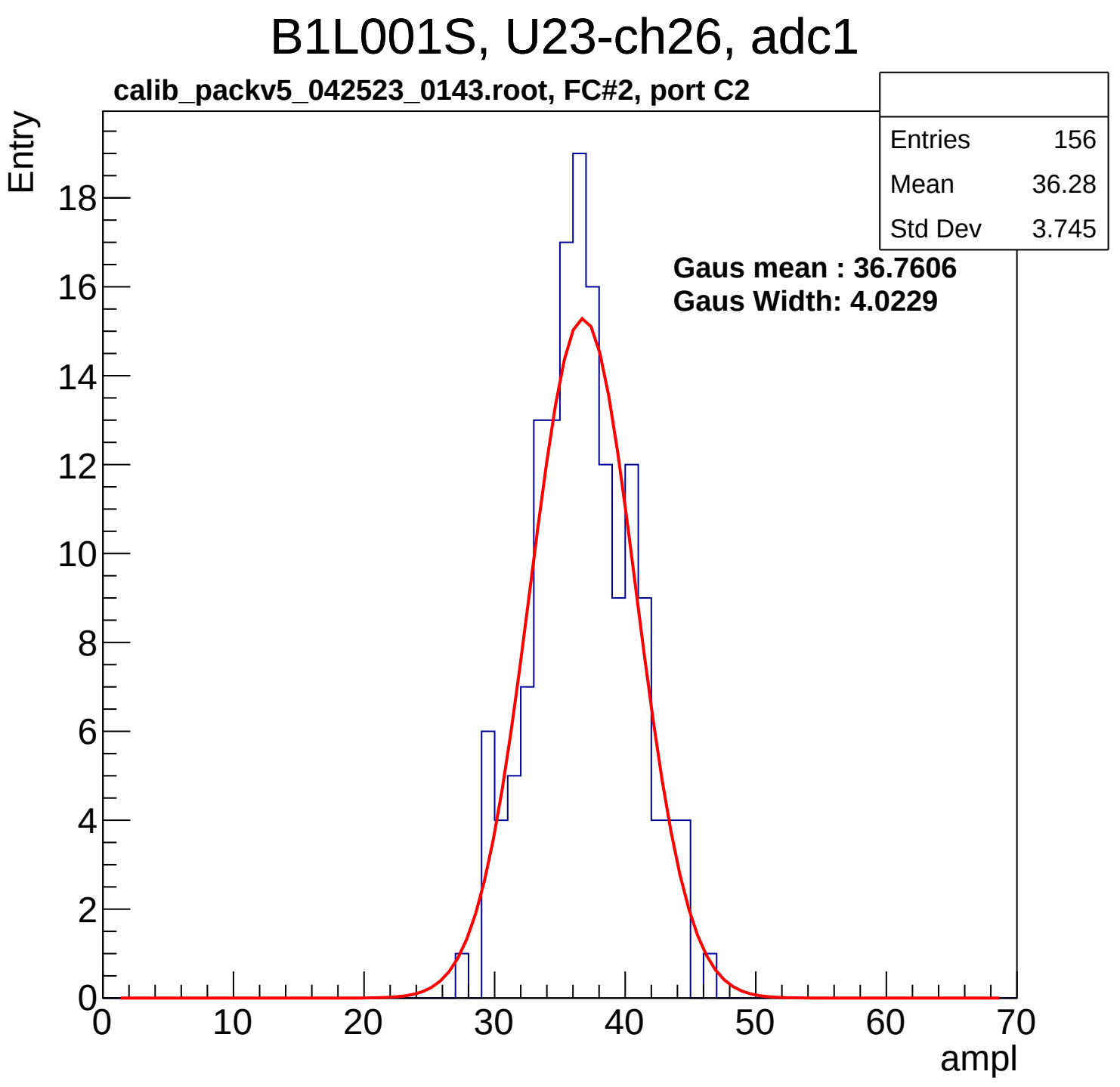
B1L001S, U23-ch26, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 36.28
Std Dev 3.745
Gaus mean : 36.7606
Gaus Width: 4.0229
Entry
ampl
0
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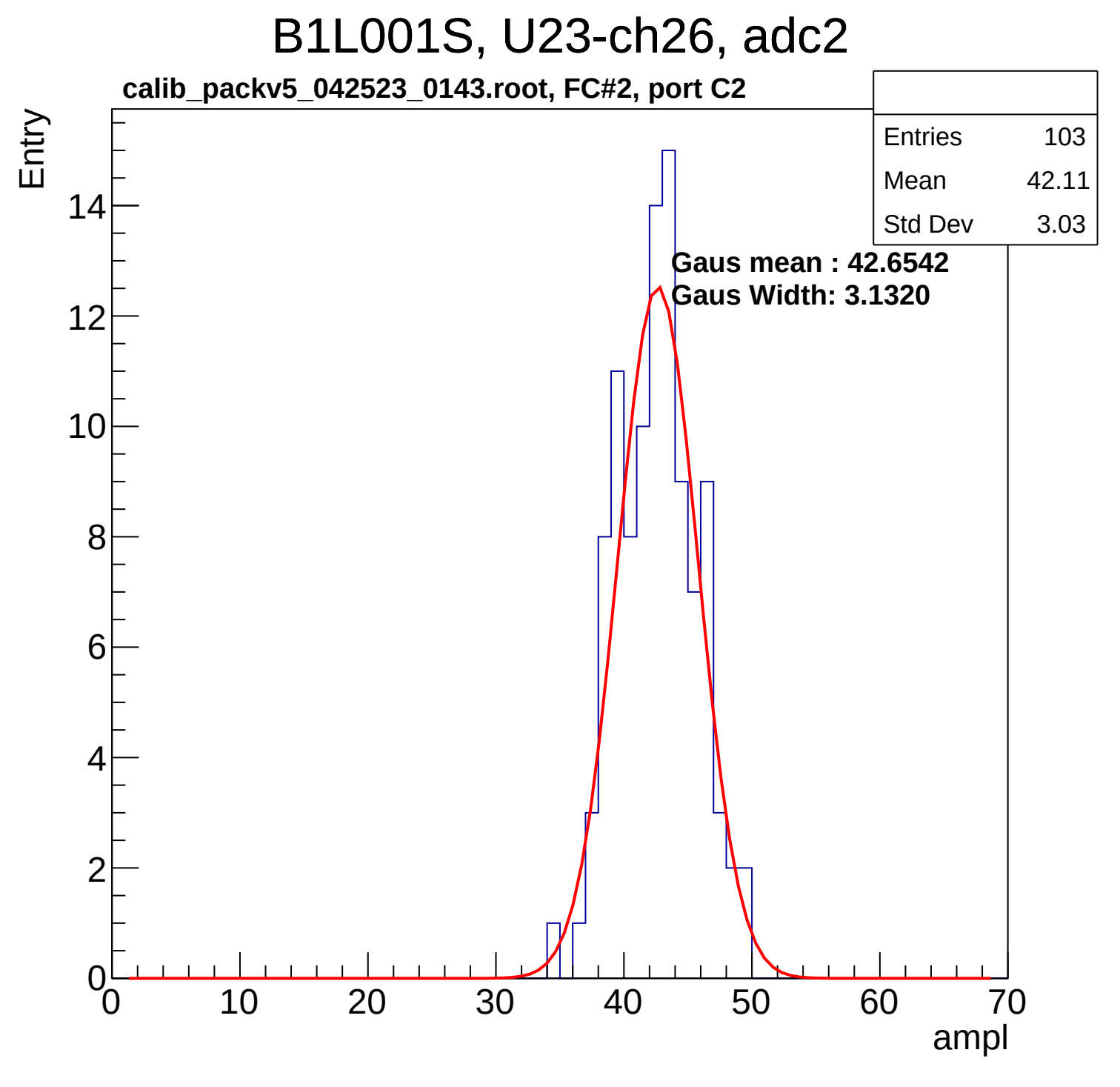
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch26, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 42.11
Std Dev 3.03
Gaus mean : 42.6542
Gaus Width: 3.1320
Entry
ampl
0
2
4
6
8
10
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0
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60
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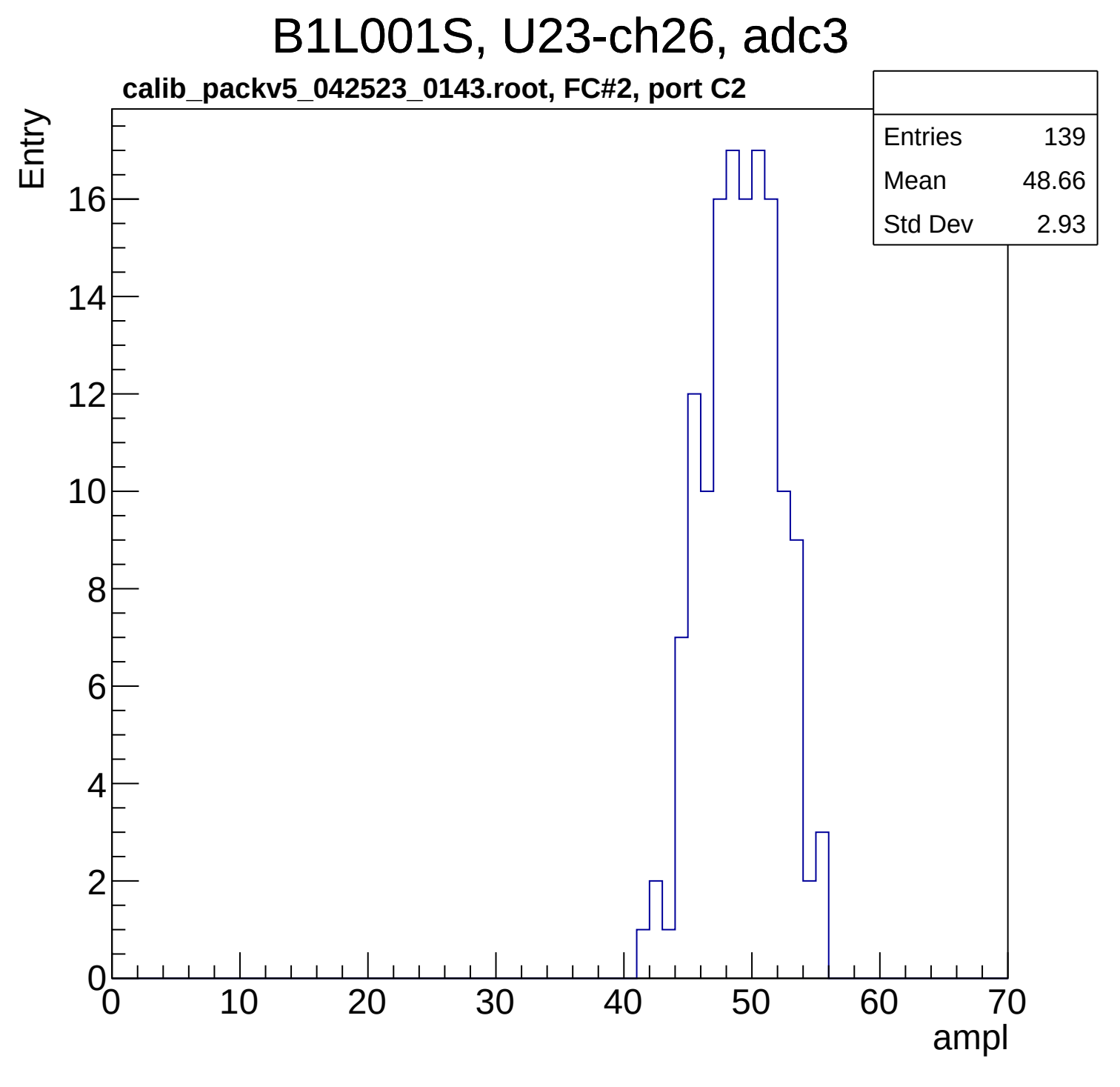
B1L001S, U23-ch26, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 48.66
Std Dev 2.93
Entry
ampl
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6
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10
12
14
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0
10
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60
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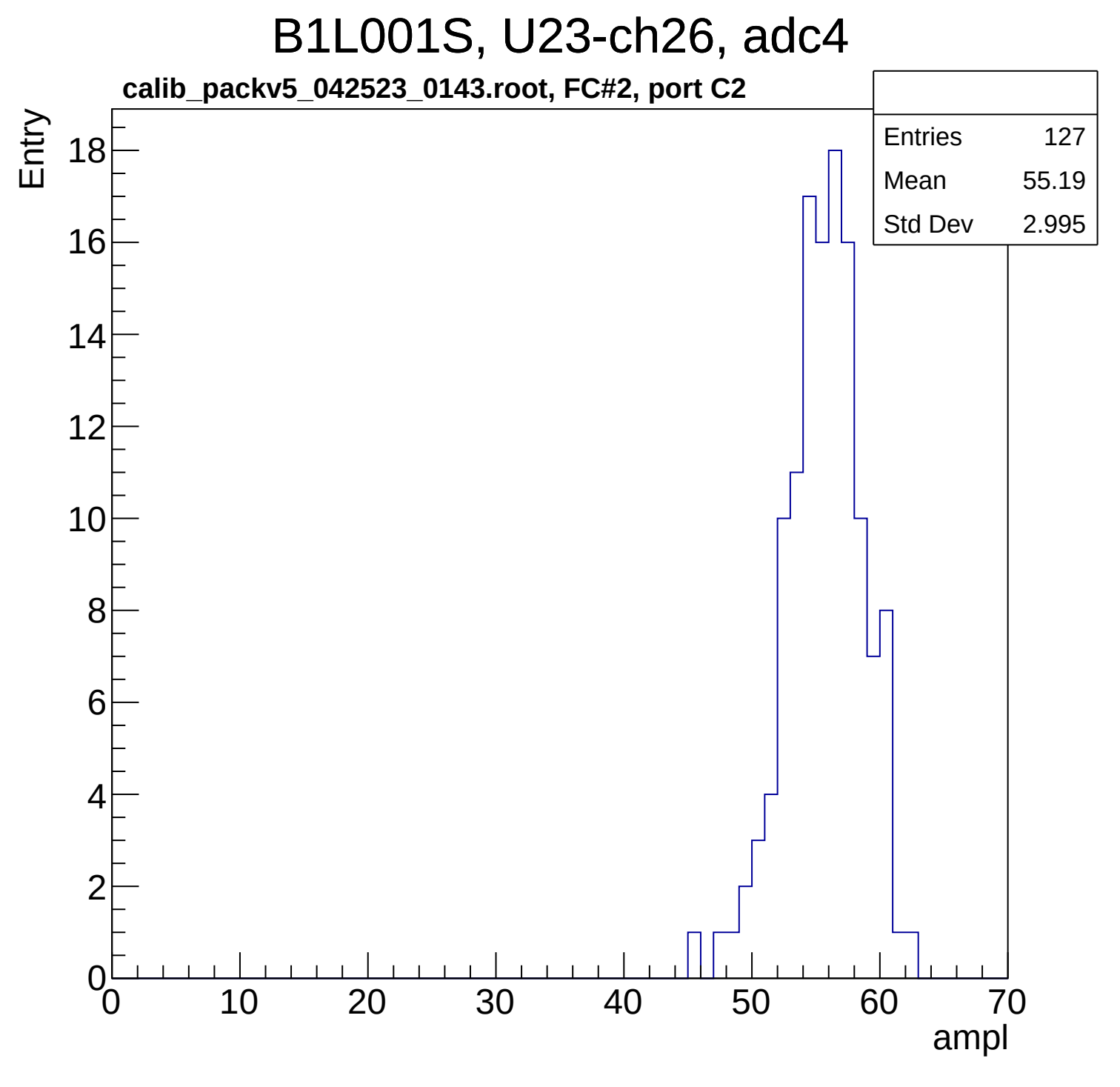
B1L001S, U23-ch26, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 55.19
Std Dev 2.995
Entry
ampl
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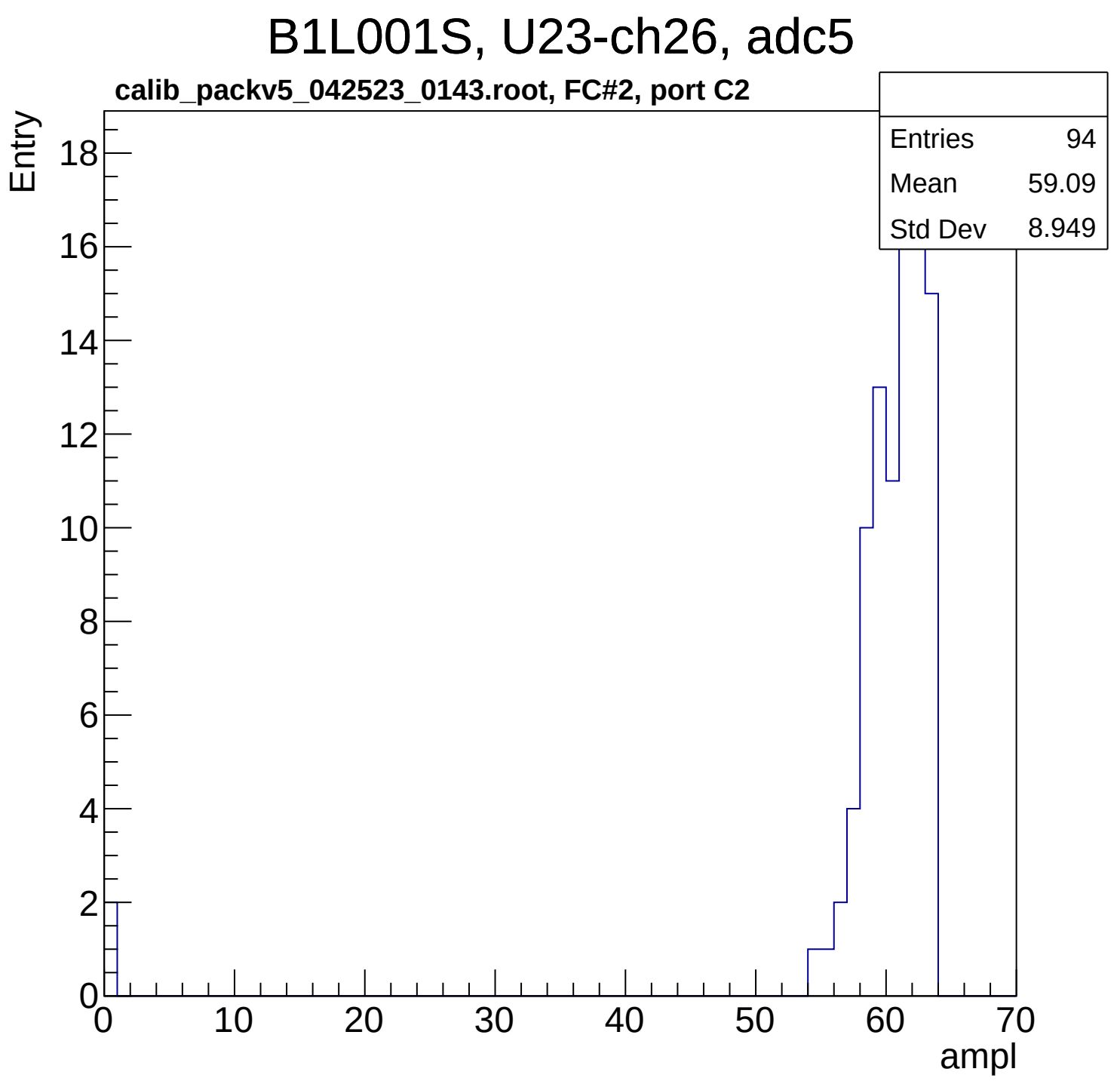
B1L001S, U23-ch26, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.09
Std Dev 8.949
Entry
ampl
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6
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10
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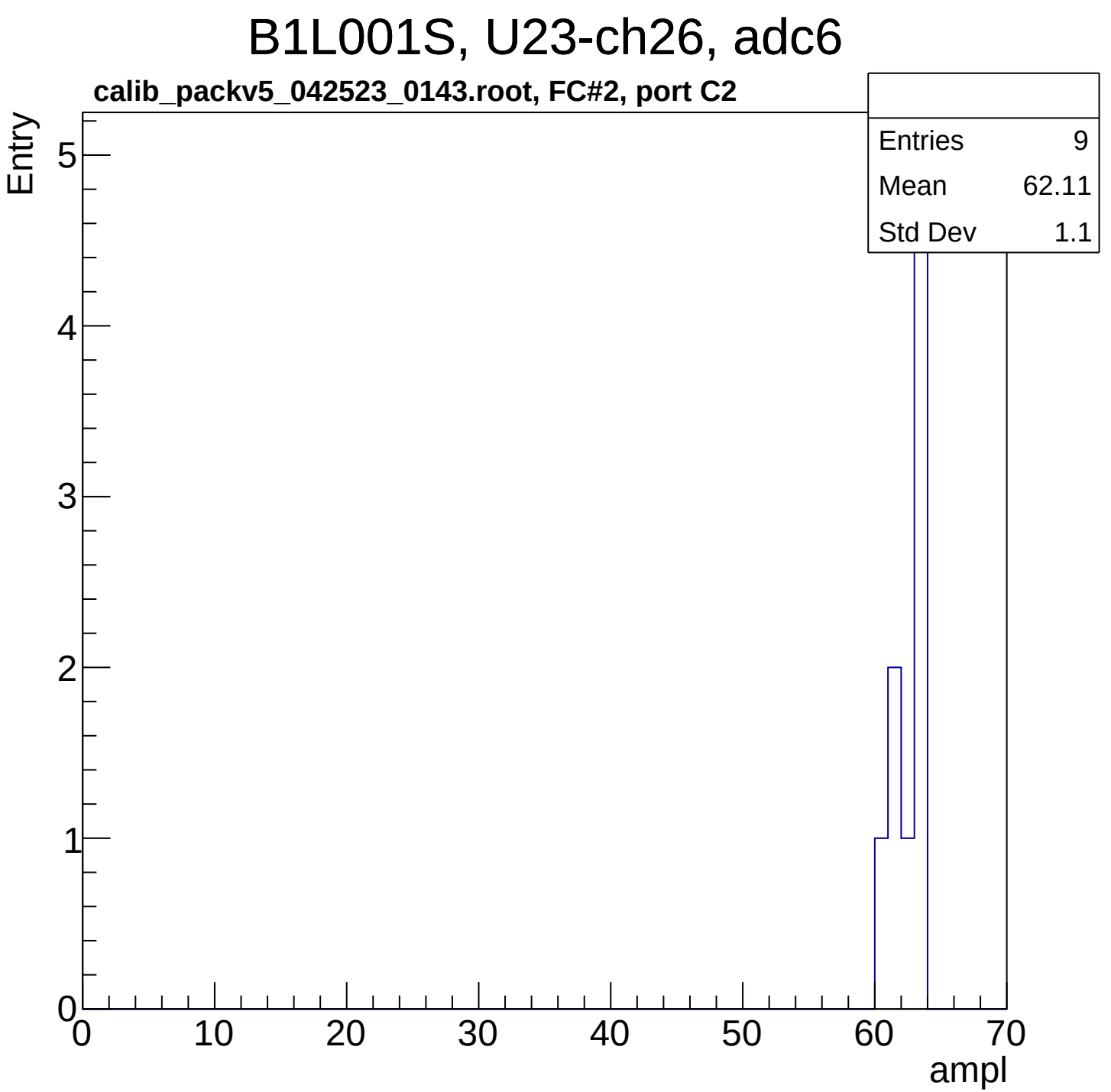
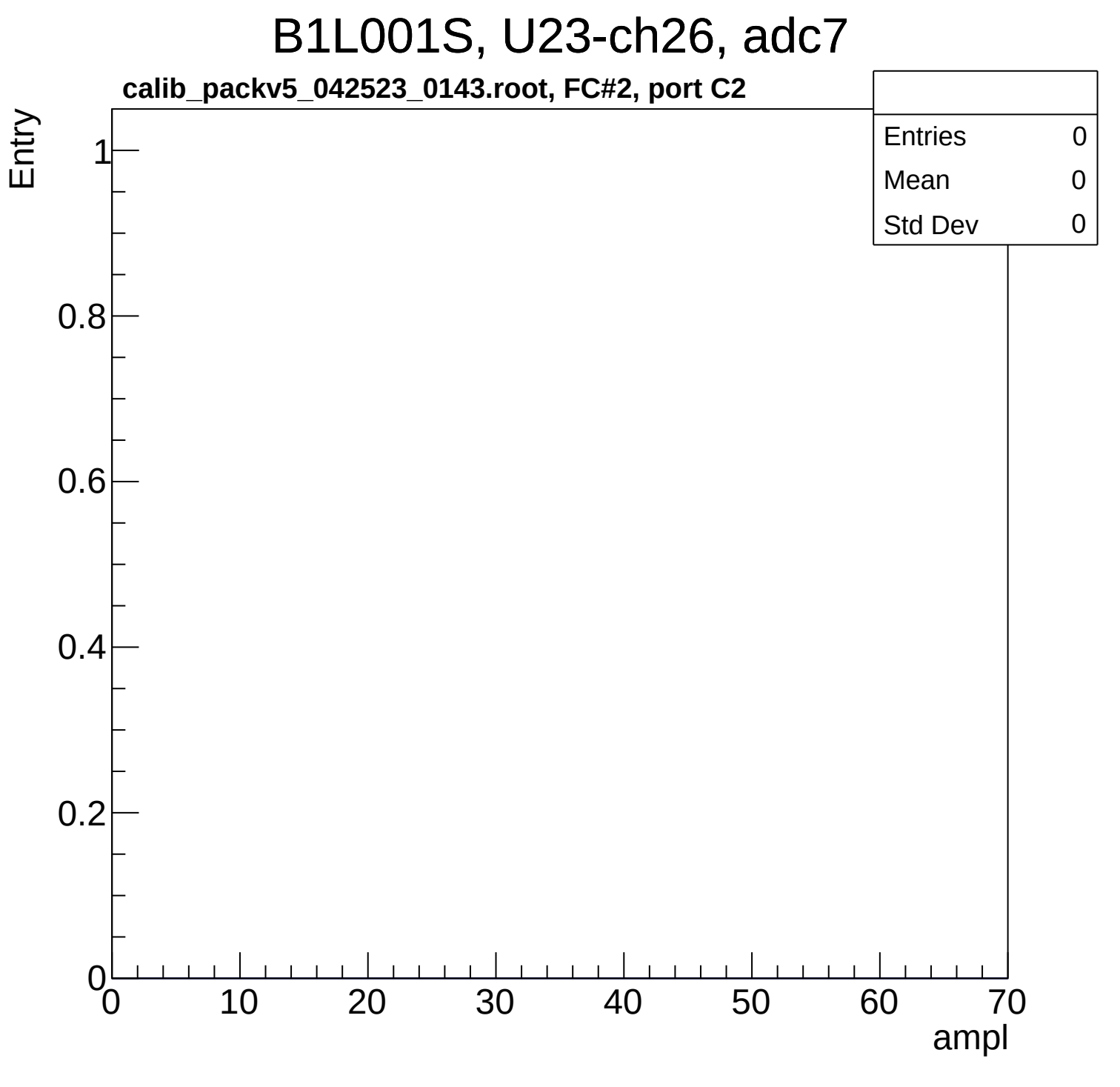

B1L001S, U23-ch26, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 62.11
Std Dev 1.1
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch26, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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10
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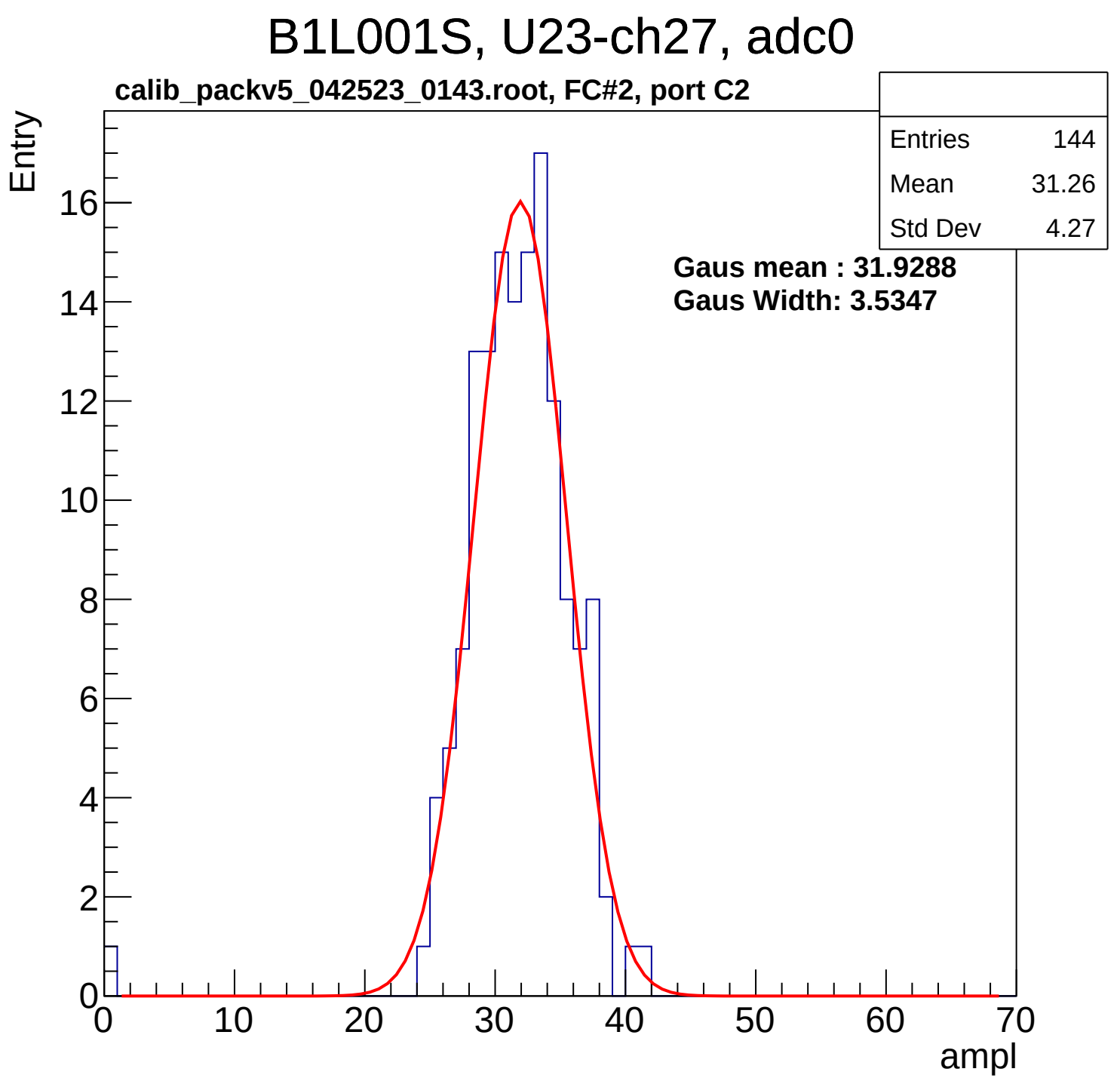
40
50
60
70
ampl


B1L001S, U23-ch27, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 31.26
Std Dev 4.27
Gaus mean : 31.9288
Gaus Width: 3.5347
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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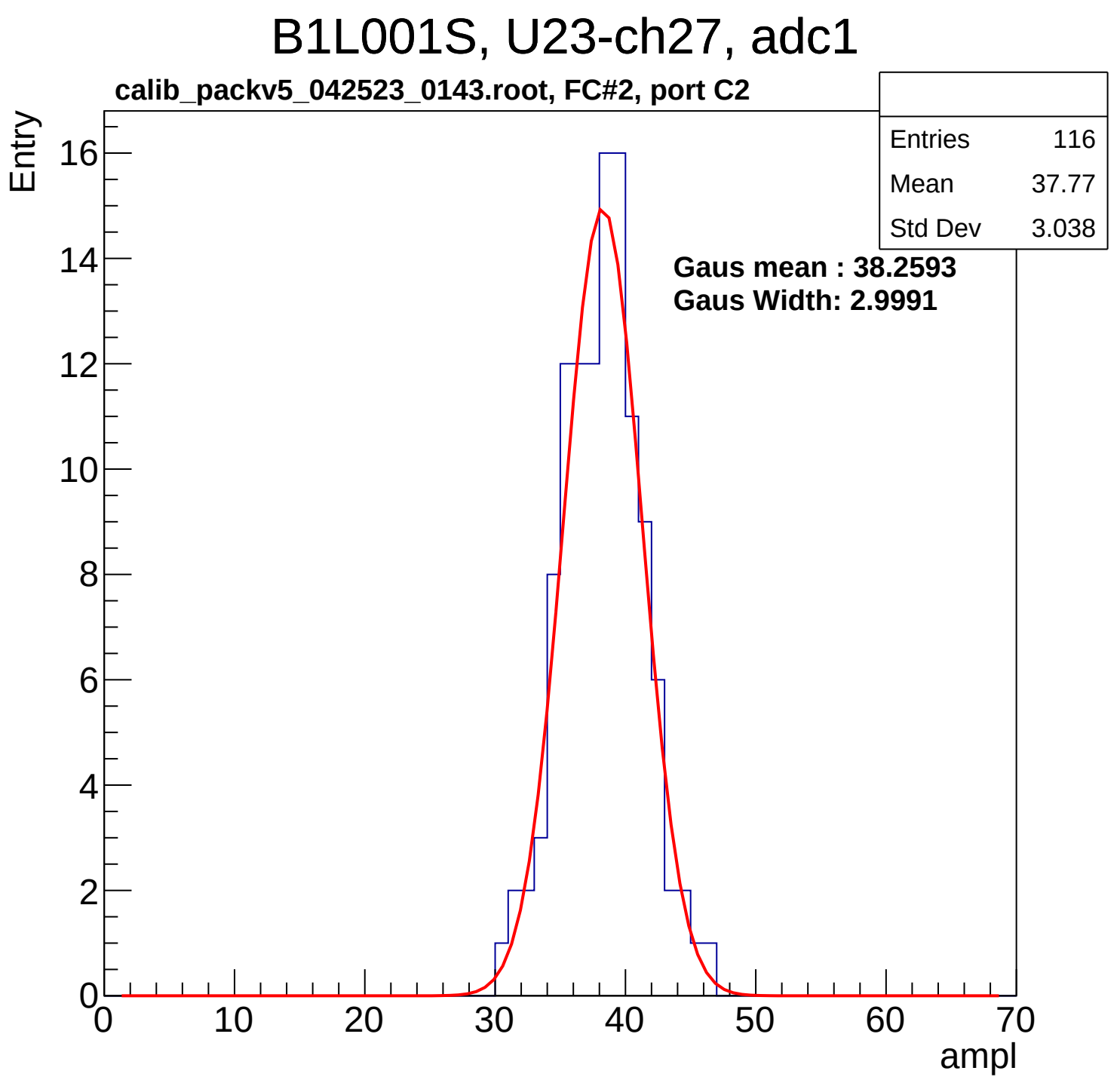
B1L001S, U23-ch27, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 37.77
Std Dev 3.038
Gaus mean : 38.2593
Gaus Width: 2.9991
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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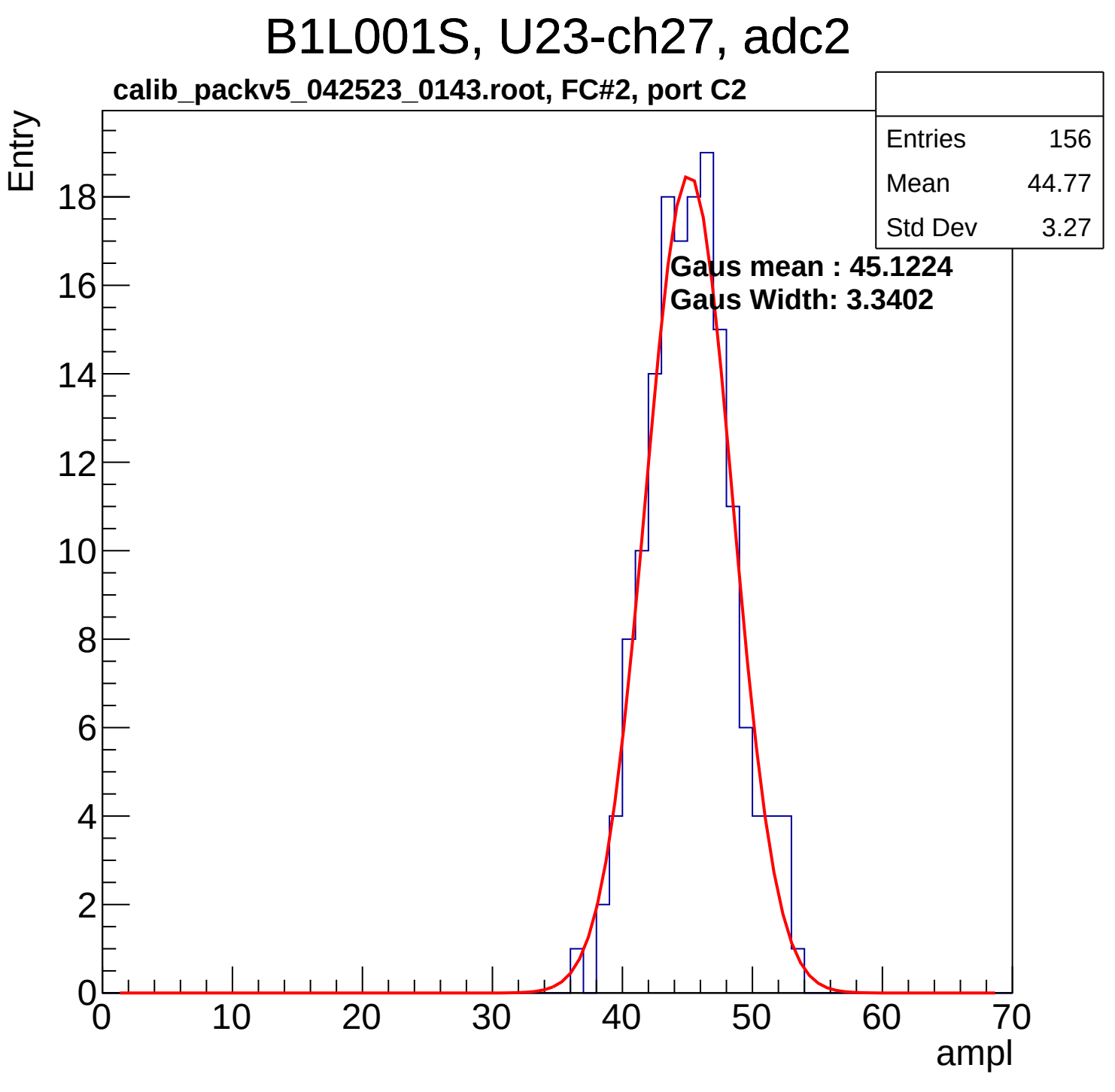
B1L001S, U23-ch27, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 44.77
Std Dev 3.27
Gaus mean : 45.1224
Gaus Width: 3.3402
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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60
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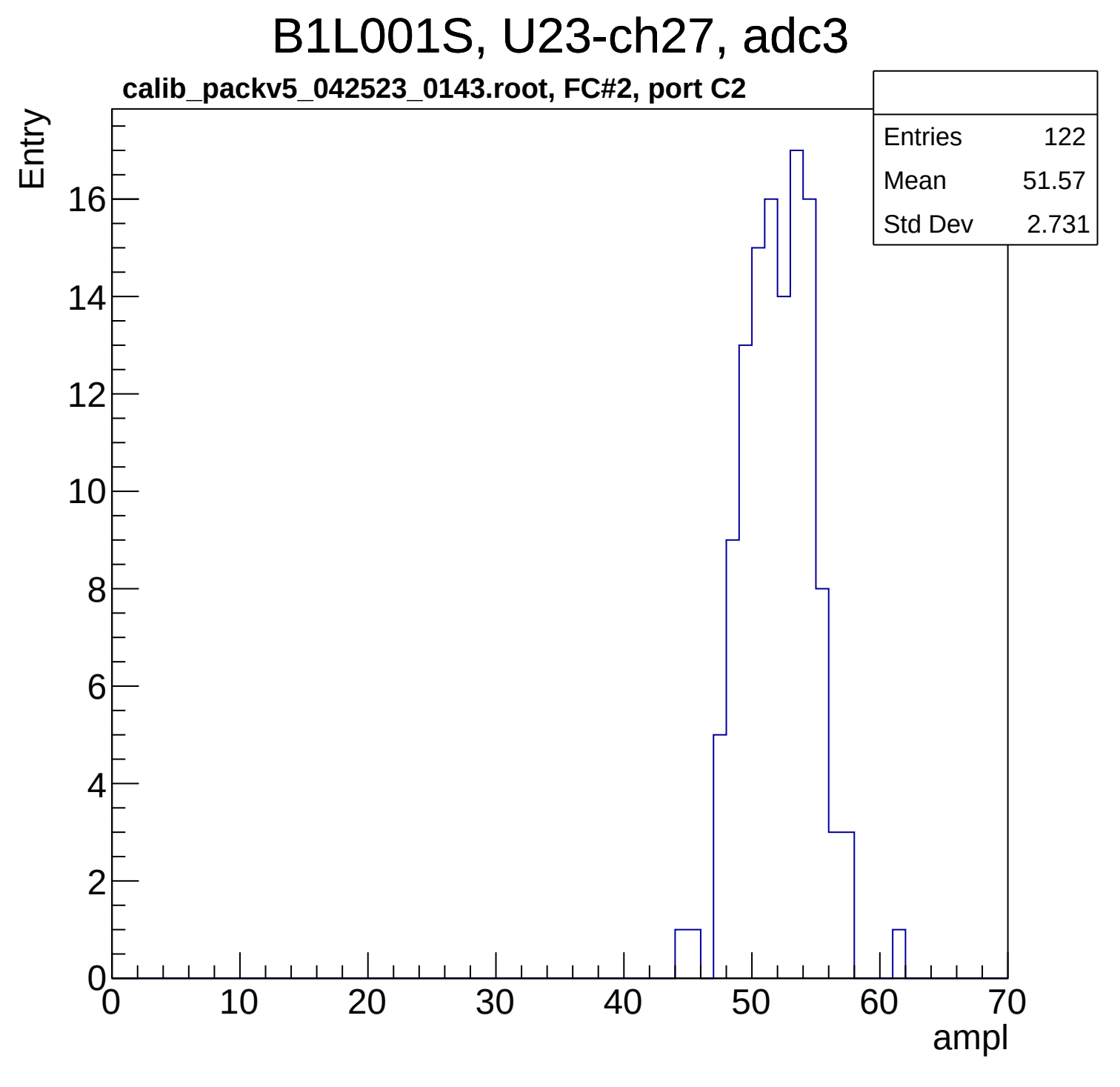
B1L001S, U23-ch27, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 51.57
Std Dev 2.731
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
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40
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60
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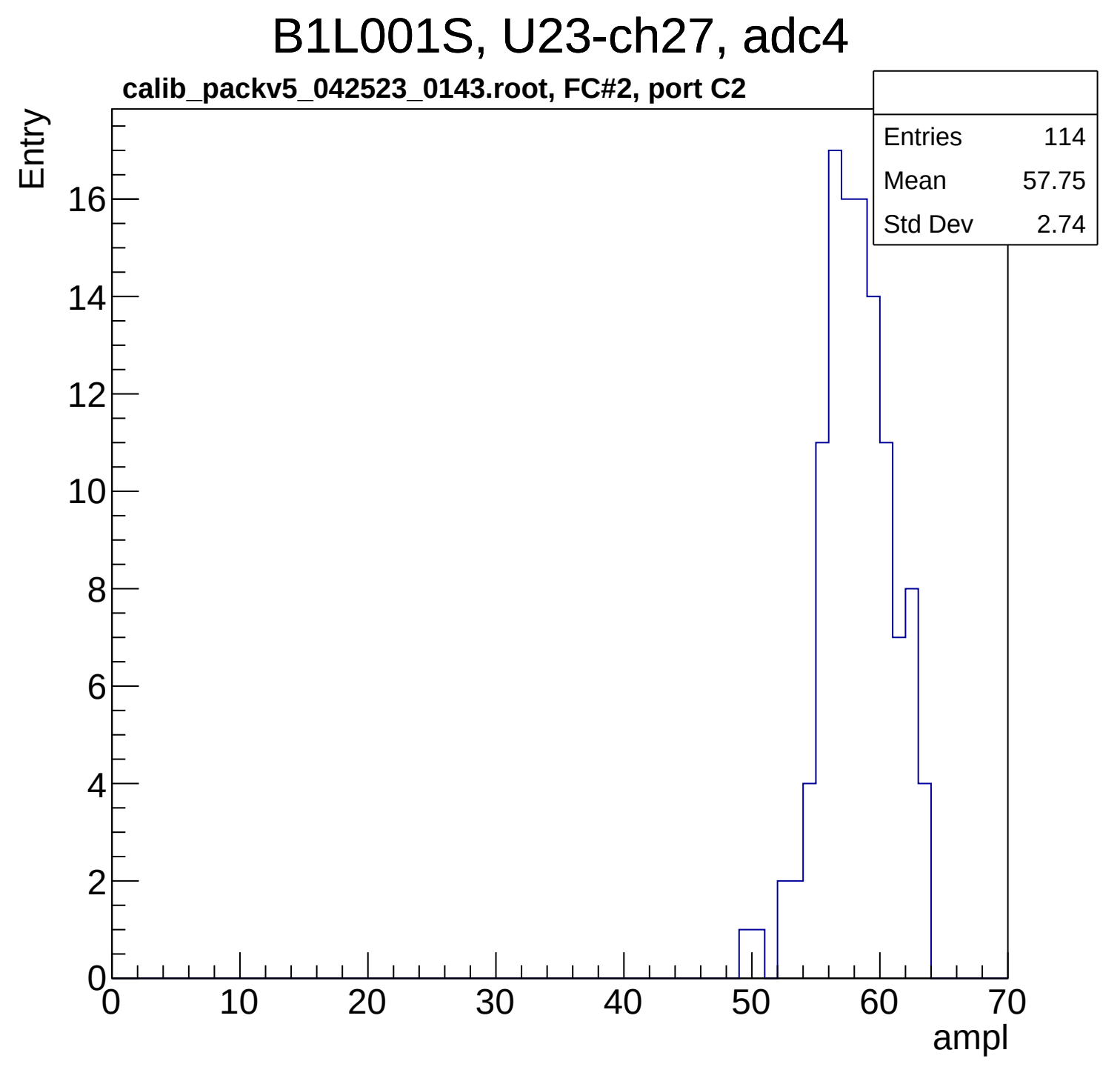
B1L001S, U23-ch27, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 57.75
Std Dev 2.74
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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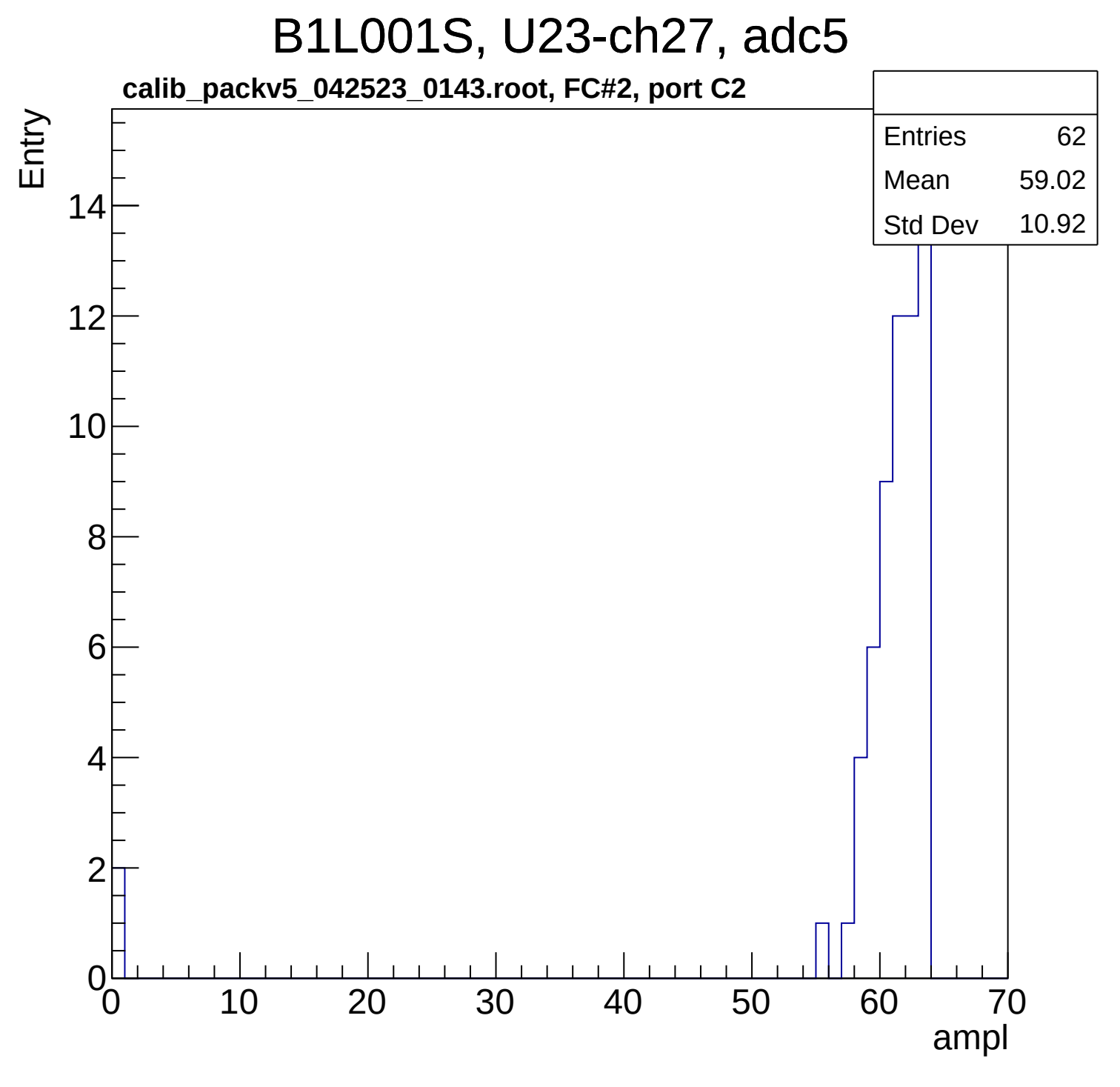
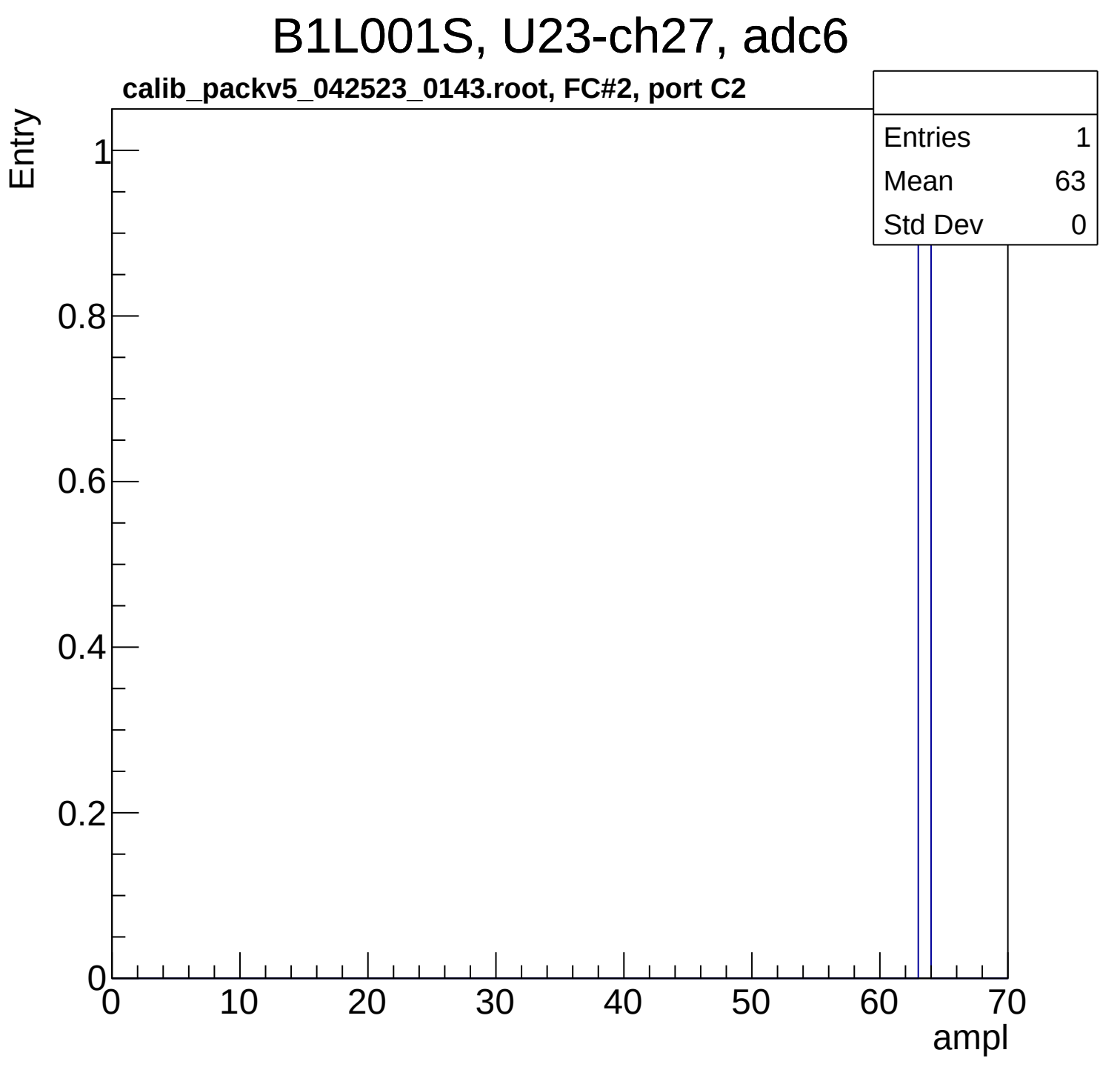
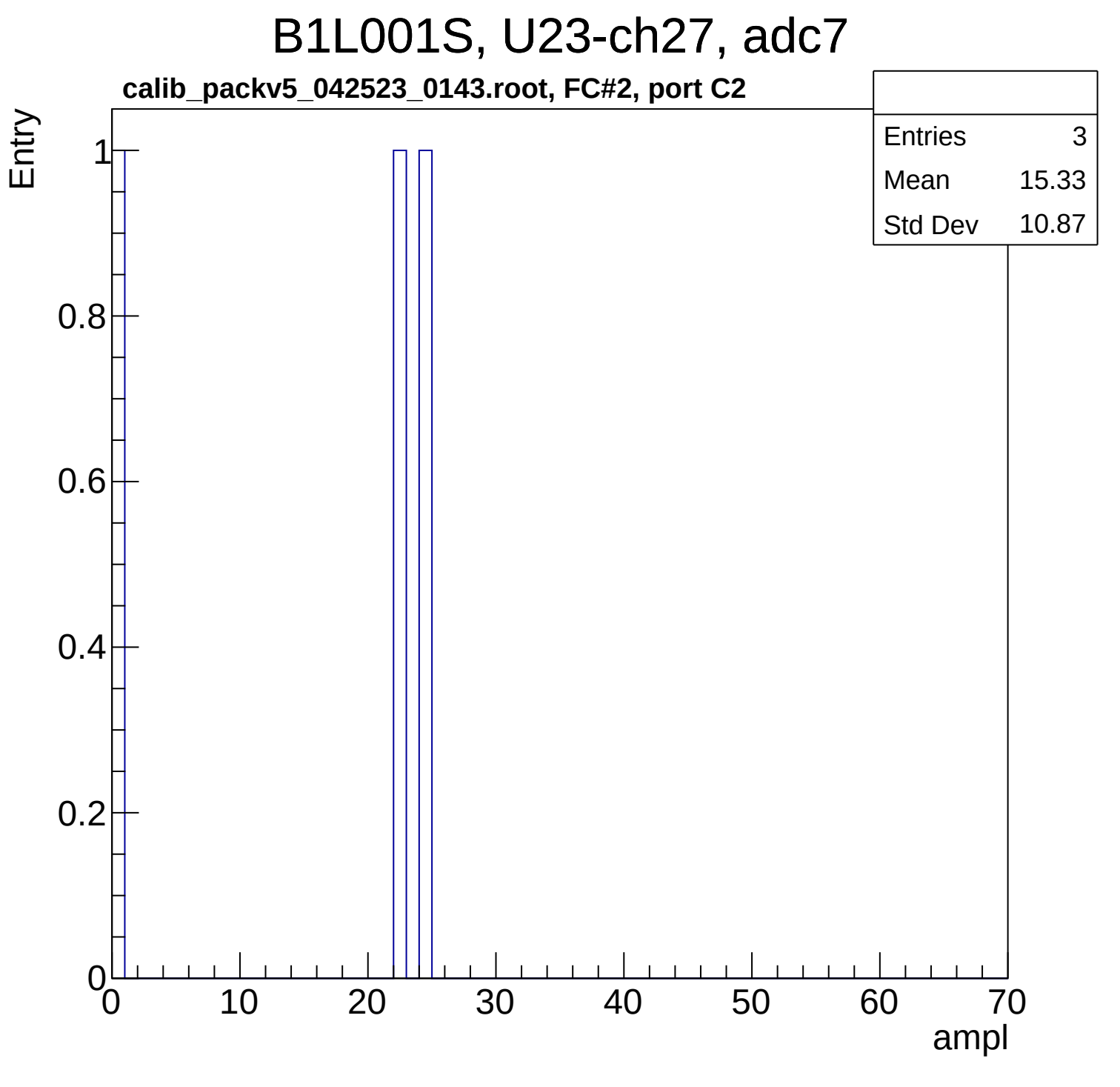
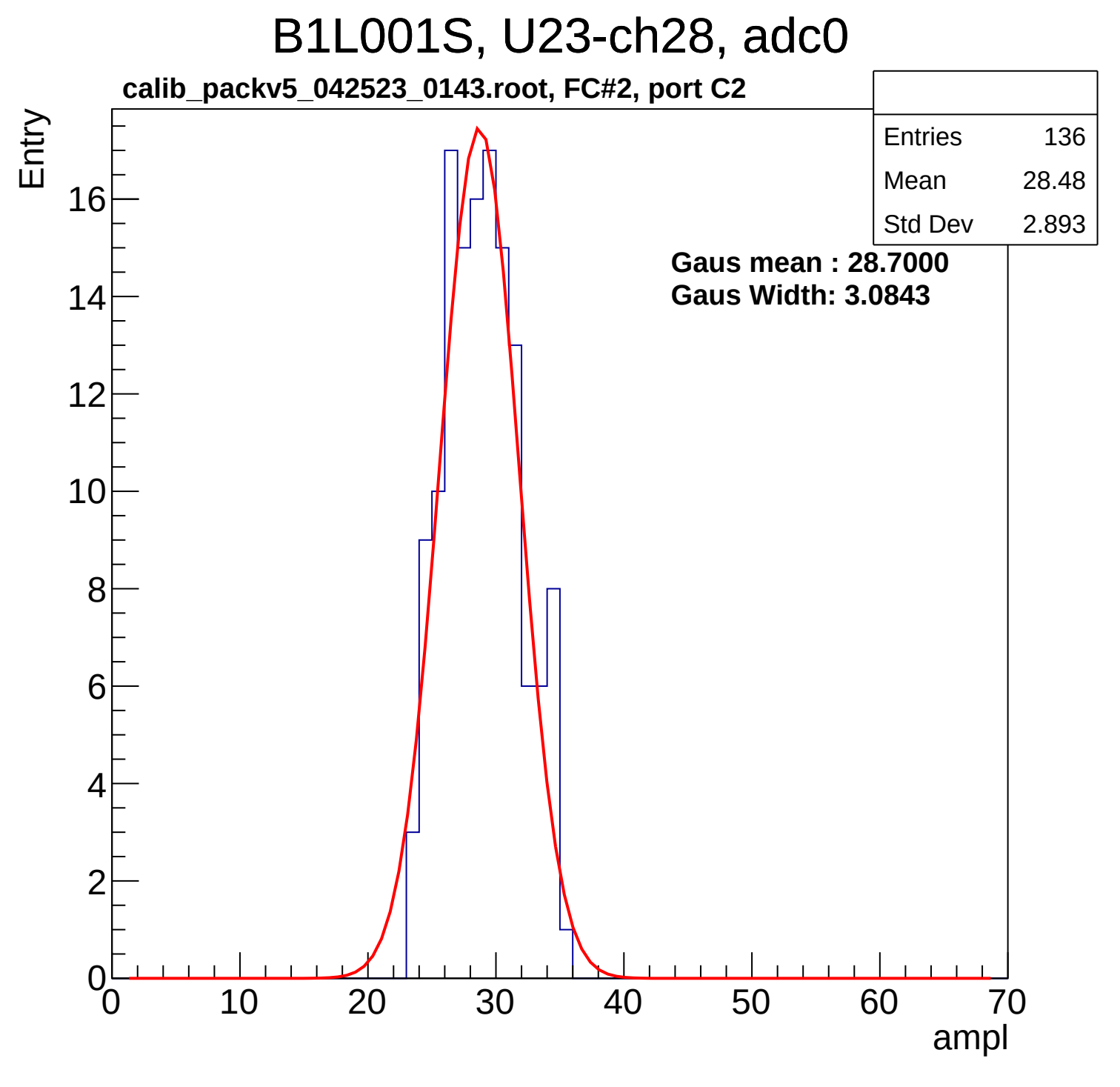
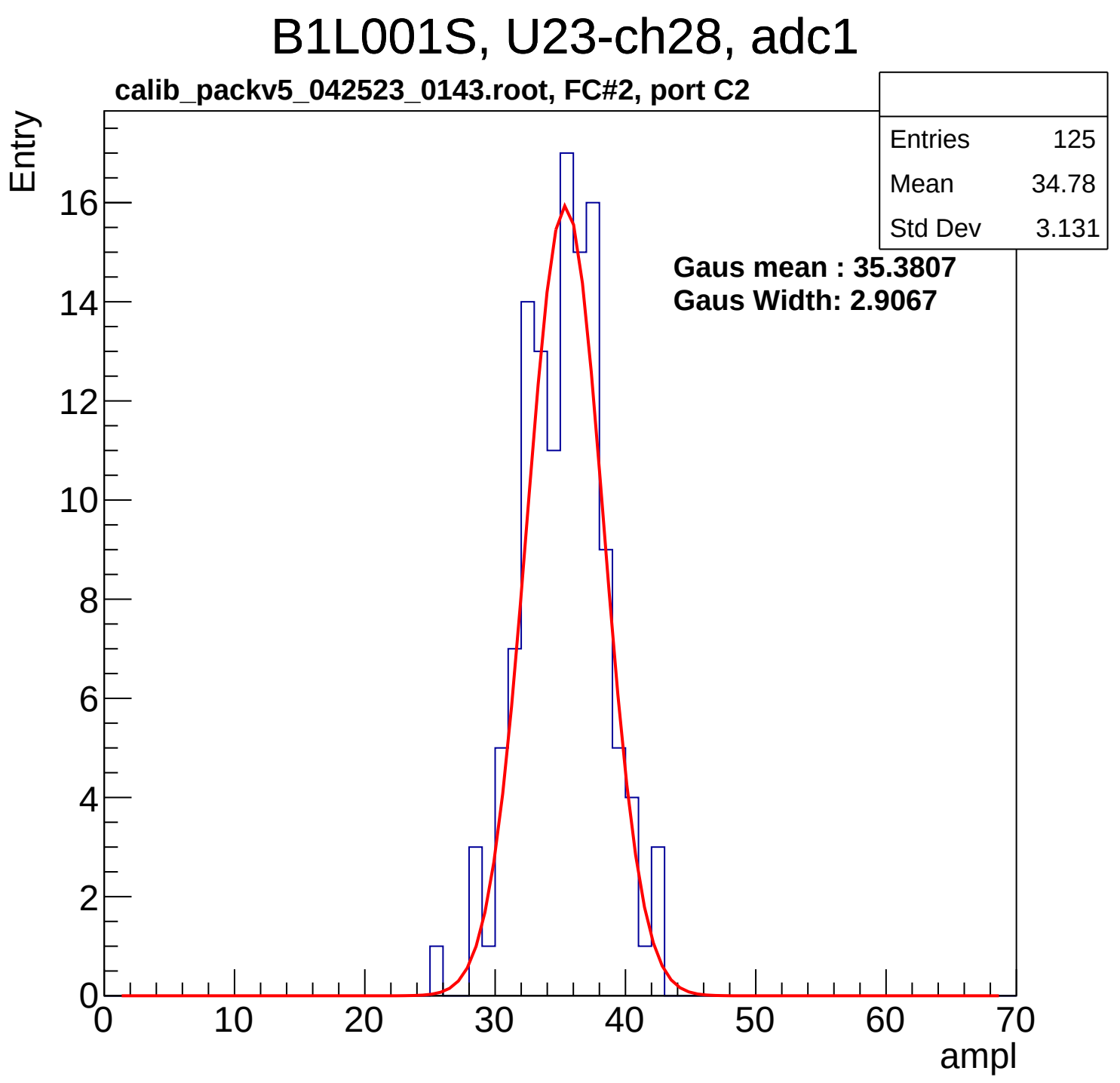

B1L001S, U23-ch27, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 62
Mean 59.02
Std Dev 10.92
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch27, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch27, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 15.33
Std Dev 10.87
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 28.48
Std Dev 2.893
Gaus mean : 28.7000
Gaus Width: 3.0843
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 34.78
Std Dev 3.131
Gaus mean : 35.3807
Gaus Width: 2.9067
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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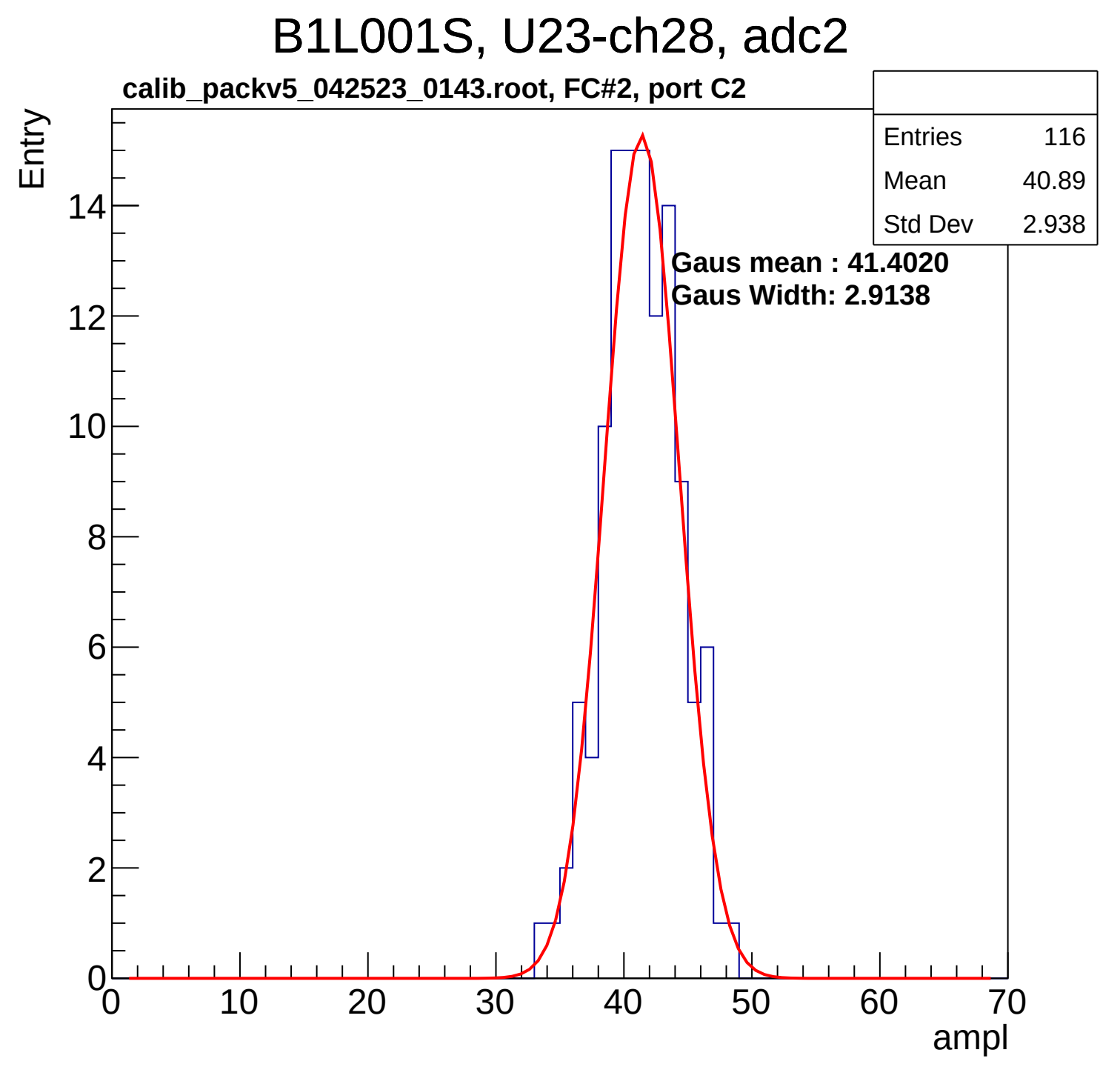
B1L001S, U23-ch28, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 40.89
Std Dev 2.938
Gaus mean : 41.4020
Gaus Width: 2.9138
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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60
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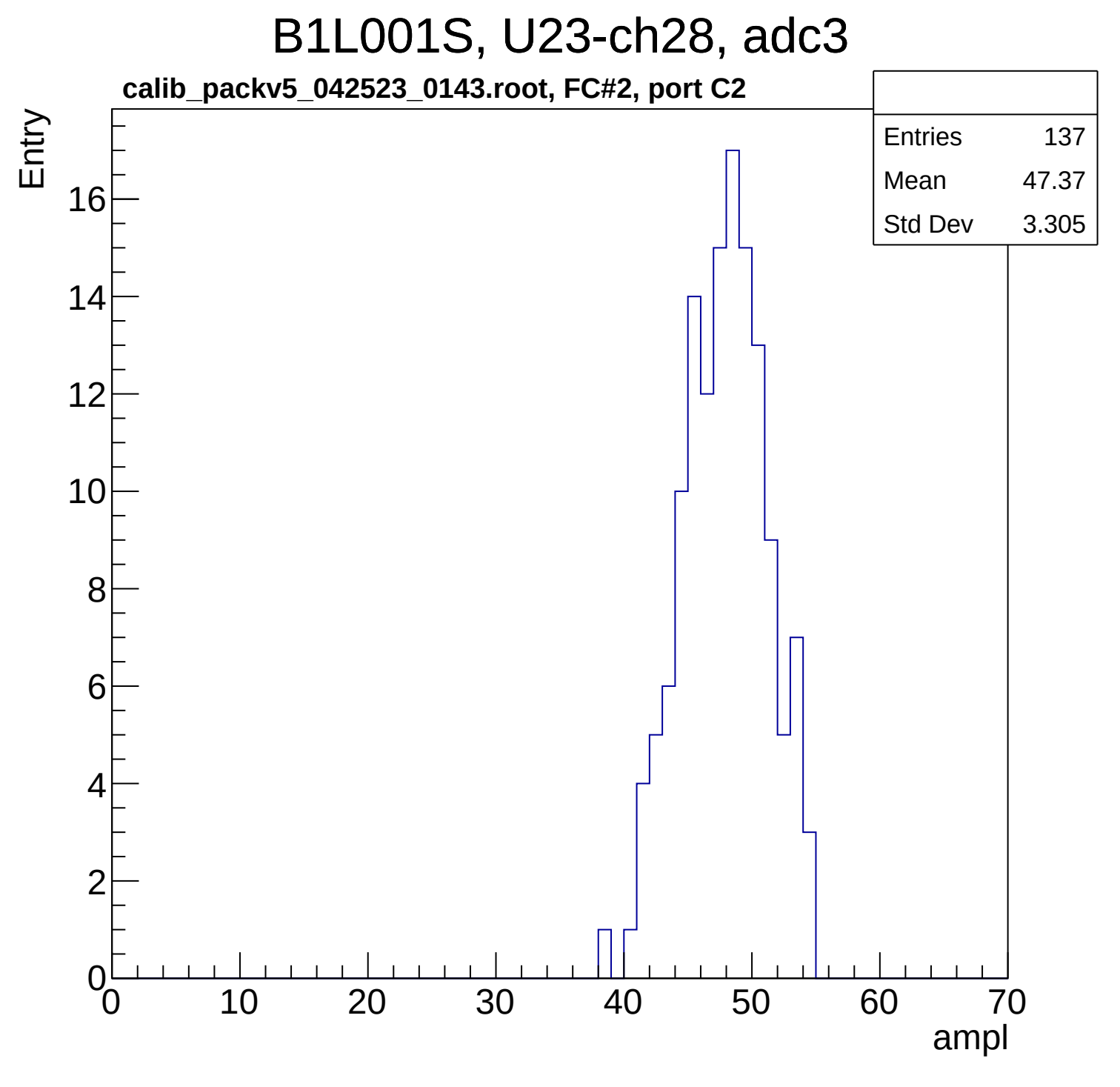
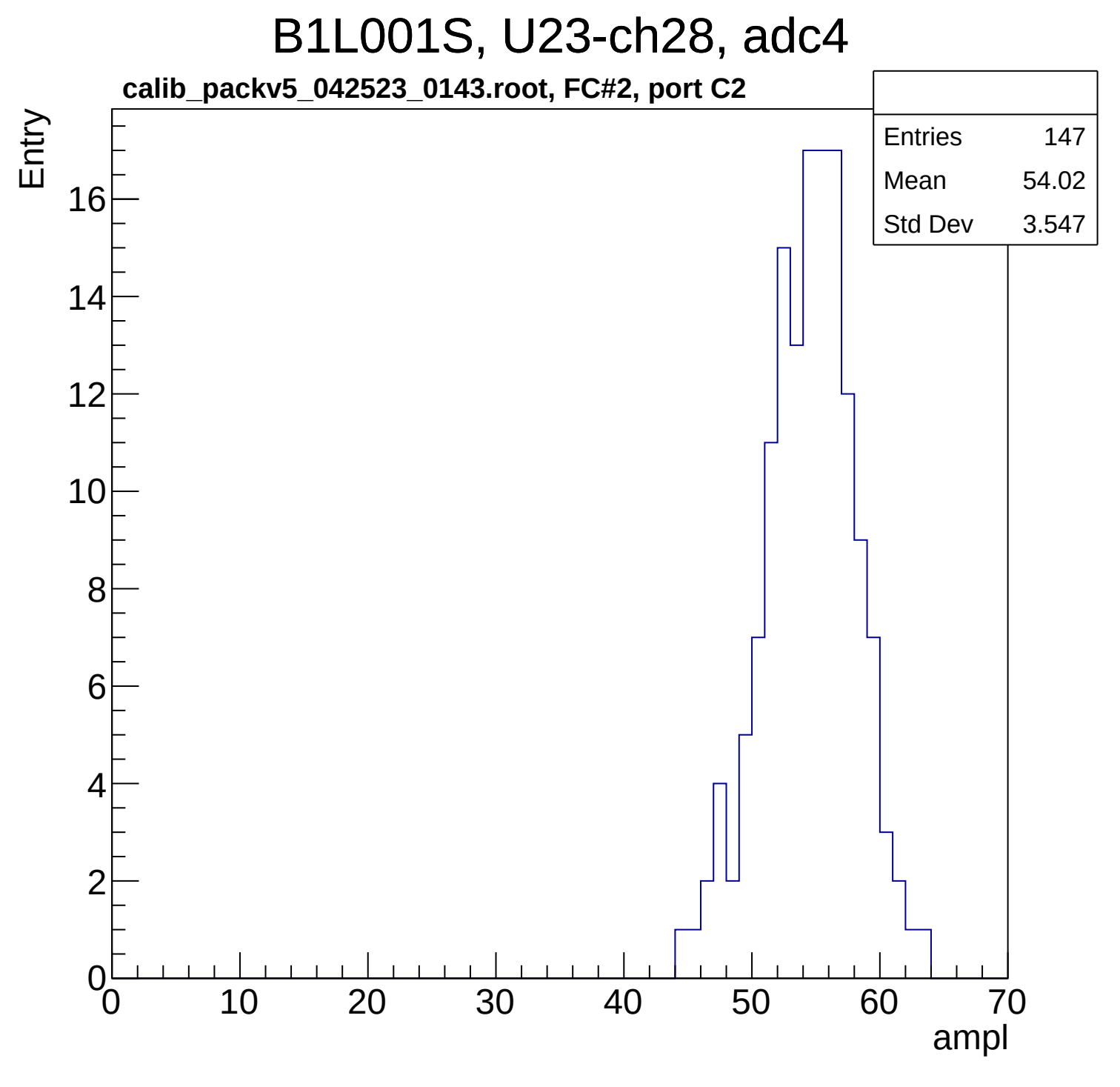
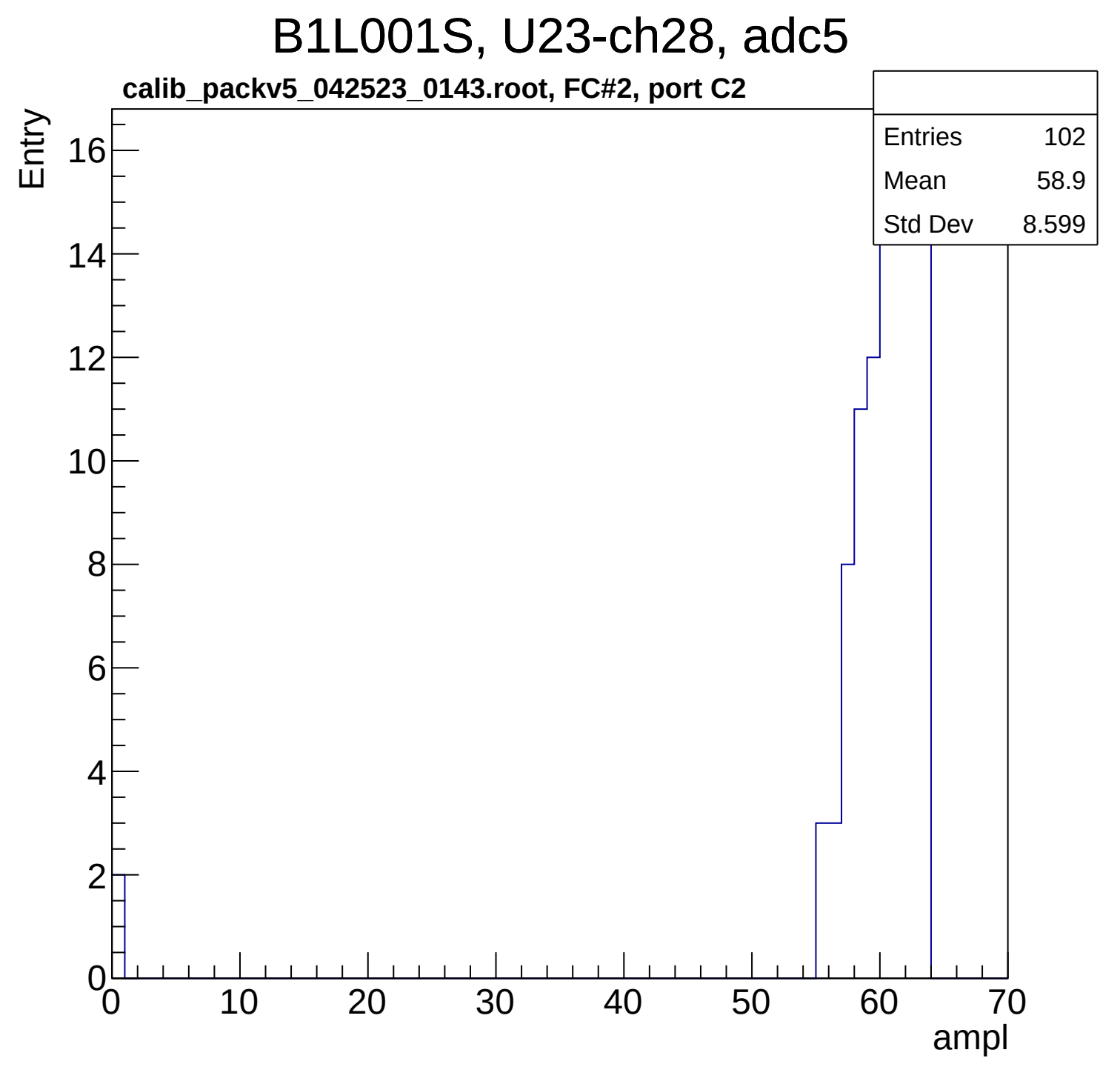

B1L001S, U23-ch28, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 47.37
Std Dev 3.305
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 54.02
Std Dev 3.547
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 58.9
Std Dev 8.599
Entry
ampl
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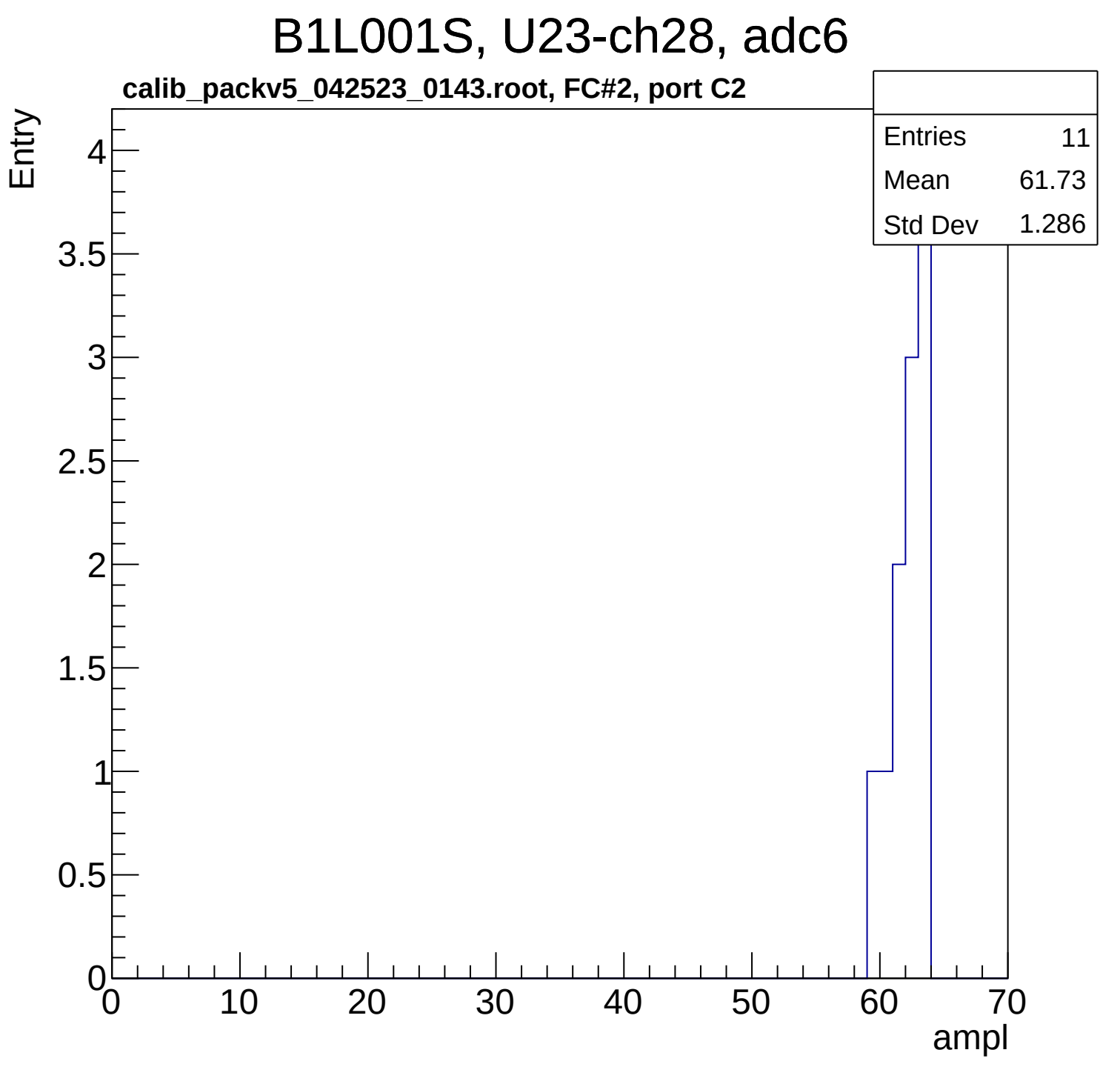
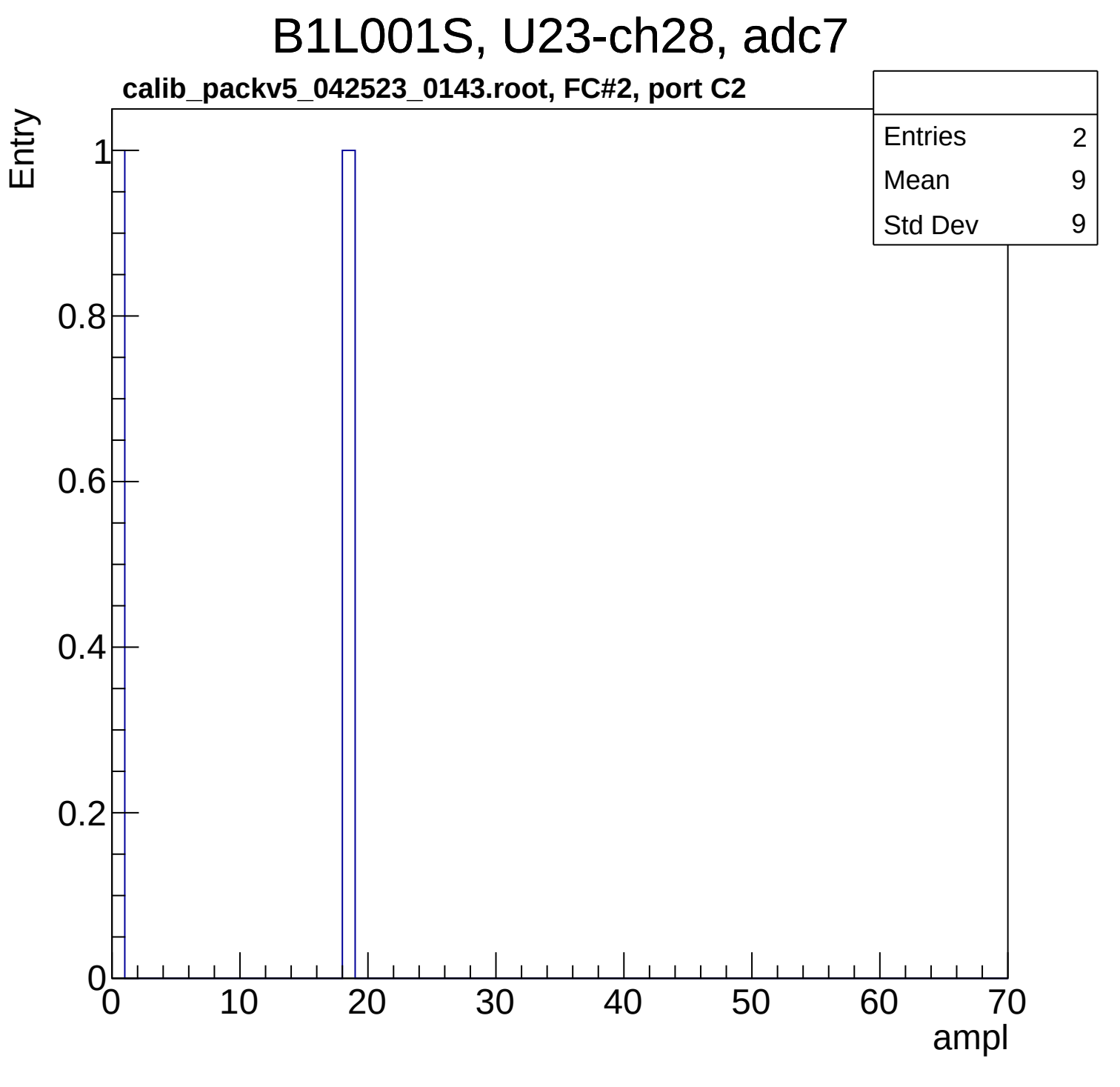
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 61.73
Std Dev 1.286
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch28, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 9
Std Dev 9
Entry
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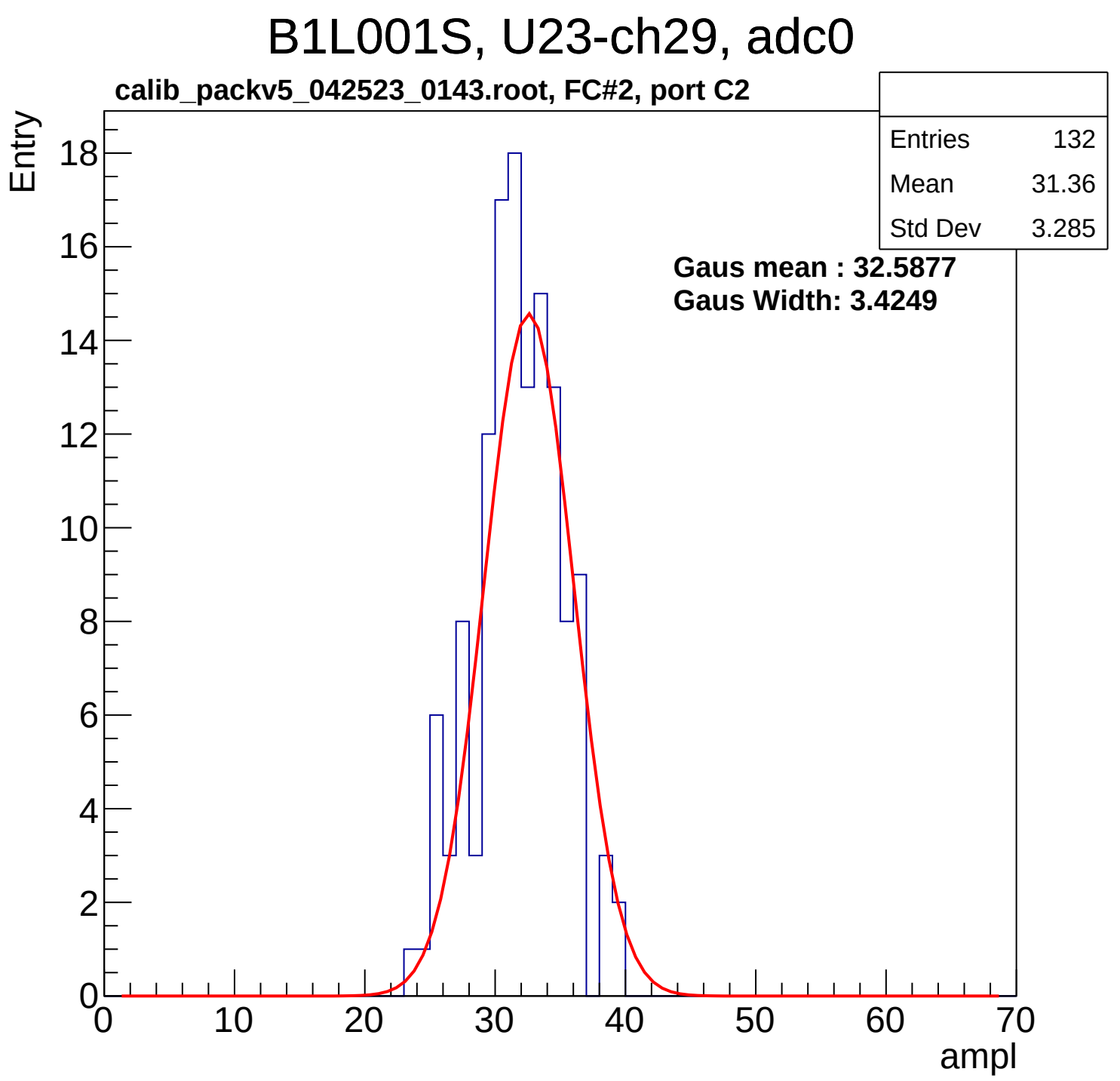
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch29, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 31.36
Std Dev 3.285
Gaus mean : 32.5877
Gaus Width: 3.4249
Entry
ampl
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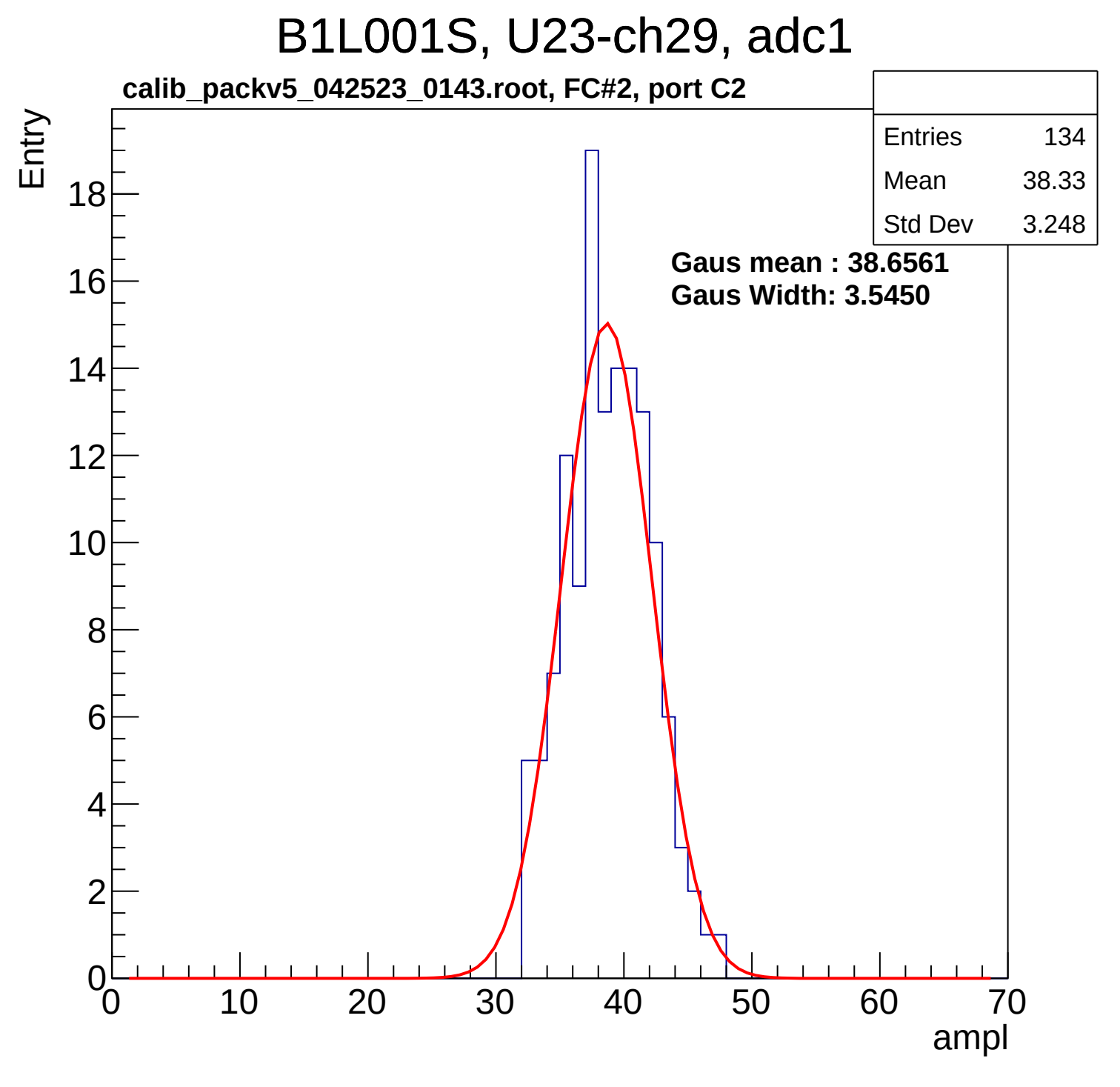
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch29, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 38.33
Std Dev 3.248
Gaus mean : 38.6561
Gaus Width: 3.5450
Entry
ampl
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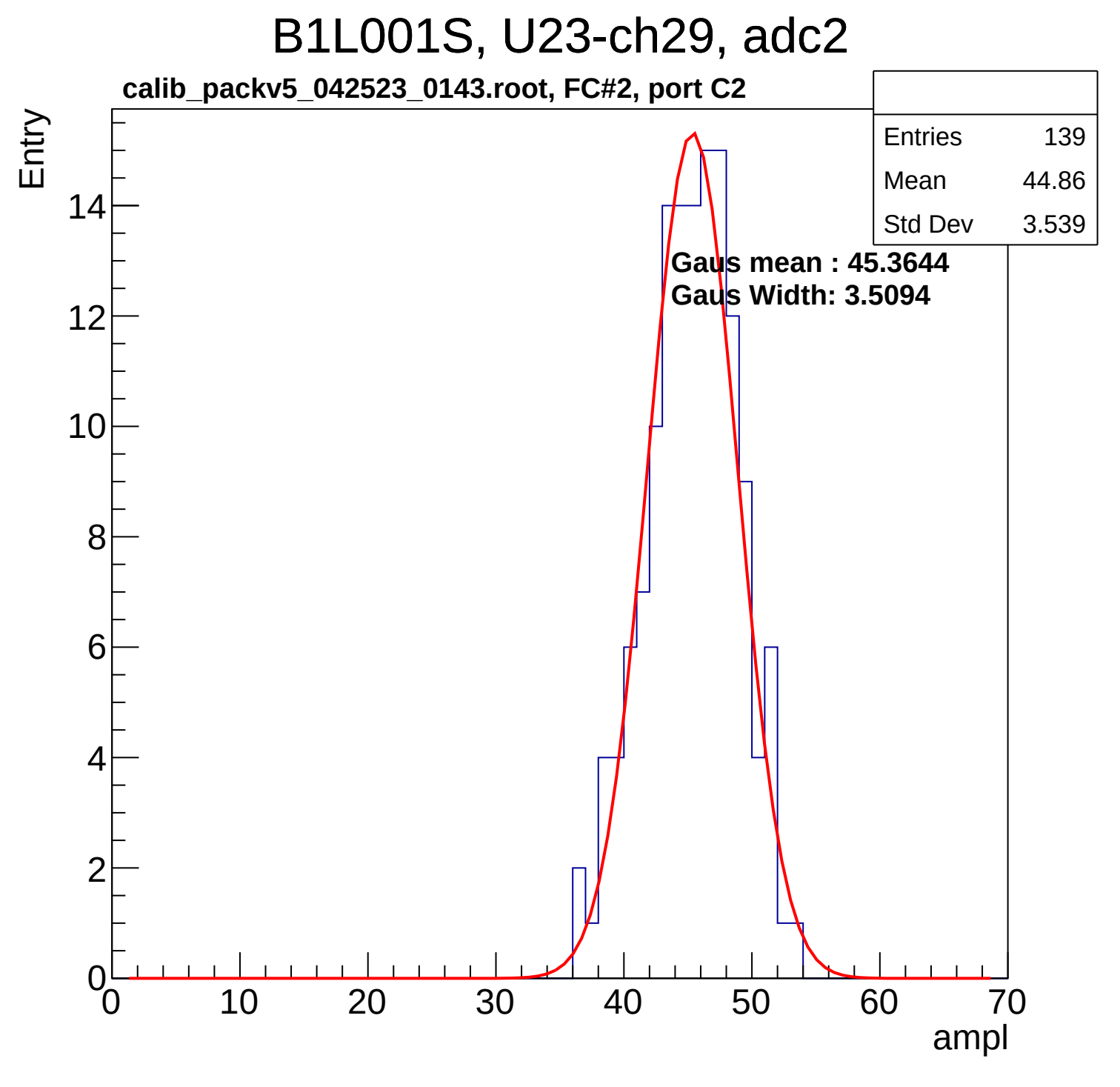
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch29, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 44.86
Std Dev 3.539
Gaus mean : 45.3644
Gaus Width: 3.5094
Entry
ampl
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2
4
6
8
10
12
14
0
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20
30
40
50
60
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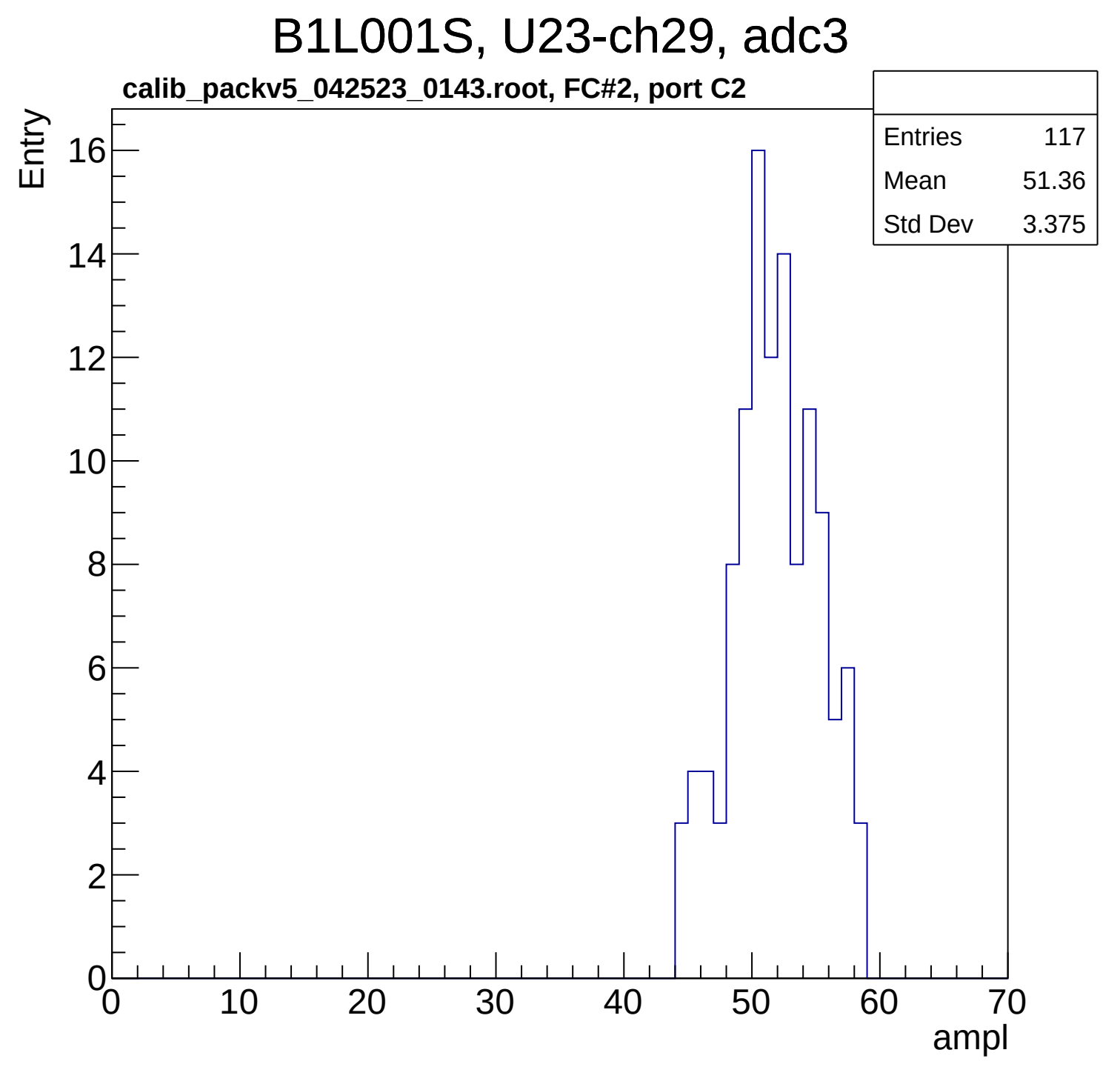
B1L001S, U23-ch29, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 51.36
Std Dev 3.375
Entry
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4
6
8
10
12
14
16
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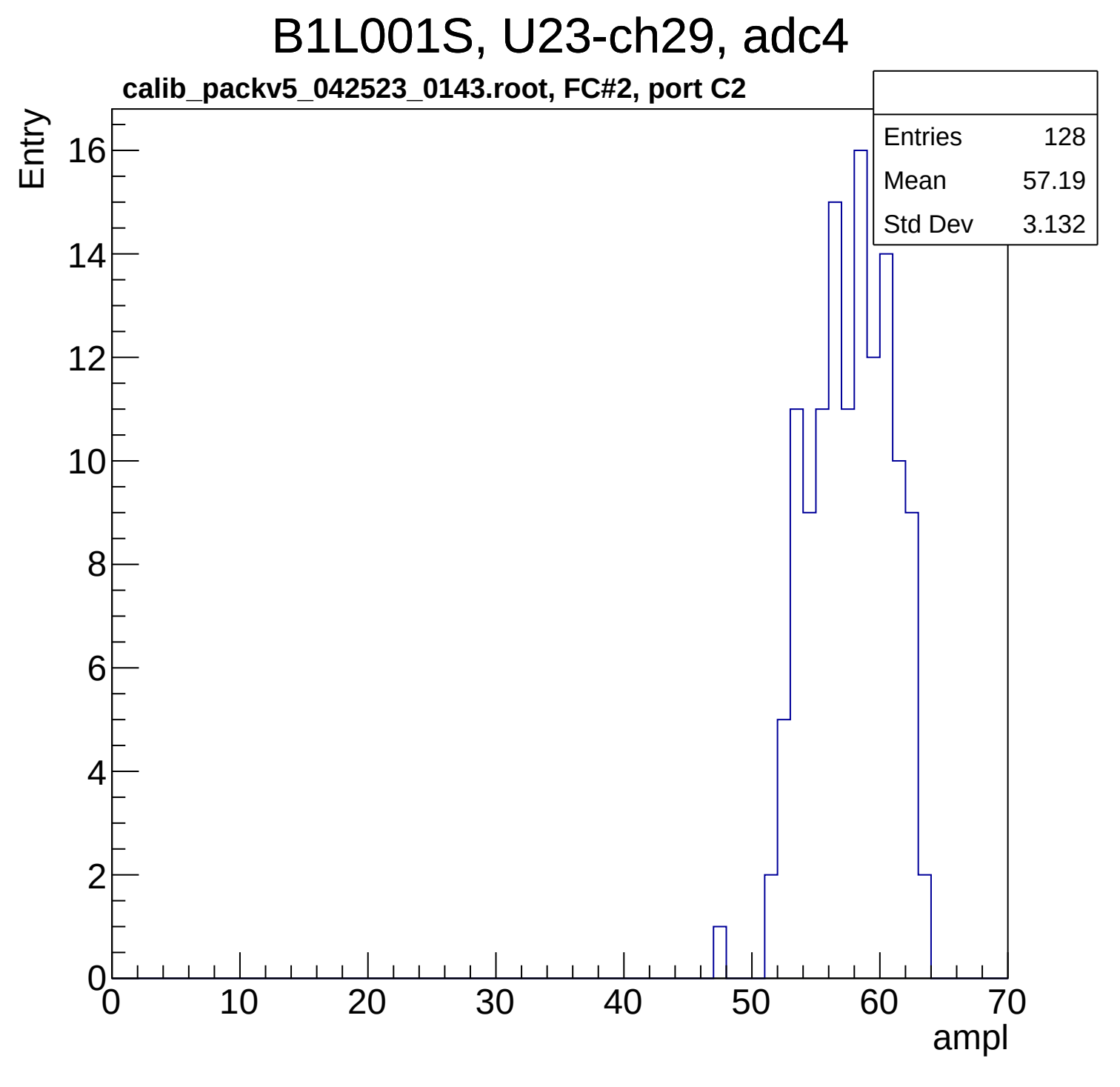
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch29, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 57.19
Std Dev 3.132
Entry
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4
6
8
10
12
14
16
0
10
20
30
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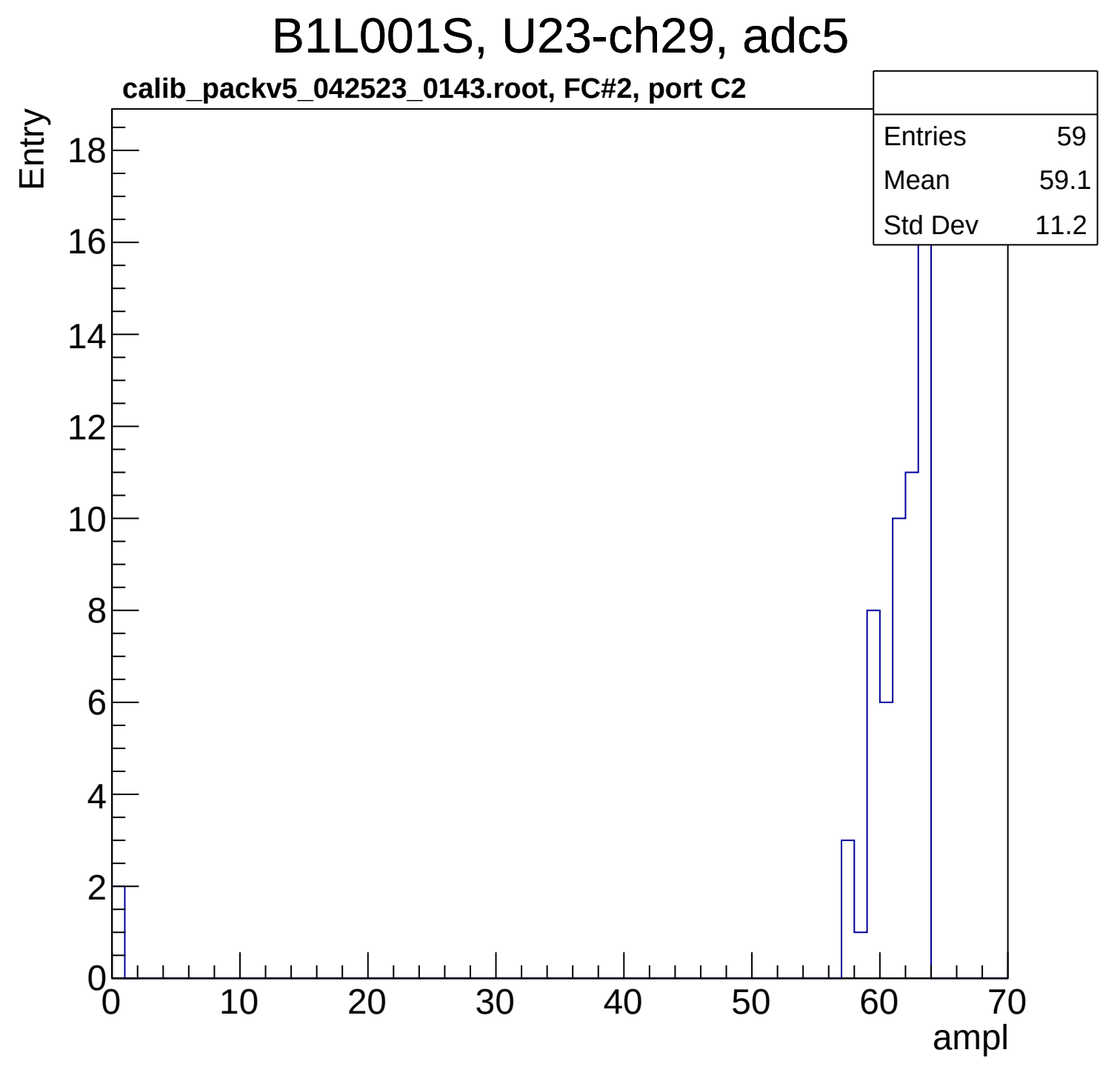
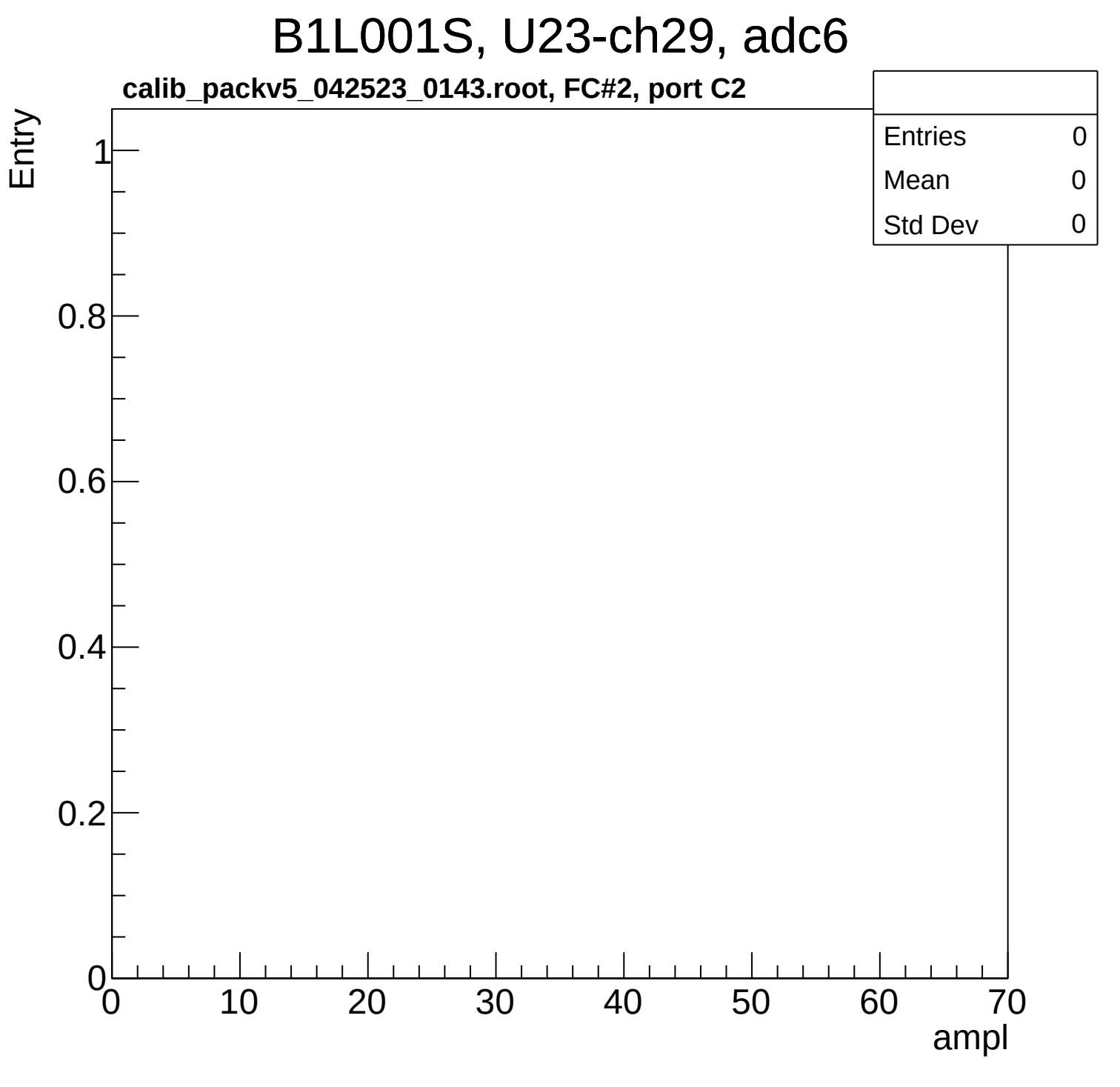
50
60
70
ampl


B1L001S, U23-ch29, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 59
Mean 59.1
Std Dev 11.2
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch29, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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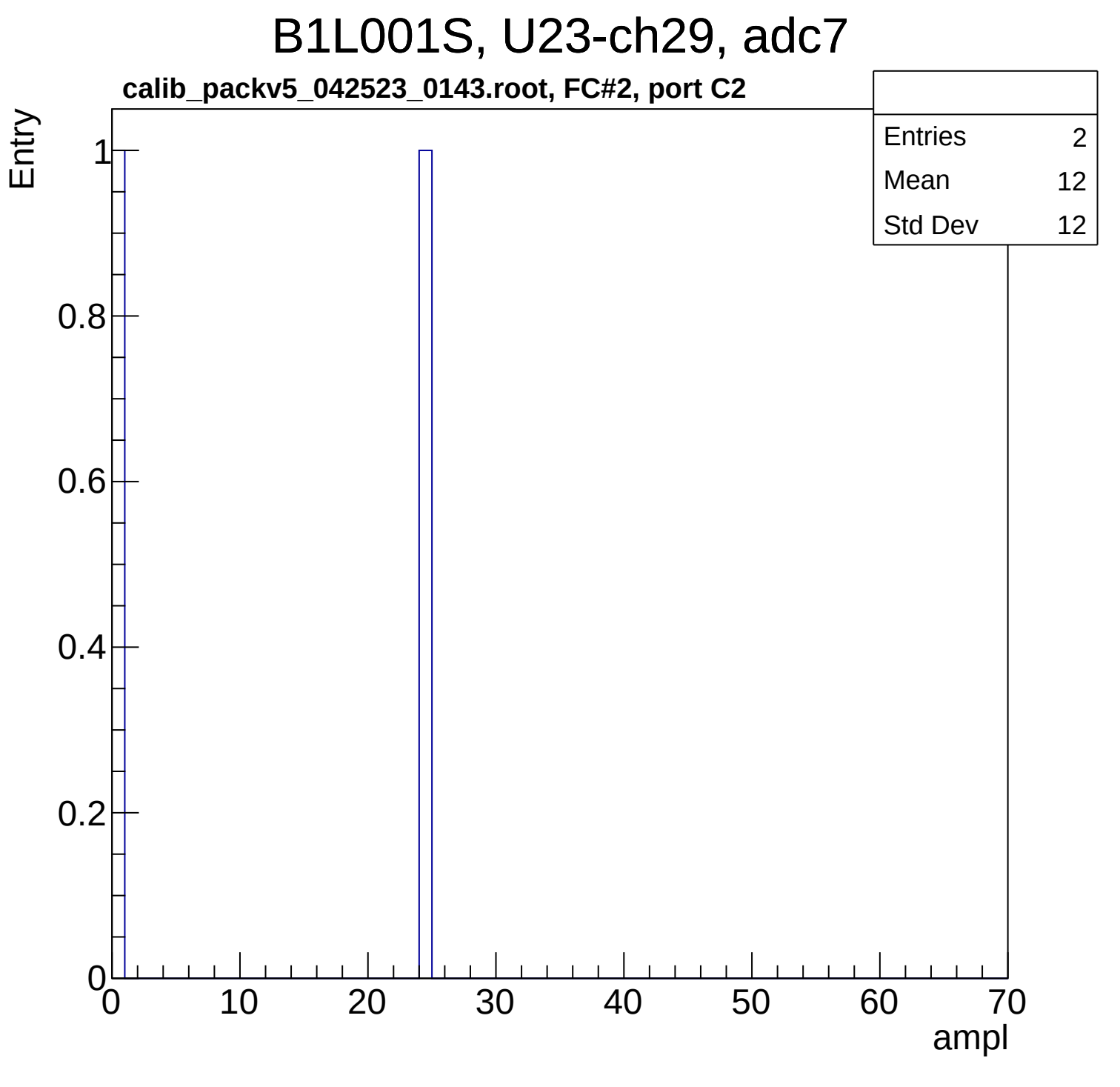
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch29, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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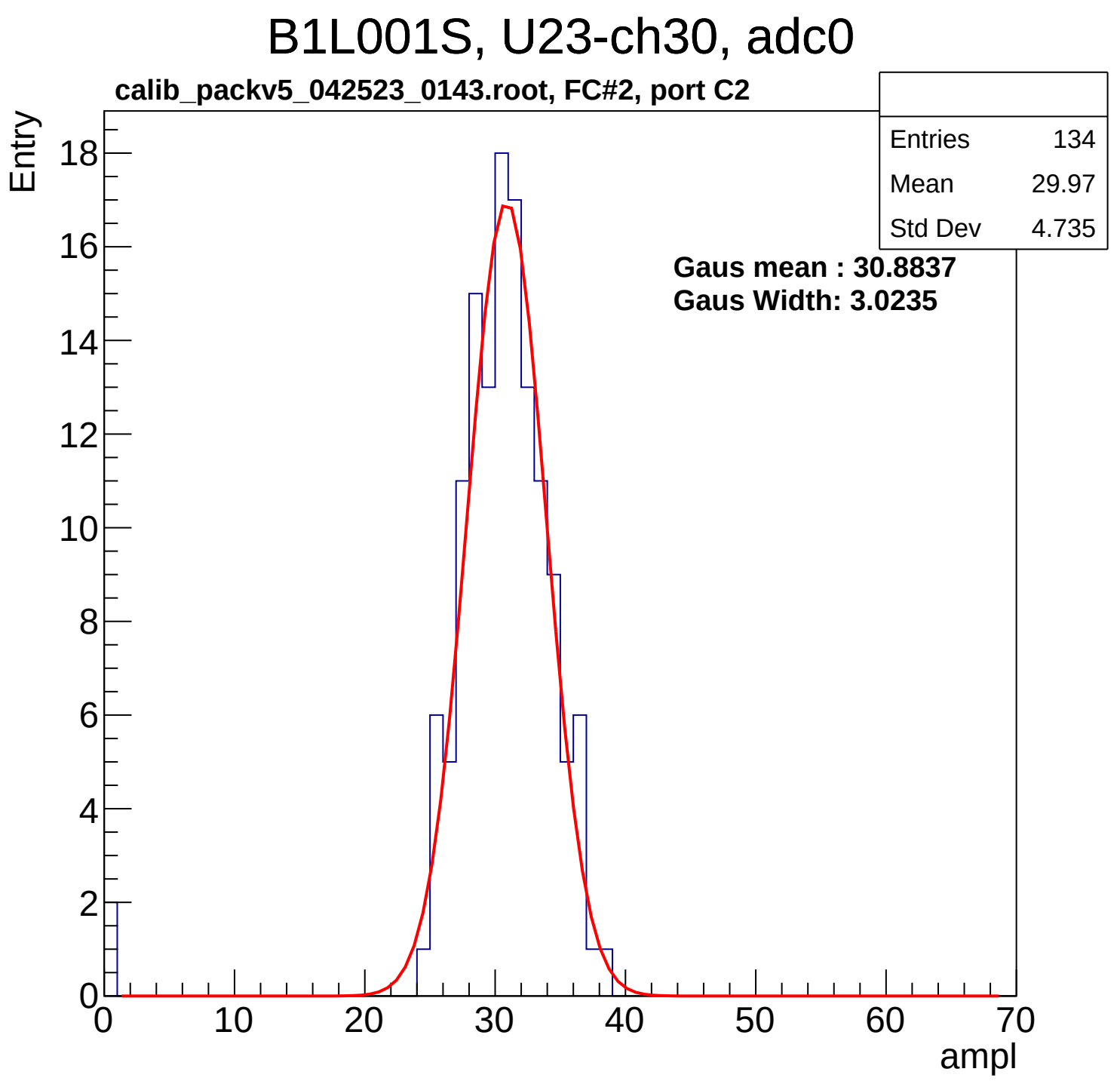
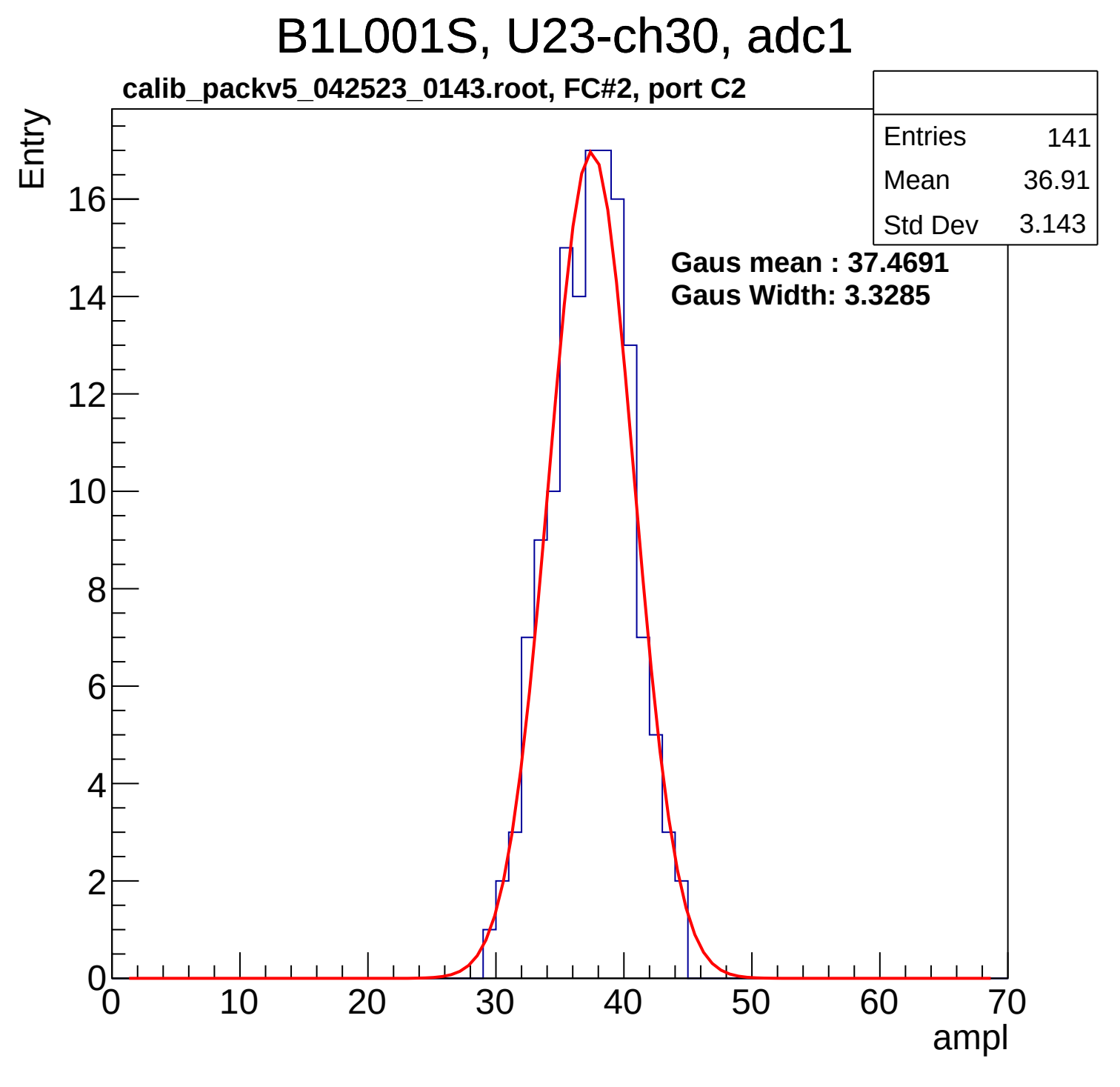
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch30, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 29.97
Std Dev 4.735
Gaus mean : 30.8837
Gaus Width: 3.0235
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch30, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 36.91
Std Dev 3.143
Gaus mean : 37.4691
Gaus Width: 3.3285
Entry
ampl
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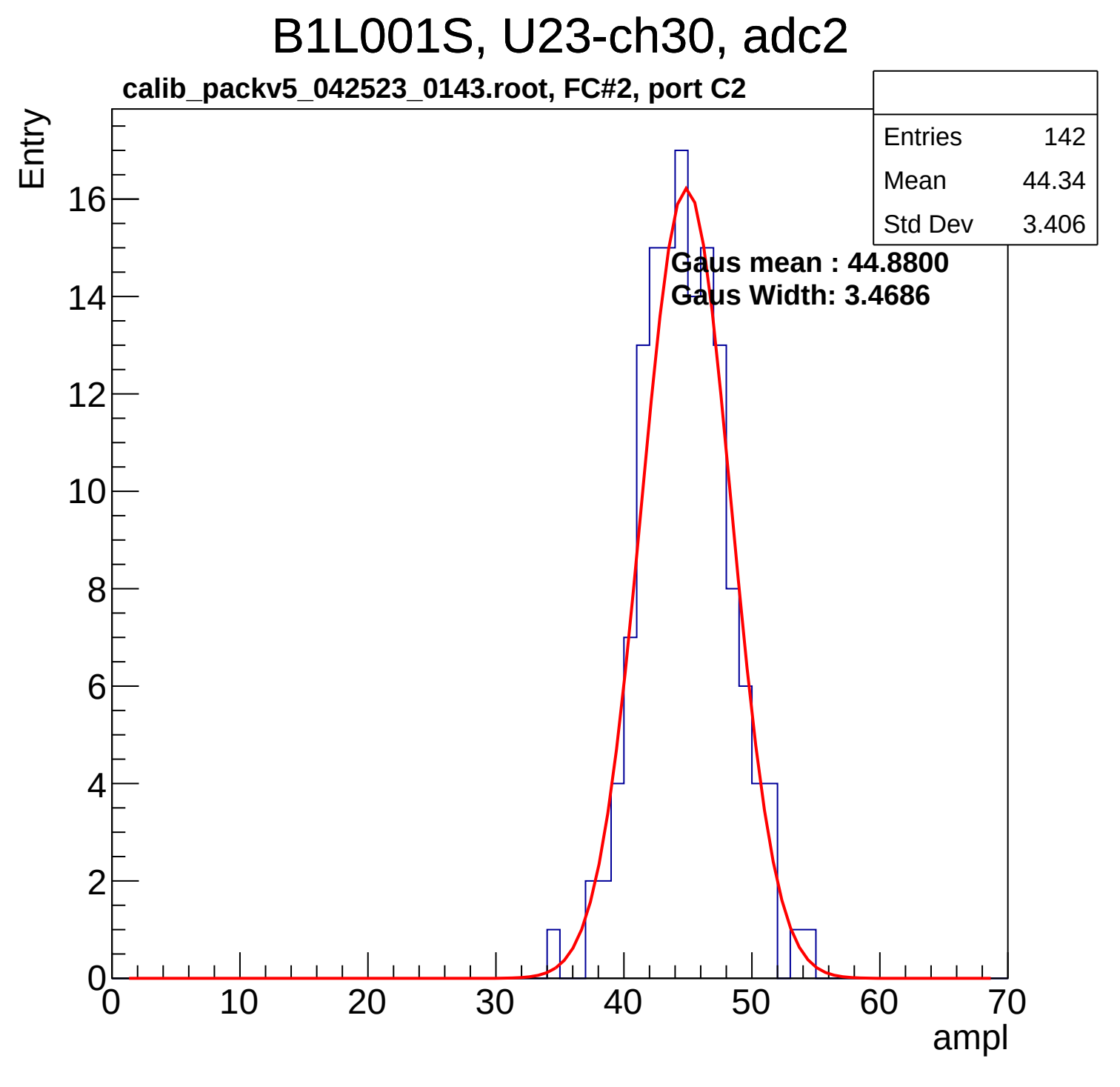
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch30, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 44.34
Std Dev 3.406
Gaus mean : 44.8800
Gaus Width: 3.4686
Entry
ampl
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2
4
6
8
10
12
14
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0
10
20
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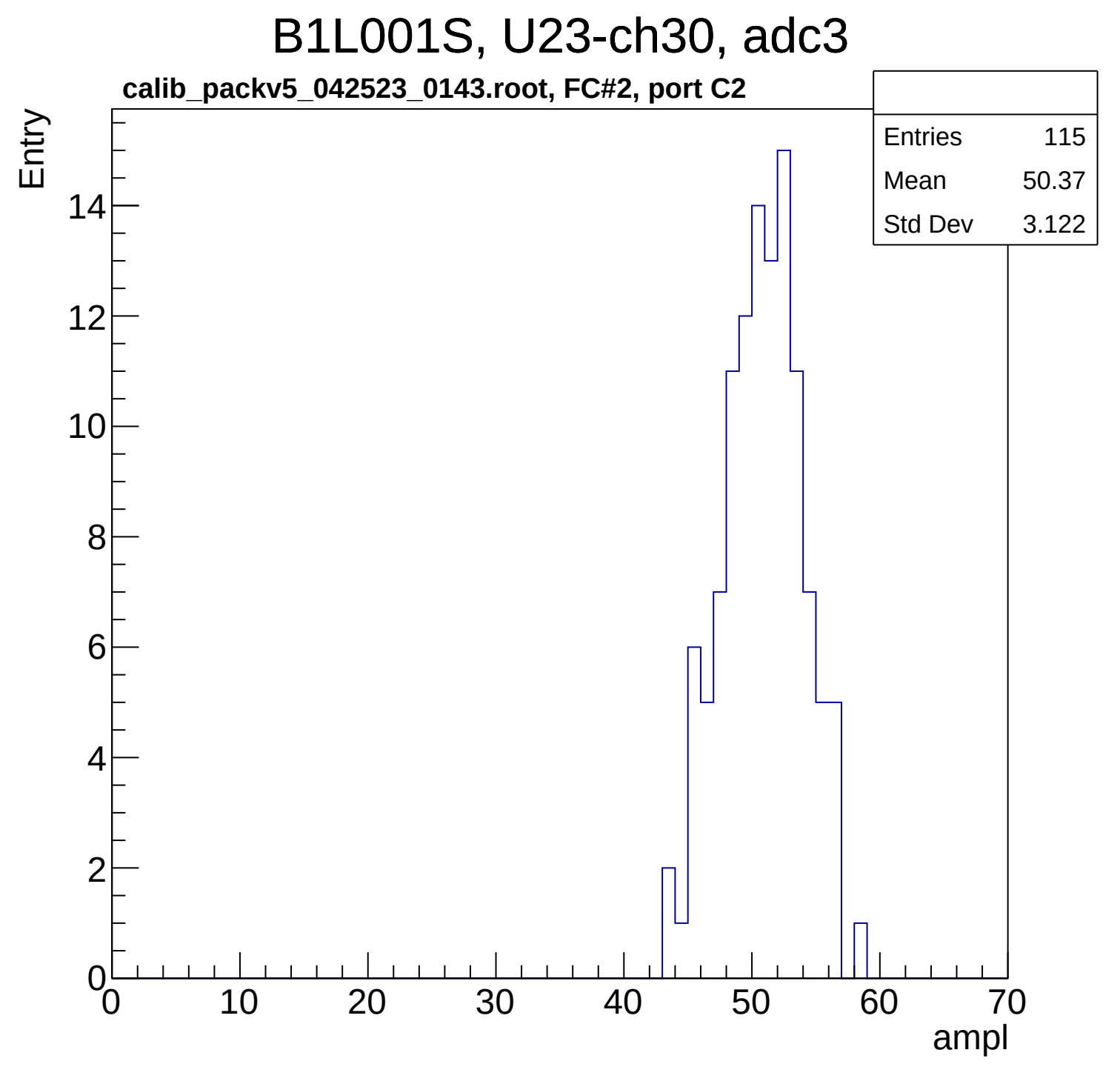
40
50
60
70


B1L001S, U23-ch30, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 50.37
Std Dev 3.122
Entry
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4
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8
10
12
14
0
10
20
30
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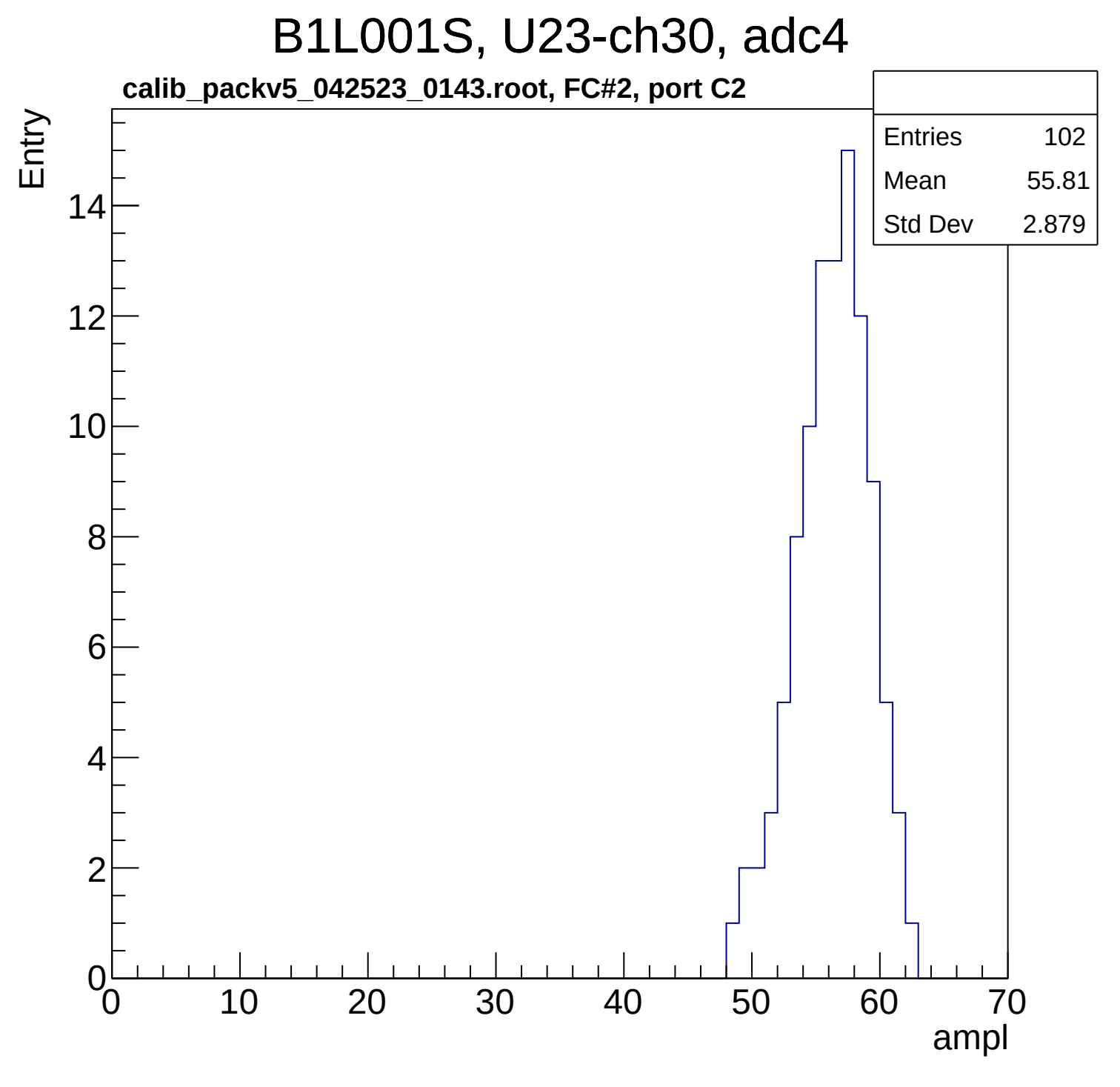
50
60
70
ampl


B1L001S, U23-ch30, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 55.81
Std Dev 2.879
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
50
60
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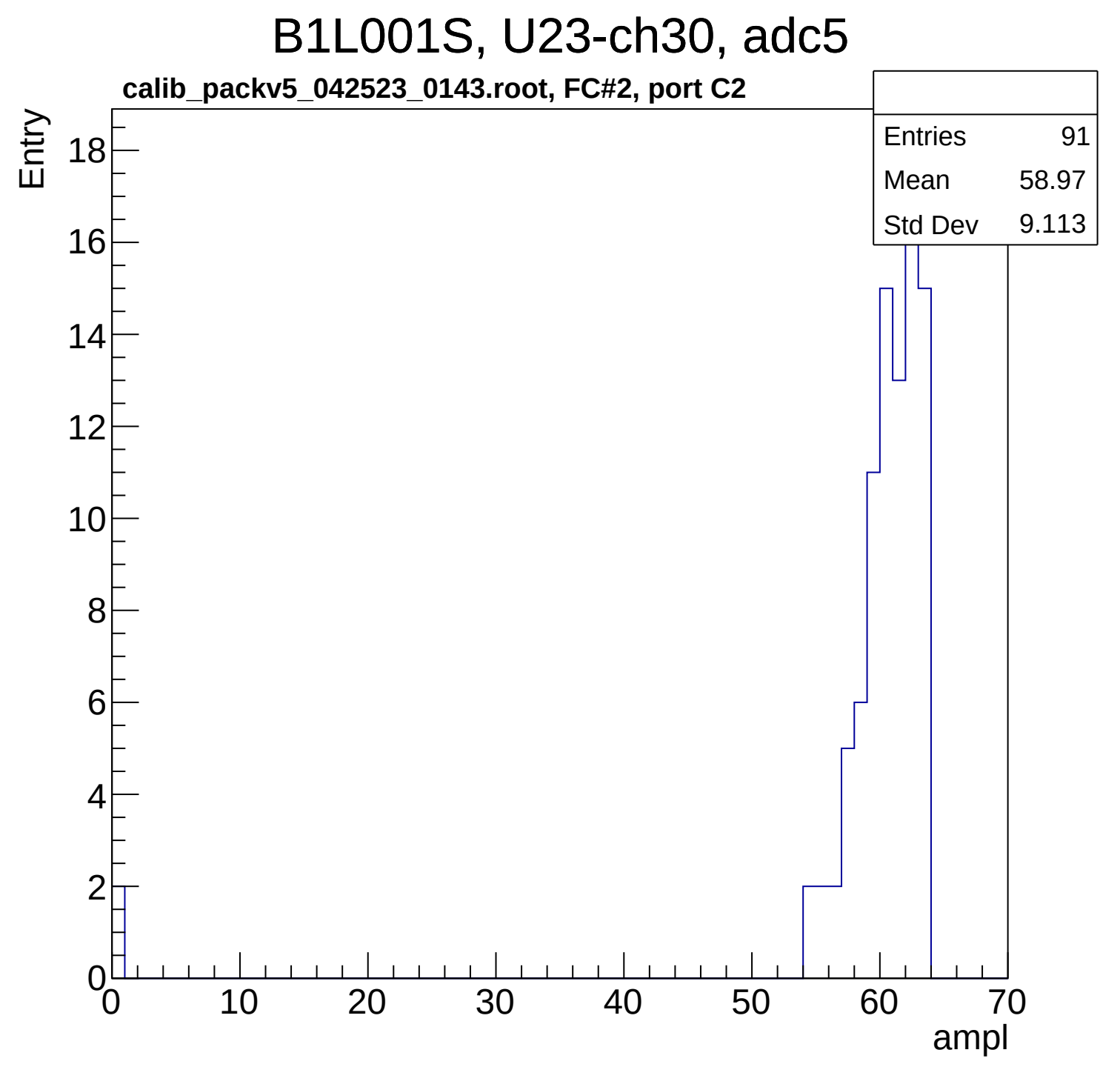
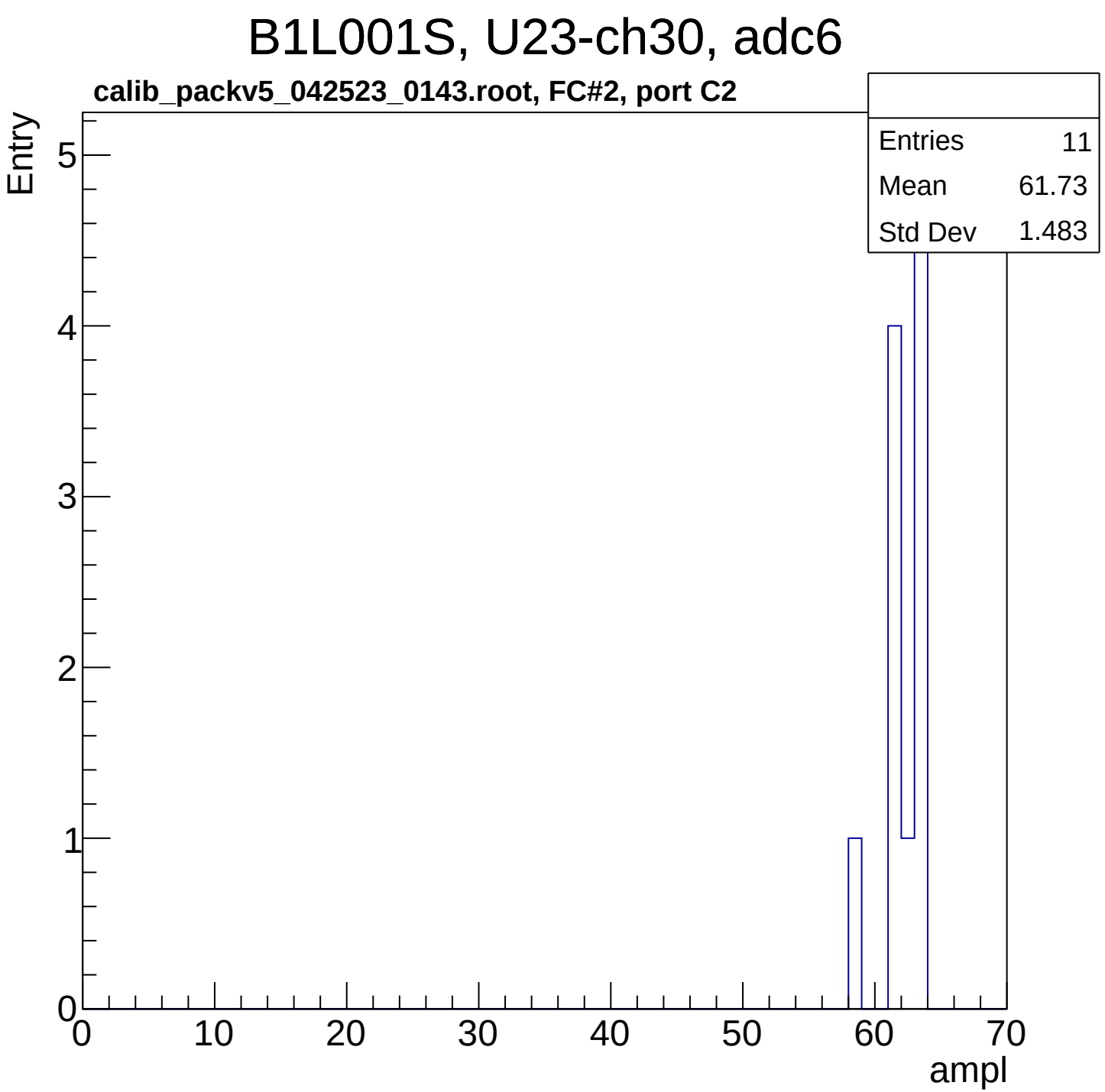

B1L001S, U23-ch30, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 58.97
Std Dev 9.113
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch30, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 61.73
Std Dev 1.483
Entry
0
1
2
3
4
5
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10
20
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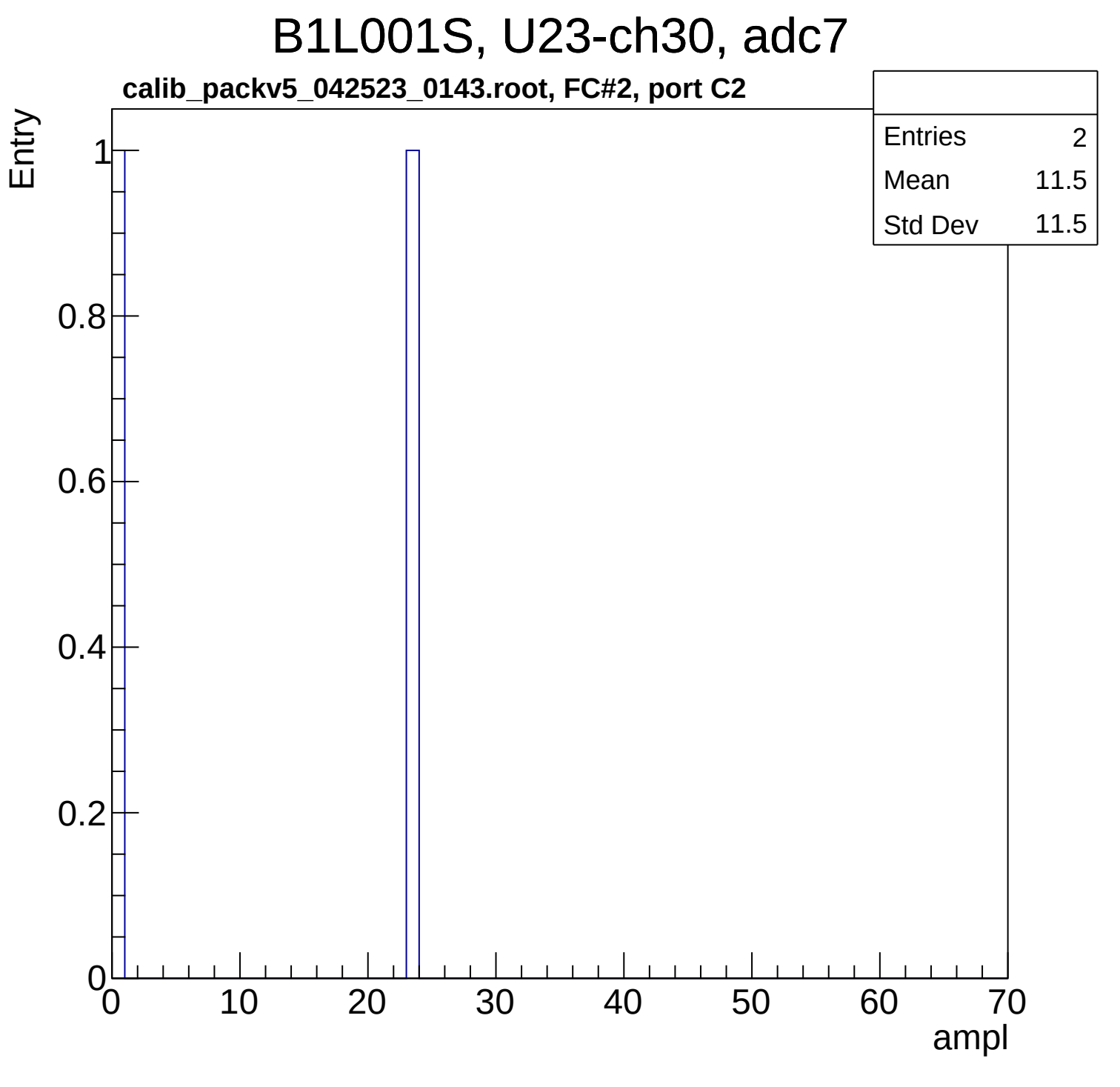
70
ampl


B1L001S, U23-ch30, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
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0
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10
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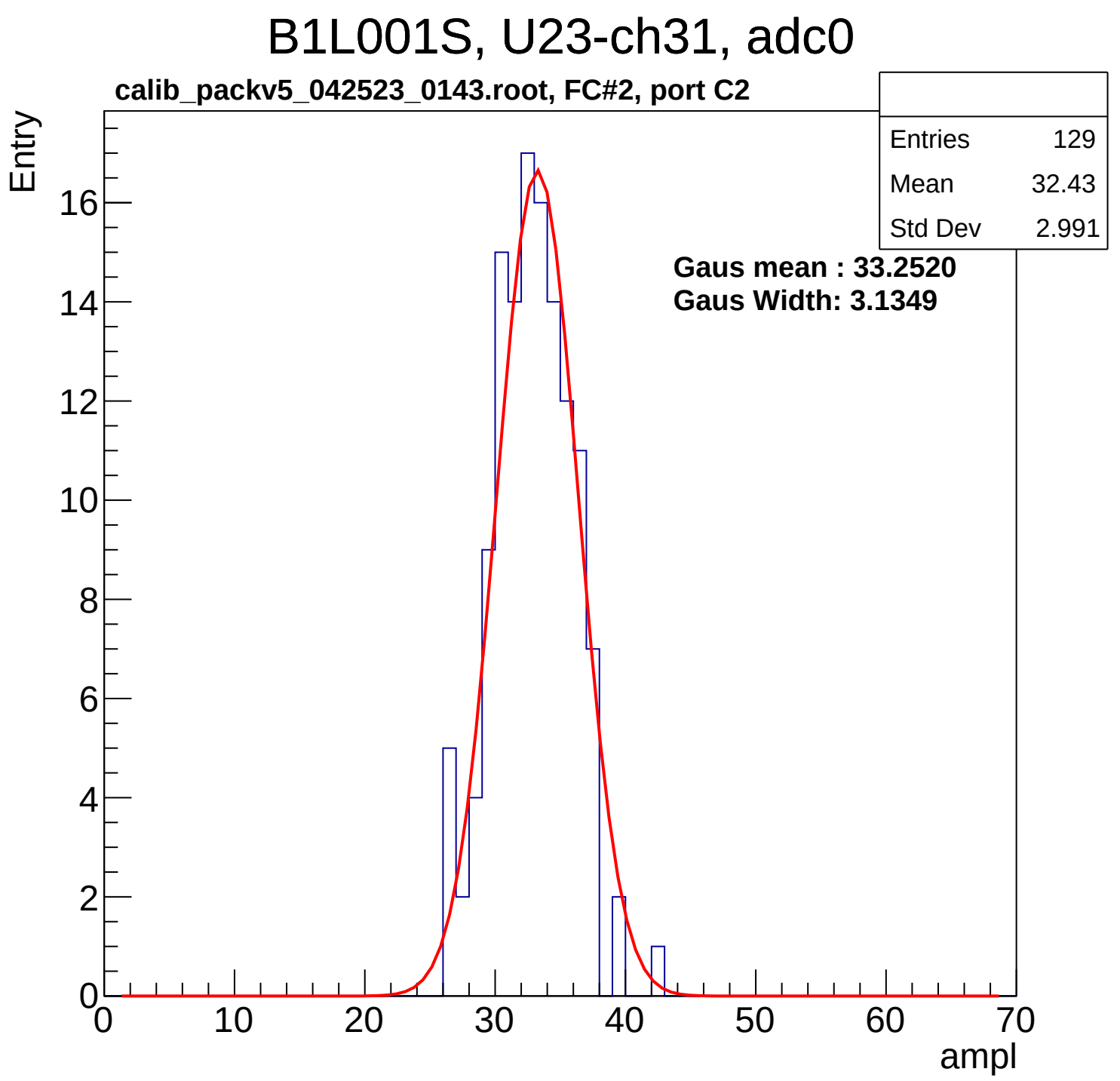
50
60
70
ampl


B1L001S, U23-ch31, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 32.43
Std Dev 2.991
Gaus mean : 33.2520
Gaus Width: 3.1349
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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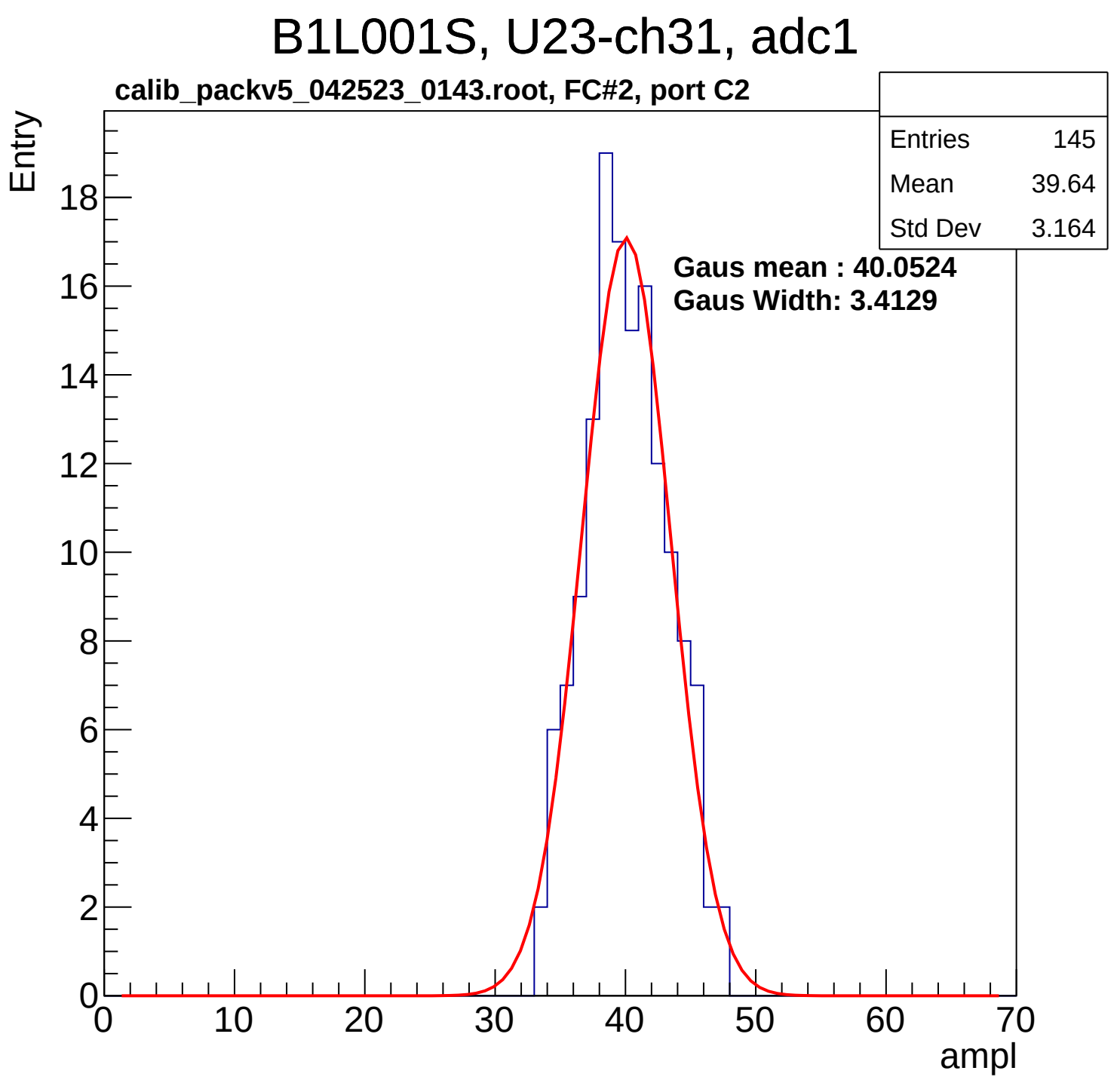
B1L001S, U23-ch31, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 39.64
Std Dev 3.164
Gaus mean : 40.0524
Gaus Width: 3.4129
Entry
ampl
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4
6
8
10
12
14
16
18
0
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40
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60
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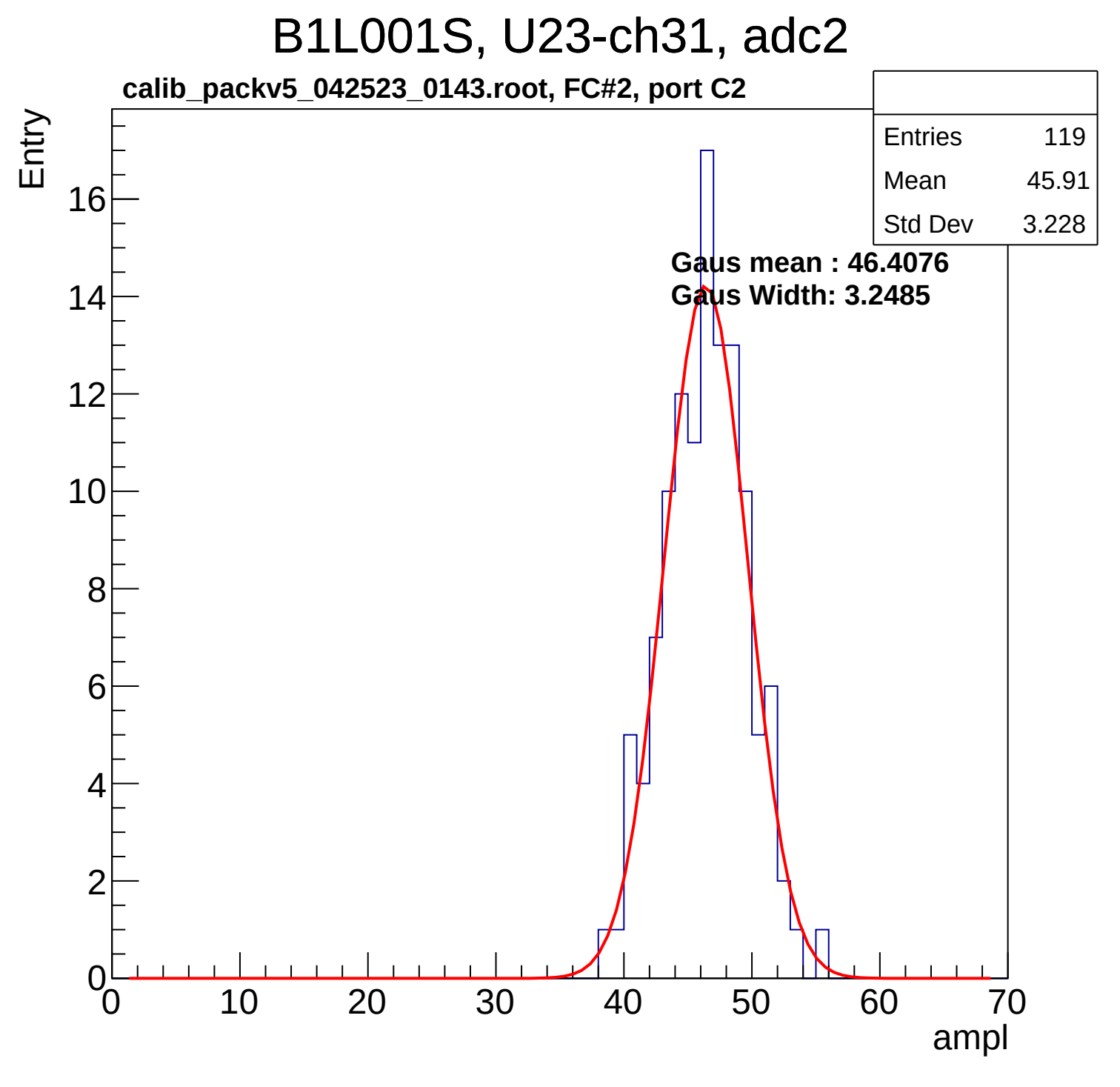
B1L001S, U23-ch31, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 45.91
Std Dev 3.228
Gaus mean : 46.4076
Gaus Width: 3.2485
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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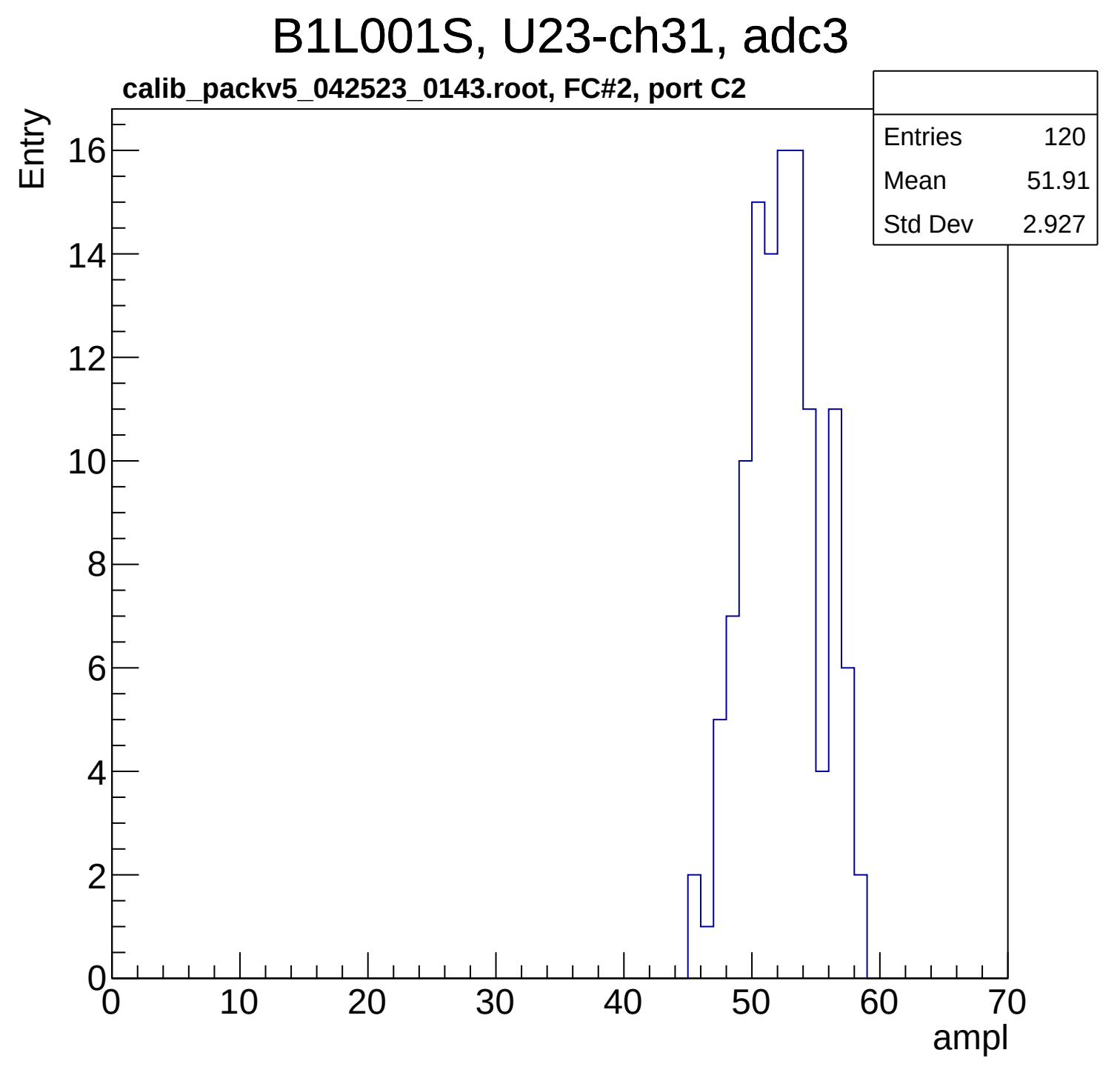
B1L001S, U23-ch31, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 51.91
Std Dev 2.927
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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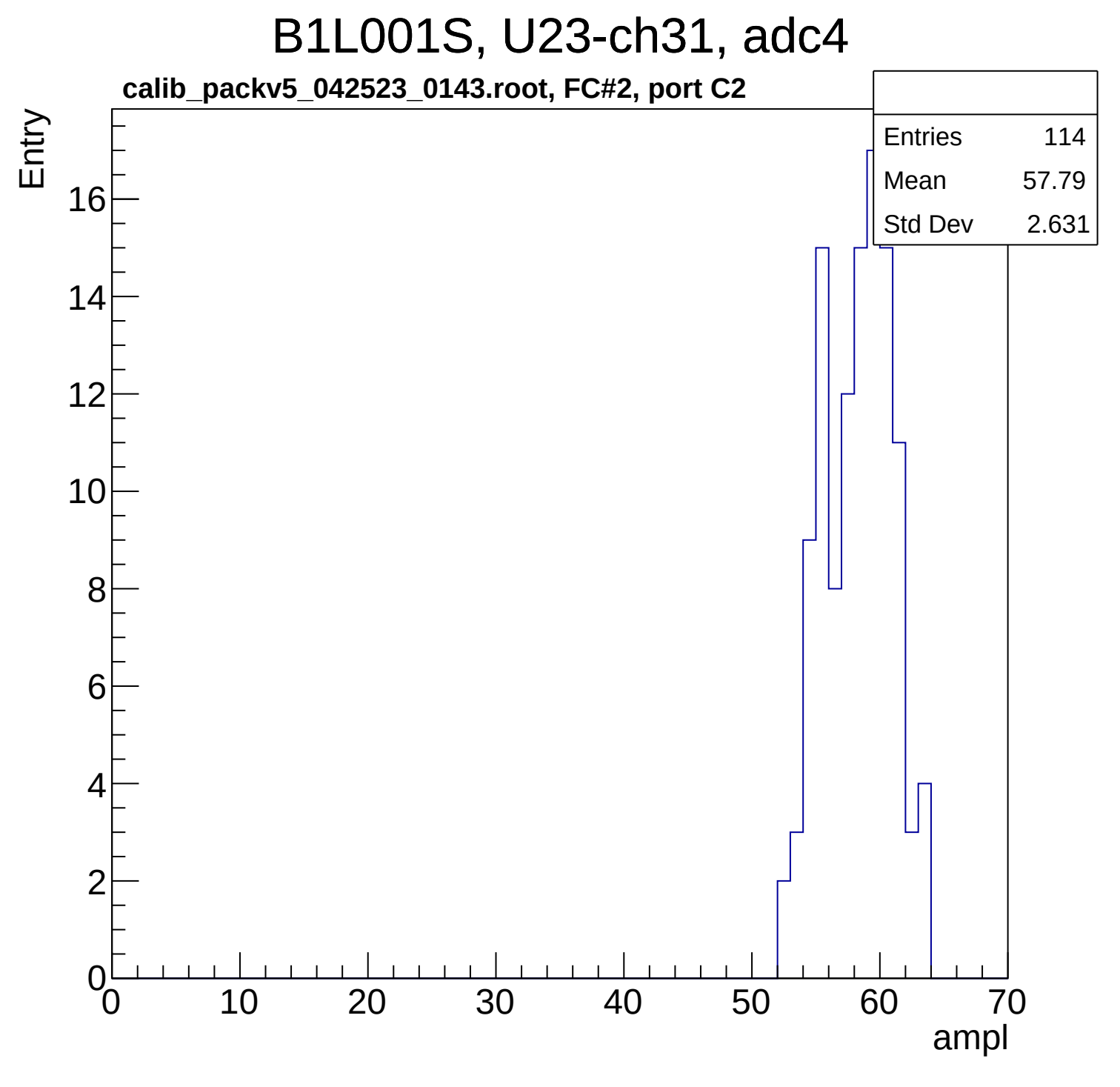
B1L001S, U23-ch31, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 57.79
Std Dev 2.631
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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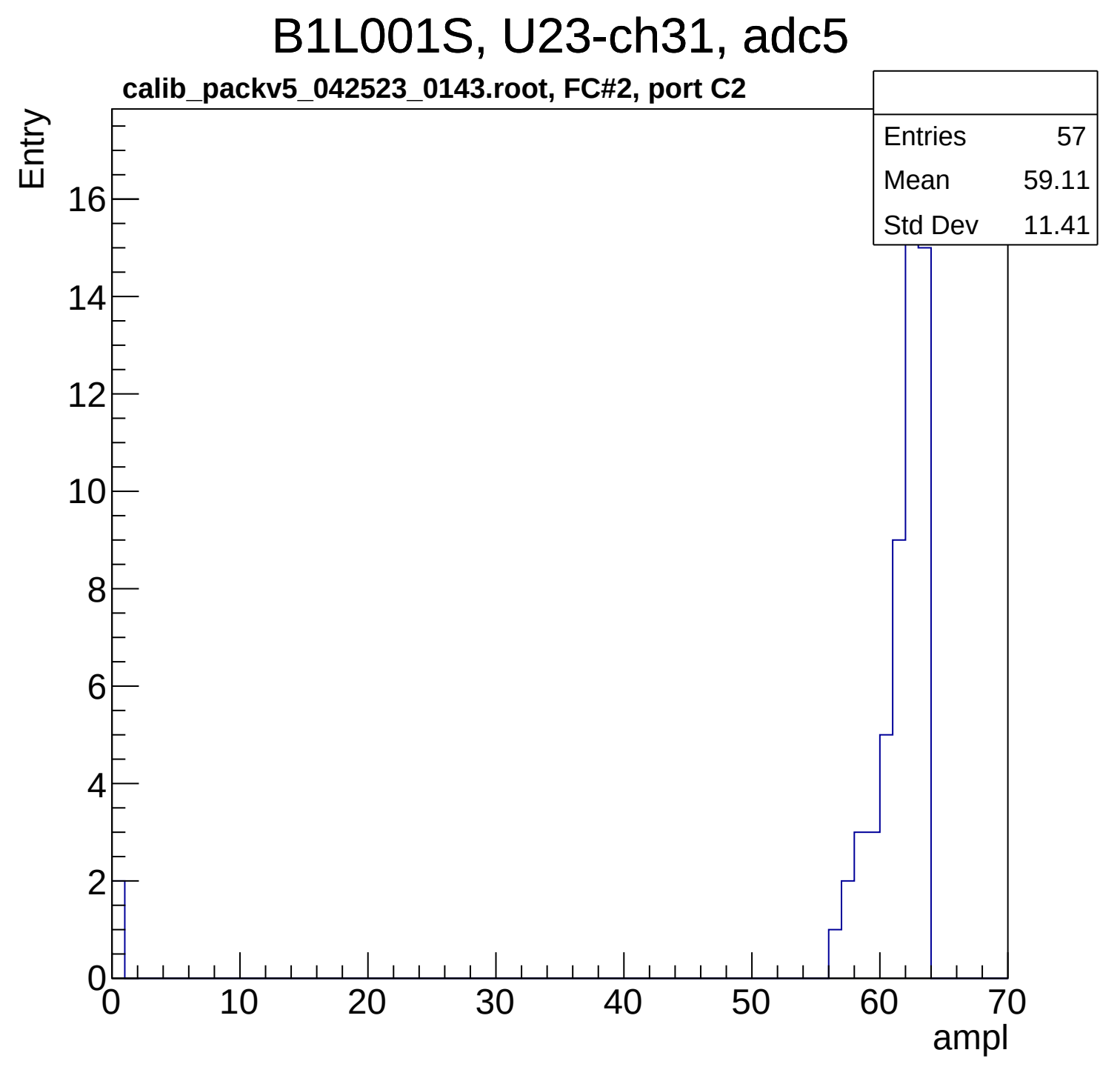
B1L001S, U23-ch31, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 57
Mean 59.11
Std Dev 11.41
Entry
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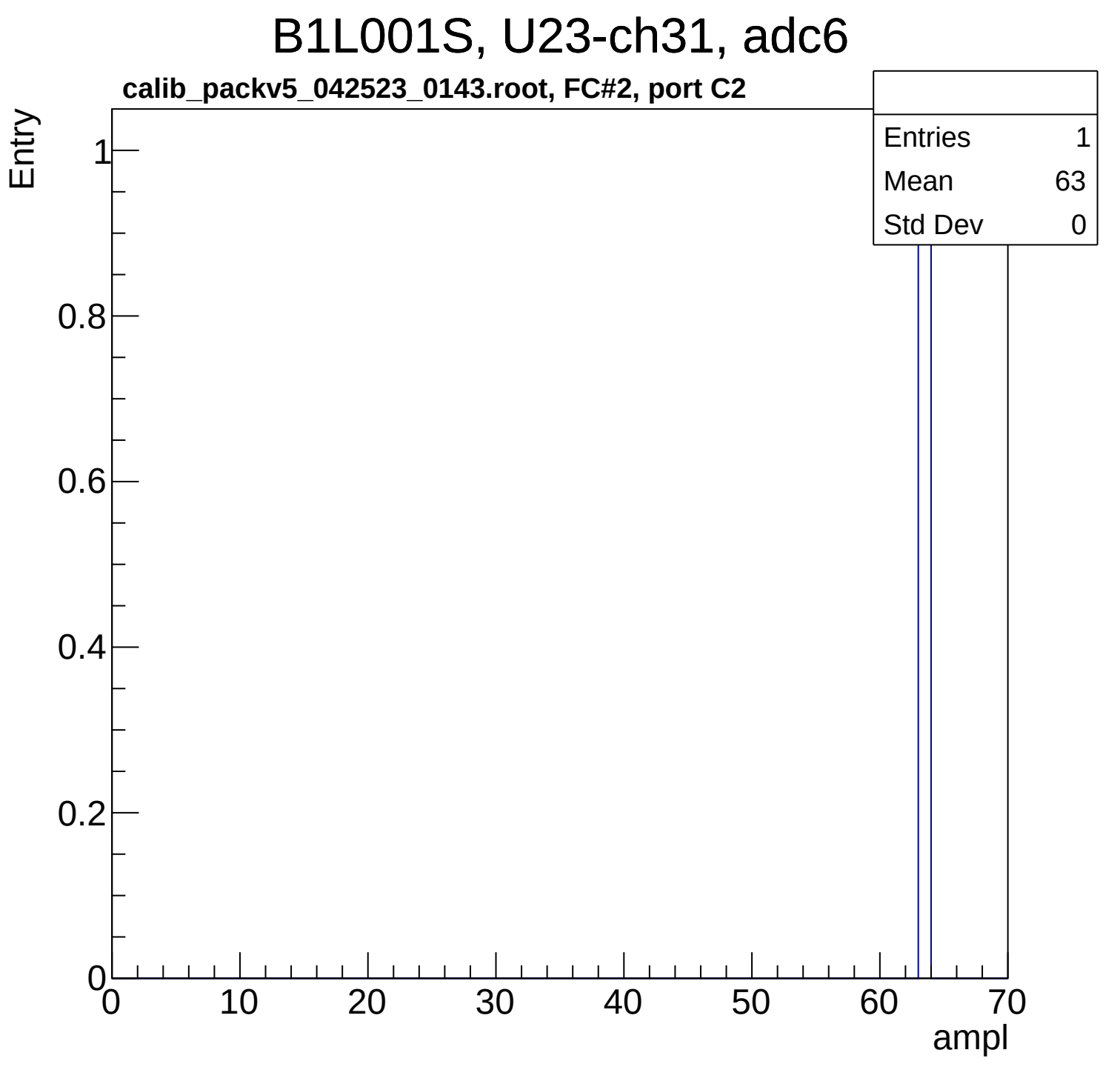
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch31, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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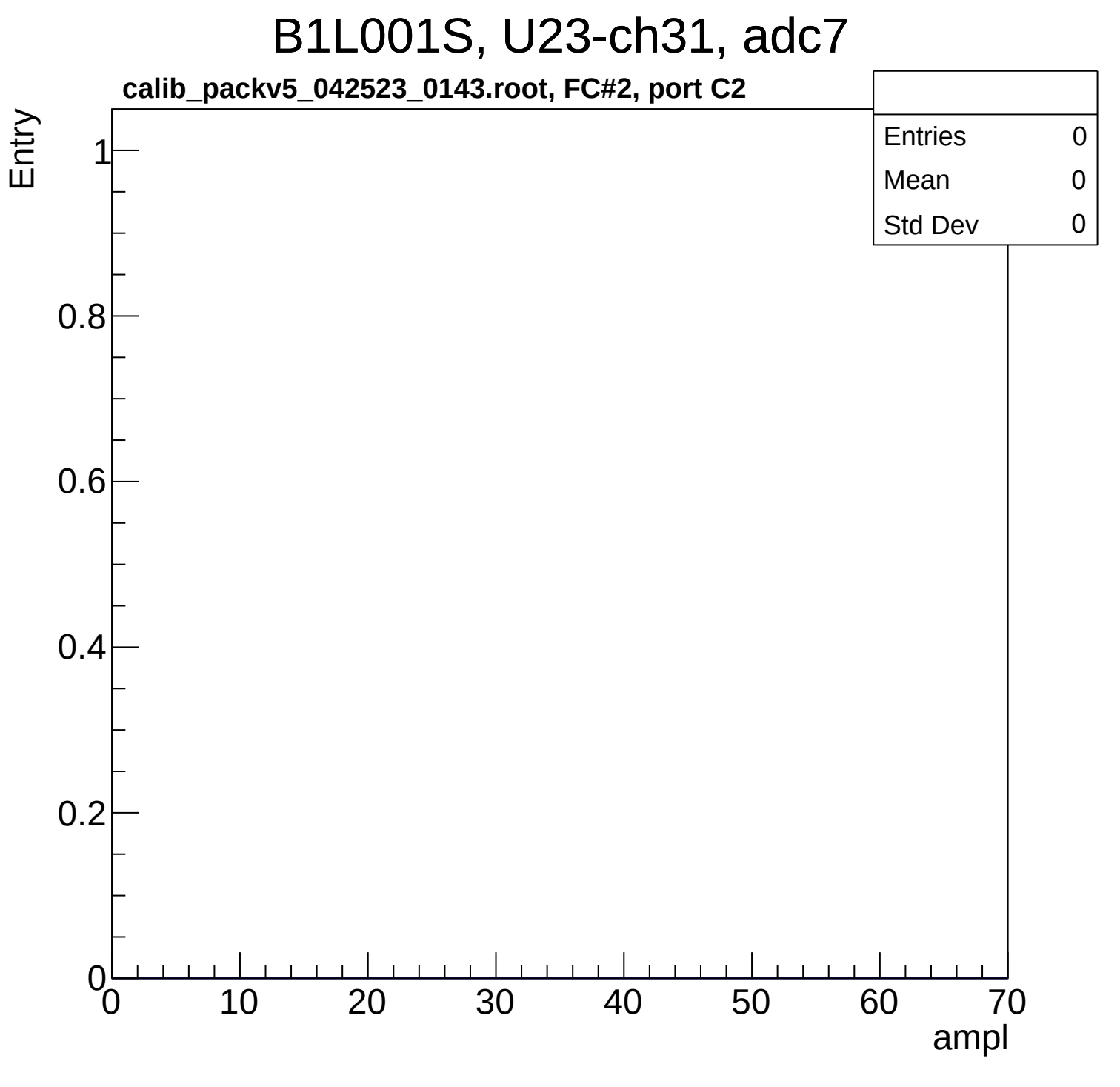
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch31, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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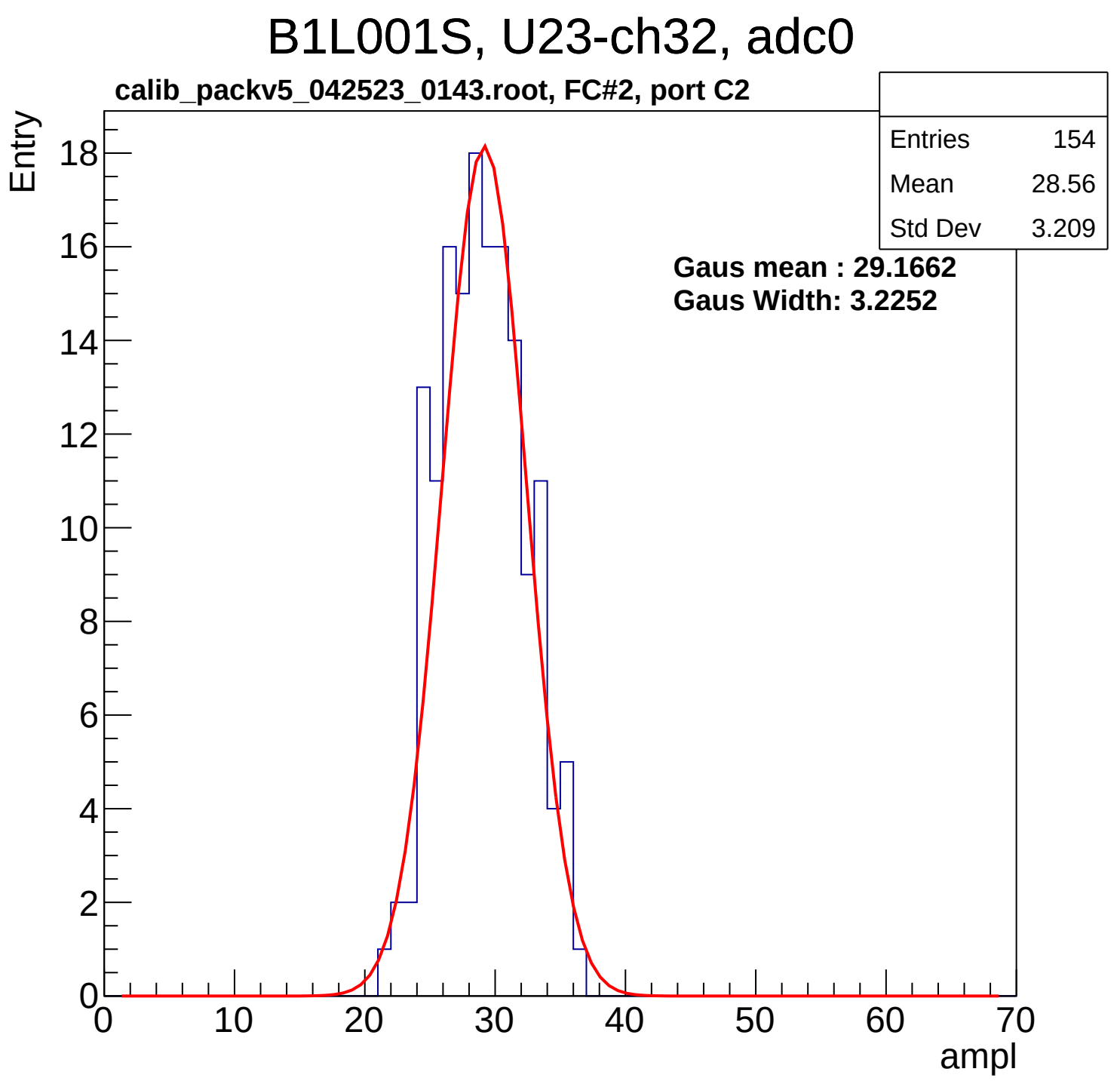
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch32, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 28.56
Std Dev 3.209
Gaus mean : 29.1662
Gaus Width: 3.2252
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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60
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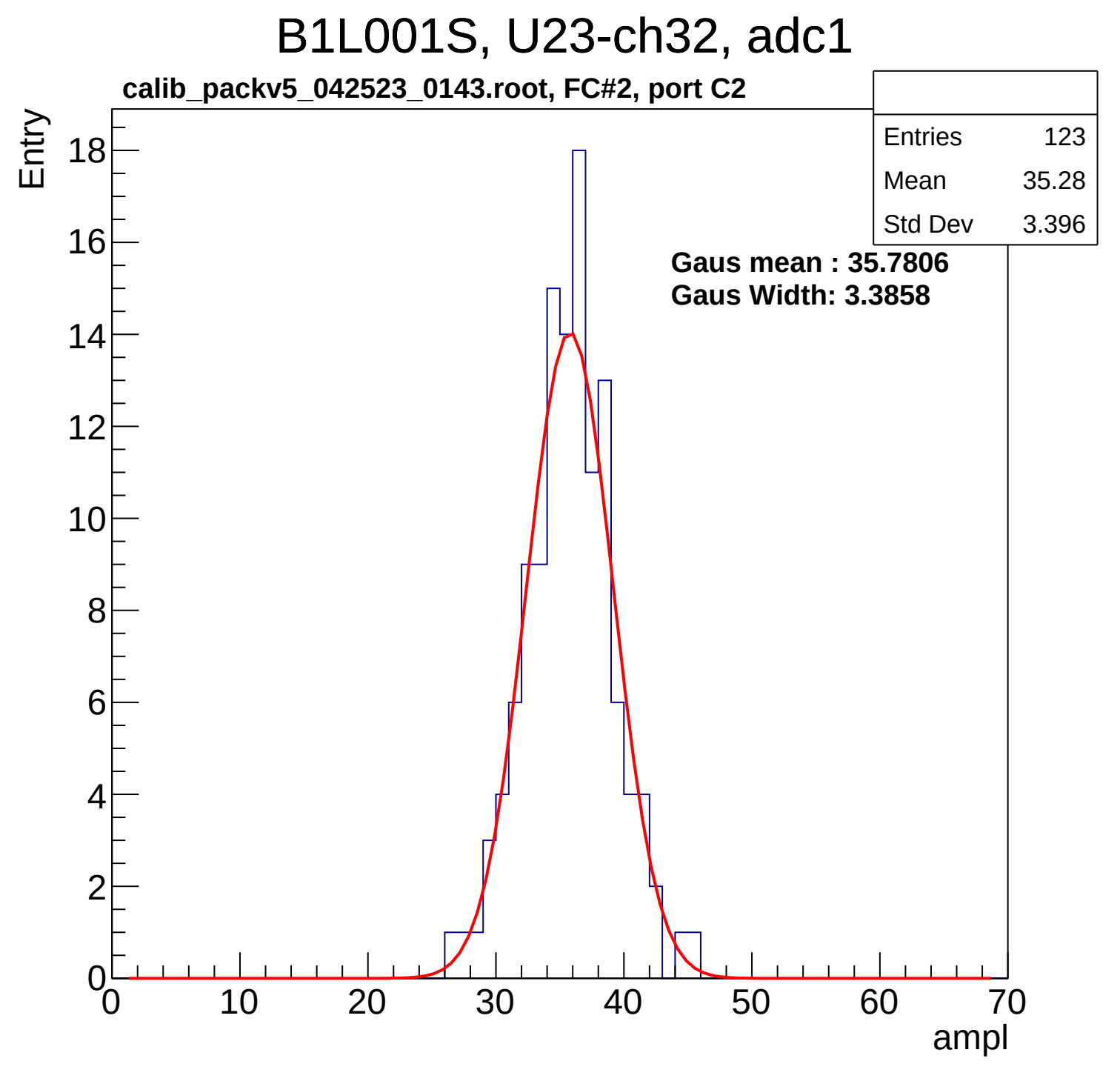
B1L001S, U23-ch32, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 35.28
Std Dev 3.396
Gaus mean : 35.7806
Gaus Width: 3.3858
Entry
ampl
0
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4
6
8
10
12
14
16
18
0
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20
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60
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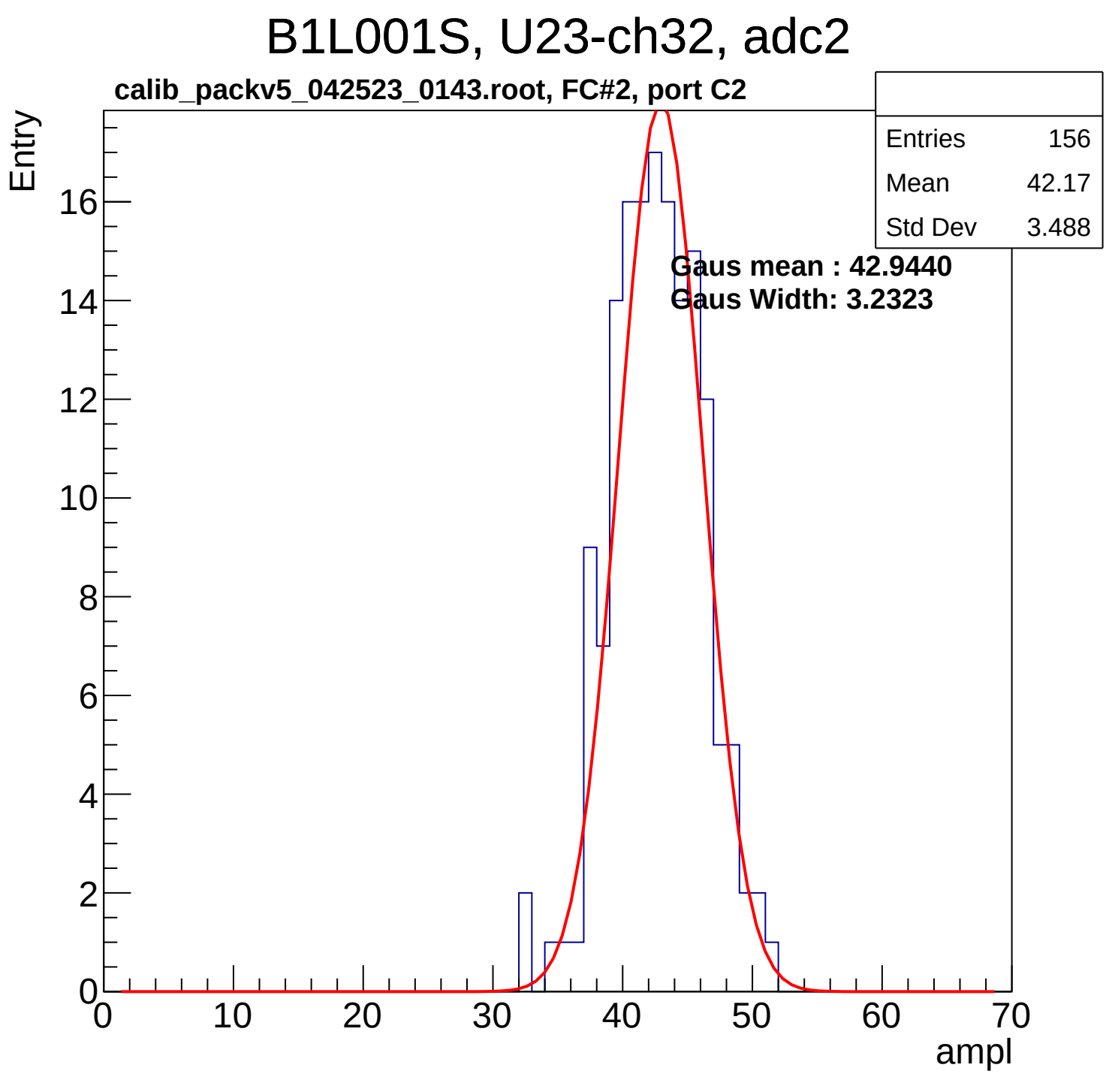
B1L001S, U23-ch32, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 42.17
Std Dev 3.488
Gaus mean : 42.9440
Gaus Width: 3.2323
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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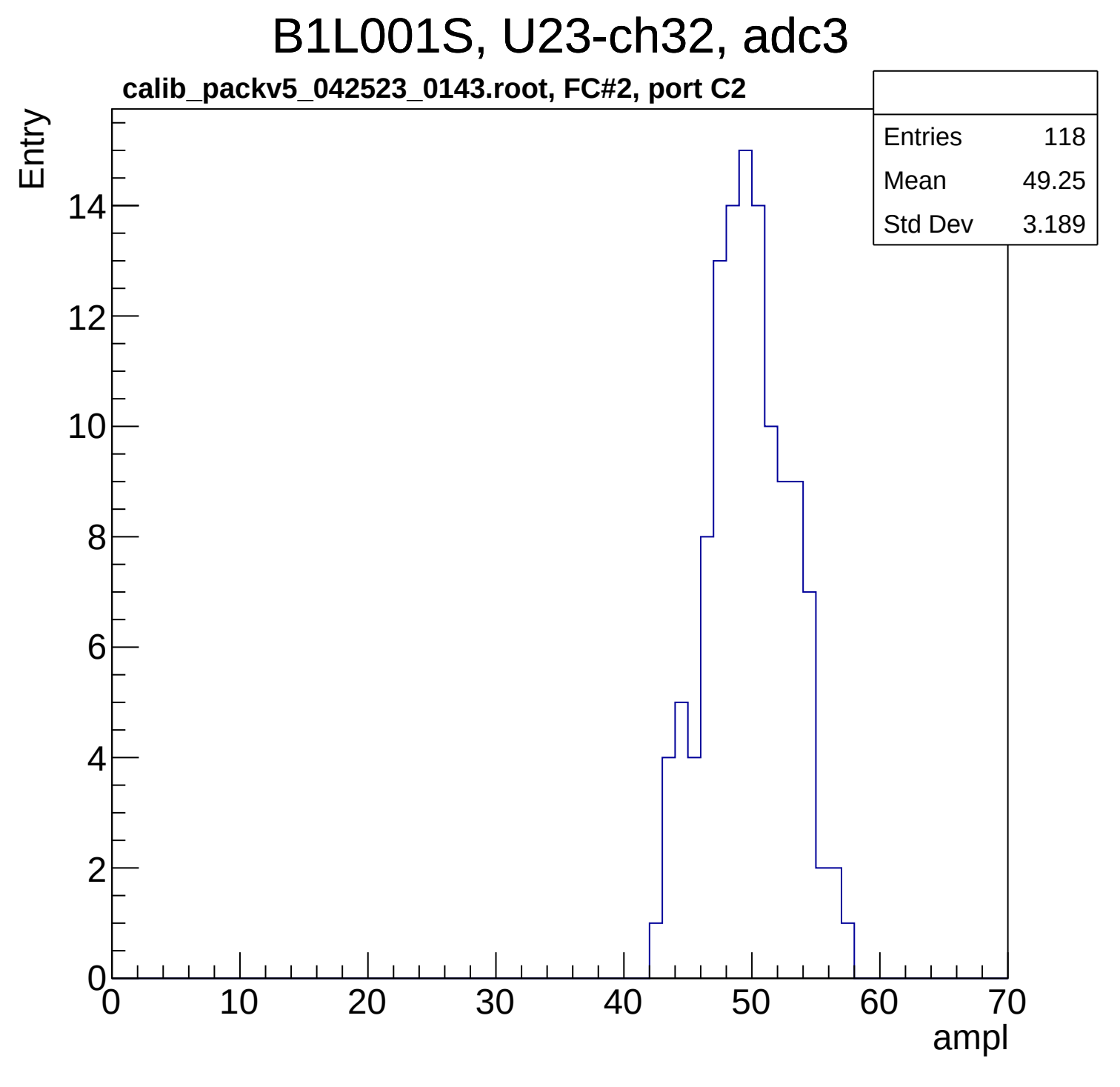
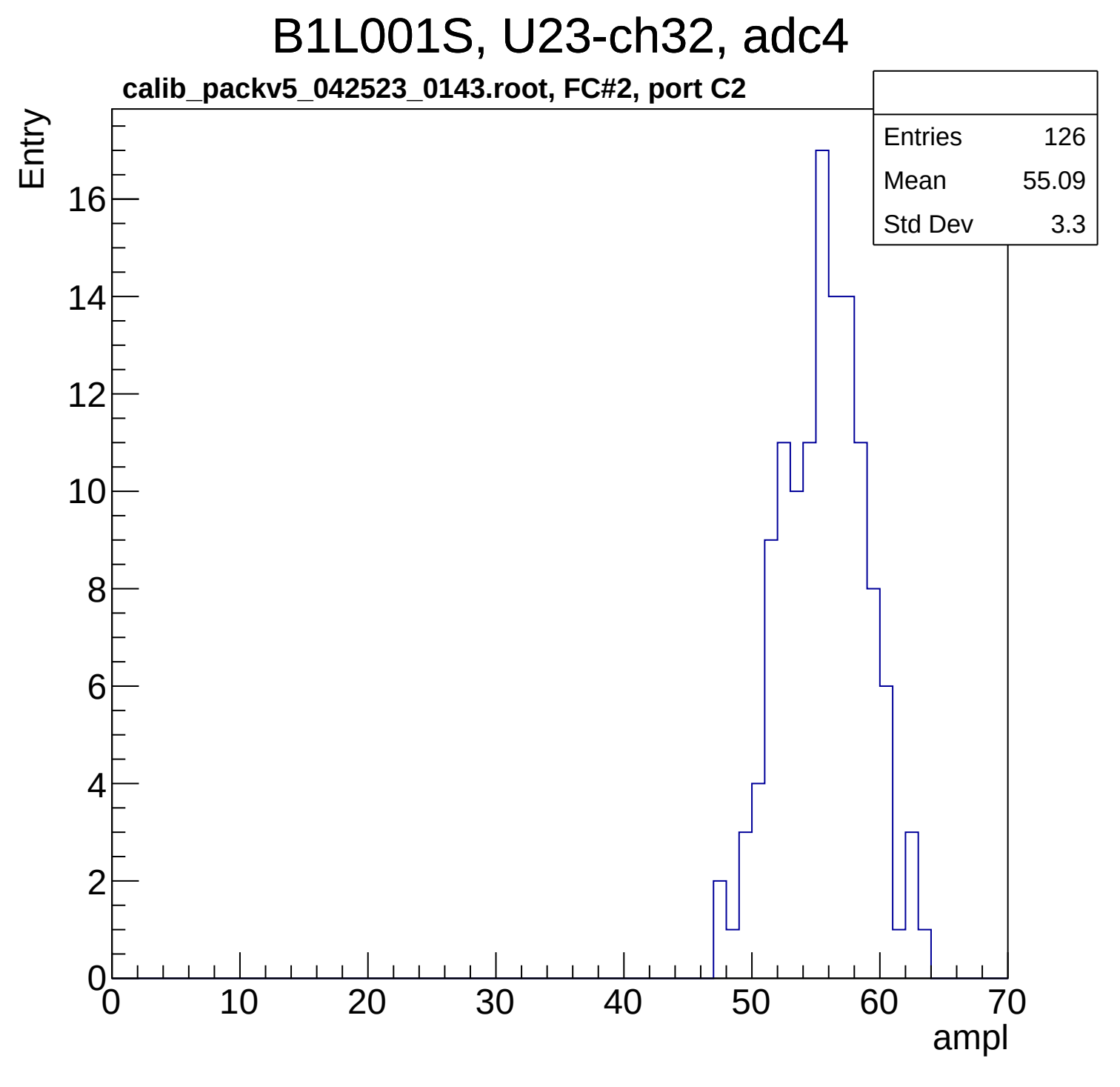

B1L001S, U23-ch32, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 49.25
Std Dev 3.189
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch32, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 55.09
Std Dev 3.3
Entry
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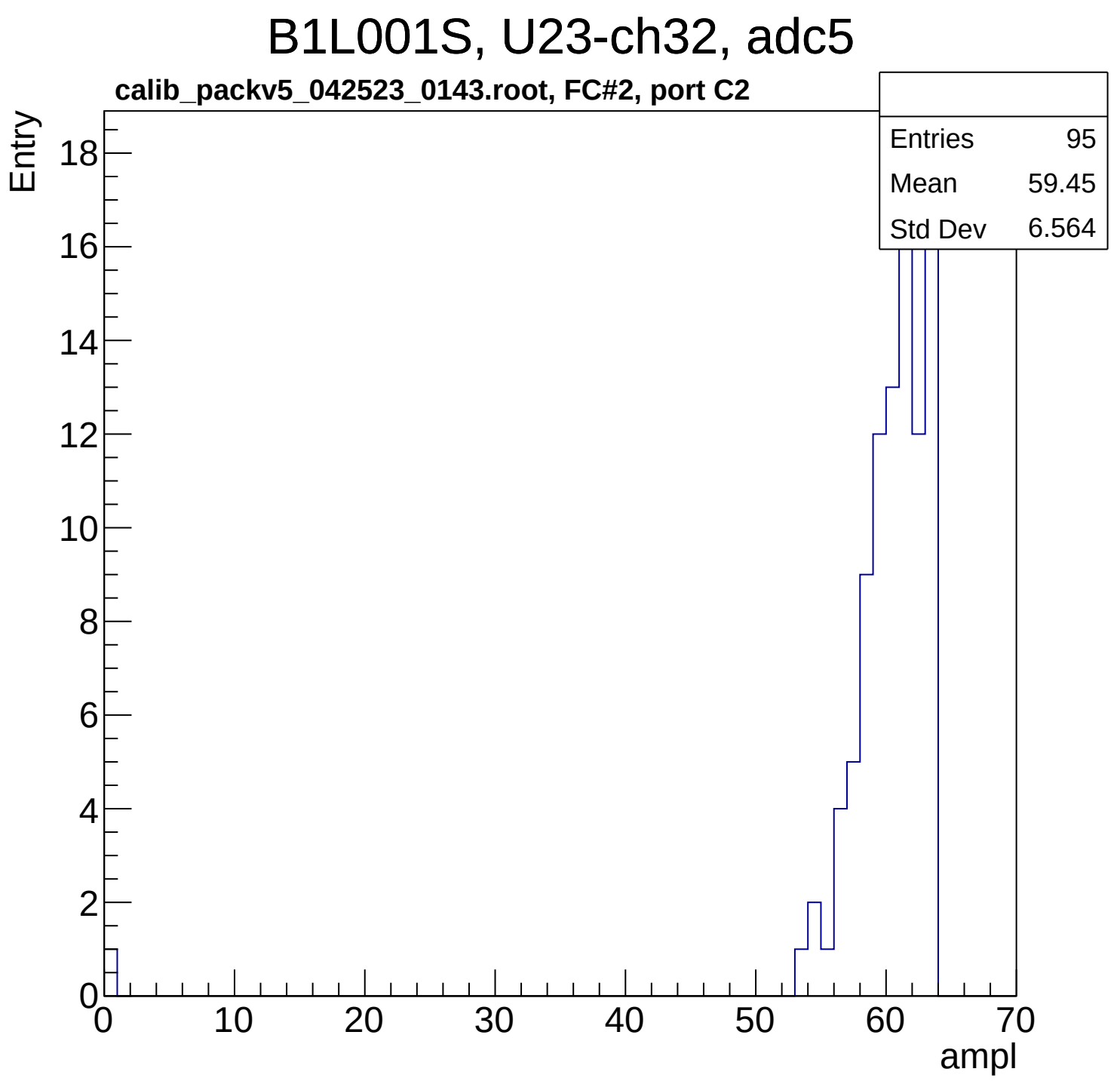
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch32, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 59.45
Std Dev 6.564
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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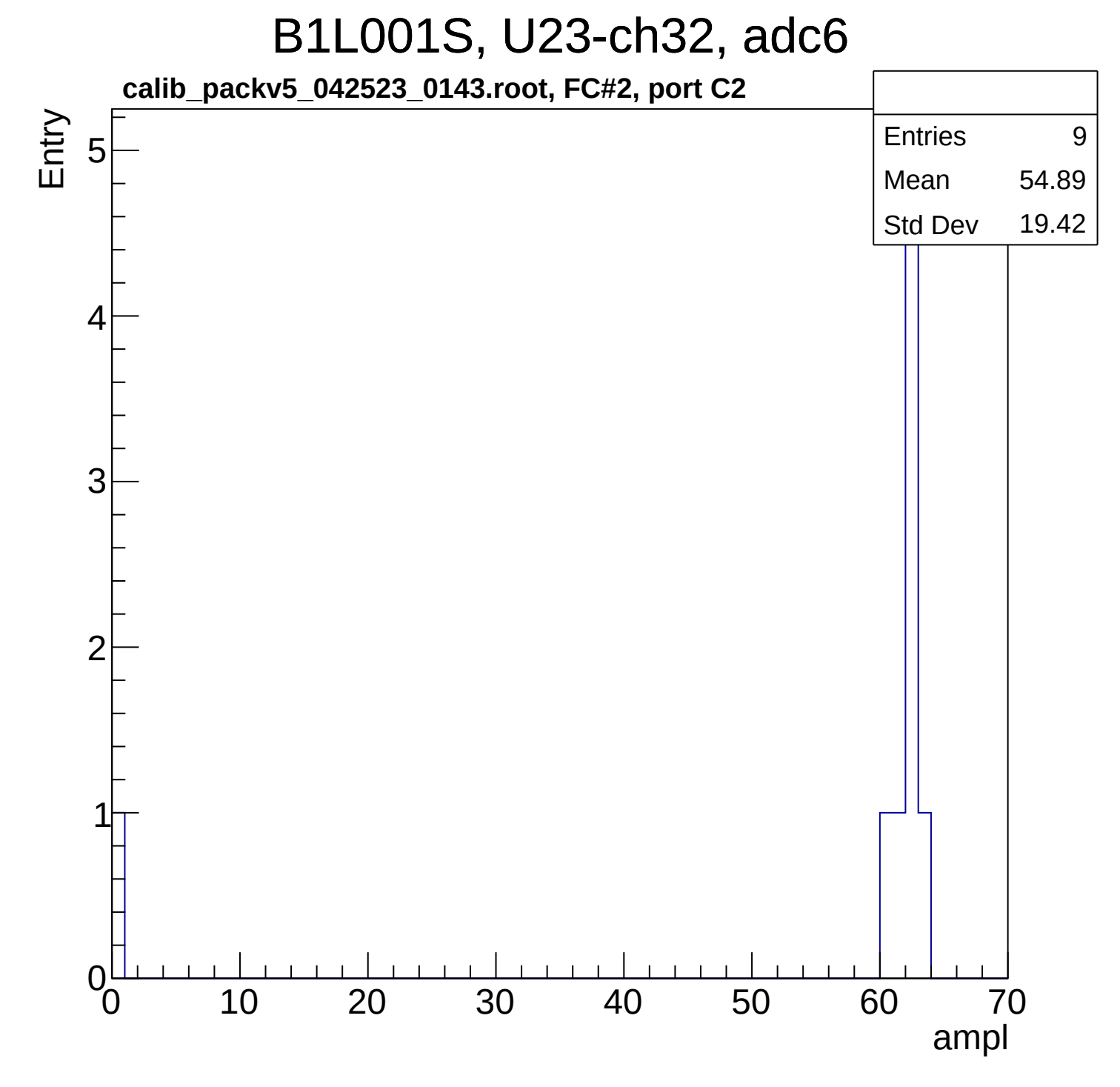
B1L001S, U23-ch32, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 54.89
Std Dev 19.42
Entry
ampl
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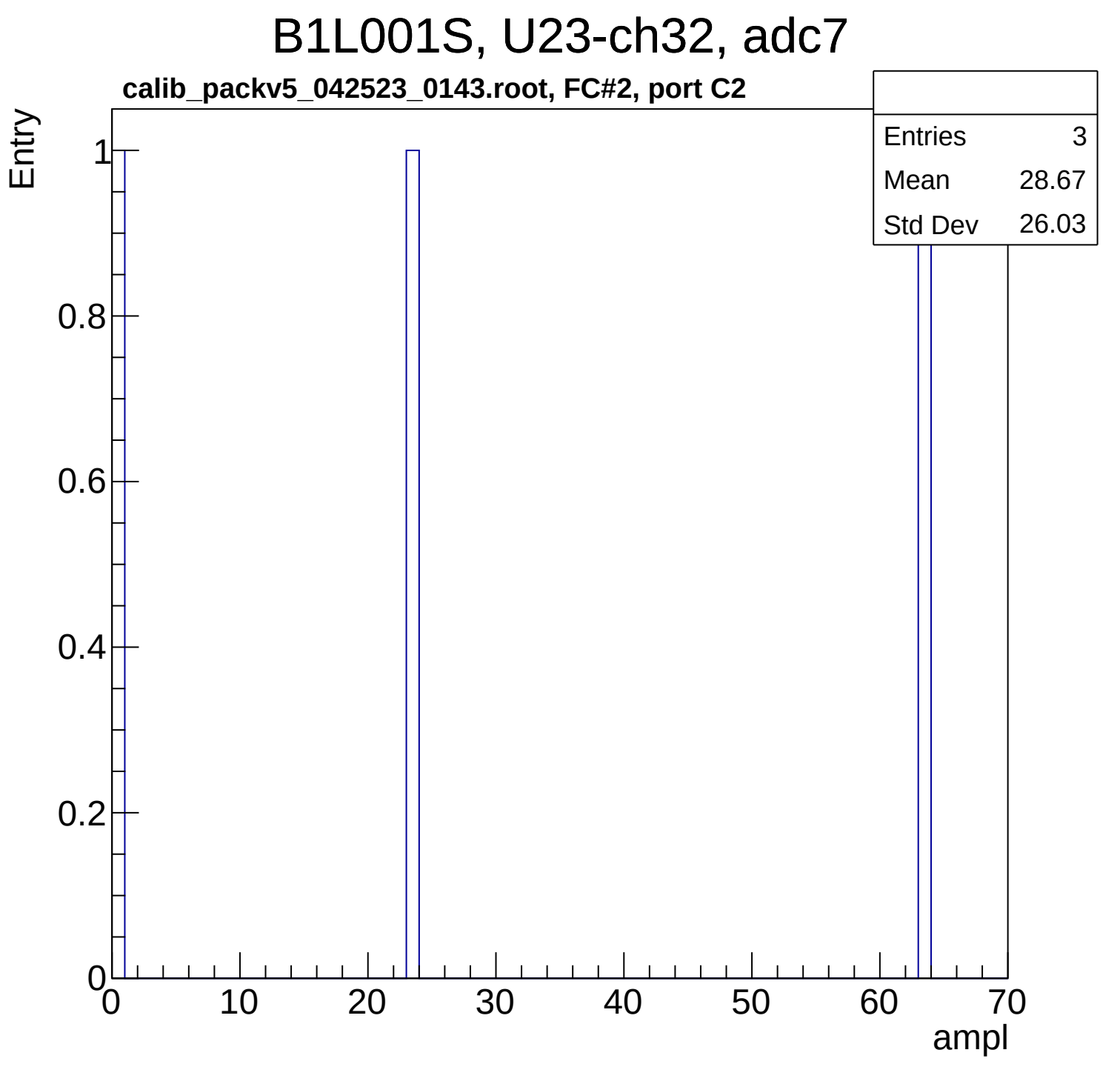
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L001S, U23-ch32, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 28.67
Std Dev 26.03
Entry
ampl
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0.4
0.6
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0
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60
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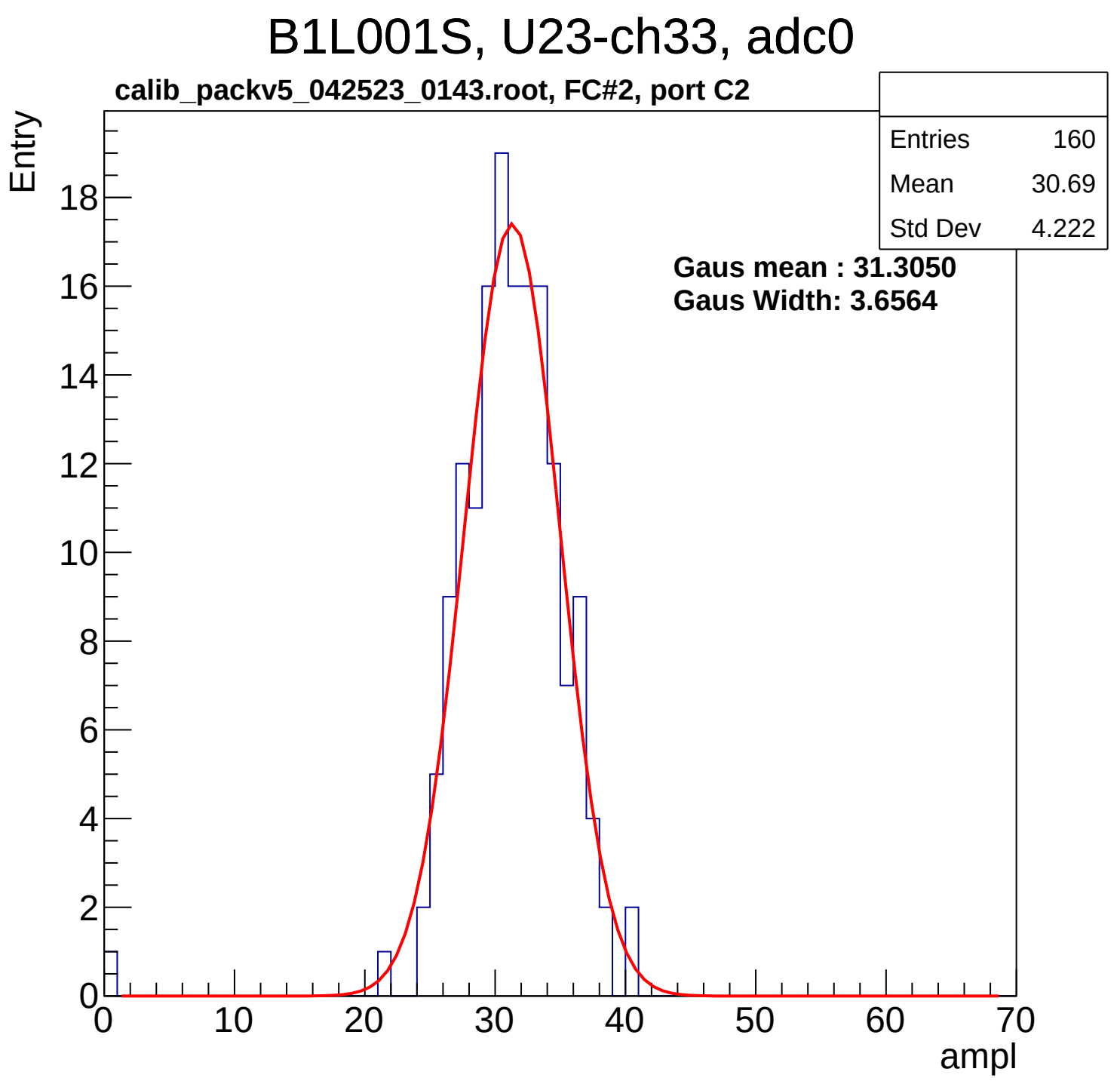
B1L001S, U23-ch33, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 30.69
Std Dev 4.222
Gaus mean : 31.3050
Gaus Width: 3.6564
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
50
60
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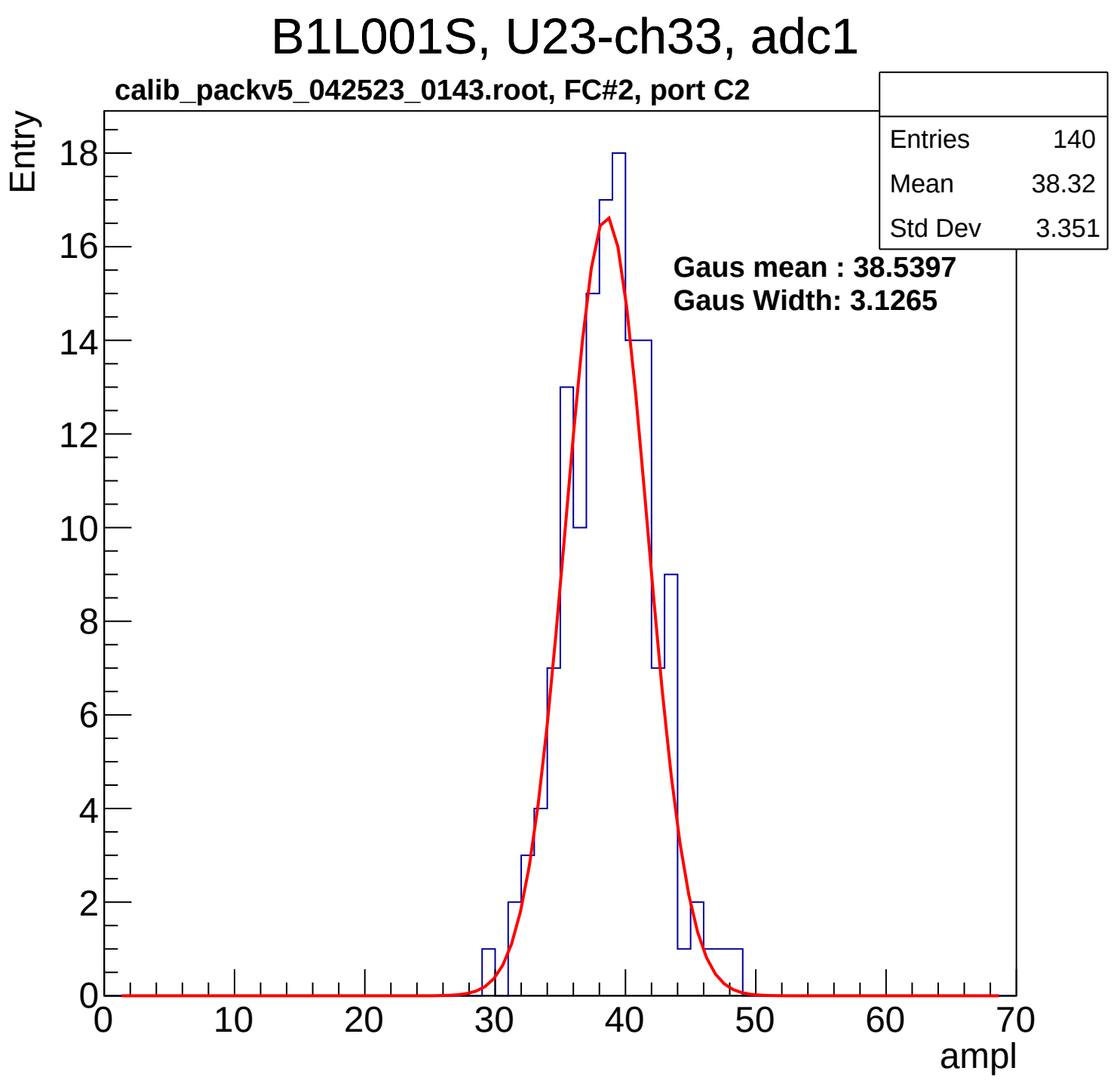
B1L001S, U23-ch33, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 38.32
Std Dev 3.351
Gaus mean : 38.5397
Gaus Width: 3.1265
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
40
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60
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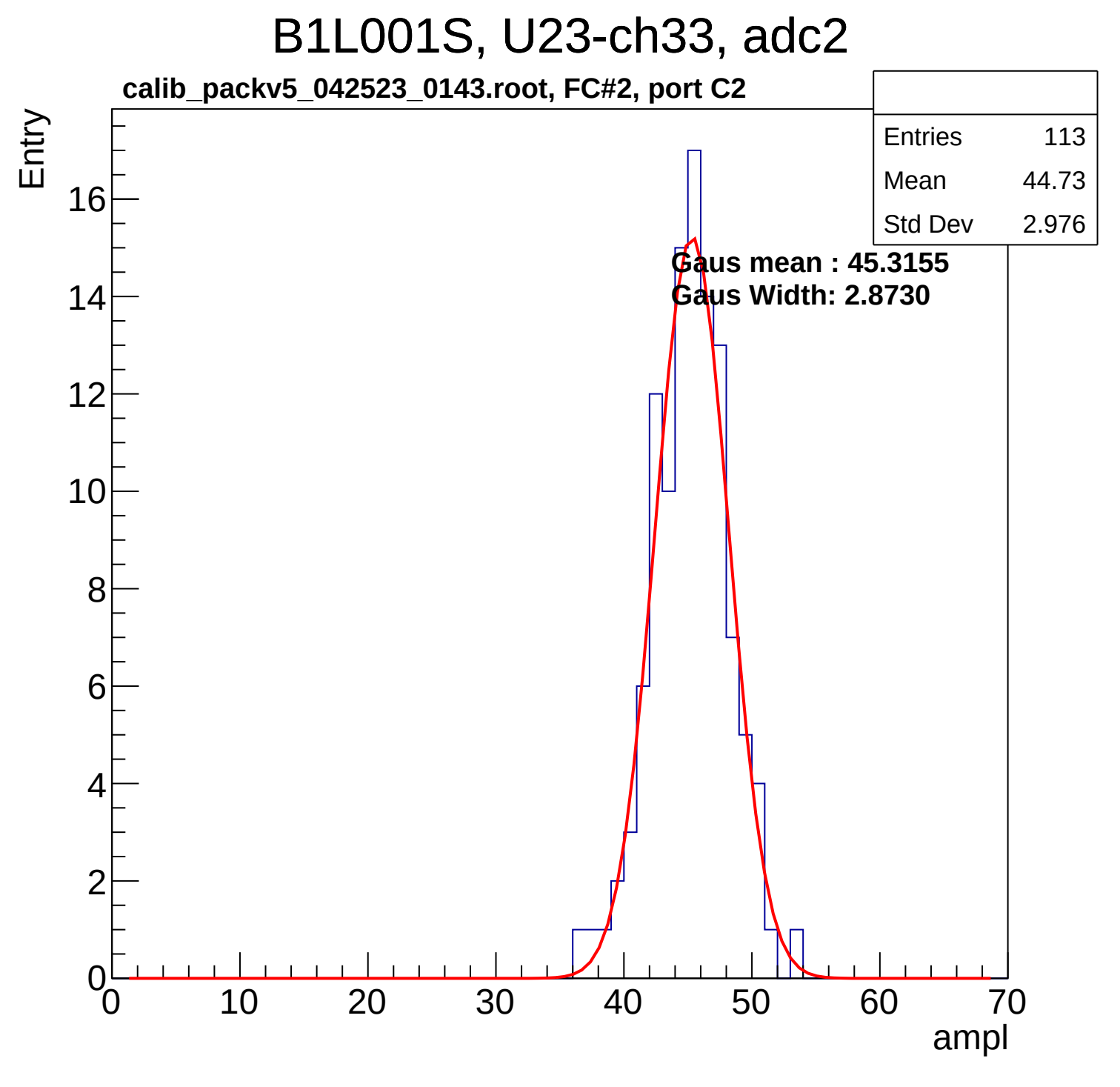
B1L001S, U23-ch33, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 44.73
Std Dev 2.976
Gaus mean : 45.3155
Gaus Width: 2.8730
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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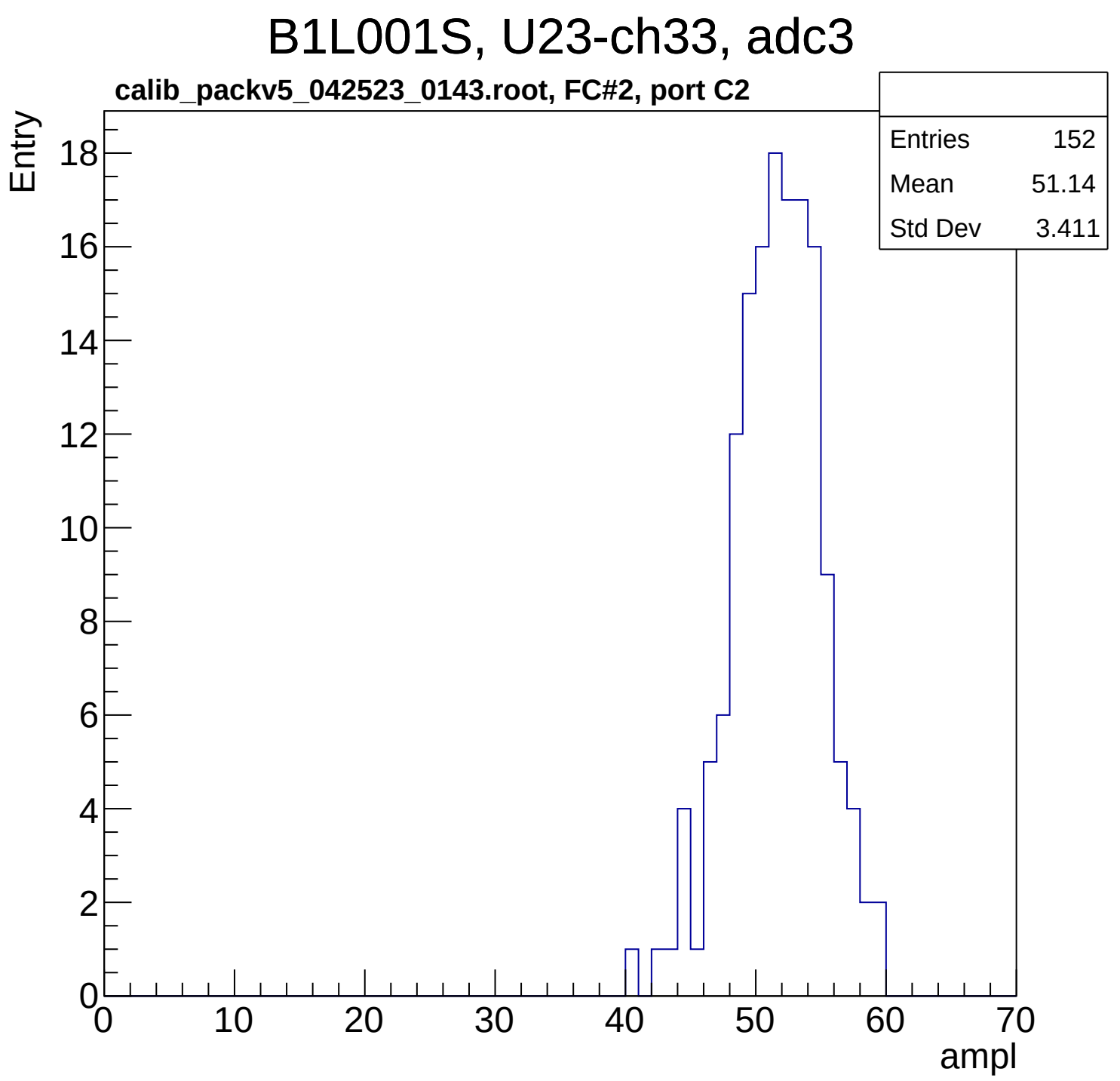
B1L001S, U23-ch33, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 51.14
Std Dev 3.411
Entry
ampl
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6
8
10
12
14
16
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0
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20
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60
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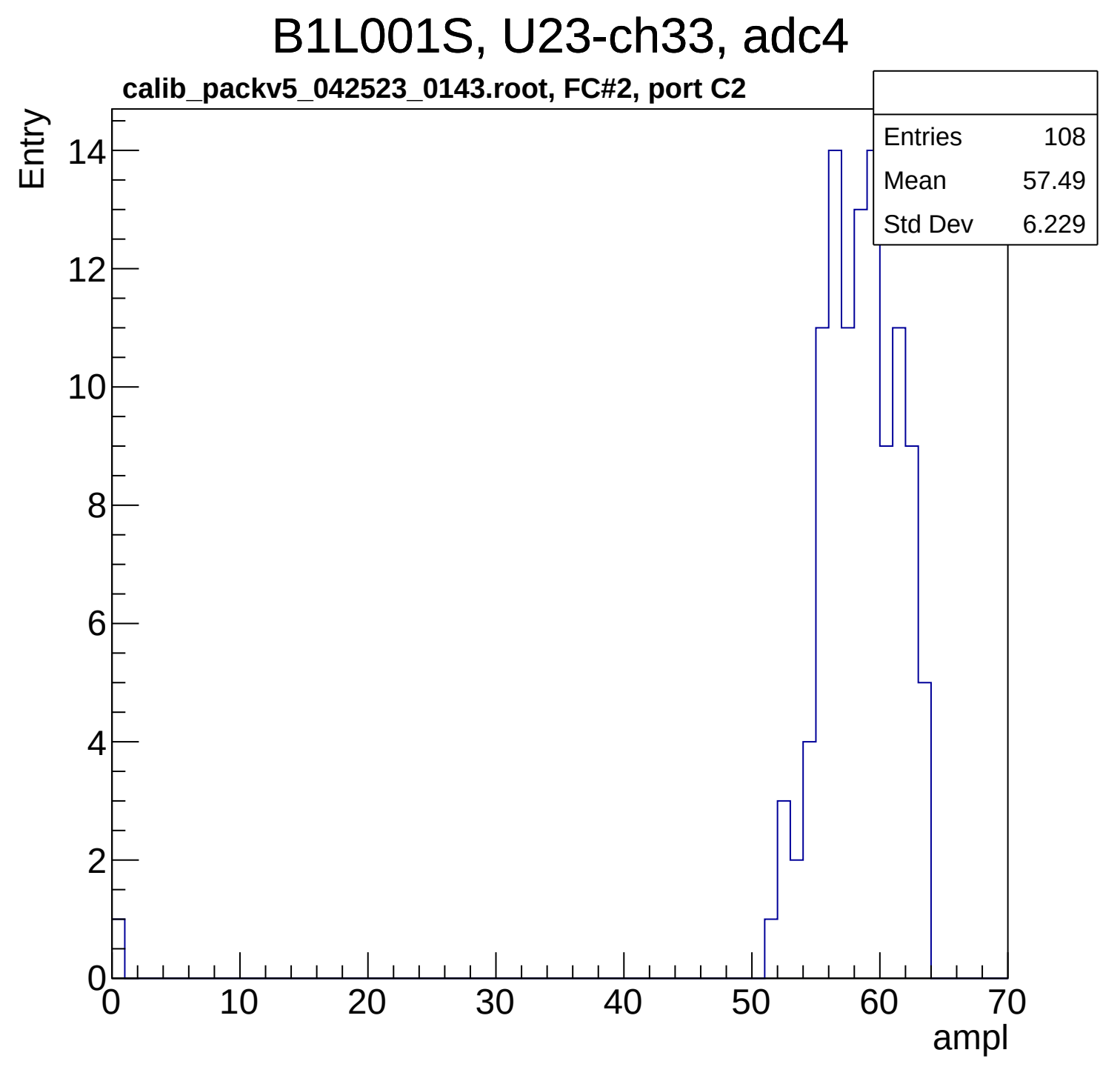
B1L001S, U23-ch33, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 57.49
Std Dev 6.229
Entry
ampl
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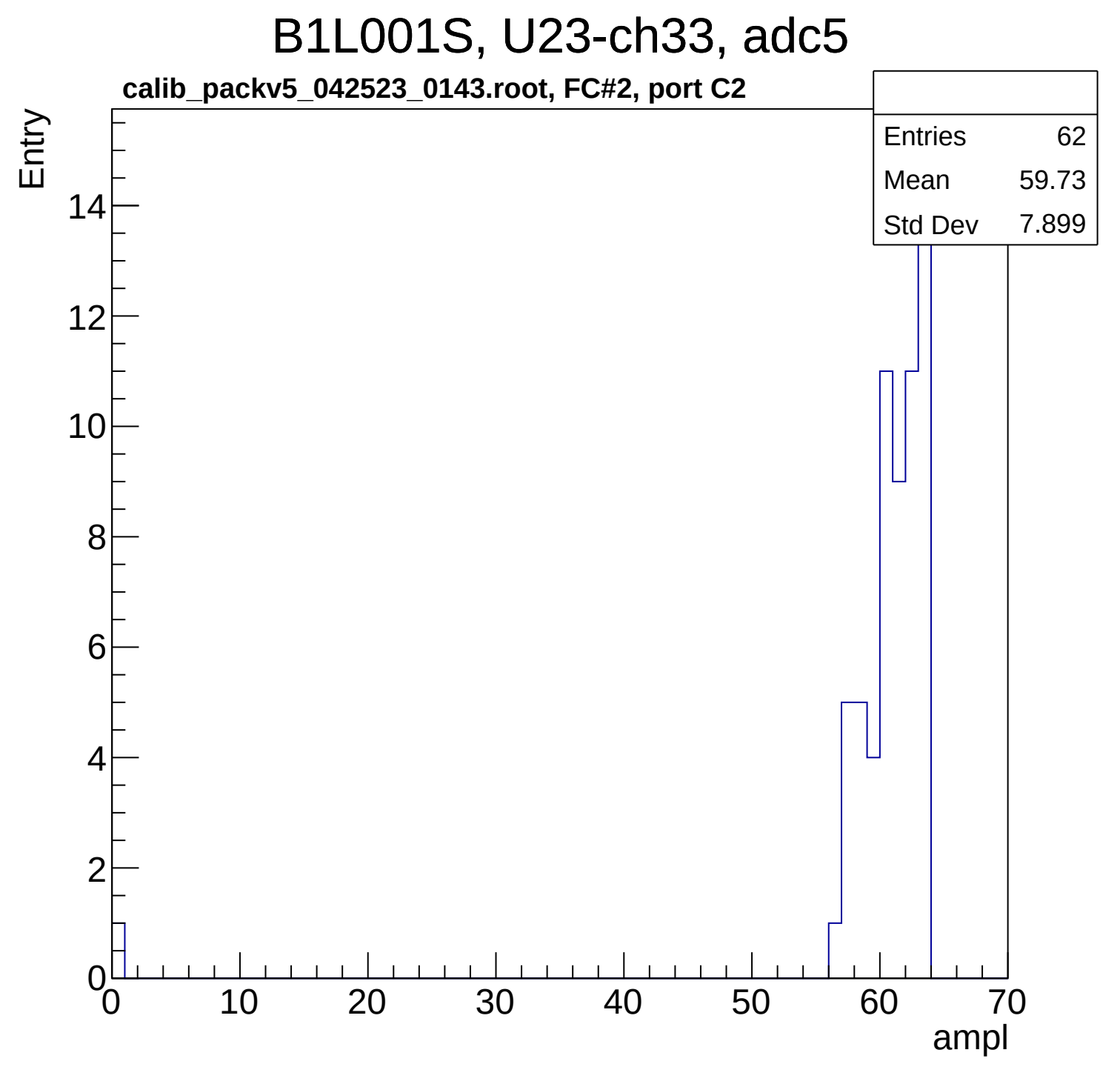
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch33, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 62
Mean 59.73
Std Dev 7.899
Entry
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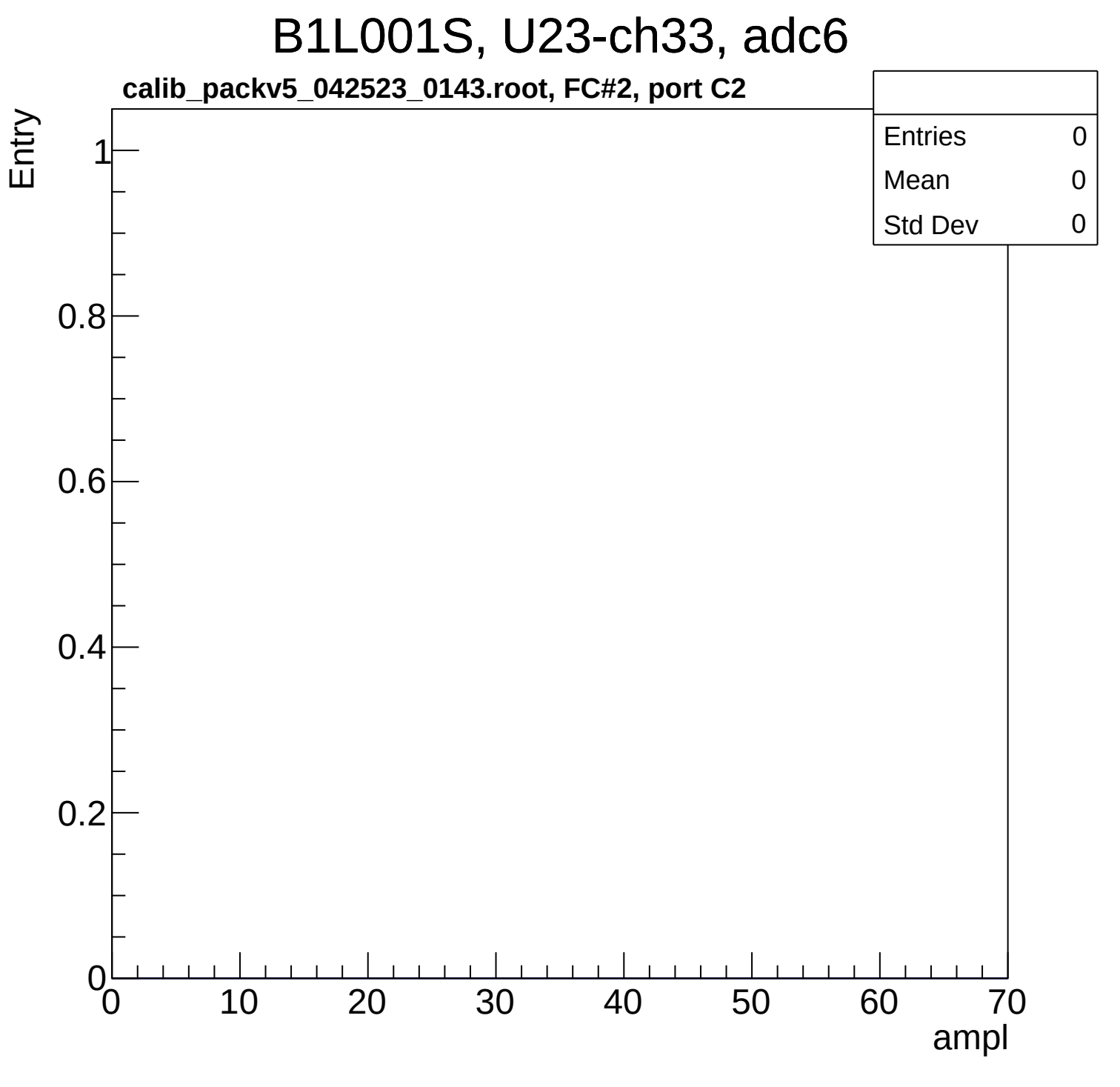
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch33, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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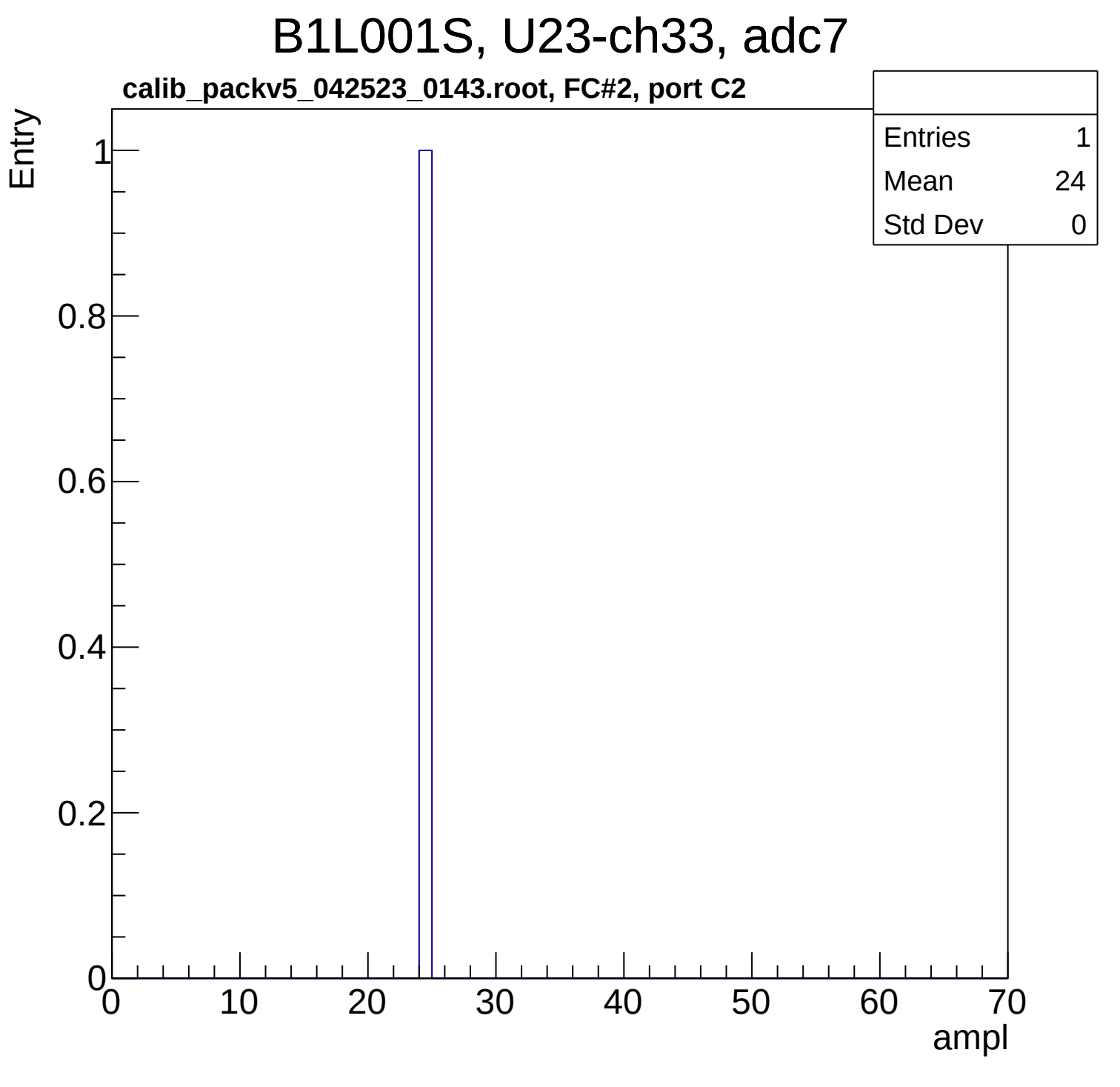
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch33, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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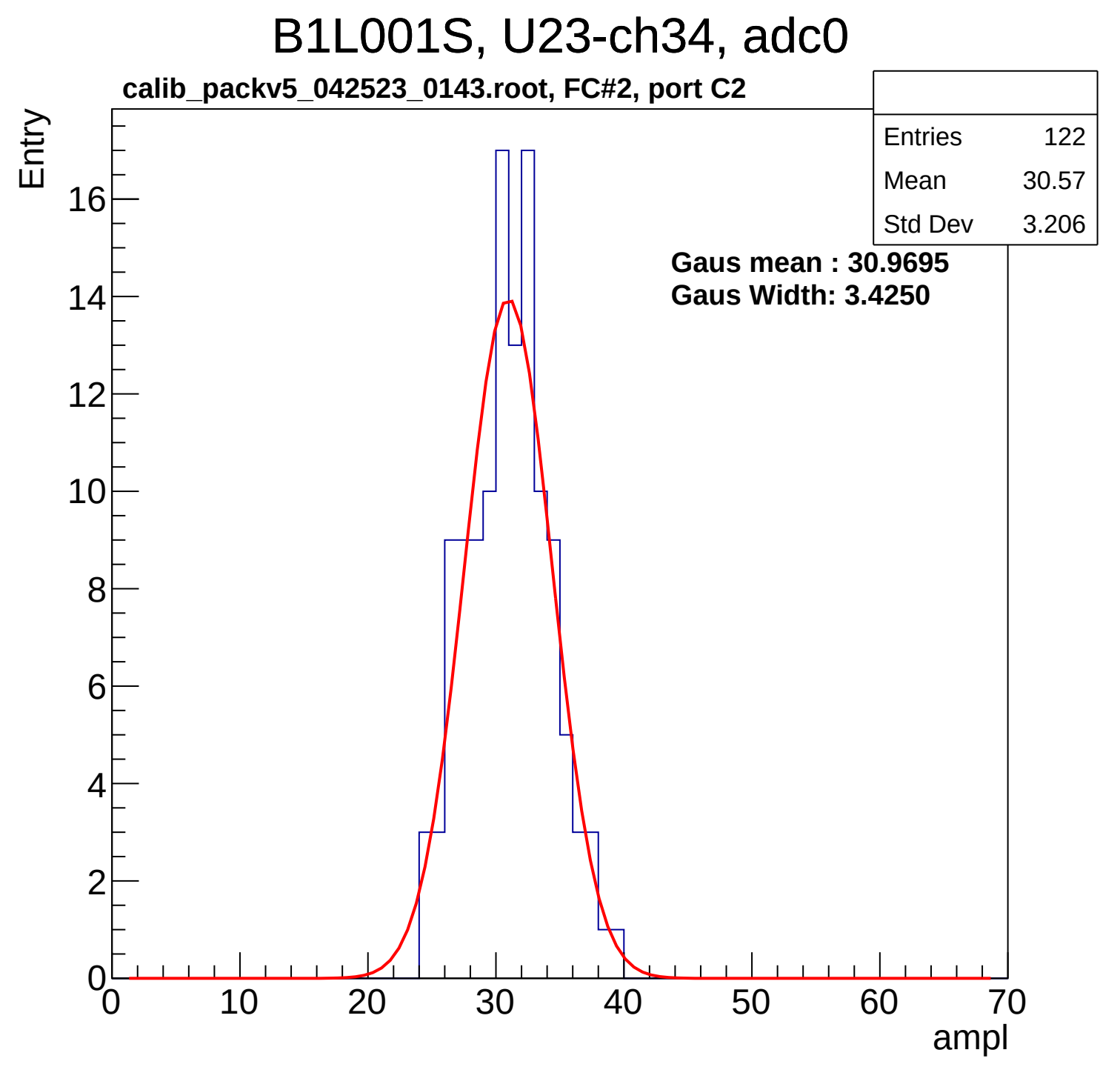
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch34, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 30.57
Std Dev 3.206
Gaus mean : 30.9695
Gaus Width: 3.4250
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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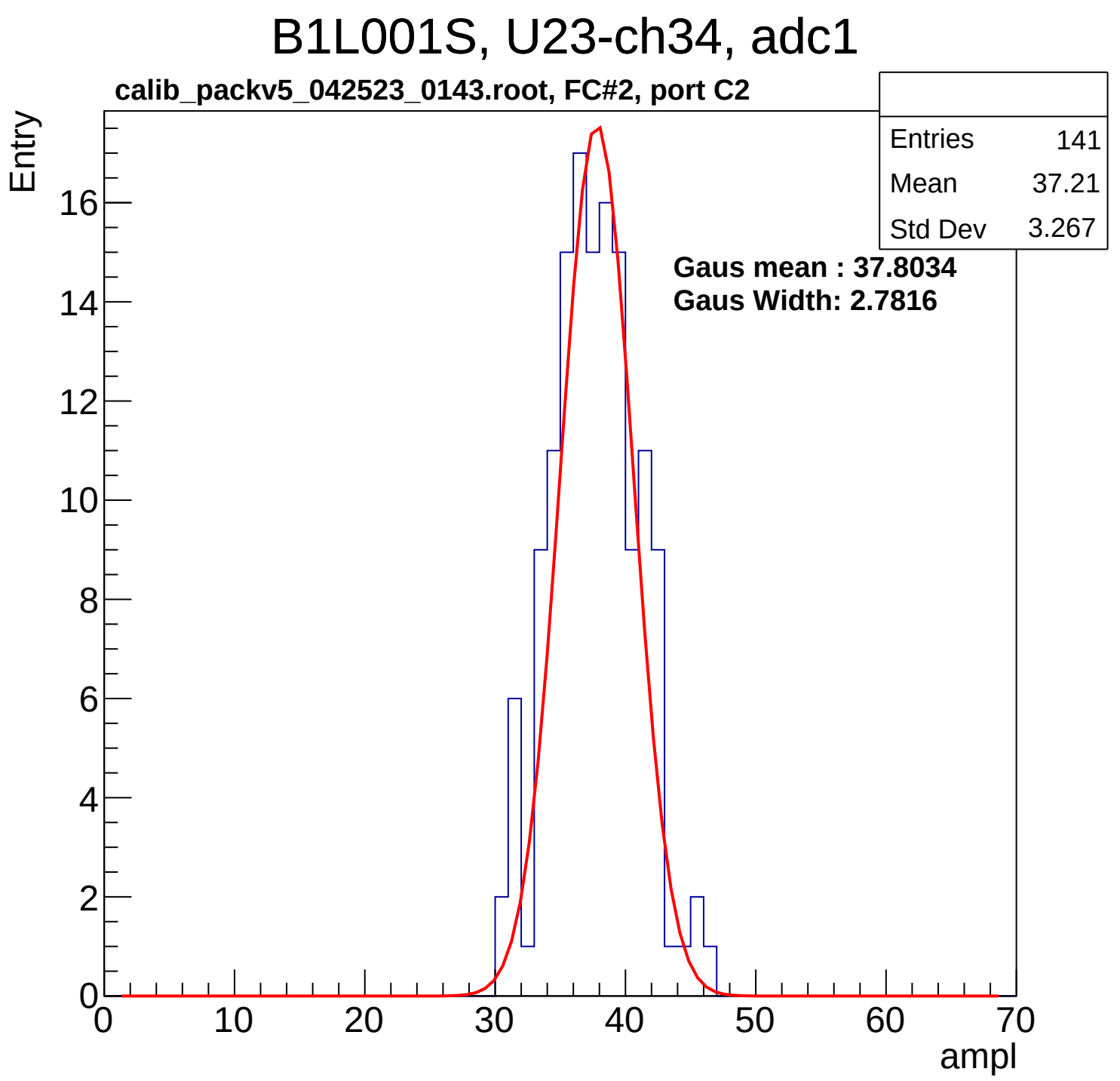
B1L001S, U23-ch34, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 37.21
Std Dev 3.267
Gaus mean : 37.8034
Gaus Width: 2.7816
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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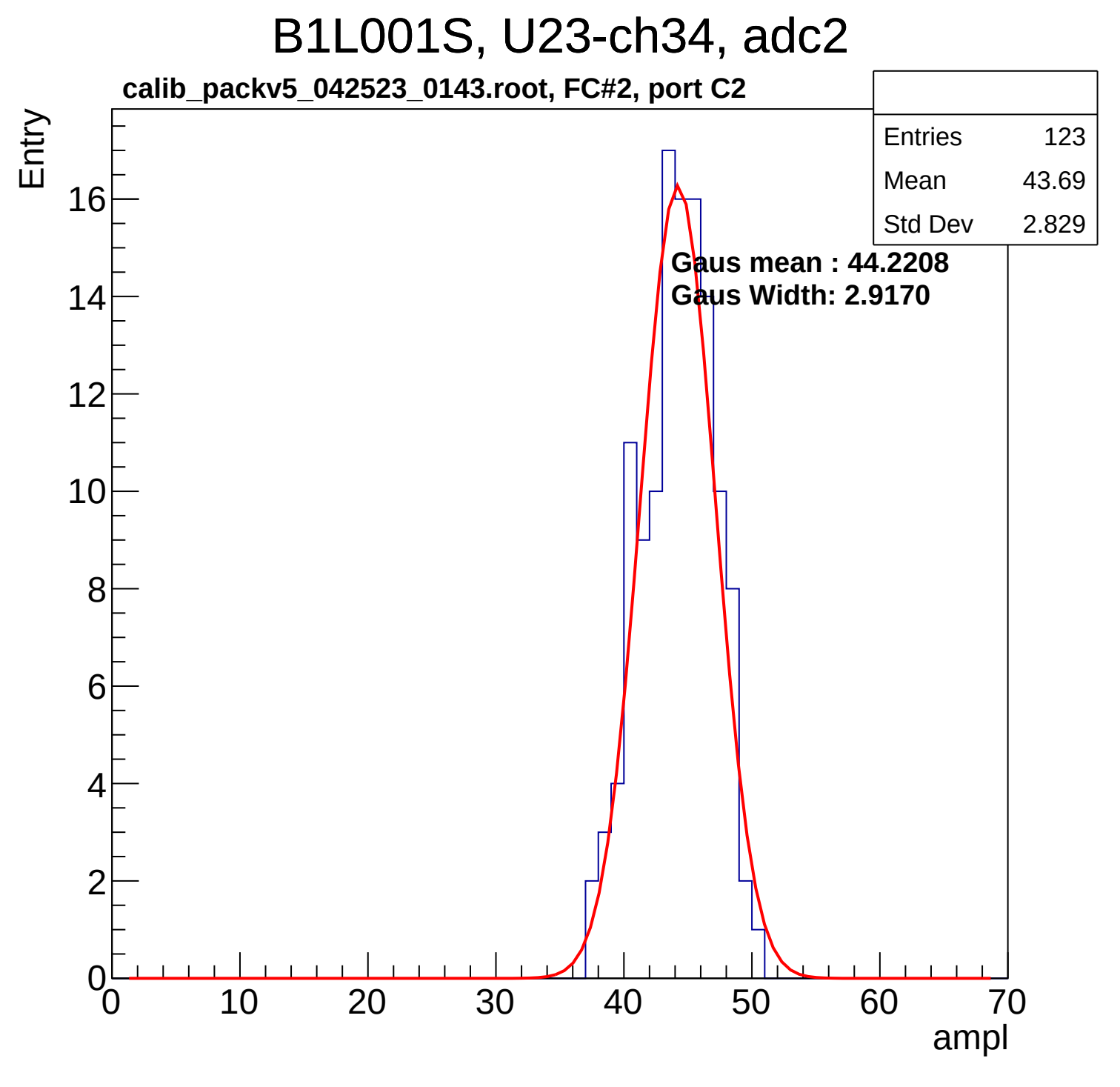
B1L001S, U23-ch34, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 43.69
Std Dev 2.829
Gaus mean : 44.2208
Gaus Width: 2.9170
Entry
ampl
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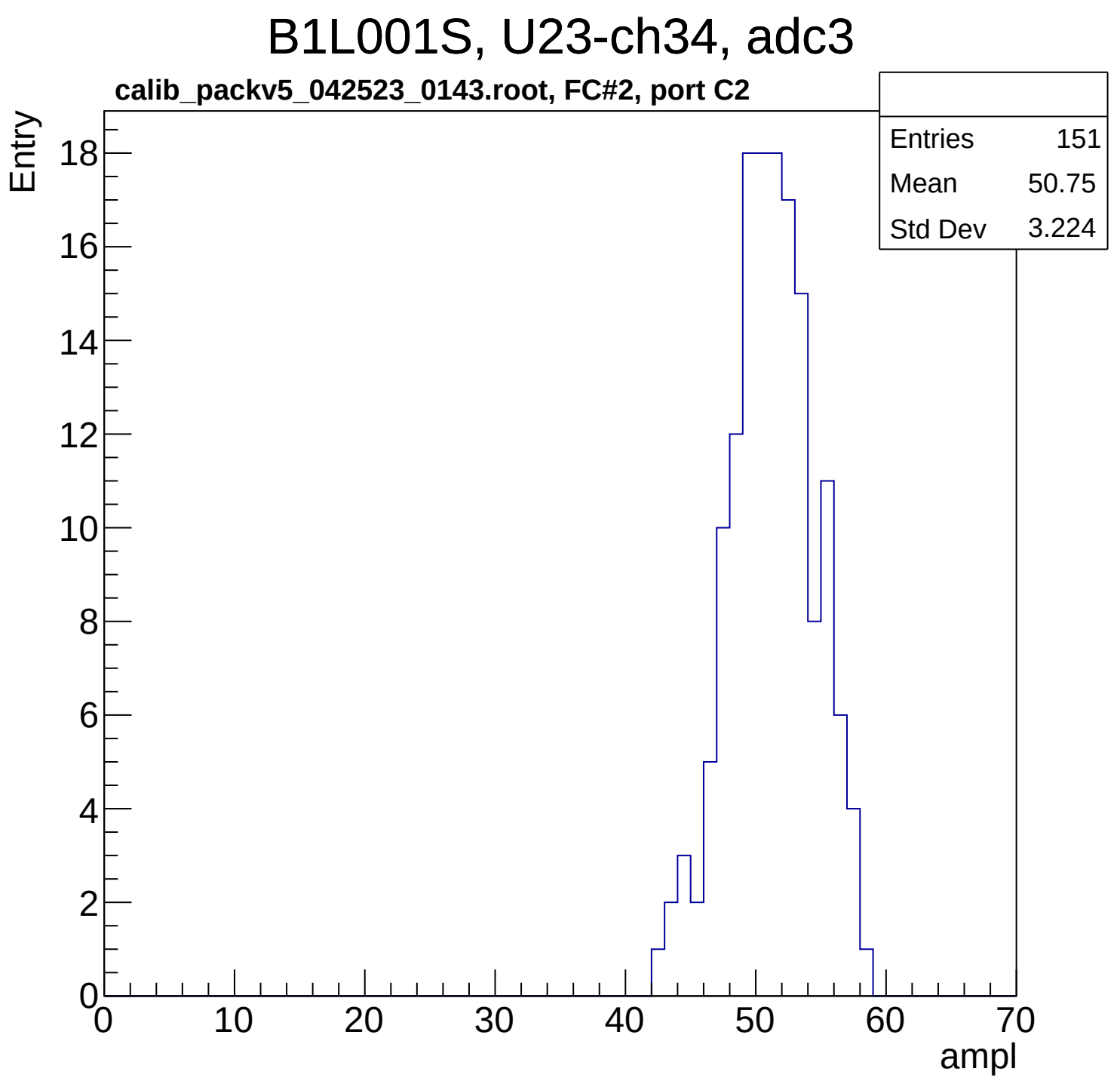
B1L001S, U23-ch34, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 50.75
Std Dev 3.224
Entry
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10
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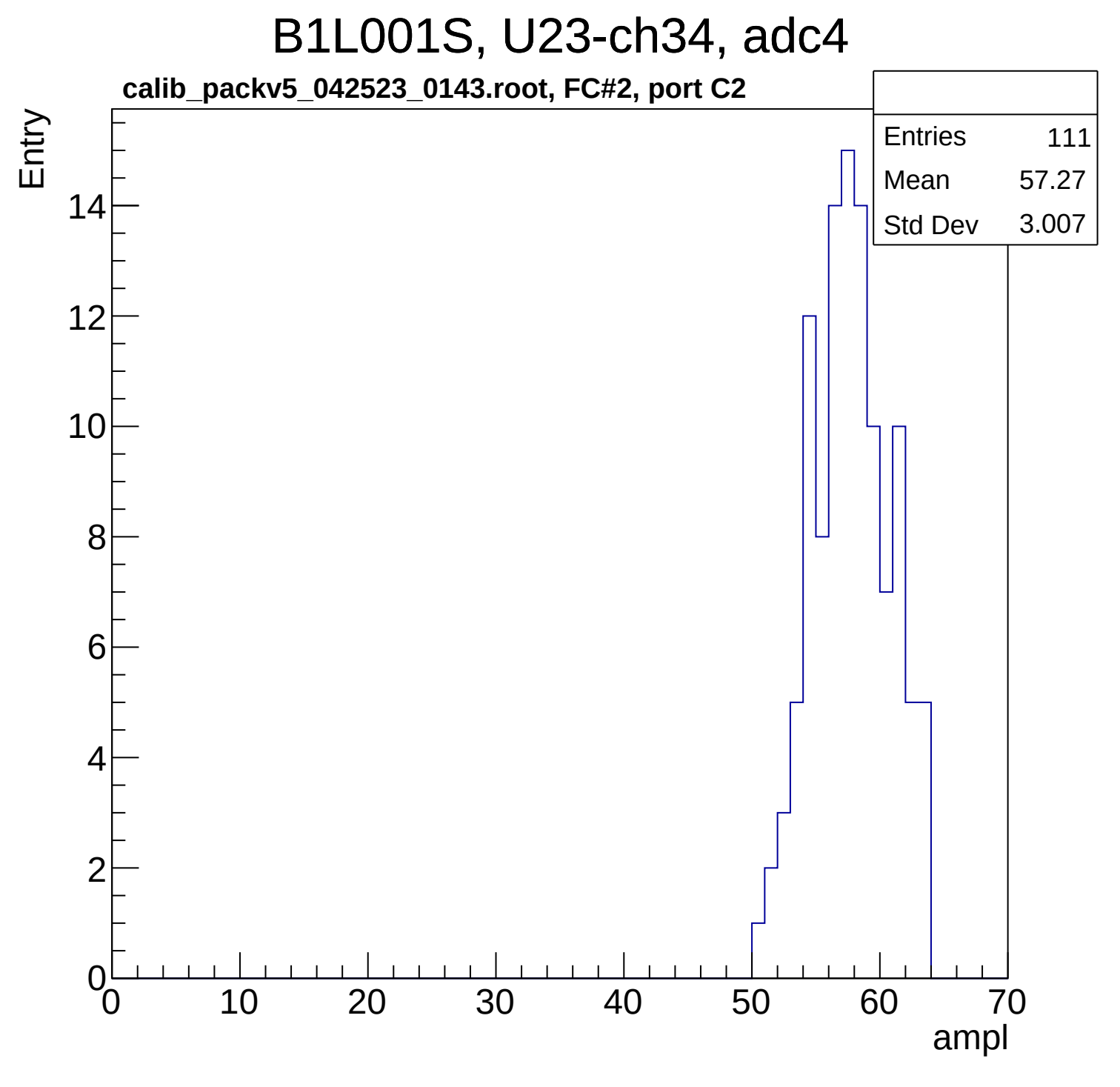
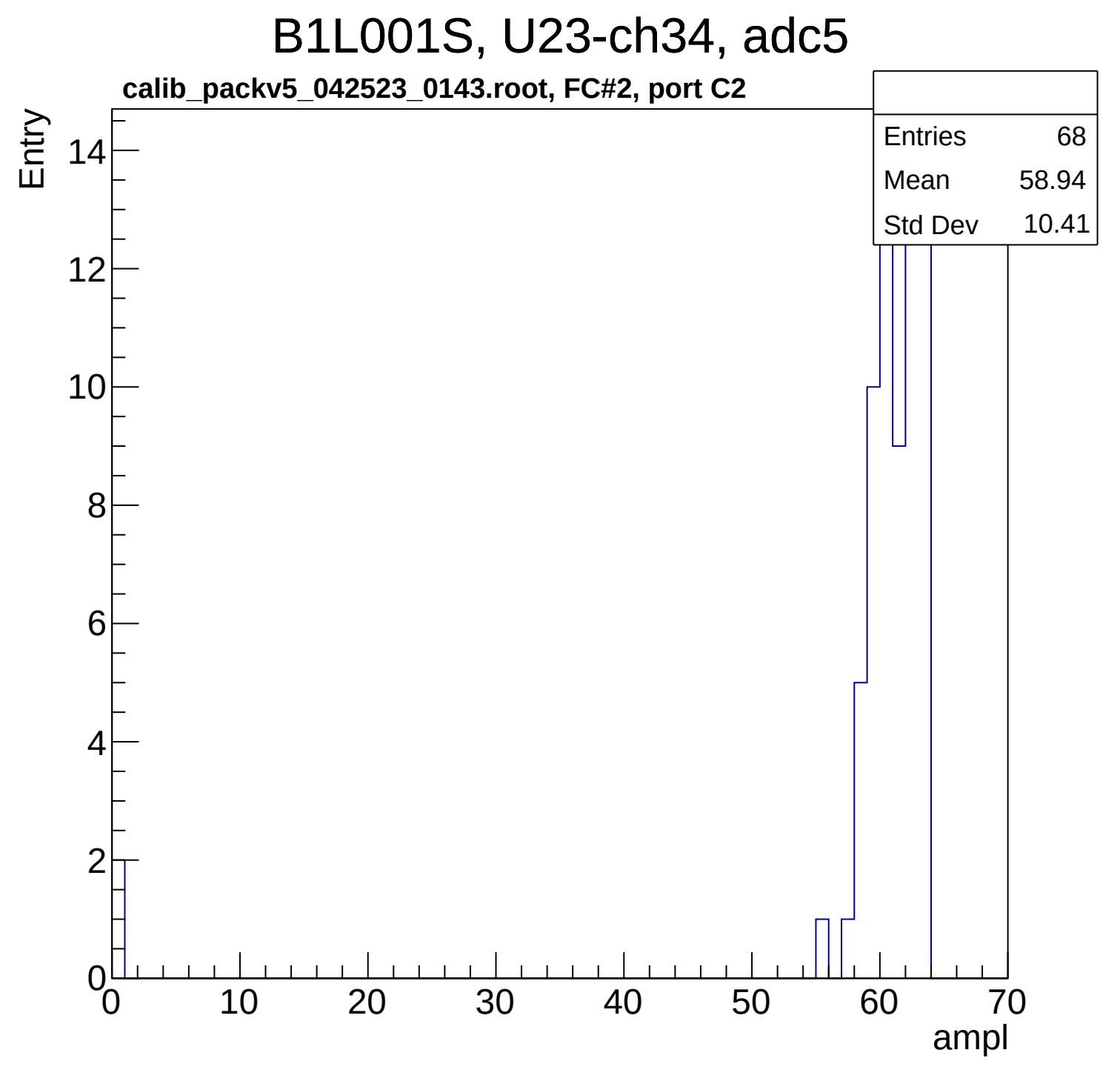
50
60
70
ampl


B1L001S, U23-ch34, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 57.27
Std Dev 3.007
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch34, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 68
Mean 58.94
Std Dev 10.41
Entry
ampl
0
2
4
6
8
10
12
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0
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20
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60
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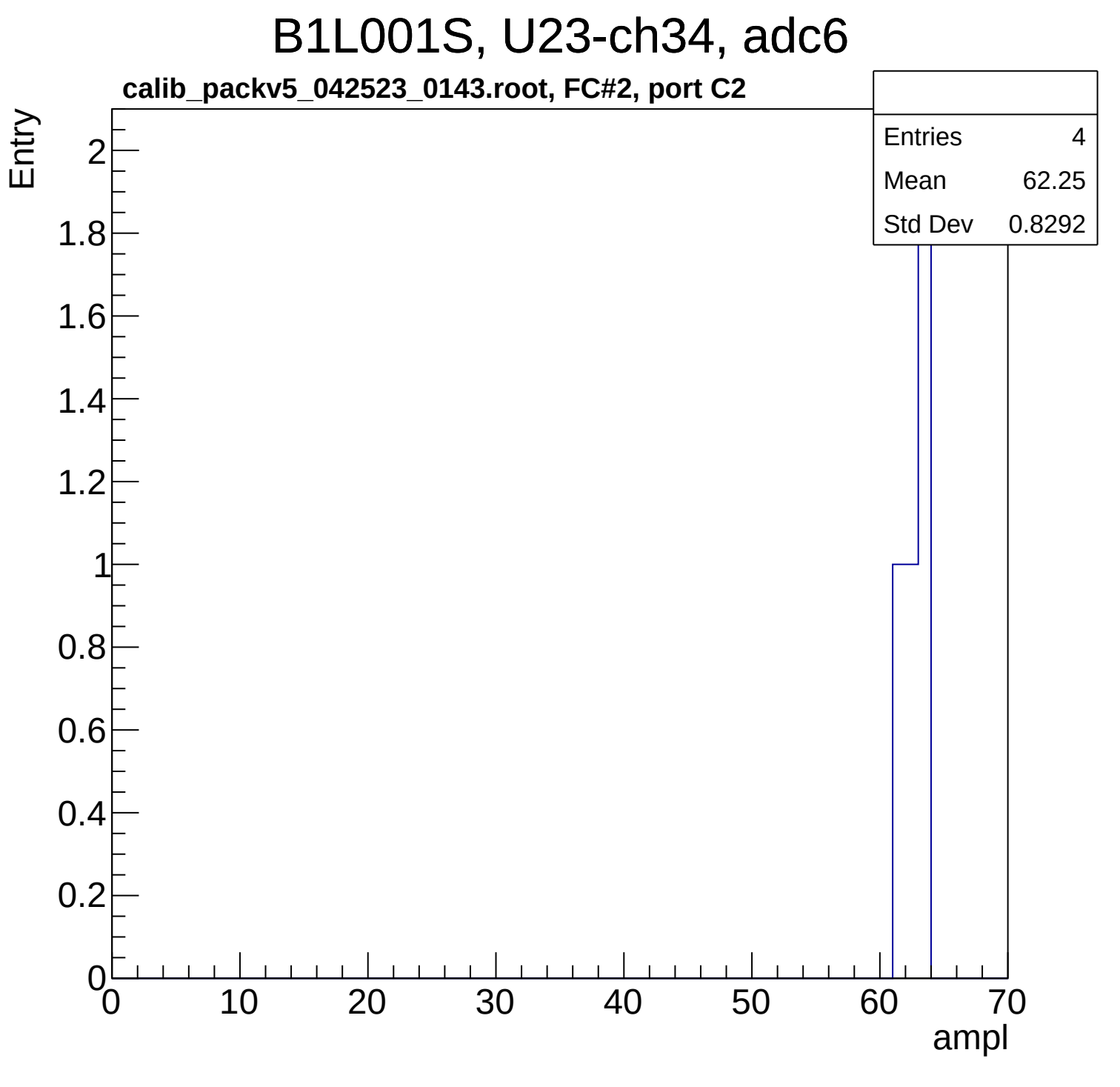
B1L001S, U23-ch34, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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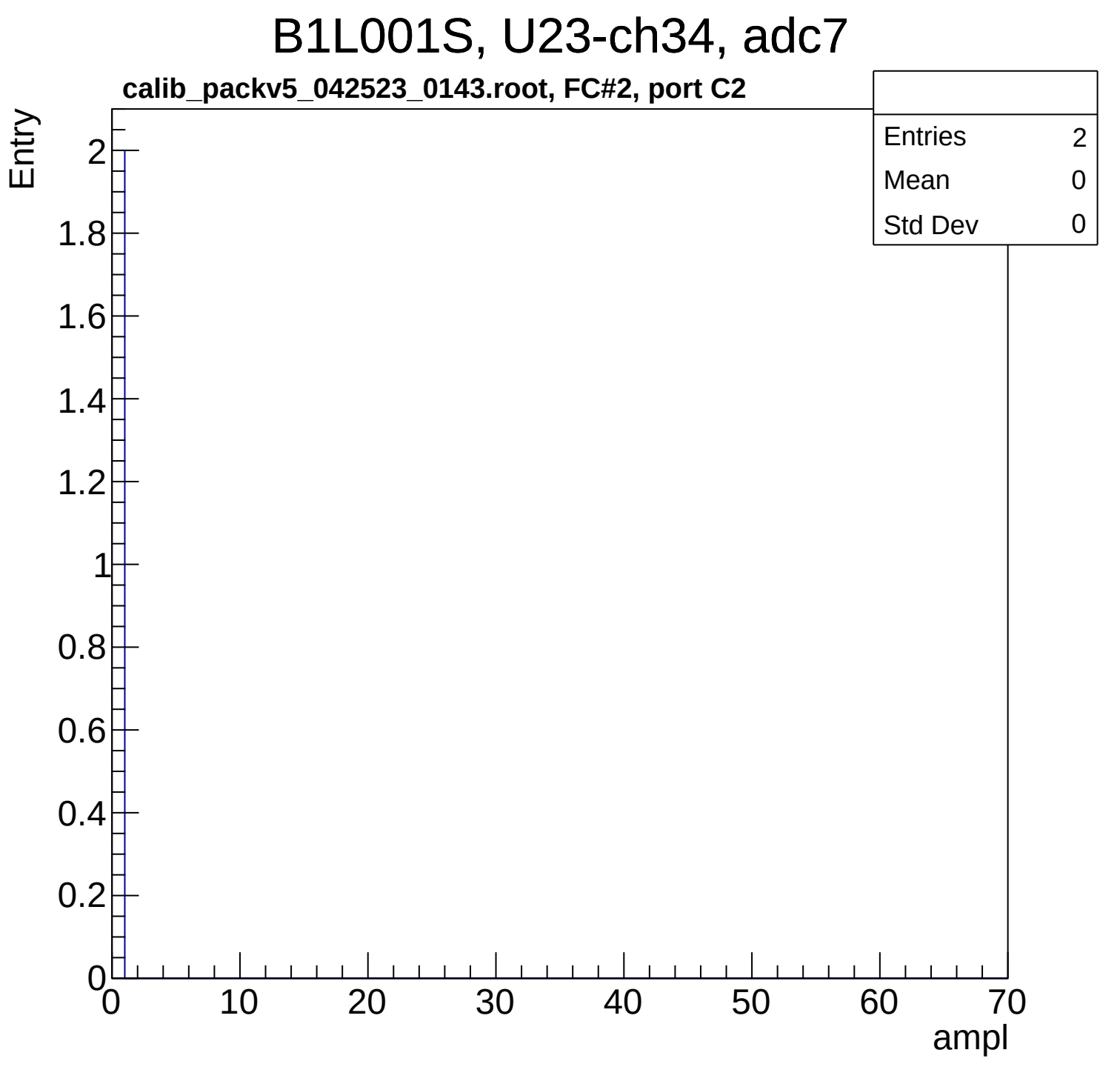
B1L001S, U23-ch34, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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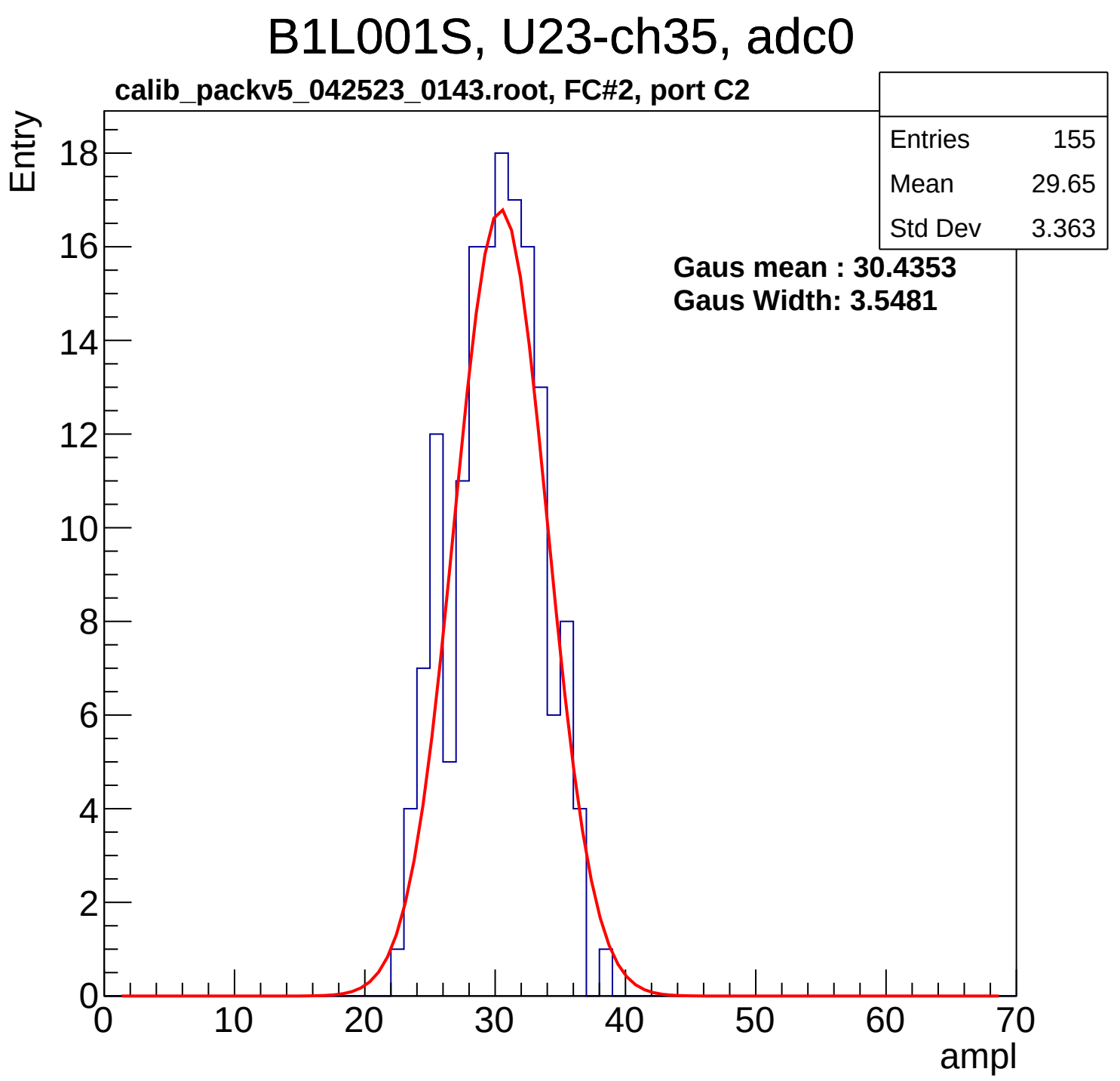
B1L001S, U23-ch35, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 29.65
Std Dev 3.363
Gaus mean : 30.4353
Gaus Width: 3.5481
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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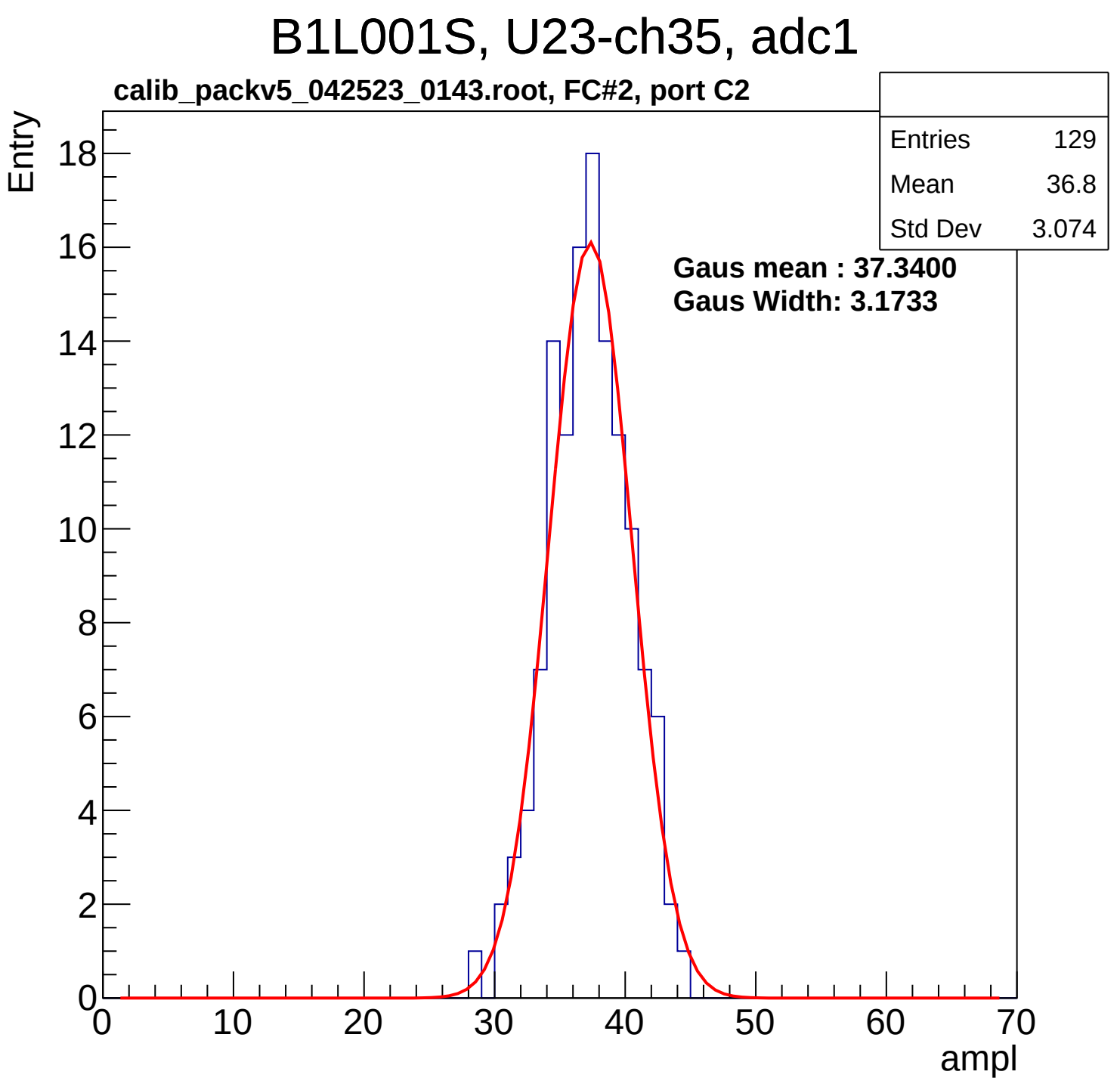
B1L001S, U23-ch35, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 36.8
Std Dev 3.074
Gaus mean : 37.3400
Gaus Width: 3.1733
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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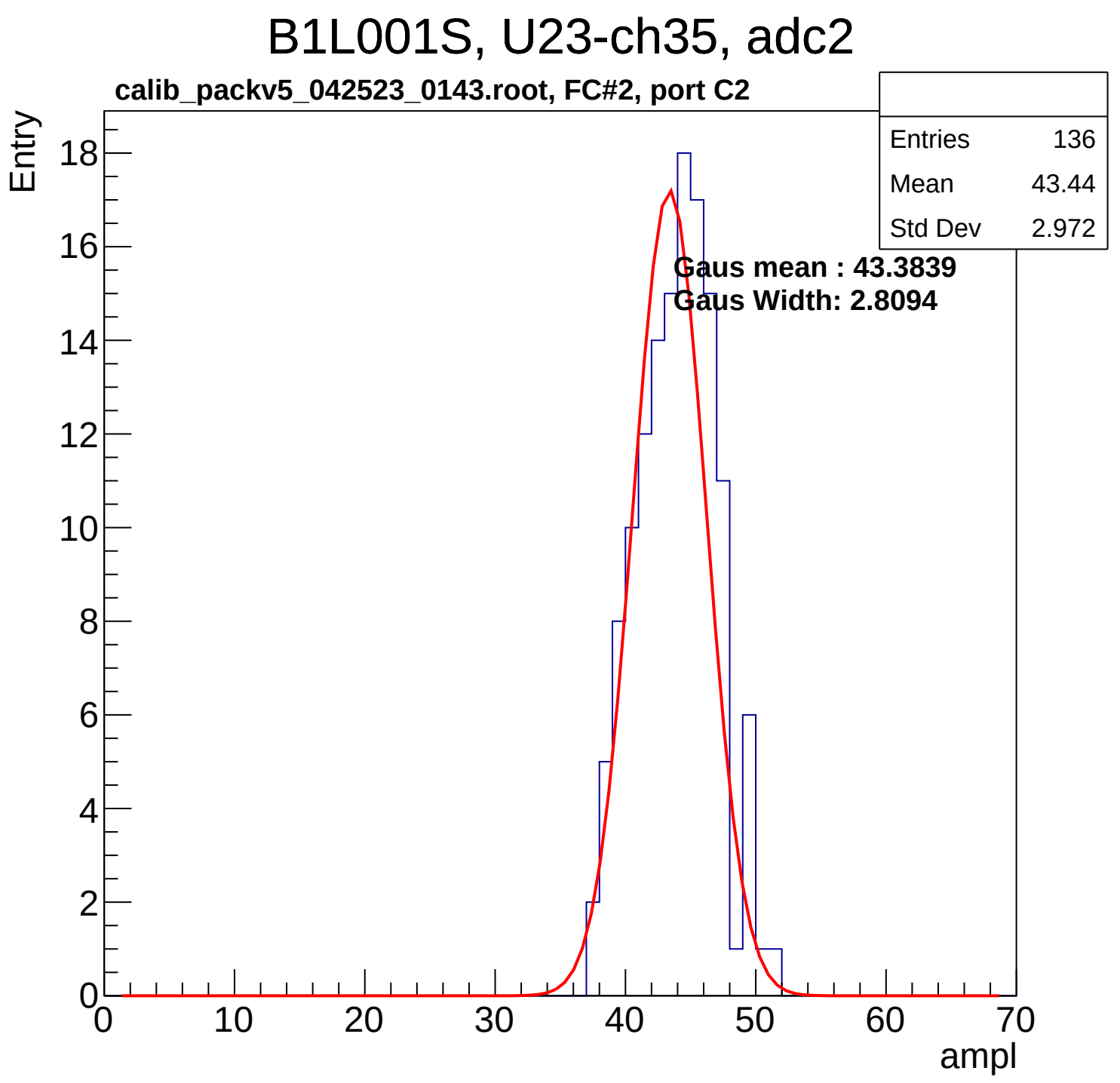
B1L001S, U23-ch35, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 43.44
Std Dev 2.972
Gaus mean : 43.3839
Gaus Width: 2.8094
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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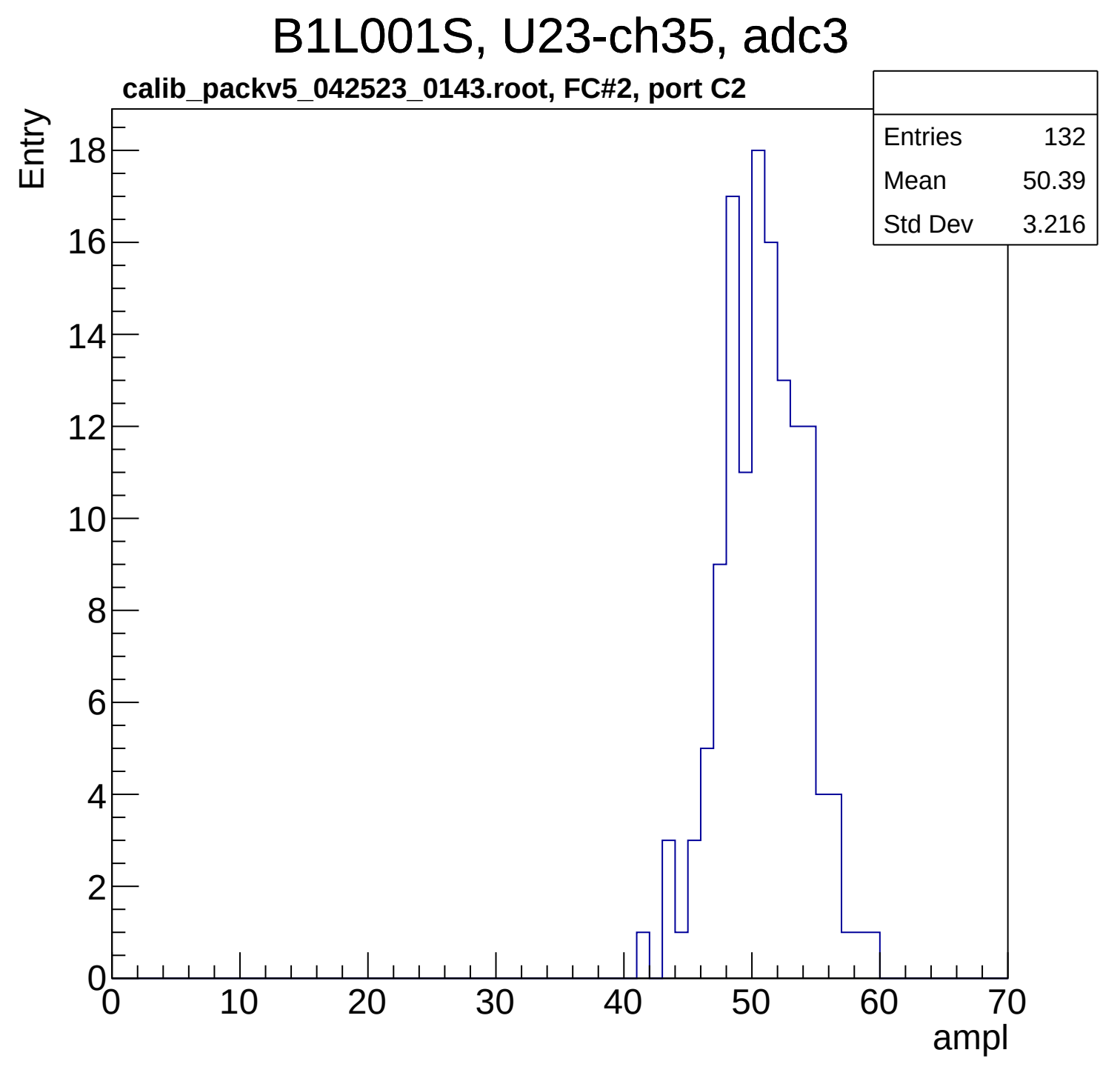
B1L001S, U23-ch35, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 50.39
Std Dev 3.216
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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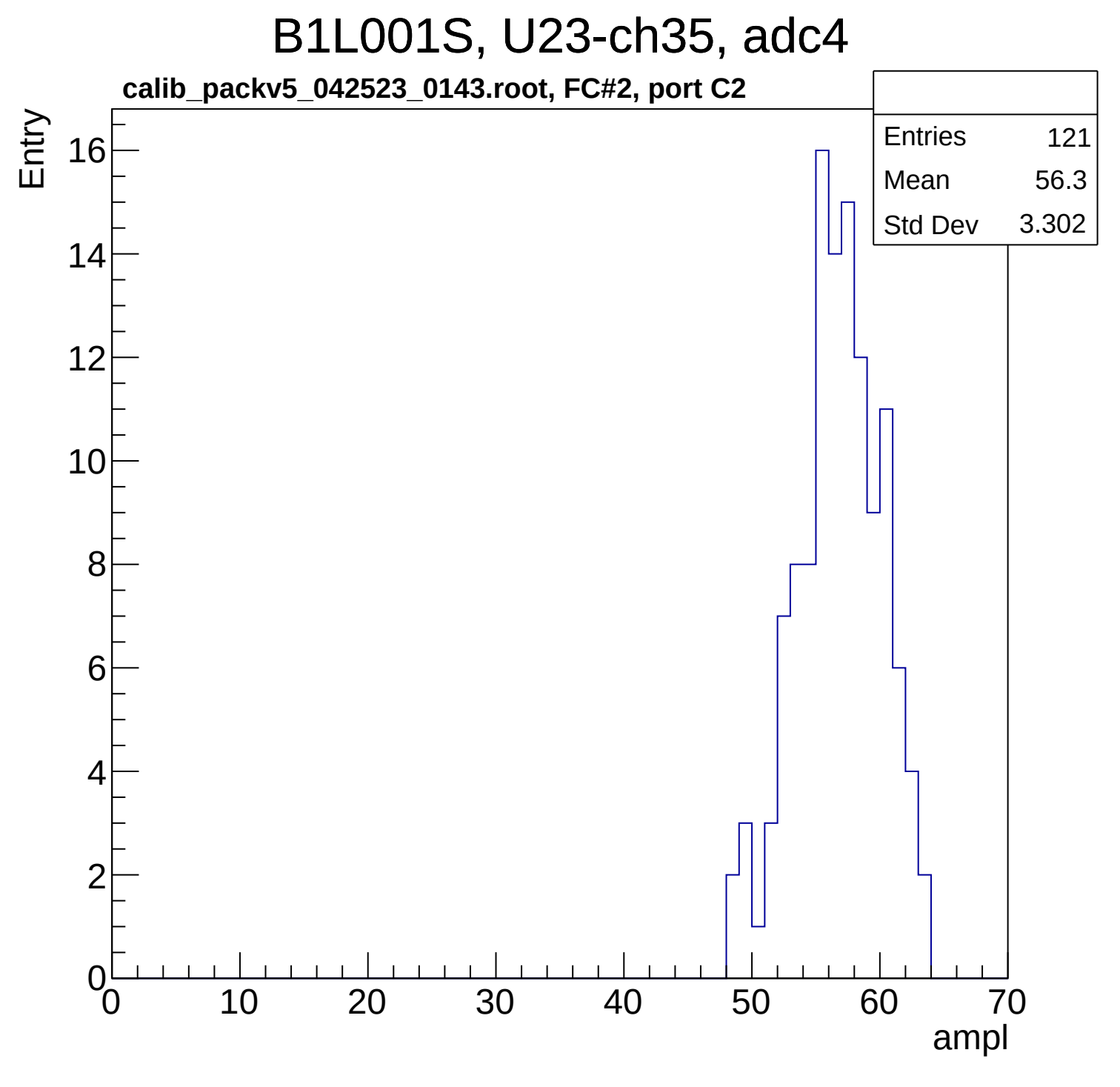
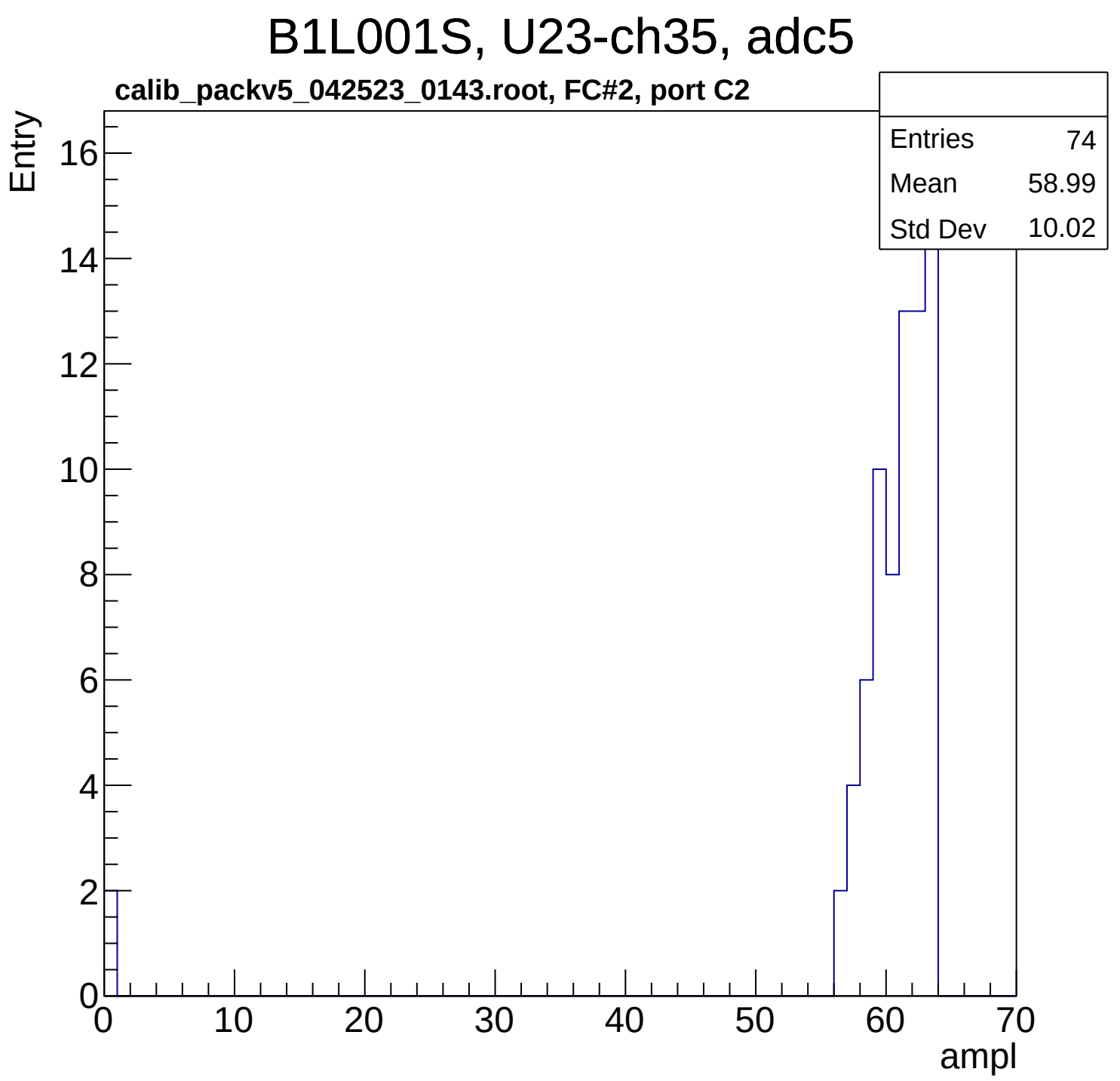

B1L001S, U23-ch35, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 56.3
Std Dev 3.302
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch35, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 58.99
Std Dev 10.02
Entry
ampl
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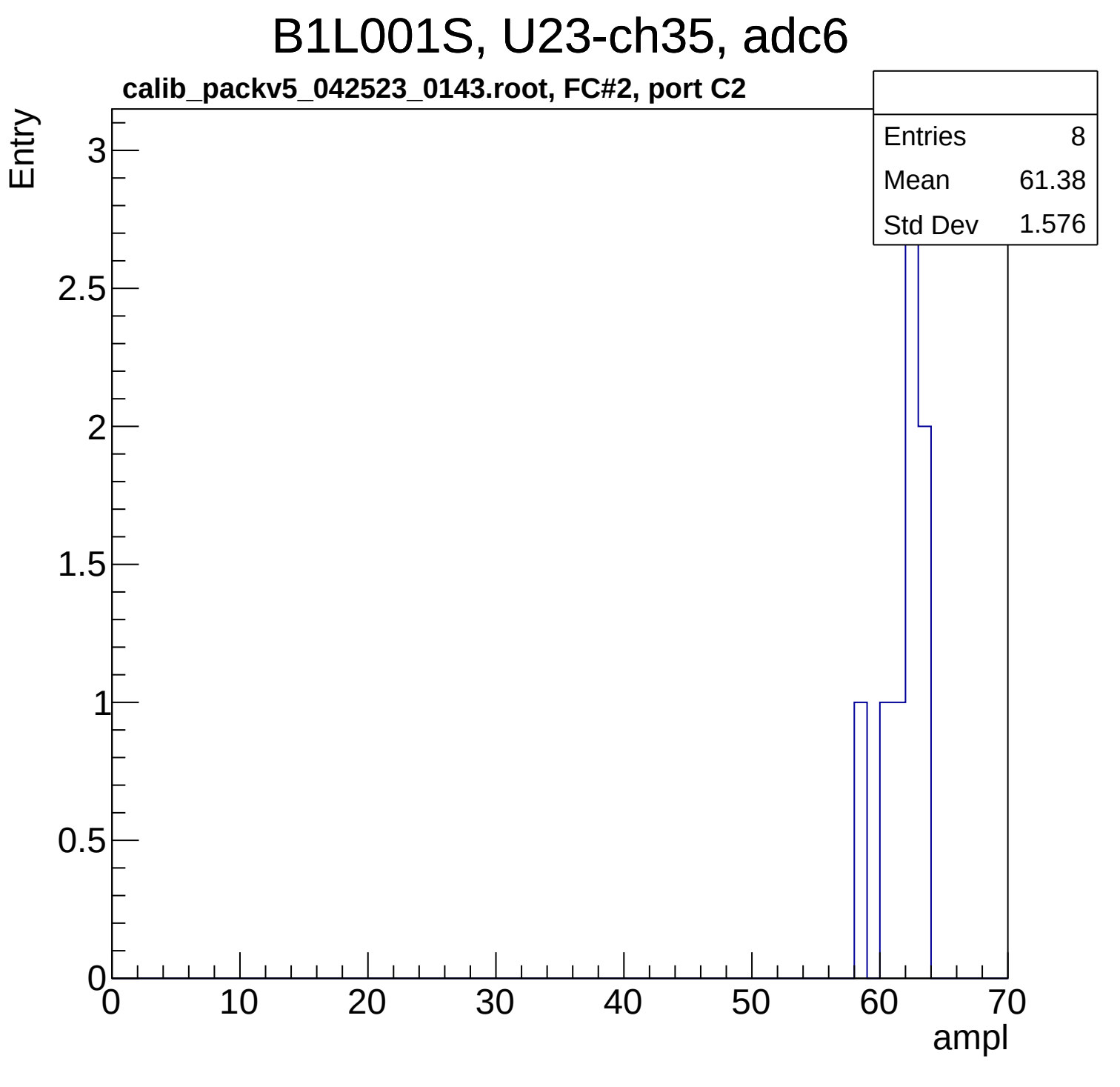
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch35, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.38
Std Dev 1.576
Entry
ampl
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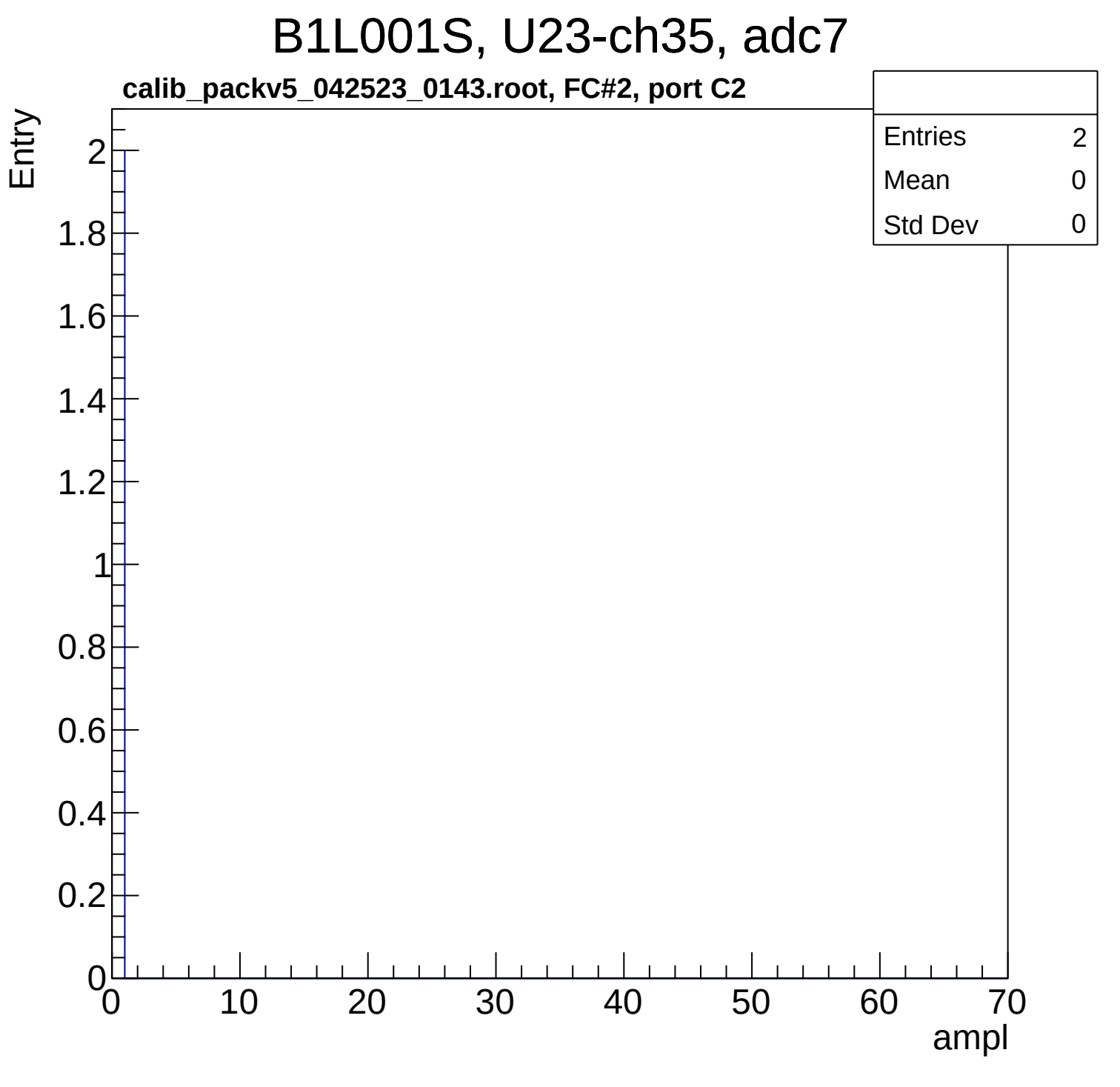
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch35, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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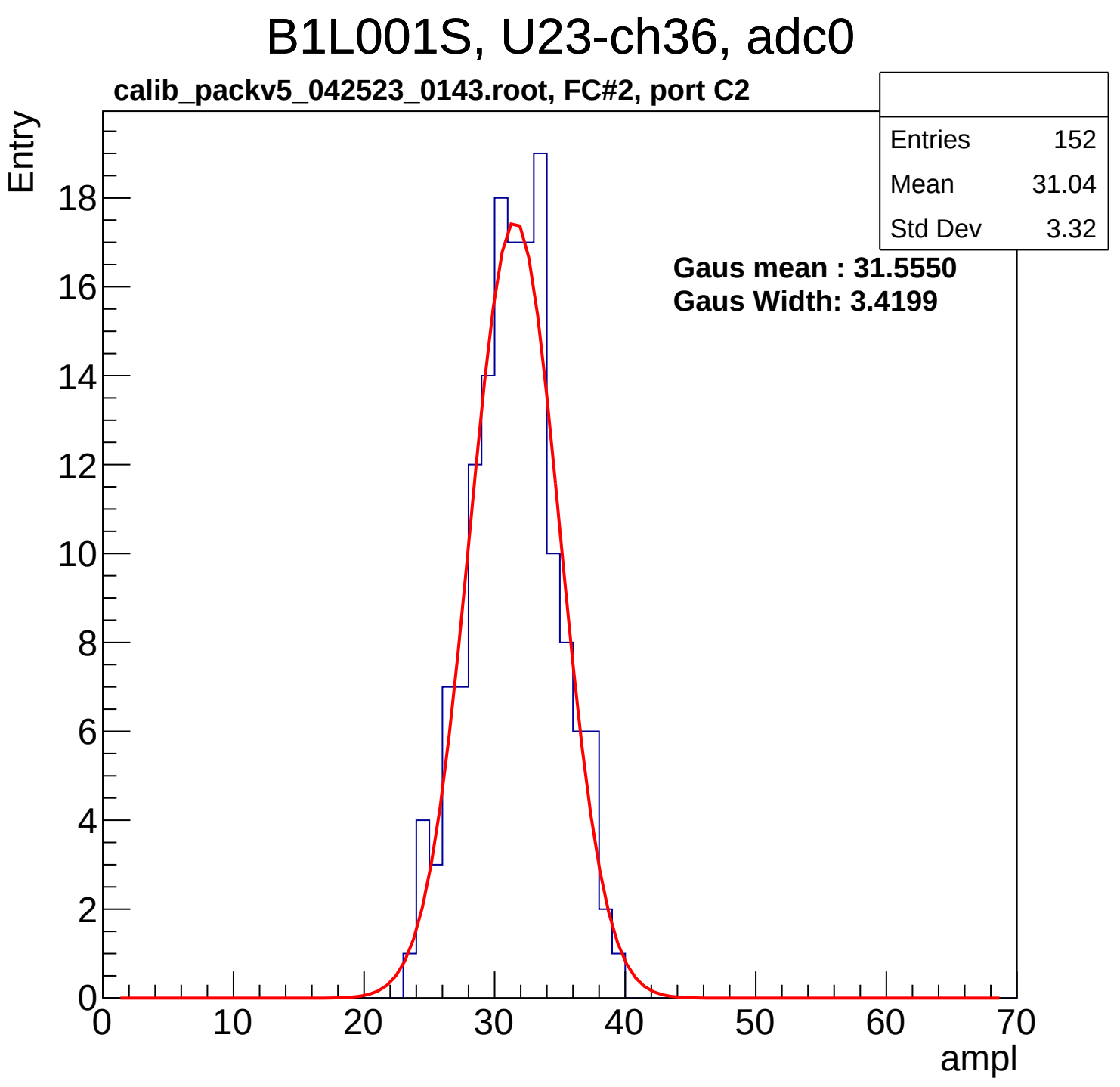
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch36, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 31.04
Std Dev 3.32
Gaus mean : 31.5550
Gaus Width: 3.4199
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
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40
50
60
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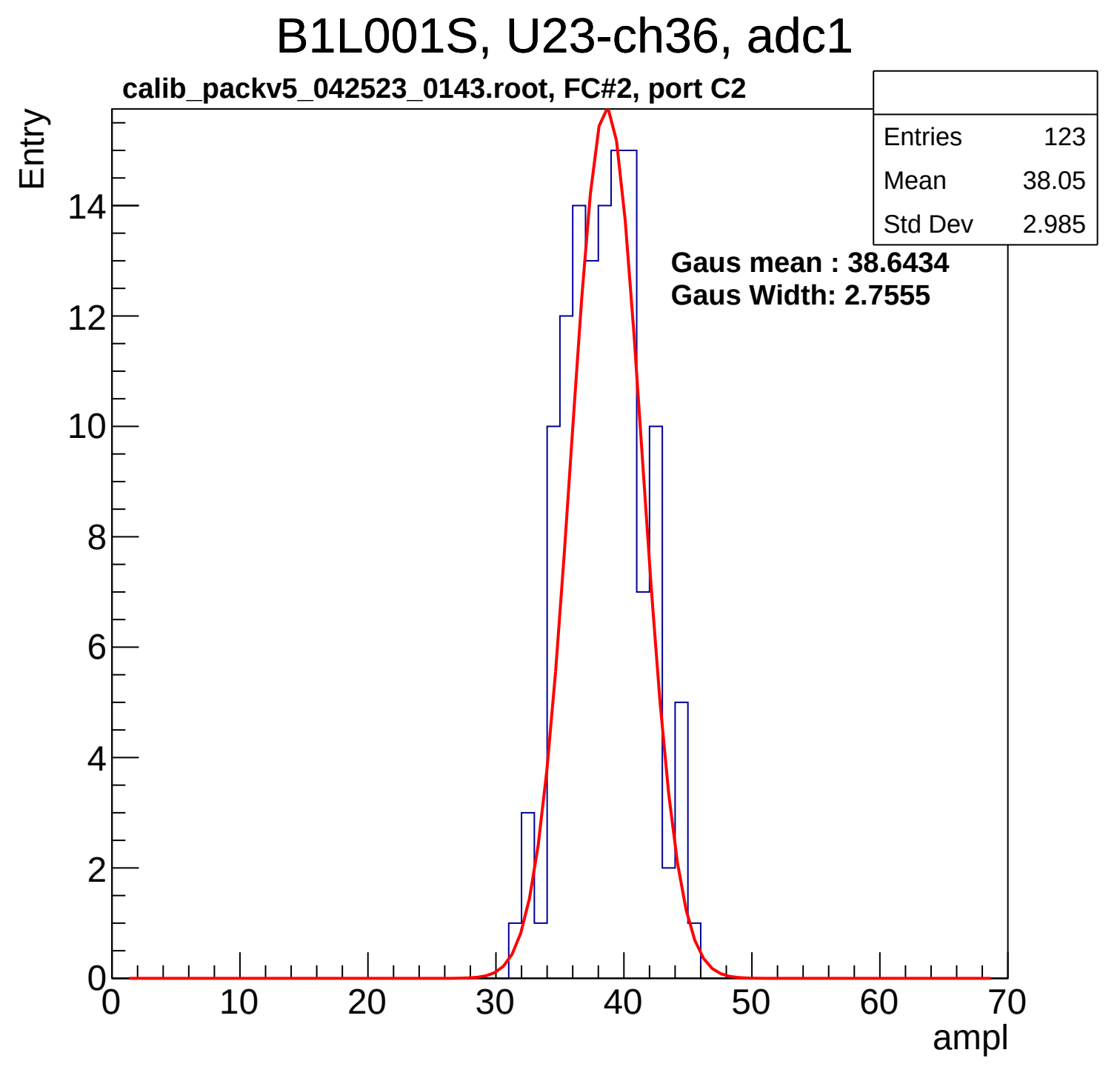
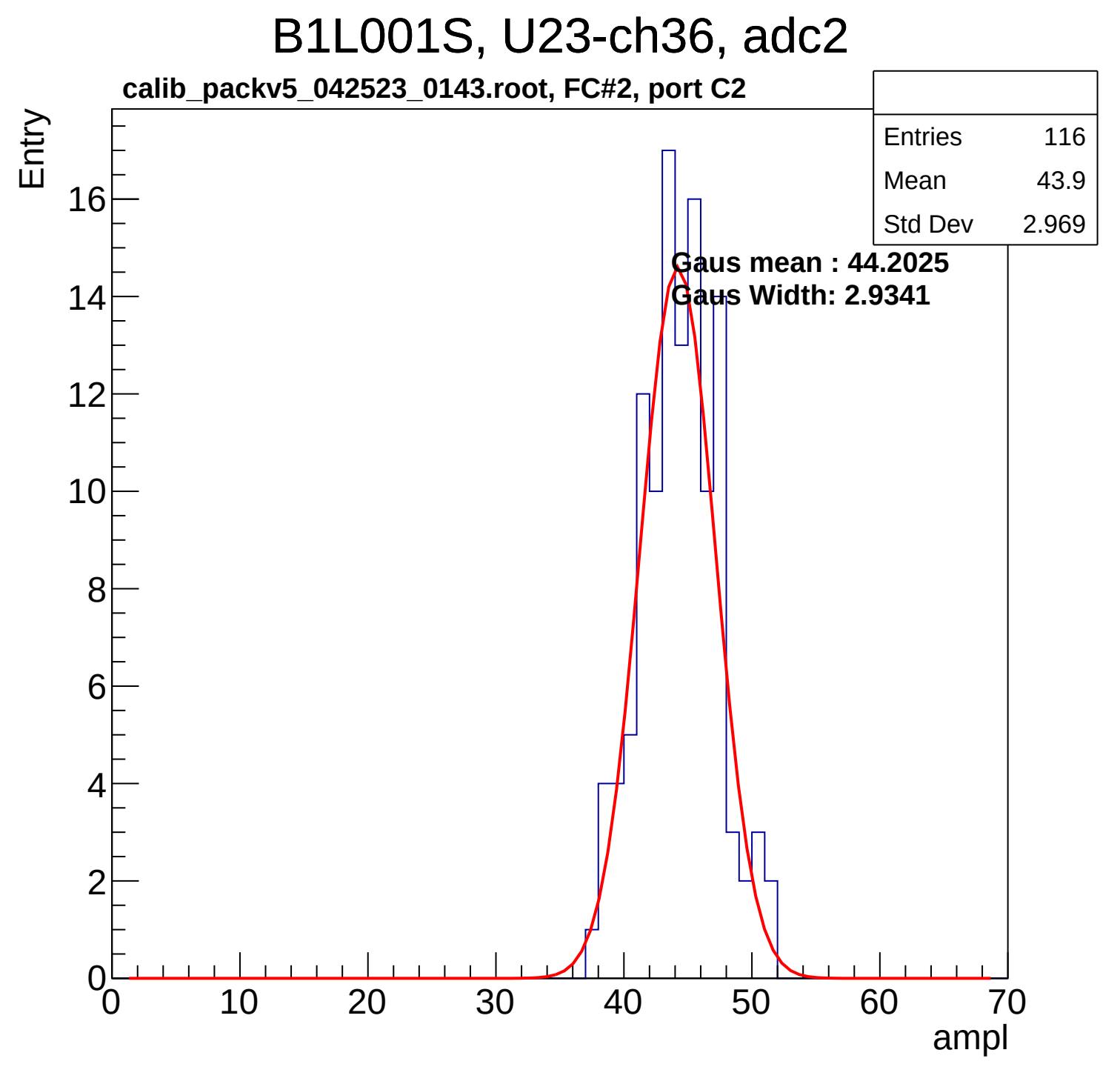

B1L001S, U23-ch36, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 38.05
Std Dev 2.985
Gaus mean : 38.6434
Gaus Width: 2.7555
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch36, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 43.9
Std Dev 2.969
Gaus mean : 44.2025
Gaus Width: 2.9341
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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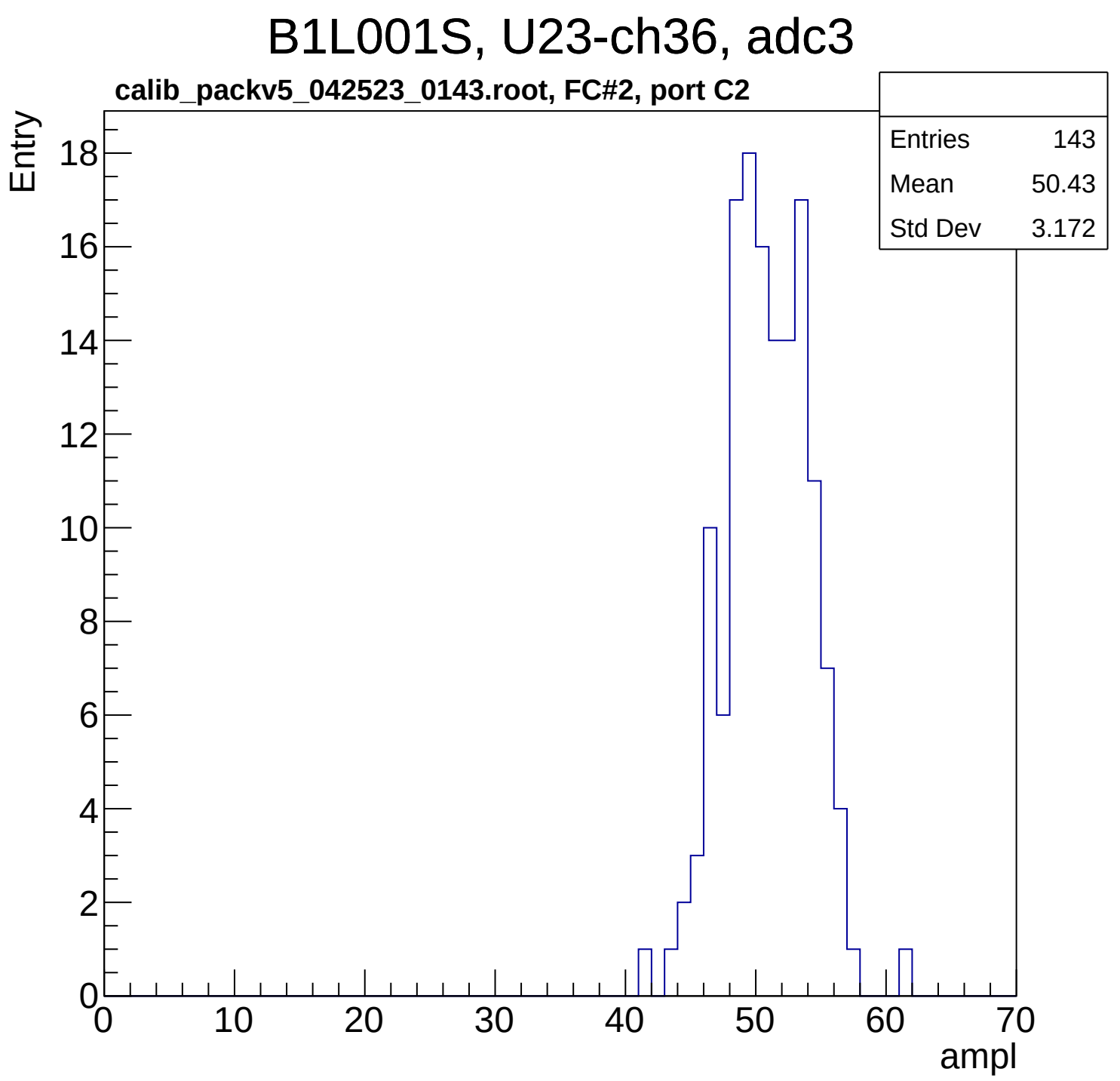
B1L001S, U23-ch36, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 50.43
Std Dev 3.172
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
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40
50
60
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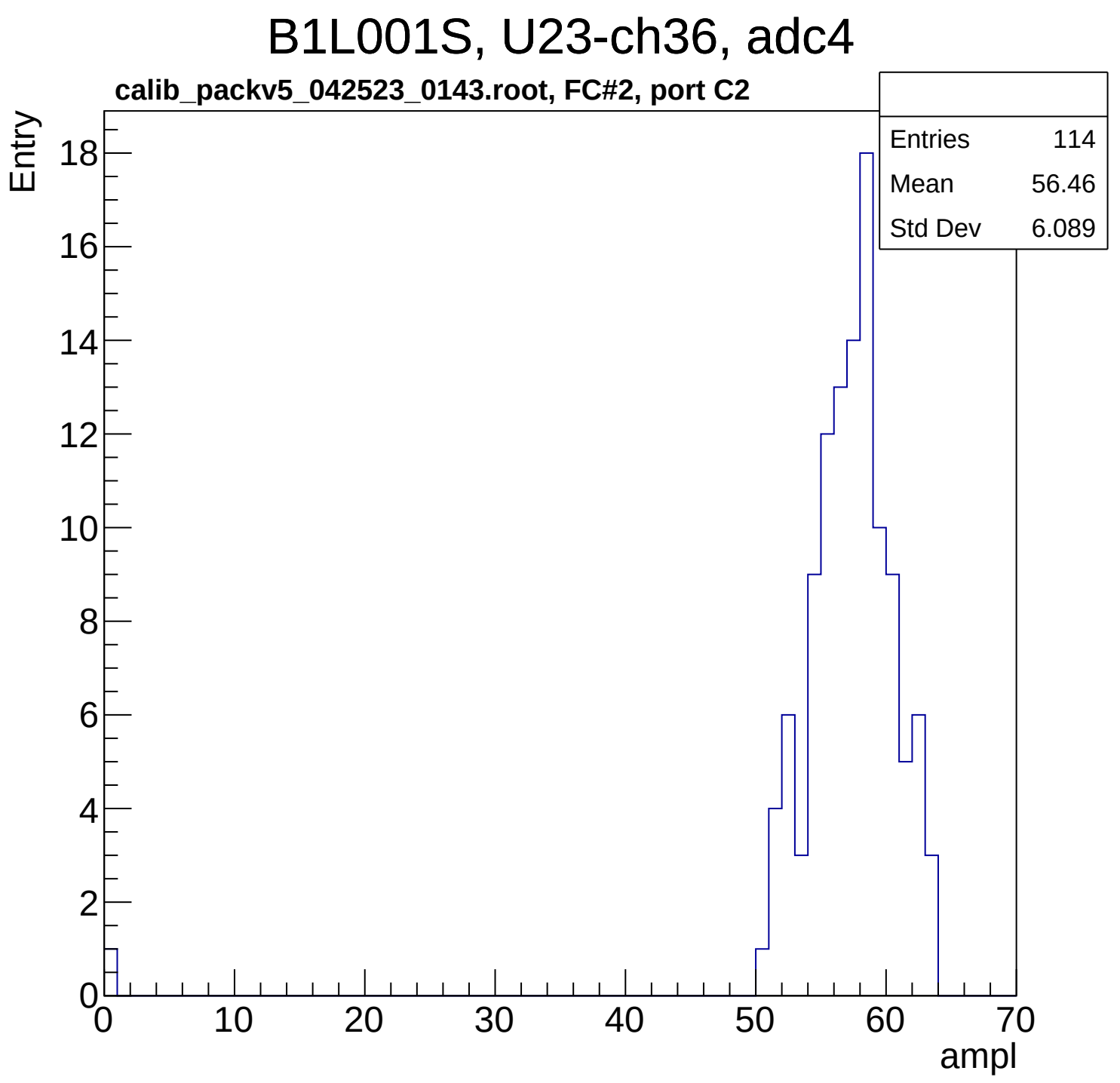
B1L001S, U23-ch36, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 56.46
Std Dev 6.089
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
50
60
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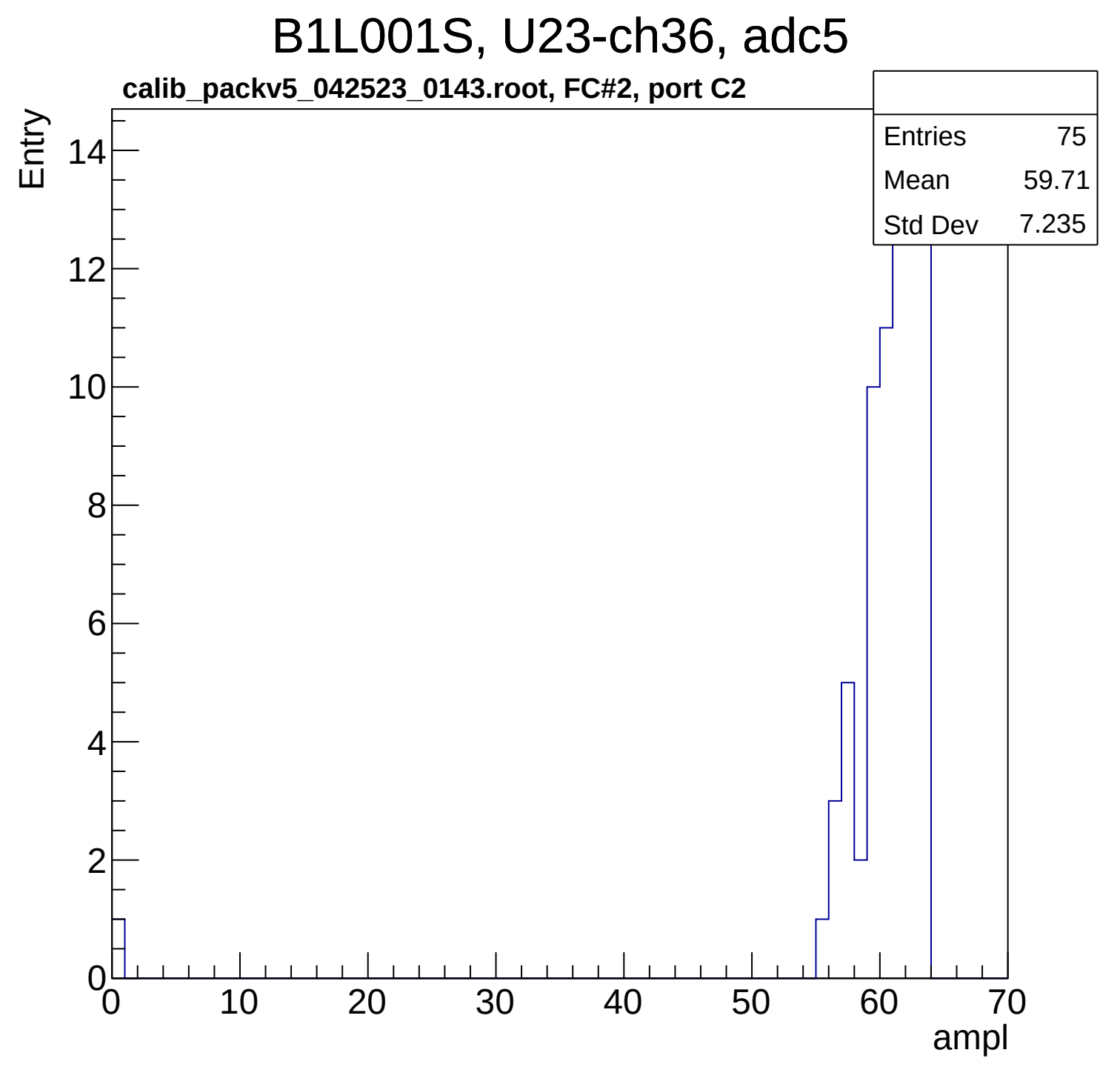
B1L001S, U23-ch36, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 59.71
Std Dev 7.235
Entry
ampl
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4
6
8
10
12
14
0
10
20
30
40
50
60
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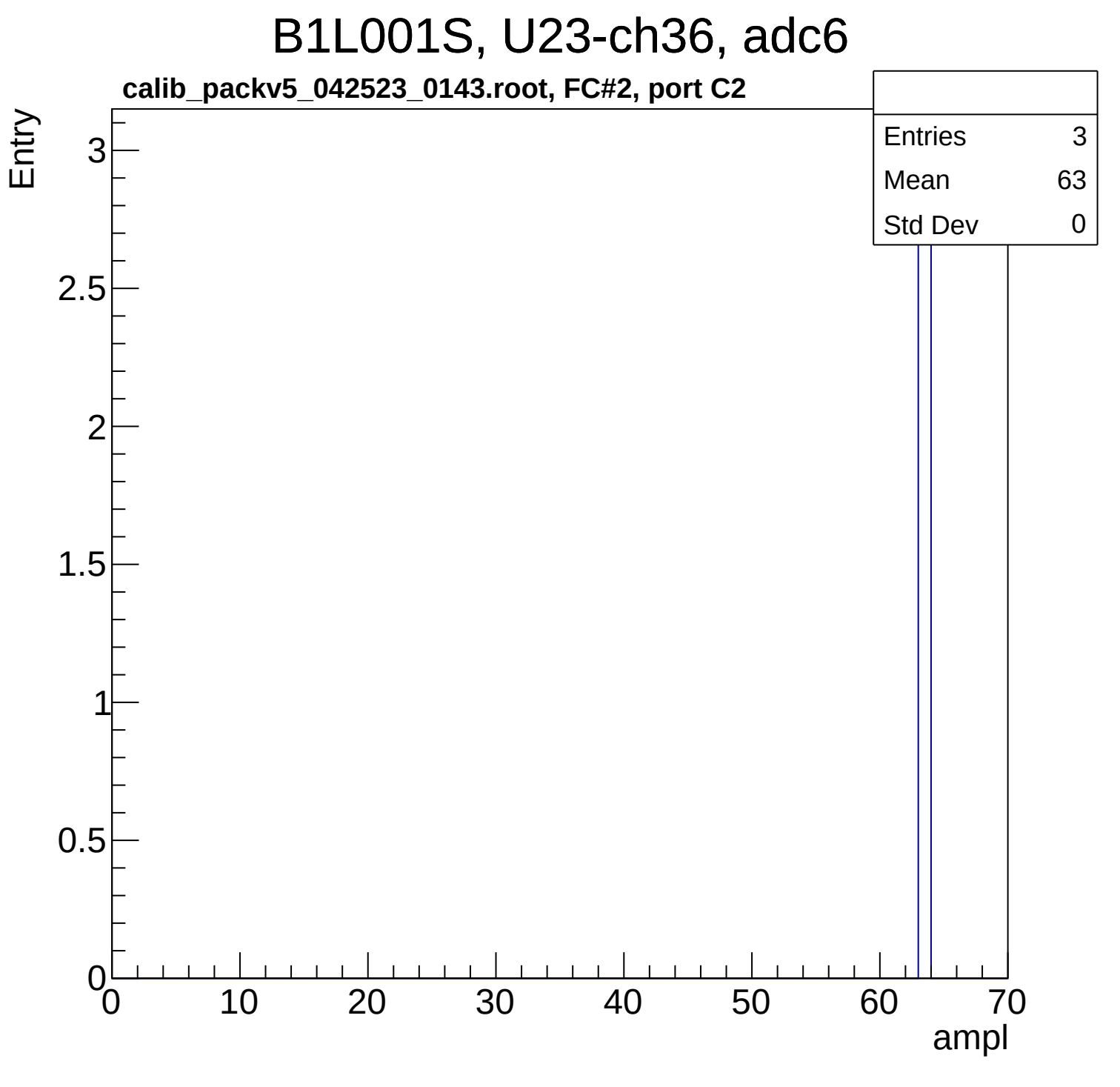
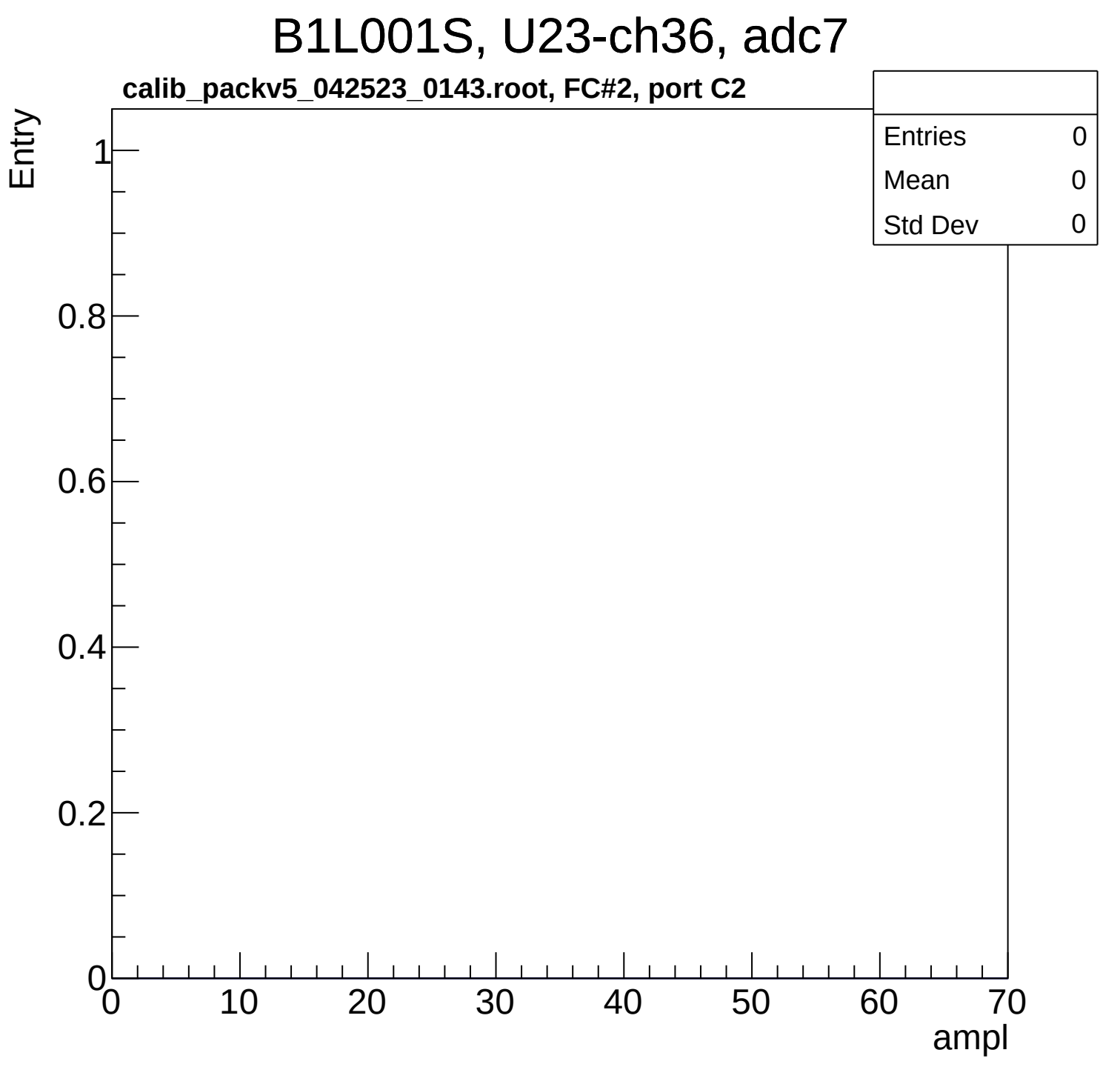

B1L001S, U23-ch36, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 63
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch36, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
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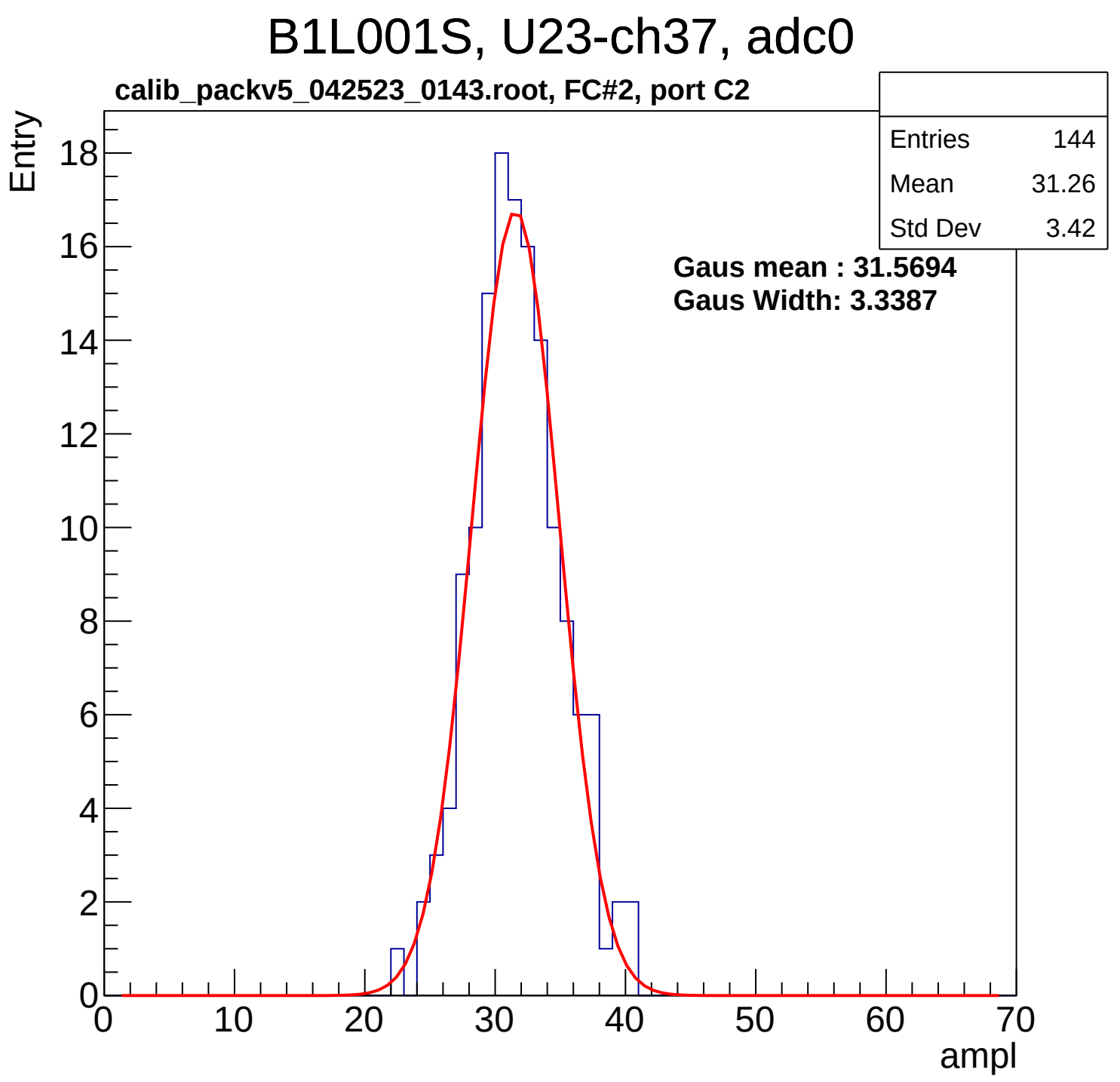
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch37, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 31.26
Std Dev 3.42
Gaus mean : 31.5694
Gaus Width: 3.3387
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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40
50
60
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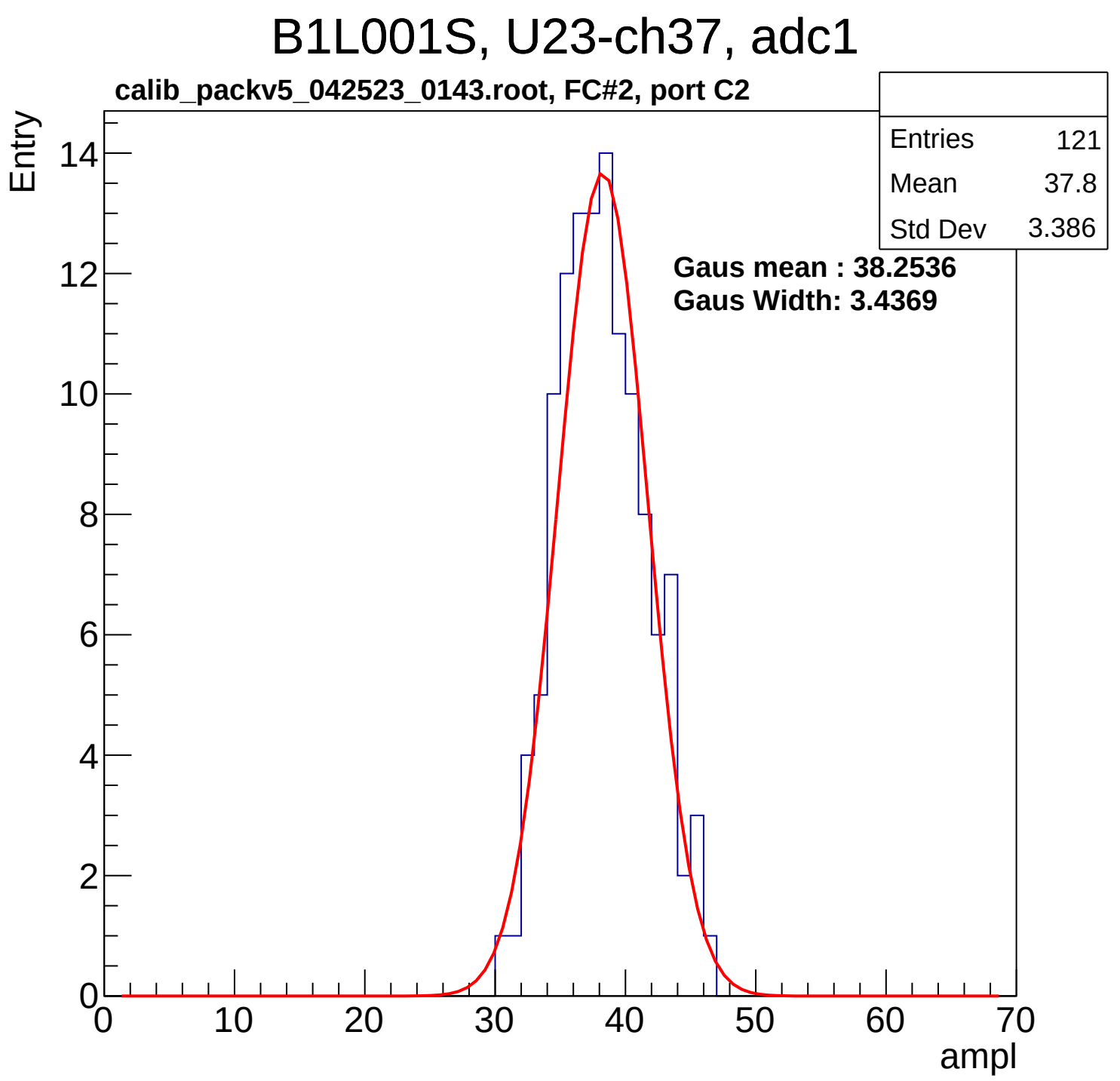
B1L001S, U23-ch37, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 37.8
Std Dev 3.386
Gaus mean : 38.2536
Gaus Width: 3.4369
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
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40
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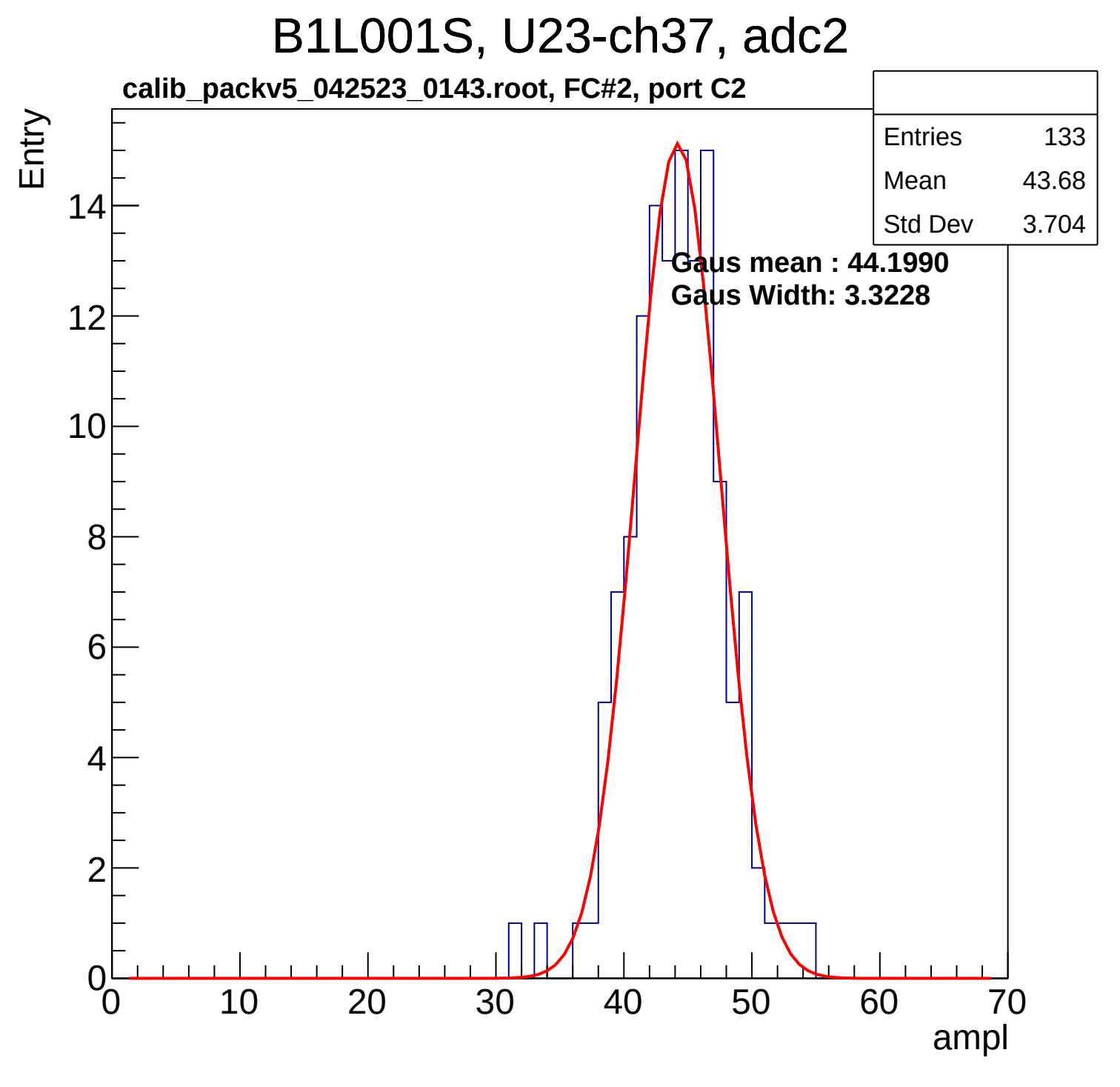
60
70


B1L001S, U23-ch37, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 43.68
Std Dev 3.704
Gaus mean : 44.1990
Gaus Width: 3.3228
Entry
ampl
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2
4
6
8
10
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0
10
20
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40
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60
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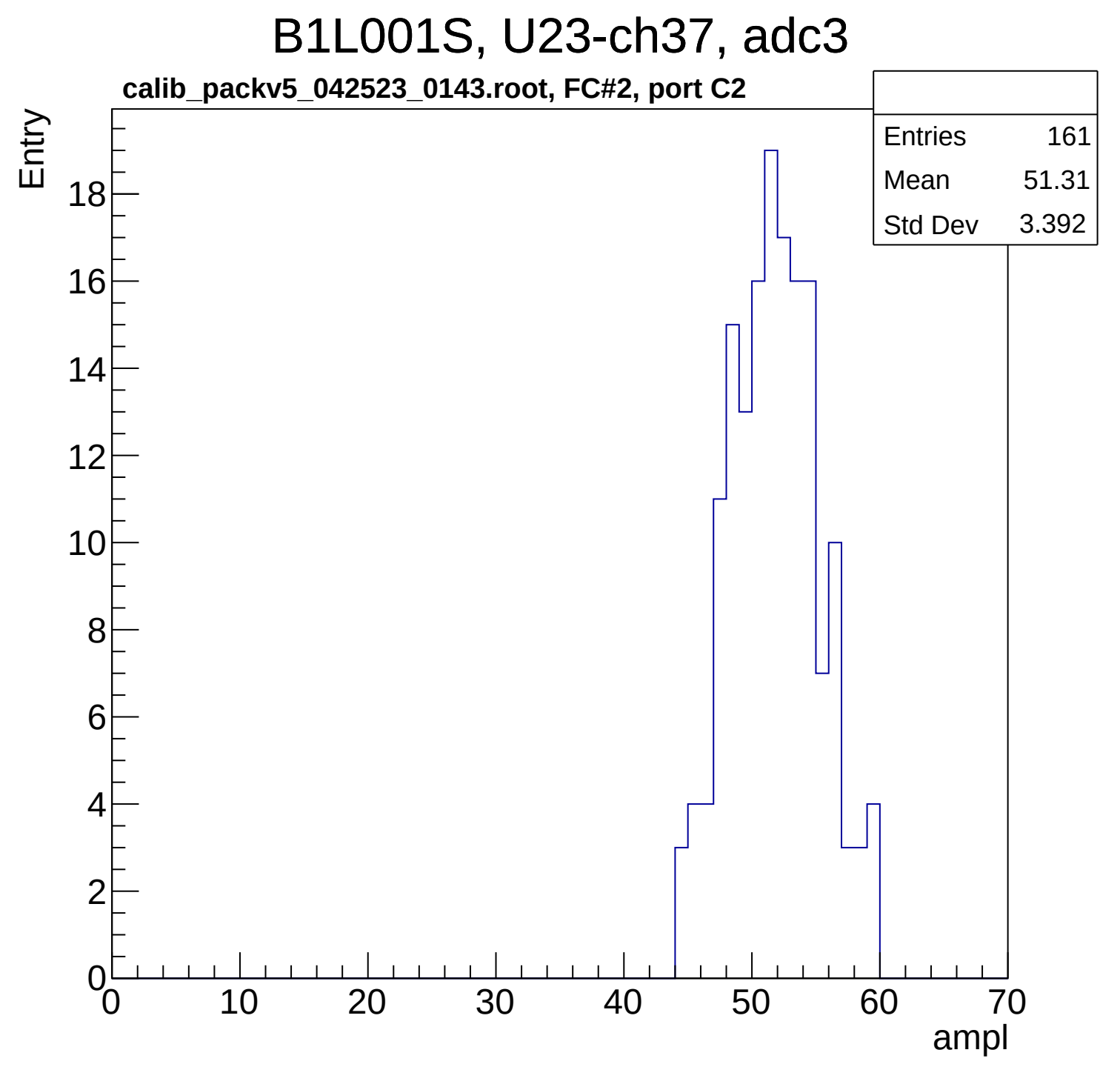
B1L001S, U23-ch37, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 51.31
Std Dev 3.392
Entry
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6
8
10
12
14
16
18
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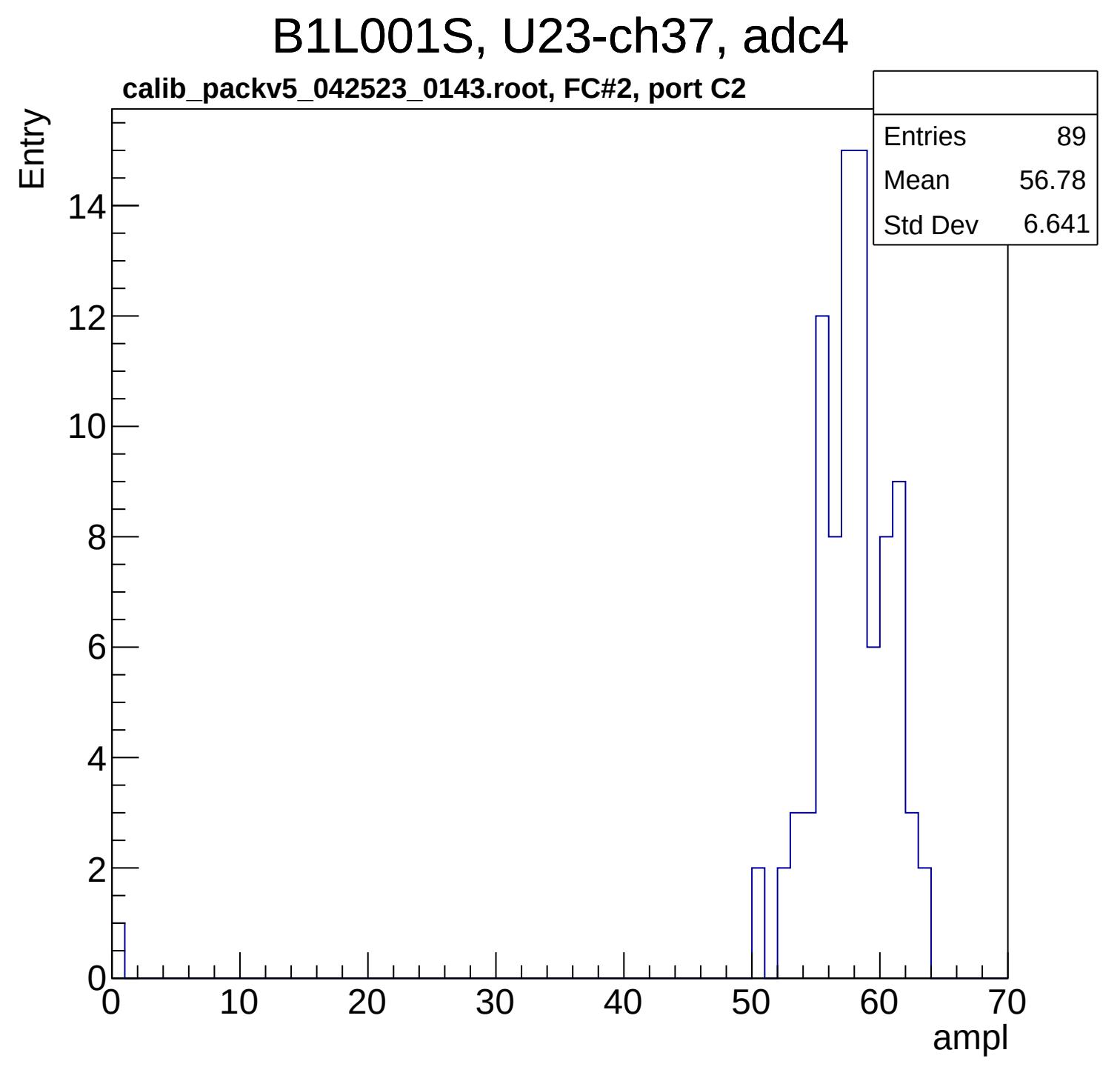
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch37, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 56.78
Std Dev 6.641
Entry
ampl
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8
10
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0
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20
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40
50
60
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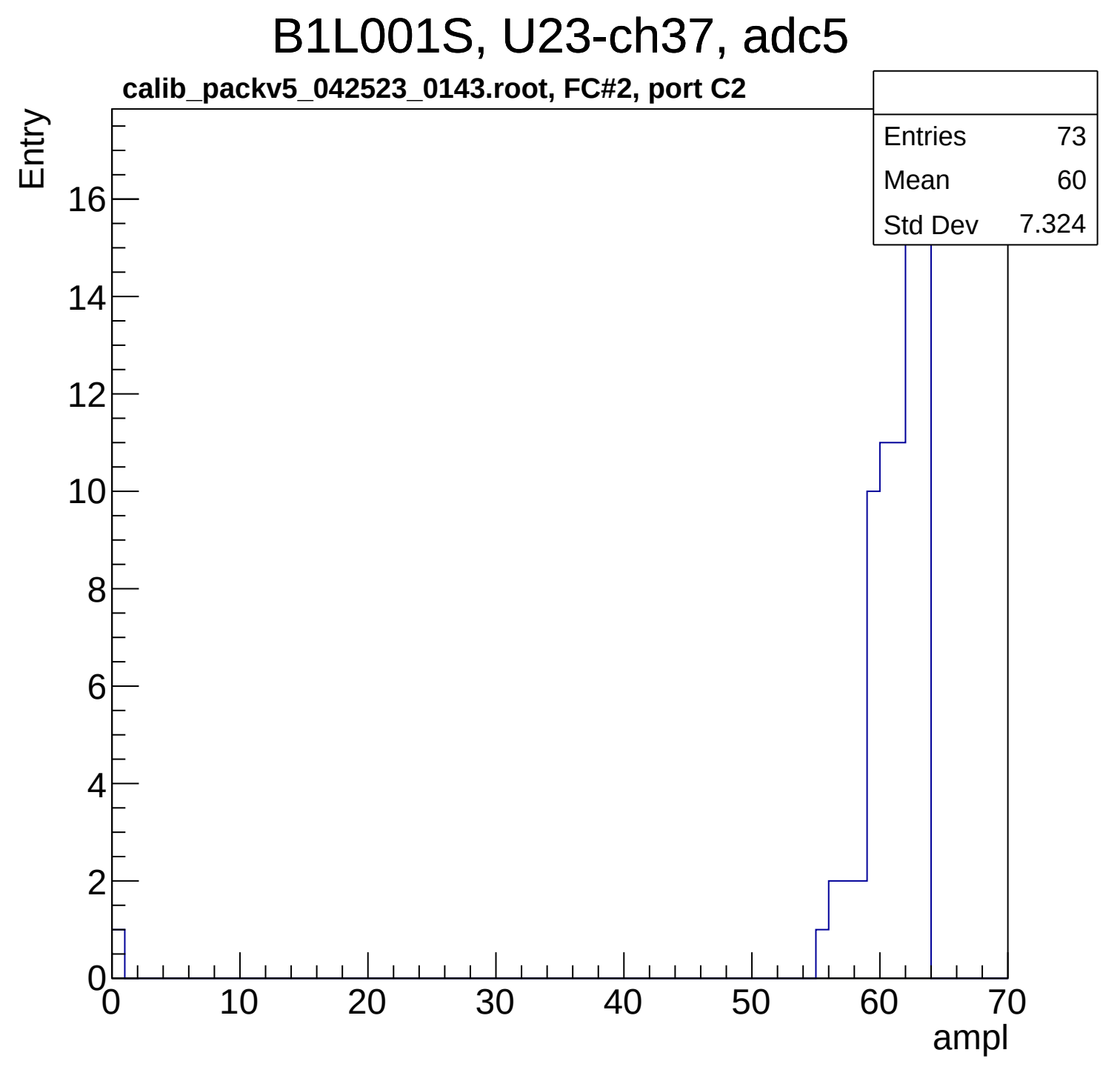
B1L001S, U23-ch37, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 73
Mean 60
Std Dev 7.324
Entry
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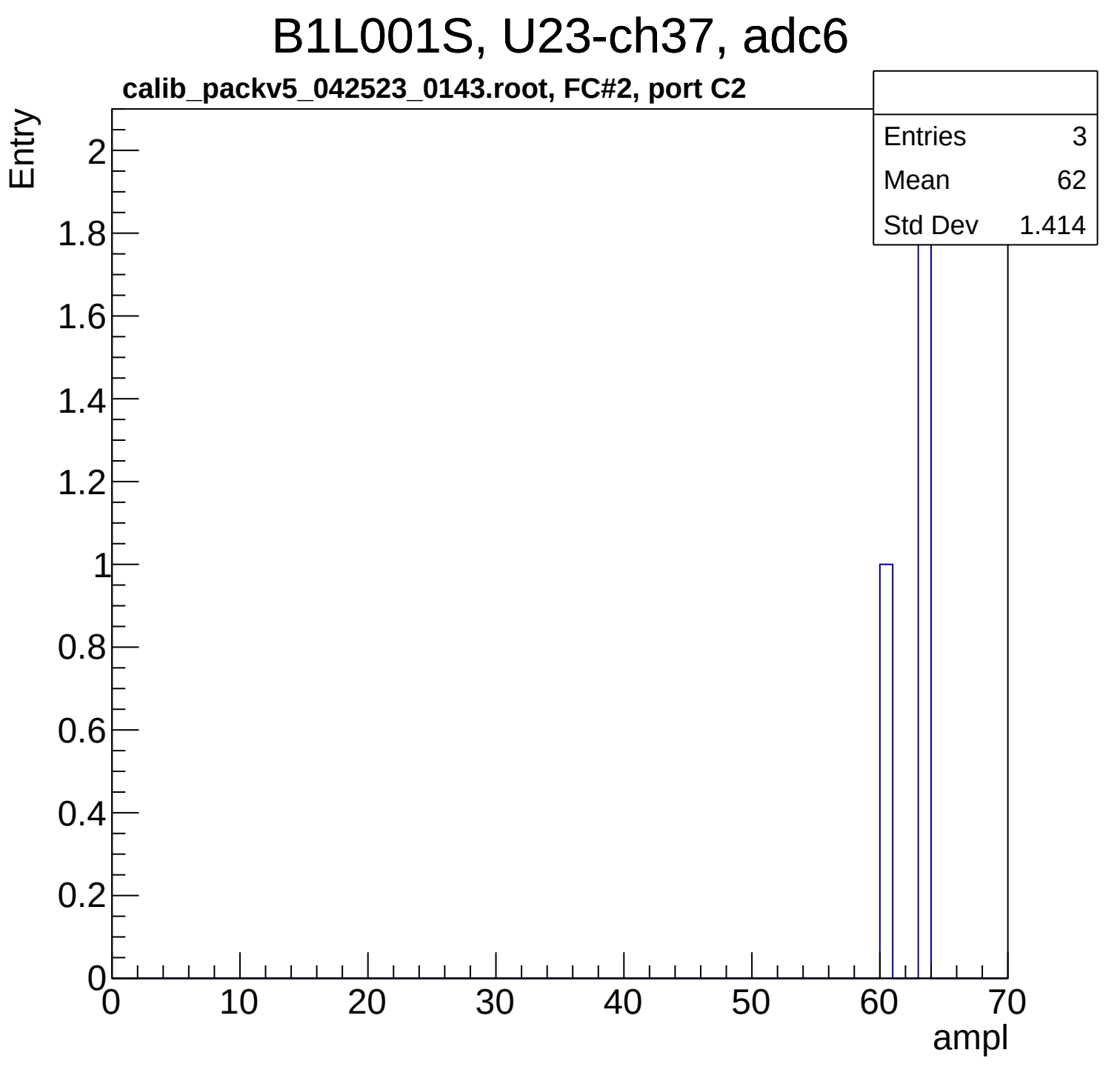
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch37, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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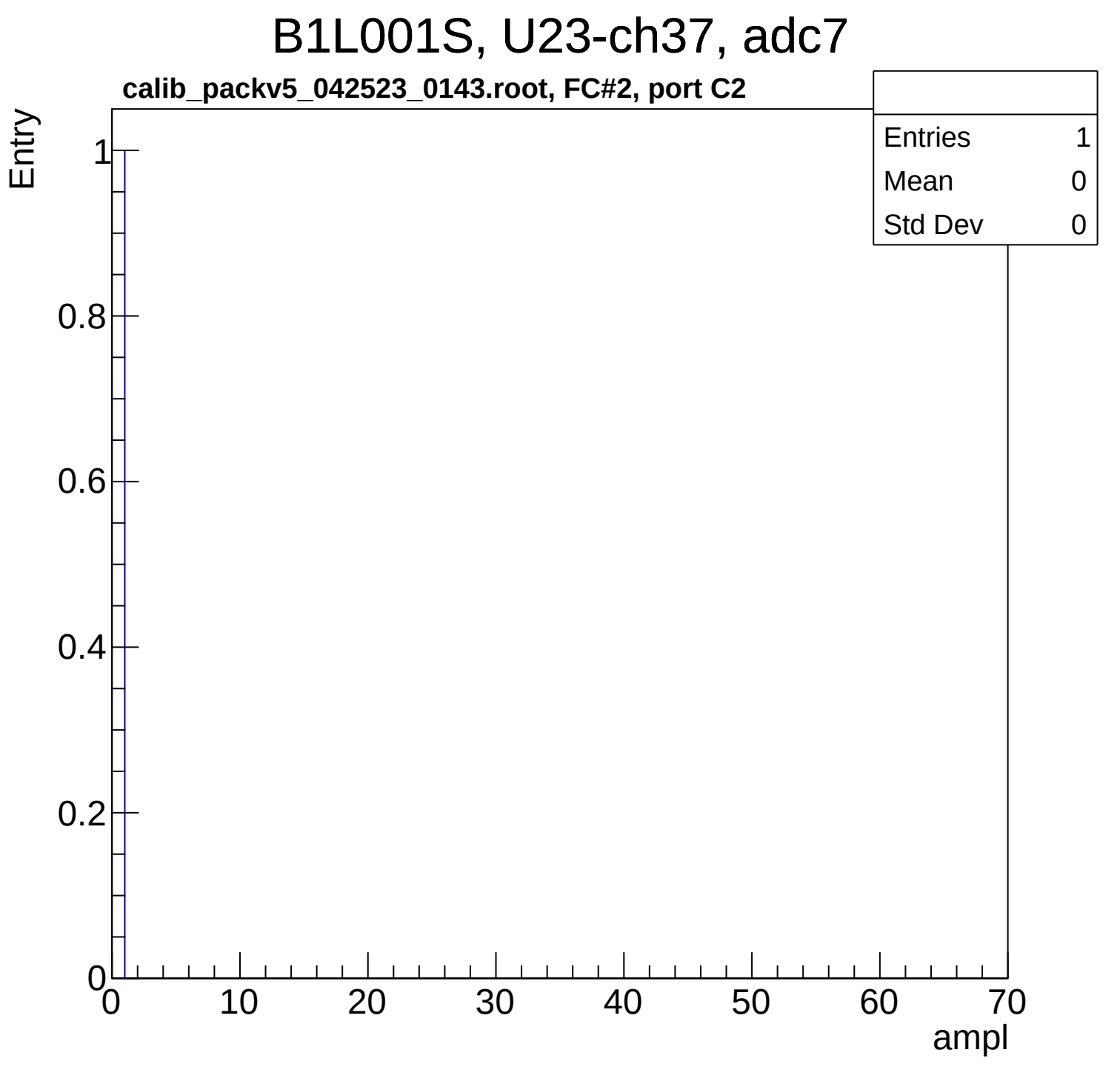
30
40
50
60
70


B1L001S, U23-ch37, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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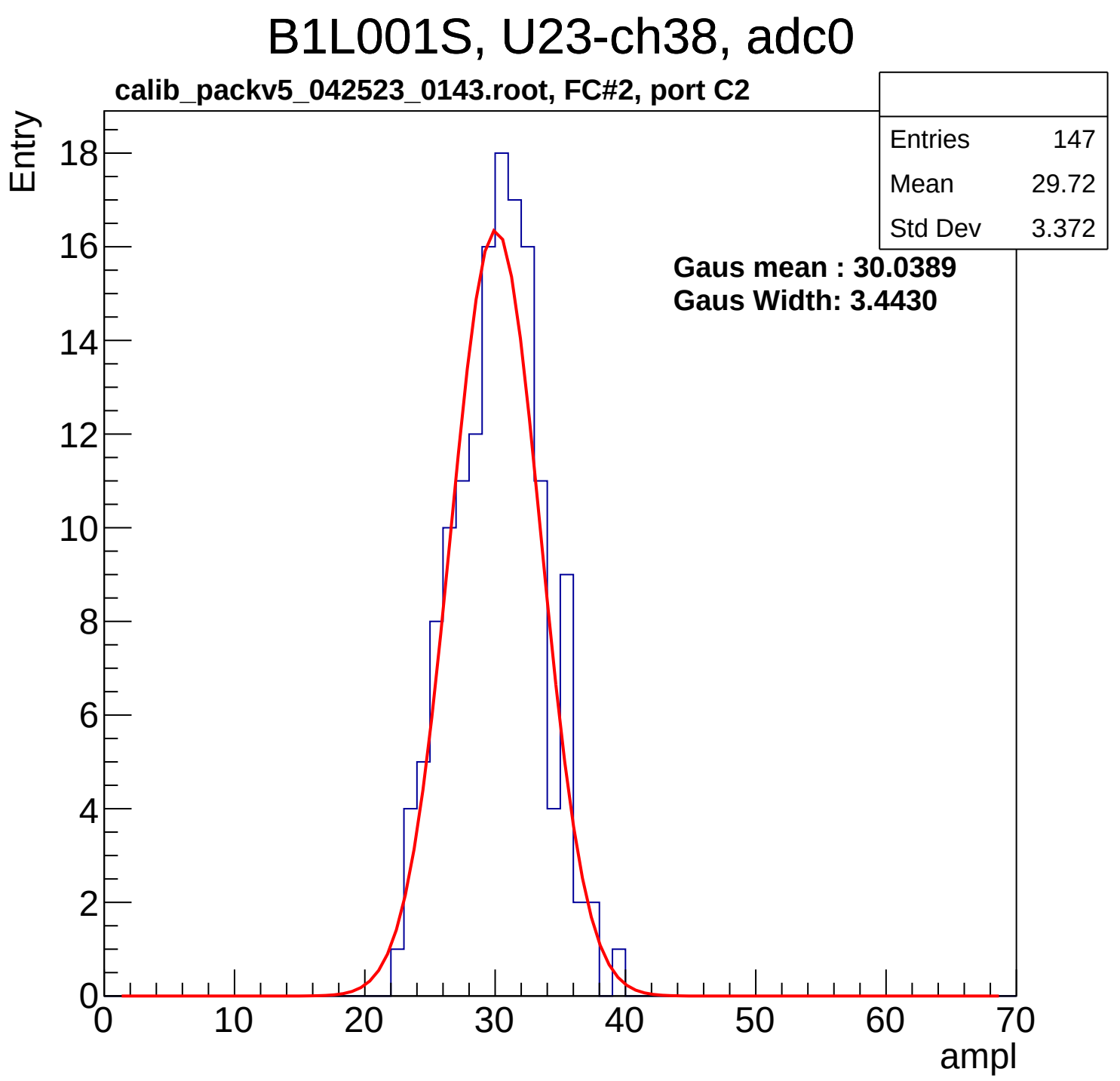
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch38, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 29.72
Std Dev 3.372
Gaus mean : 30.0389
Gaus Width: 3.4430
Entry
ampl
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2
4
6
8
10
12
14
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0
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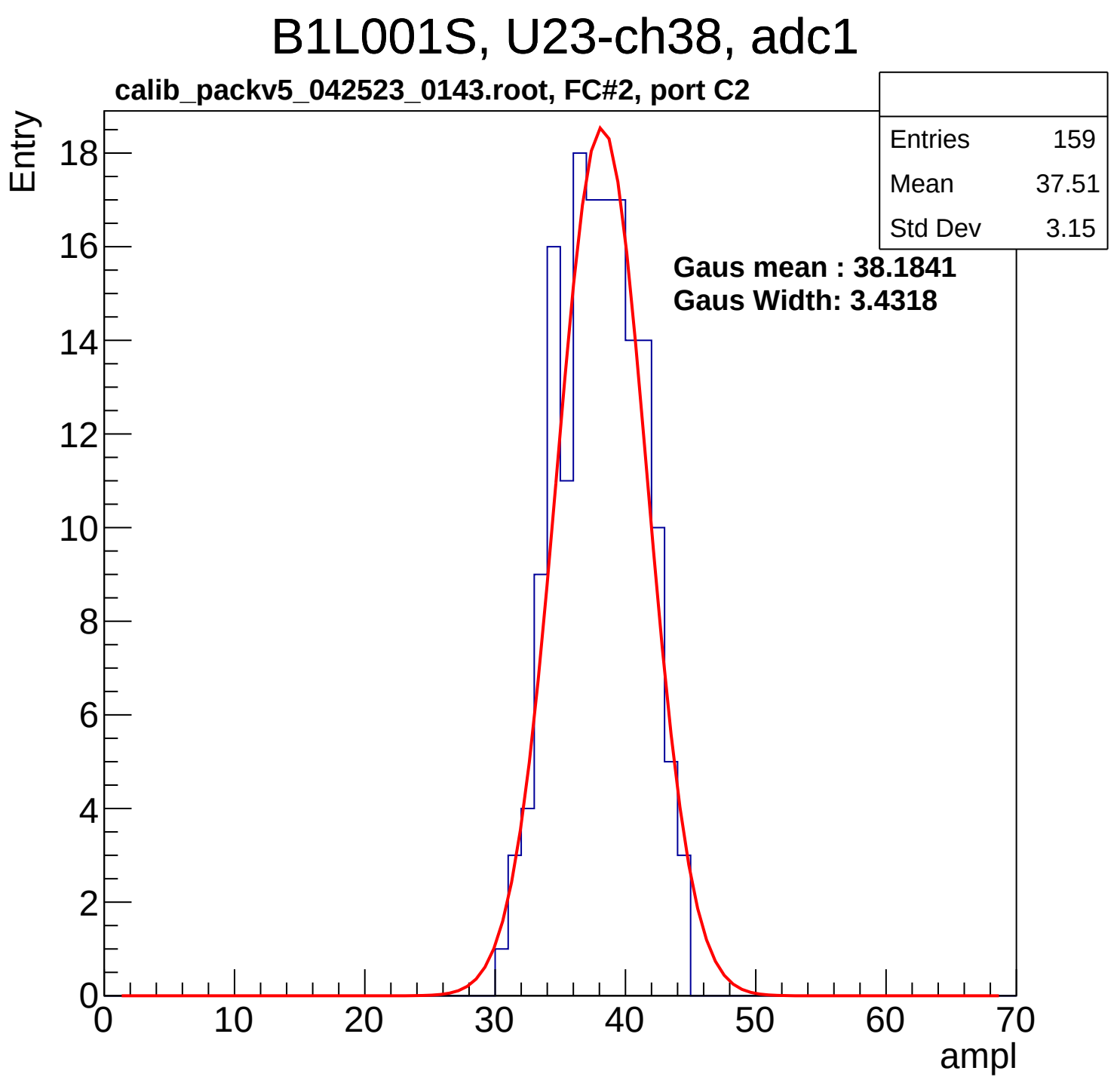
B1L001S, U23-ch38, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 37.51
Std Dev 3.15
Gaus mean : 38.1841
Gaus Width: 3.4318
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
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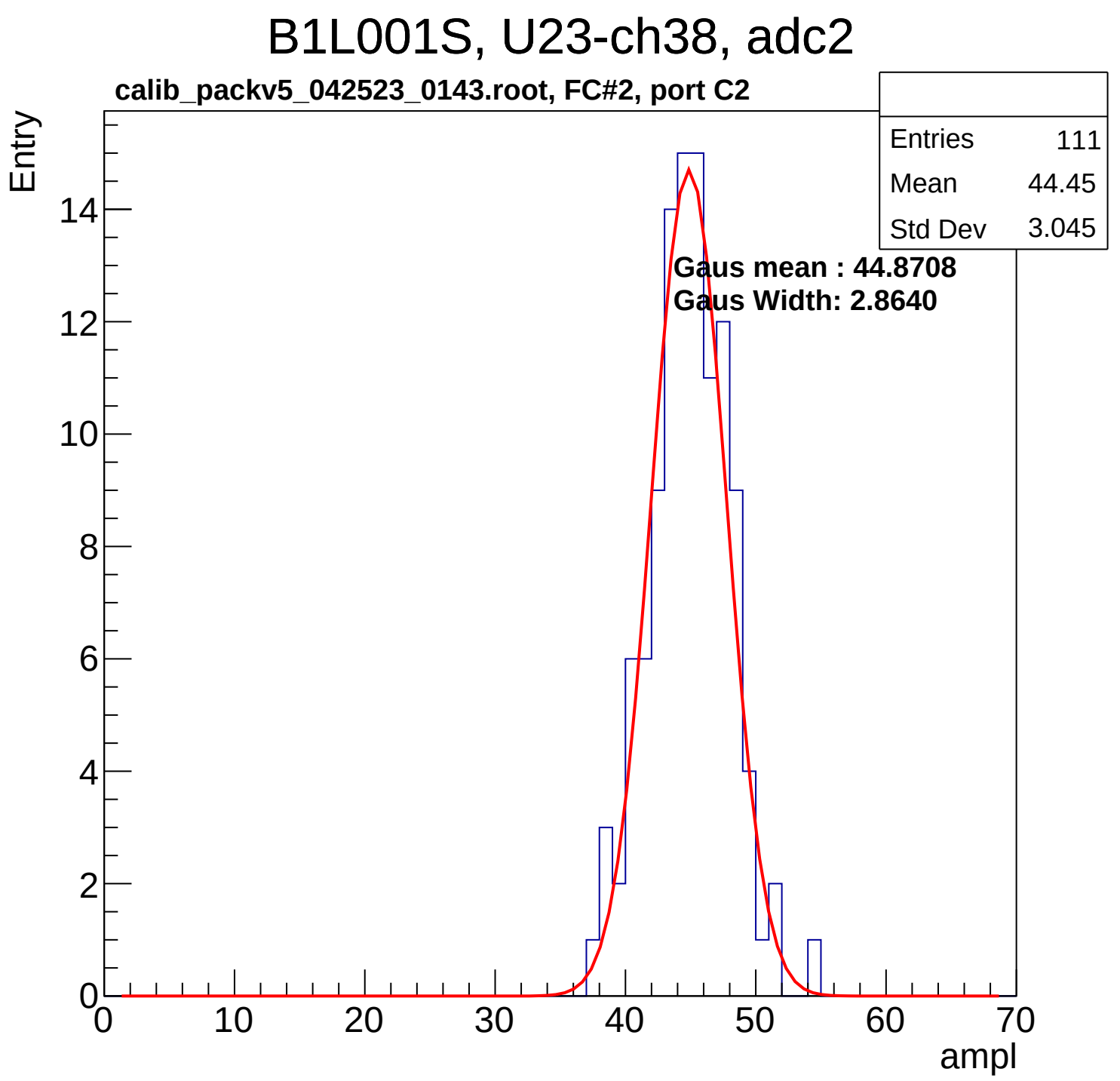
B1L001S, U23-ch38, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 44.45
Std Dev 3.045
Gaus mean : 44.8708
Gaus Width: 2.8640
Entry
ampl
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4
6
8
10
12
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0
10
20
30
40
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60
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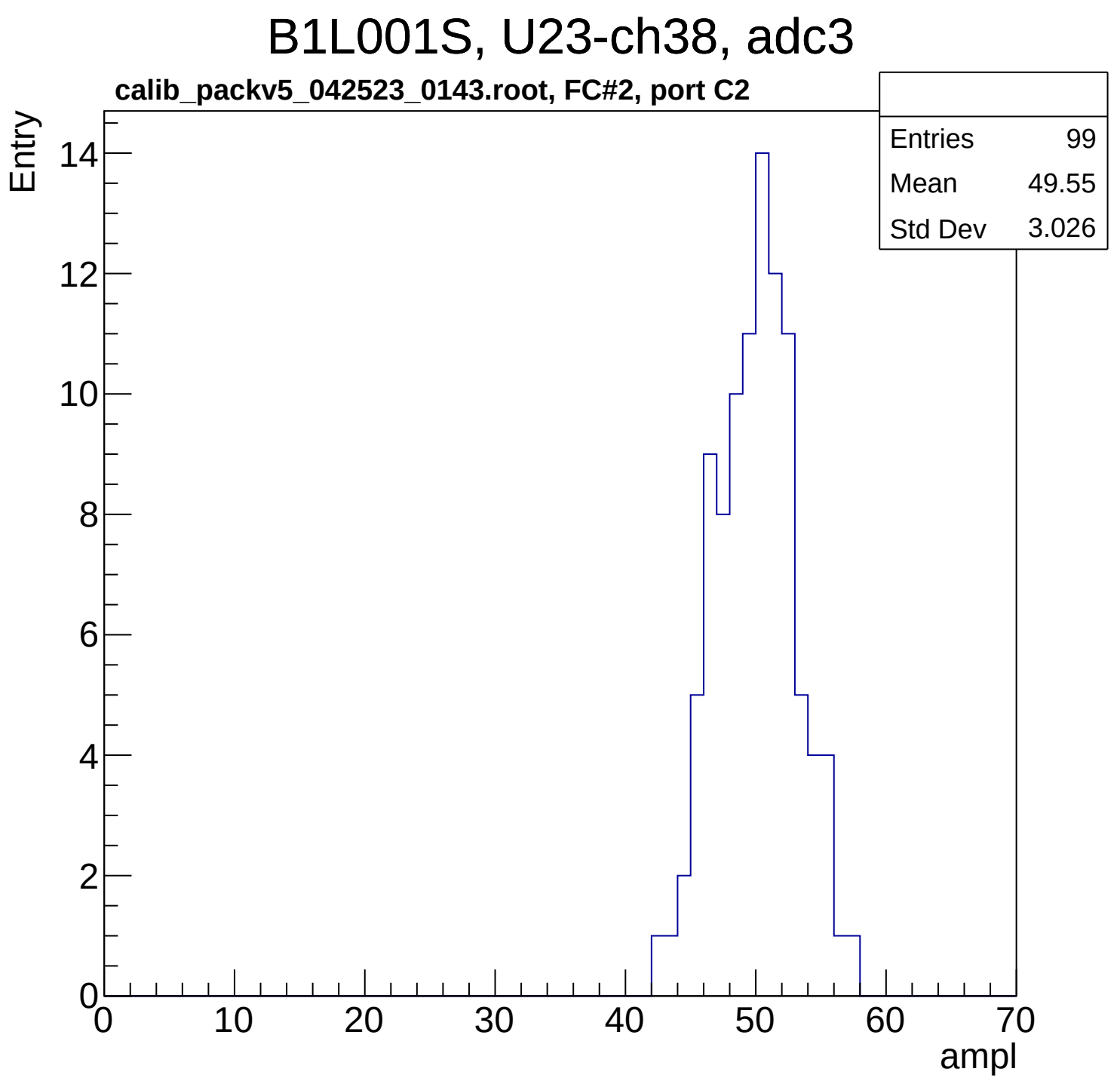
B1L001S, U23-ch38, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 49.55
Std Dev 3.026
Entry
ampl
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2
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8
10
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0
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20
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60
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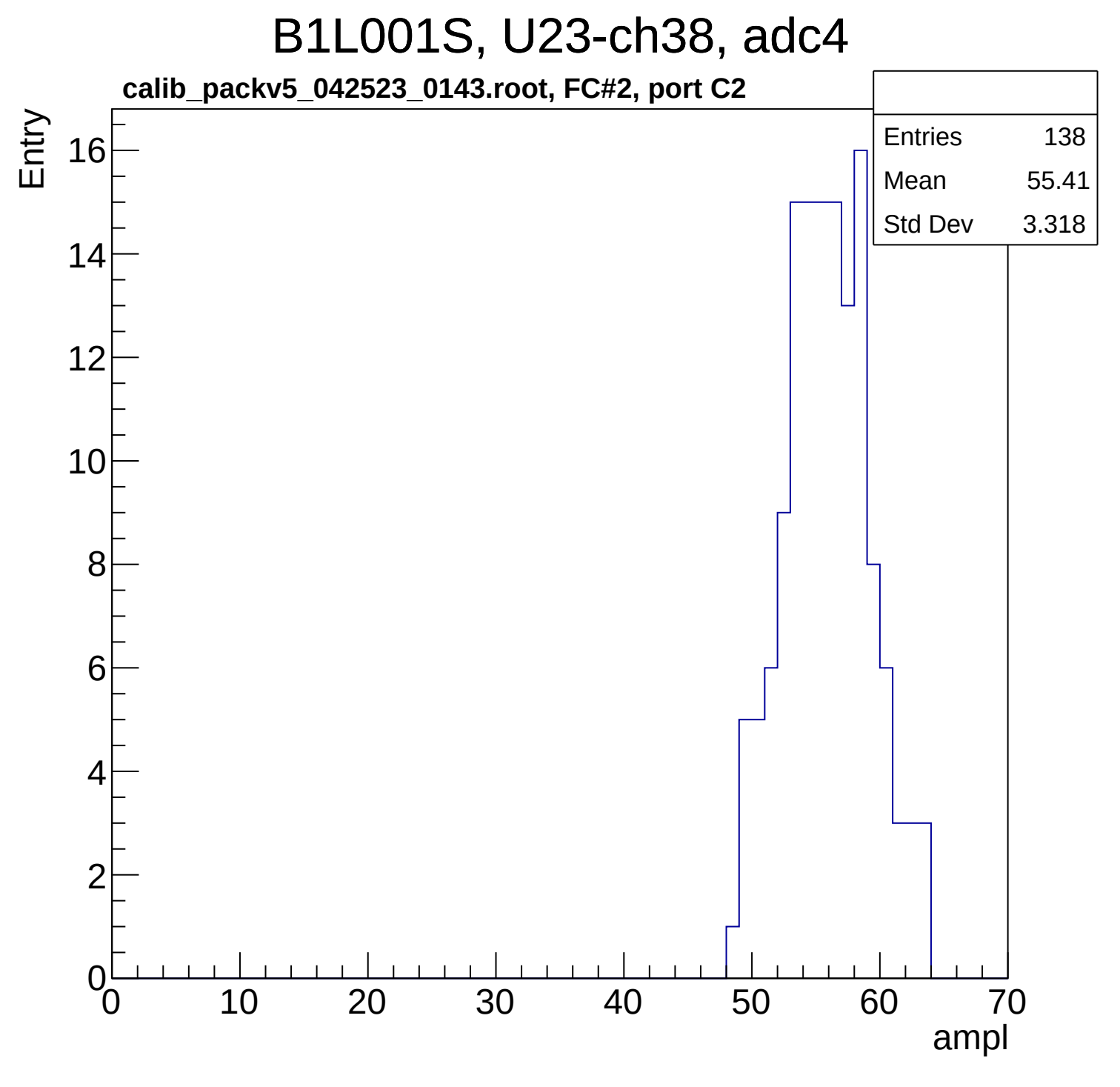
B1L001S, U23-ch38, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 55.41
Std Dev 3.318
Entry
ampl
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4
6
8
10
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60
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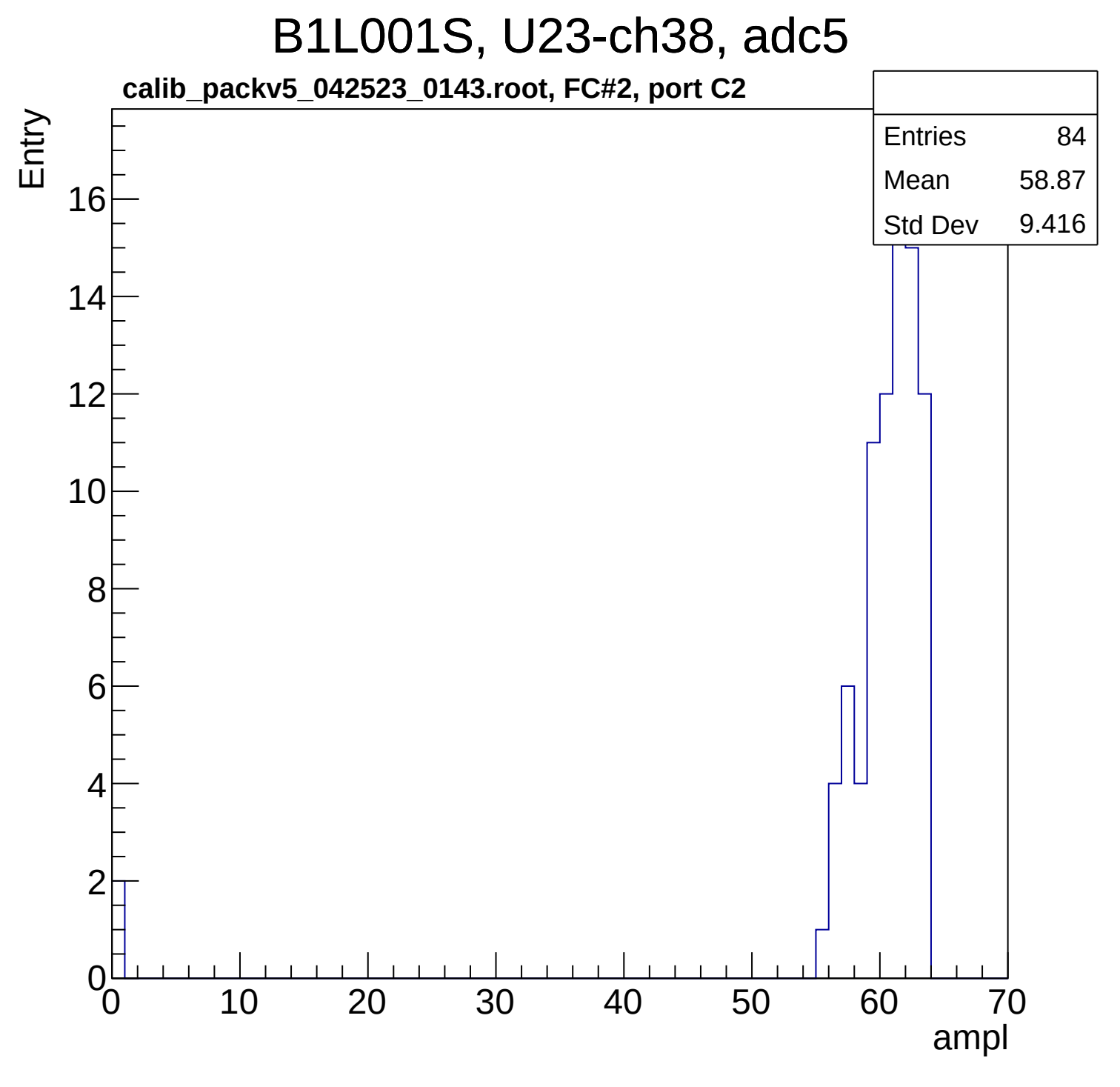
B1L001S, U23-ch38, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 58.87
Std Dev 9.416
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
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60
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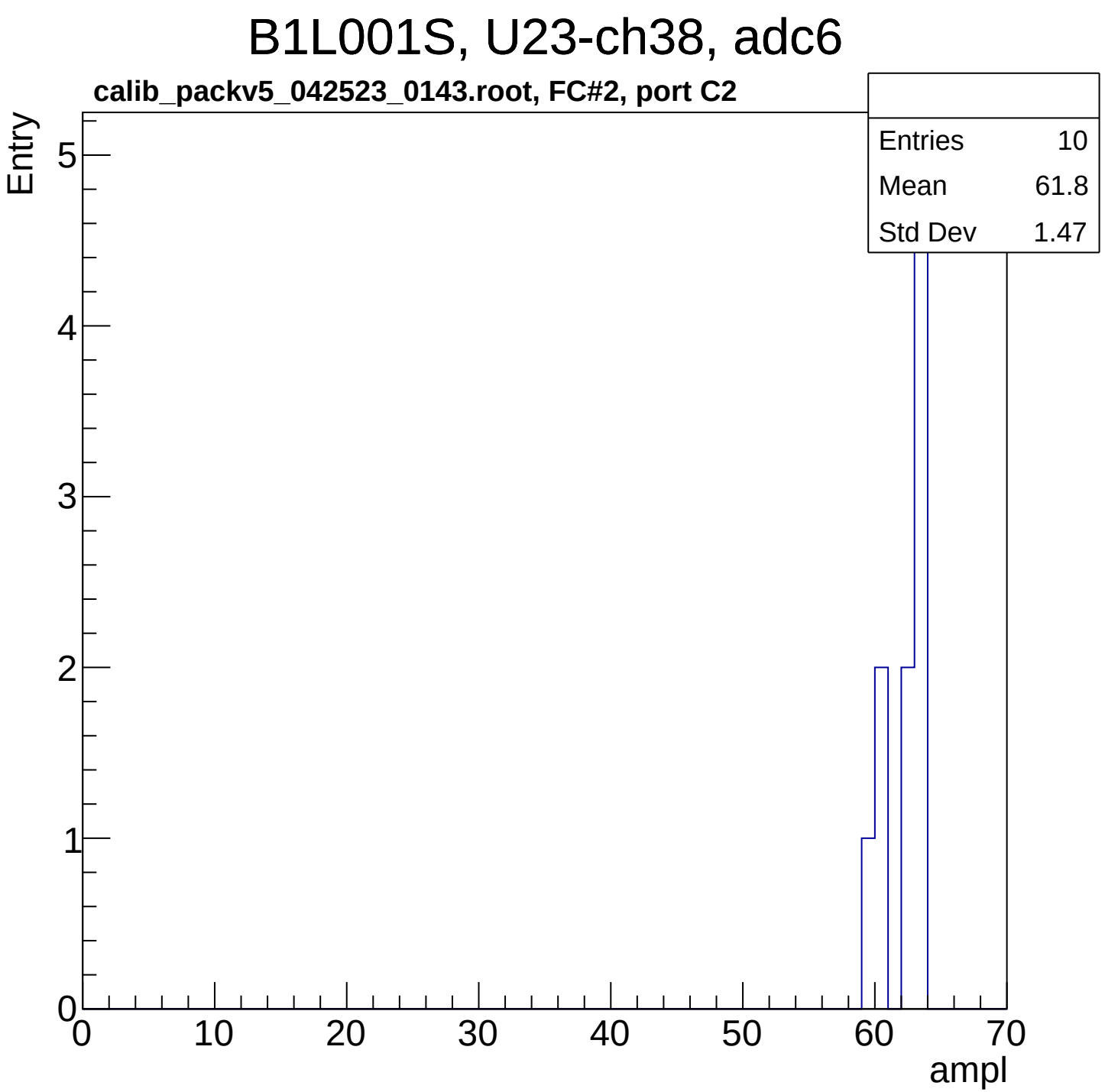
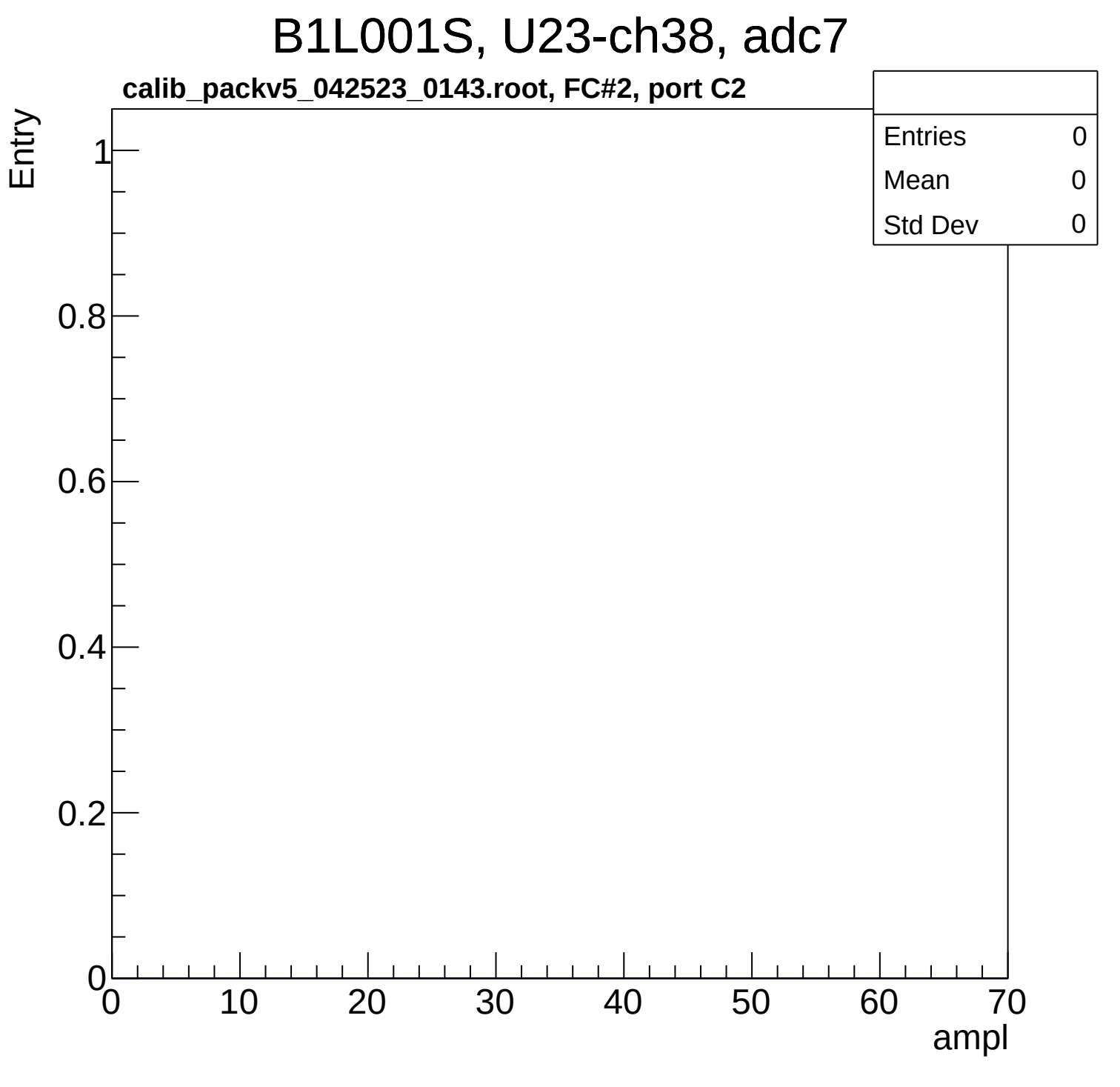

B1L001S, U23-ch38, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 61.8
Std Dev 1.47
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch38, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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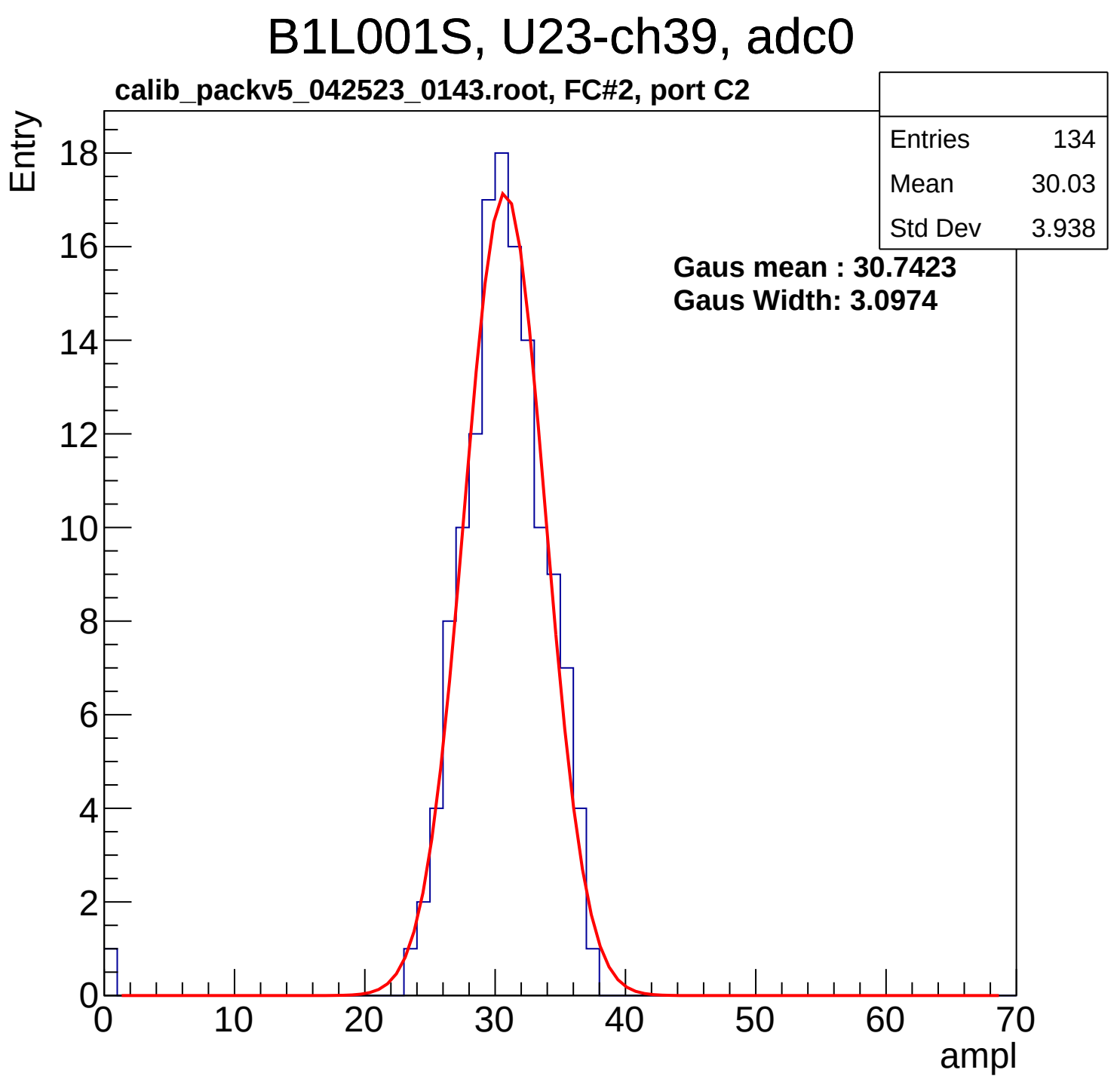
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch39, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 30.03
Std Dev 3.938
Gaus mean : 30.7423
Gaus Width: 3.0974
Entry
ampl
0
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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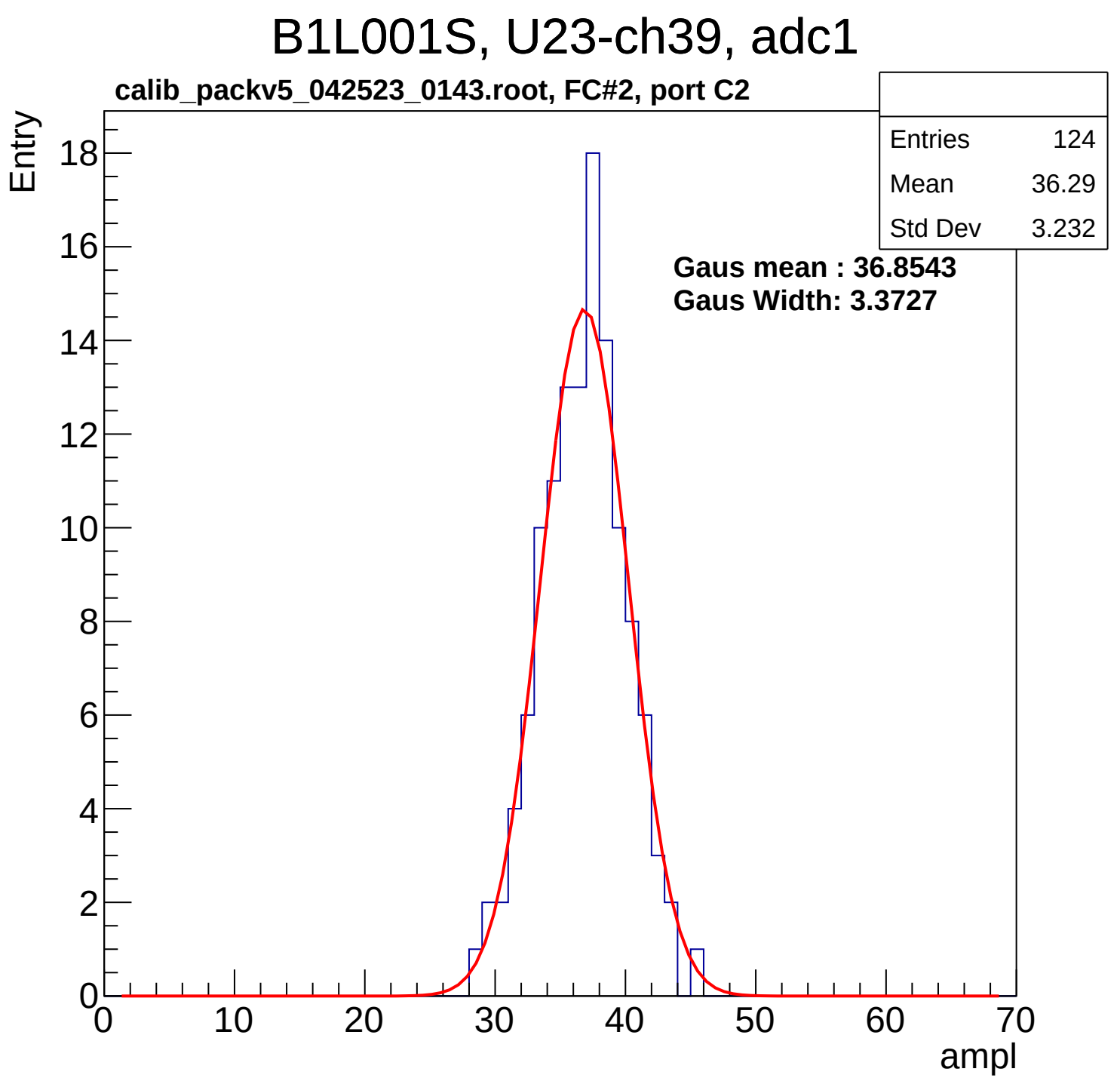
B1L001S, U23-ch39, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 36.29
Std Dev 3.232
Gaus mean : 36.8543
Gaus Width: 3.3727
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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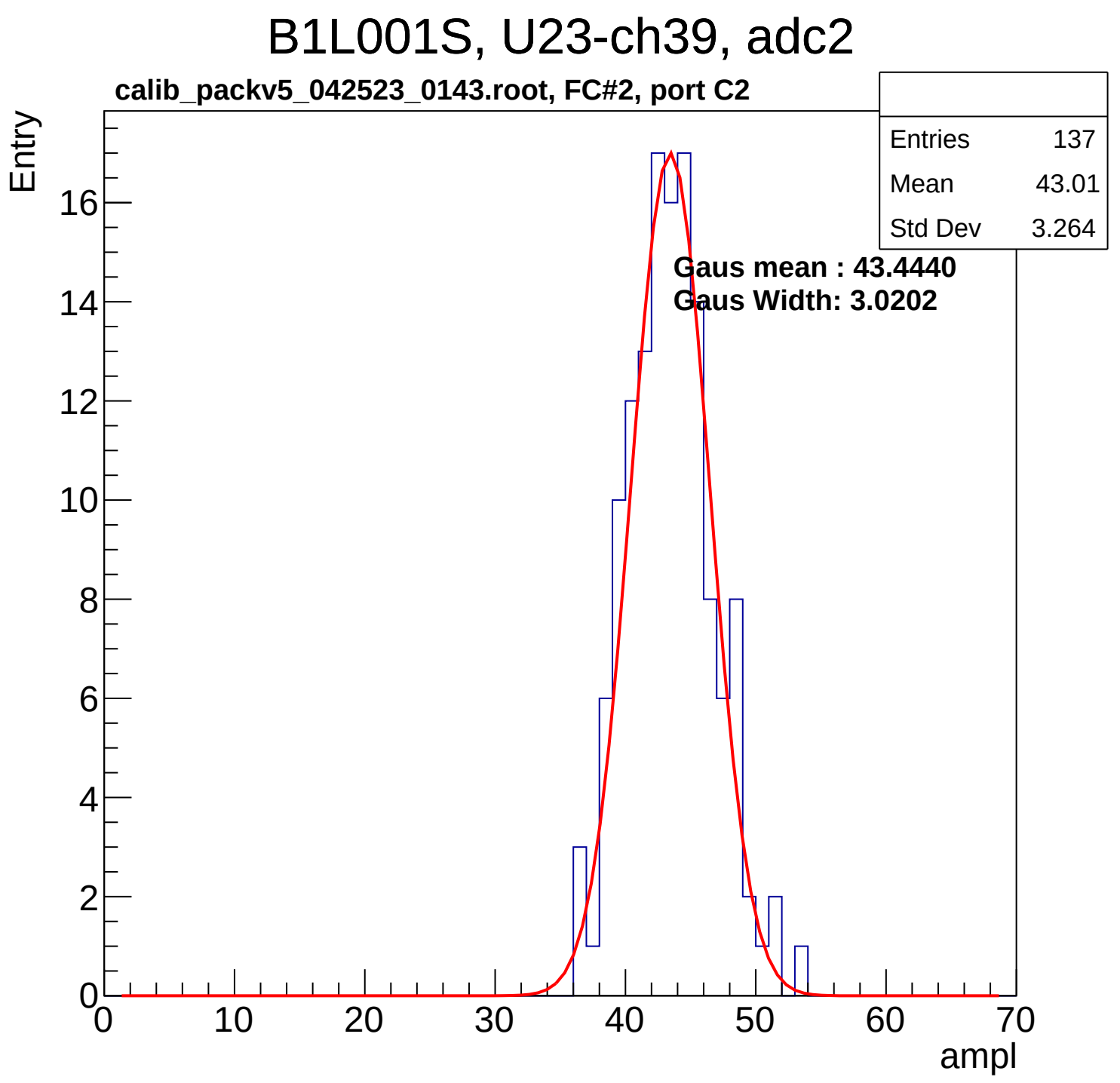
B1L001S, U23-ch39, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 43.01
Std Dev 3.264
Gaus mean : 43.4440
Gaus Width: 3.0202
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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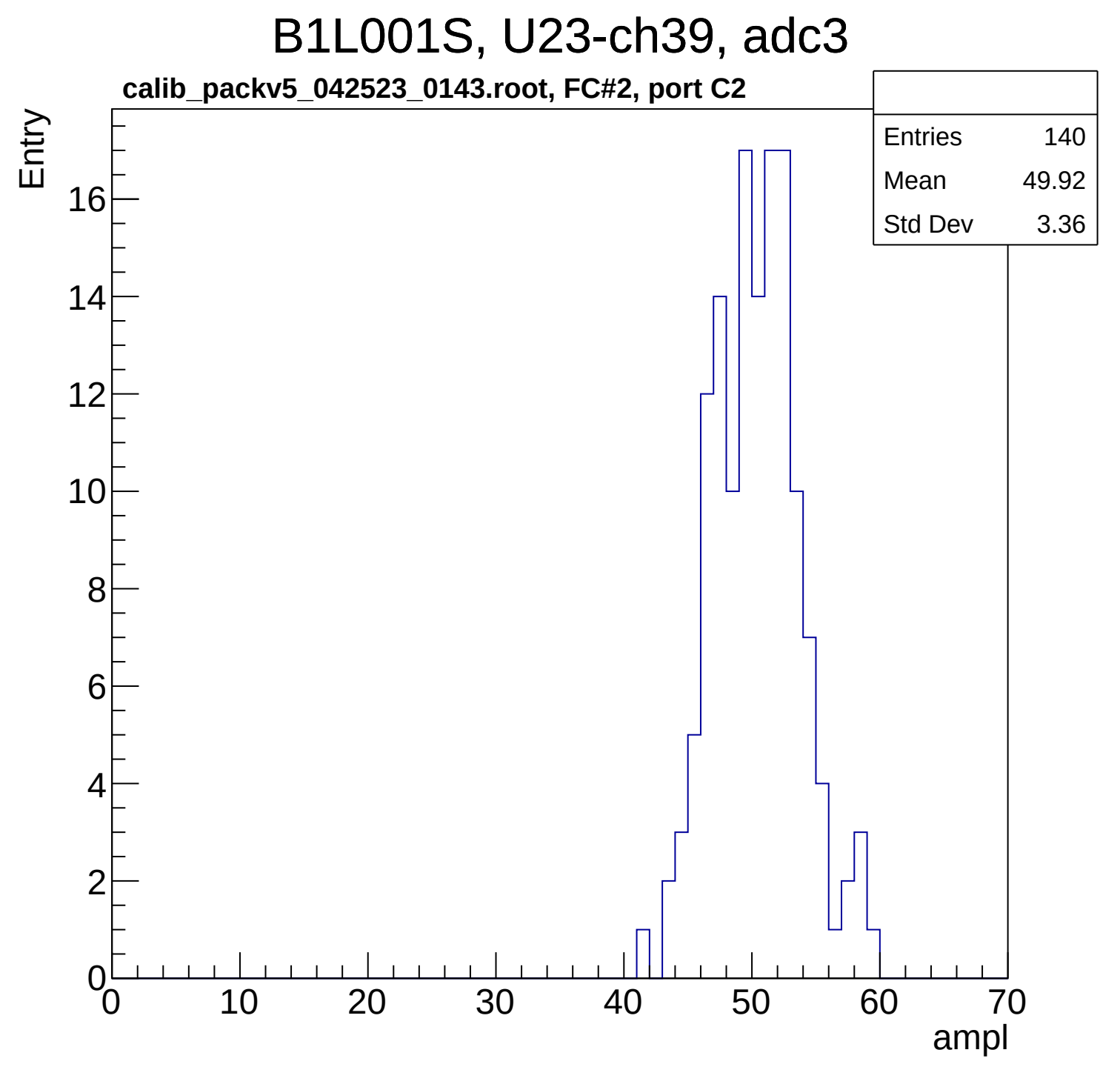
B1L001S, U23-ch39, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 49.92
Std Dev 3.36
Entry
ampl
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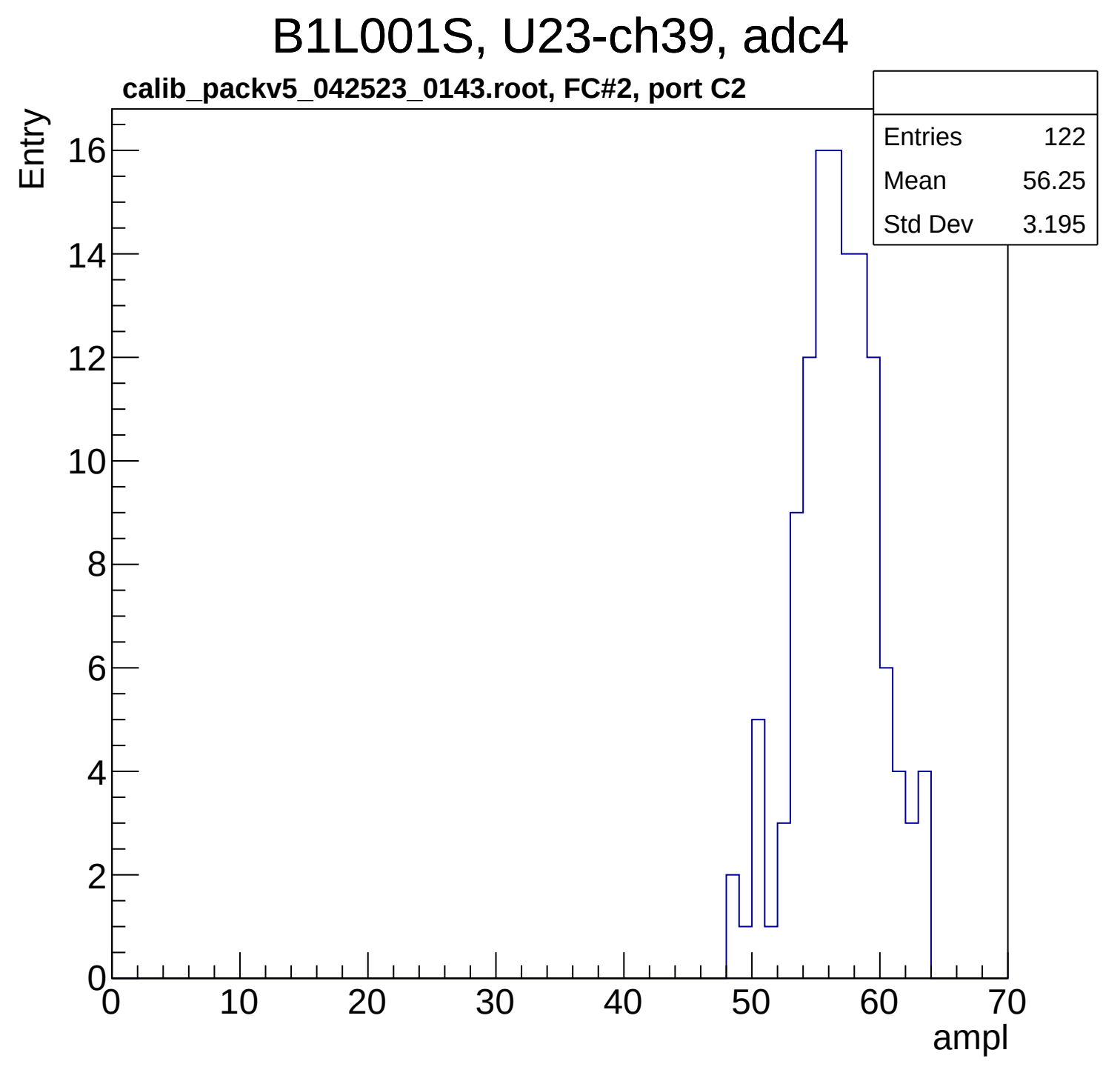
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch39, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 56.25
Std Dev 3.195
Entry
ampl
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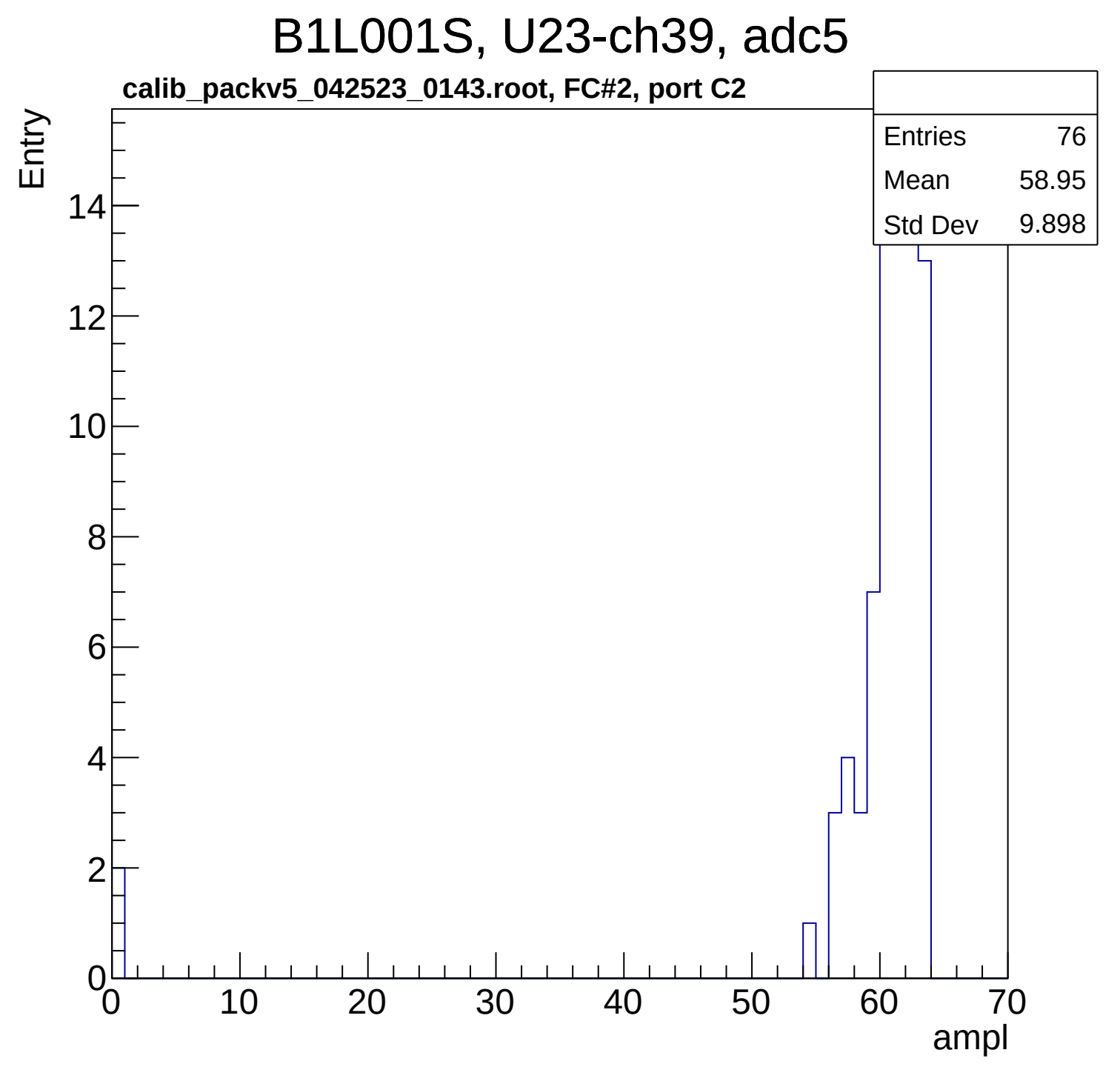
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch39, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 58.95
Std Dev 9.898
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
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40
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60
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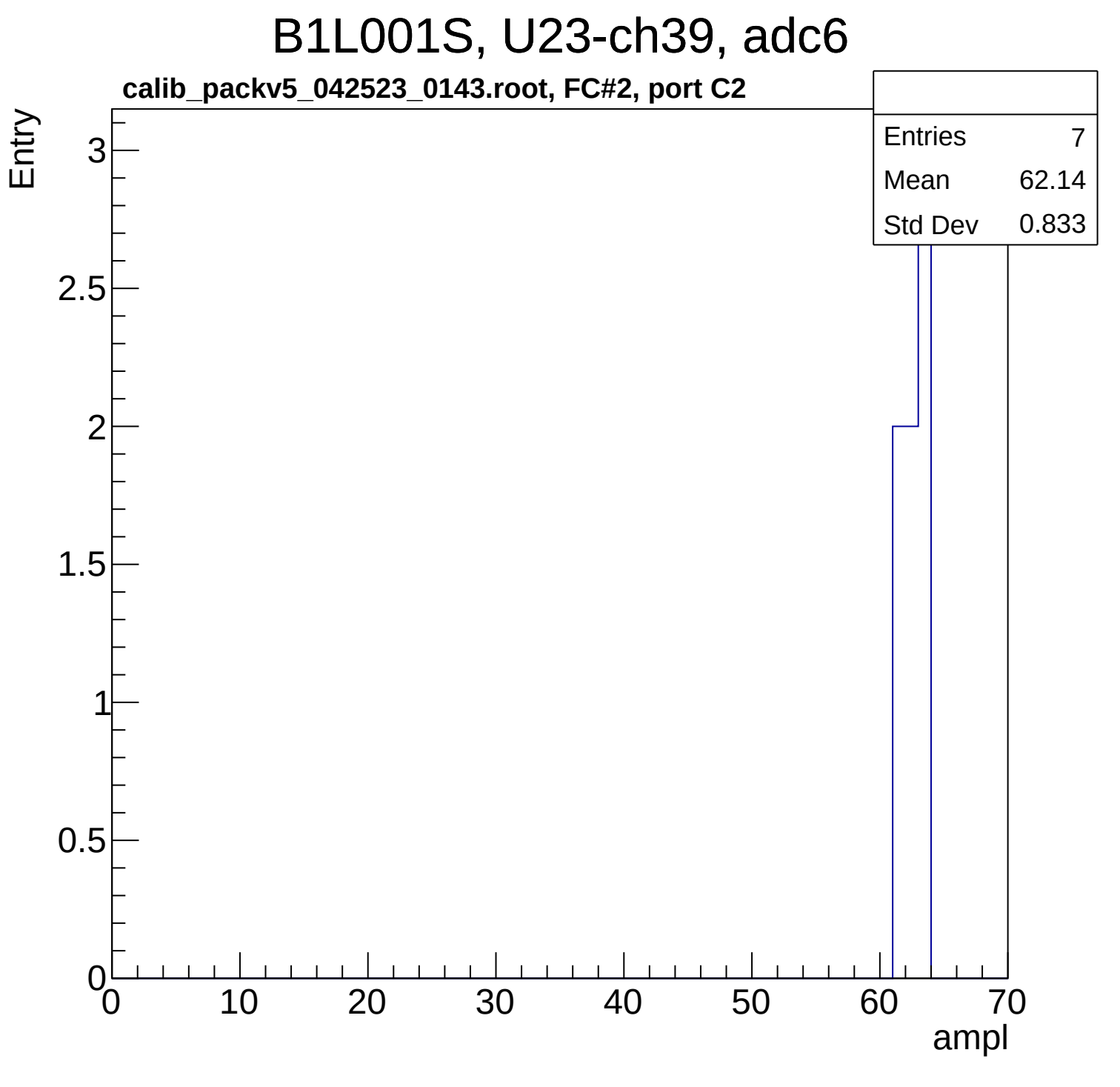
B1L001S, U23-ch39, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
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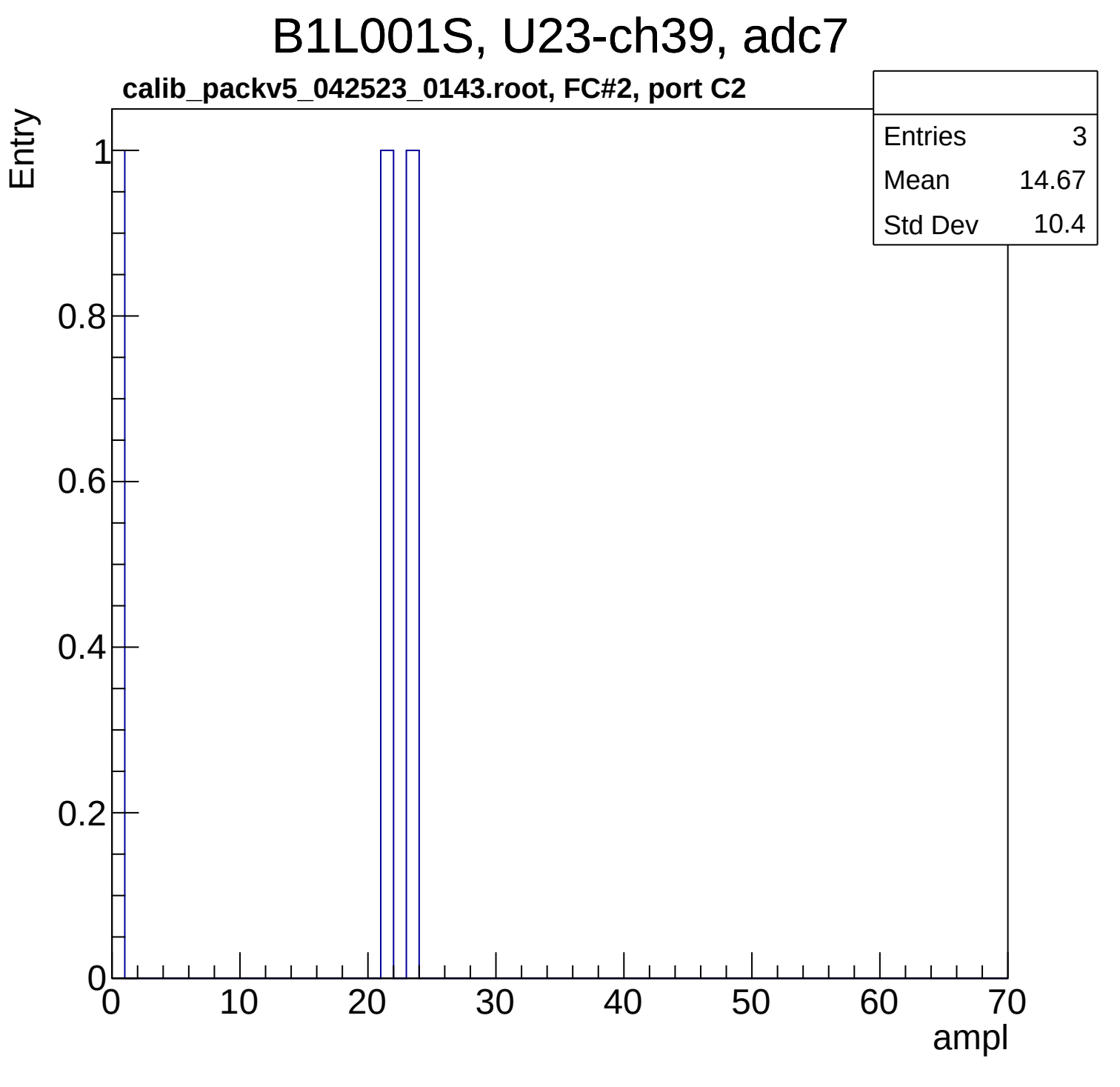
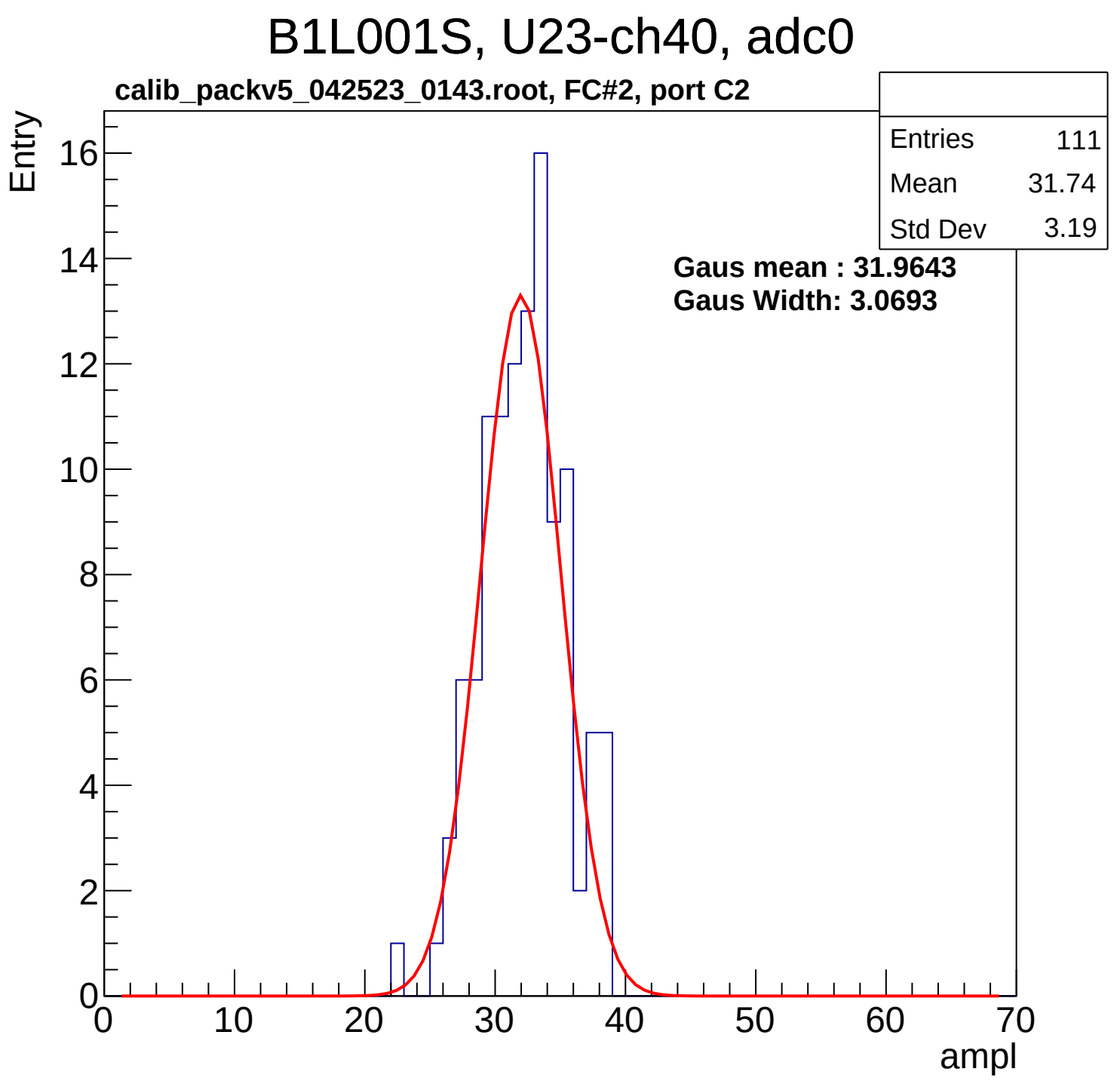
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch39, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 14.67
Std Dev 10.4
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch40, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 31.74
Std Dev 3.19
Gaus mean : 31.9643
Gaus Width: 3.0693
Entry
ampl
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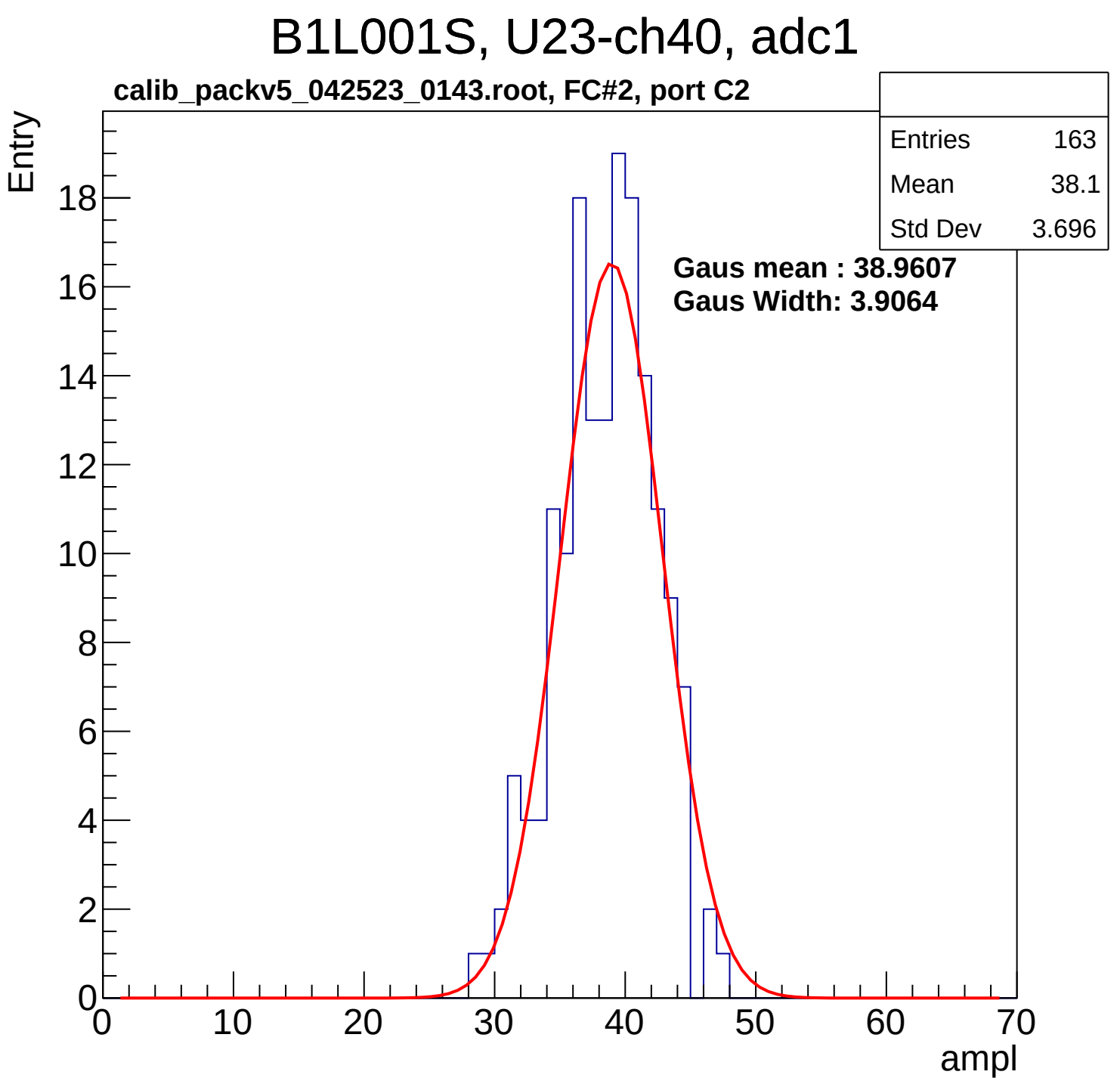
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch40, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 38.1
Std Dev 3.696
Gaus mean : 38.9607
Gaus Width: 3.9064
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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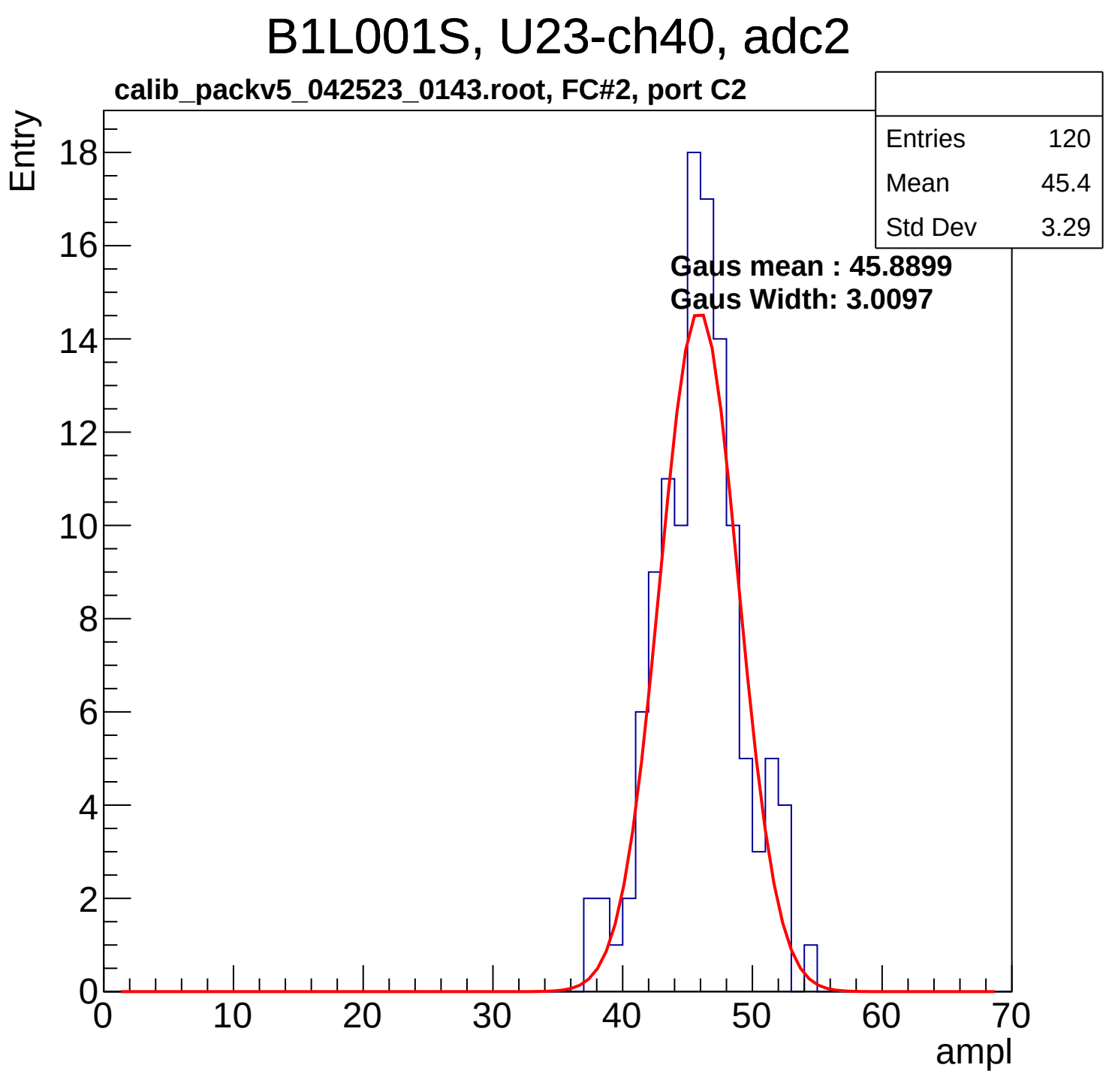
B1L001S, U23-ch40, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 45.4
Std Dev 3.29
Gaus mean : 45.8899
Gaus Width: 3.0097
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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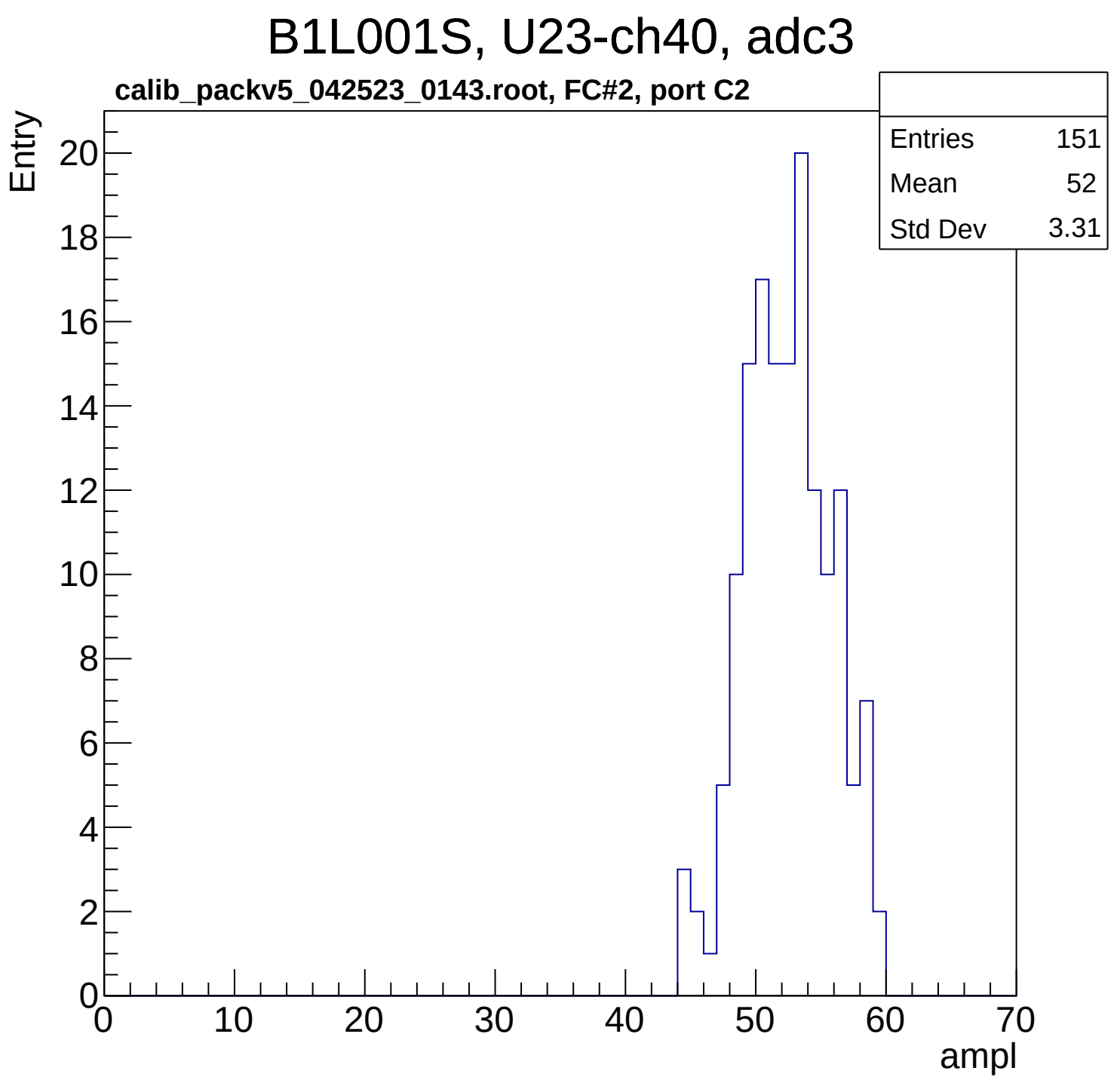
B1L001S, U23-ch40, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 52
Std Dev 3.31
Entry
ampl
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4
6
8
10
12
14
16
18
20
0
10
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40
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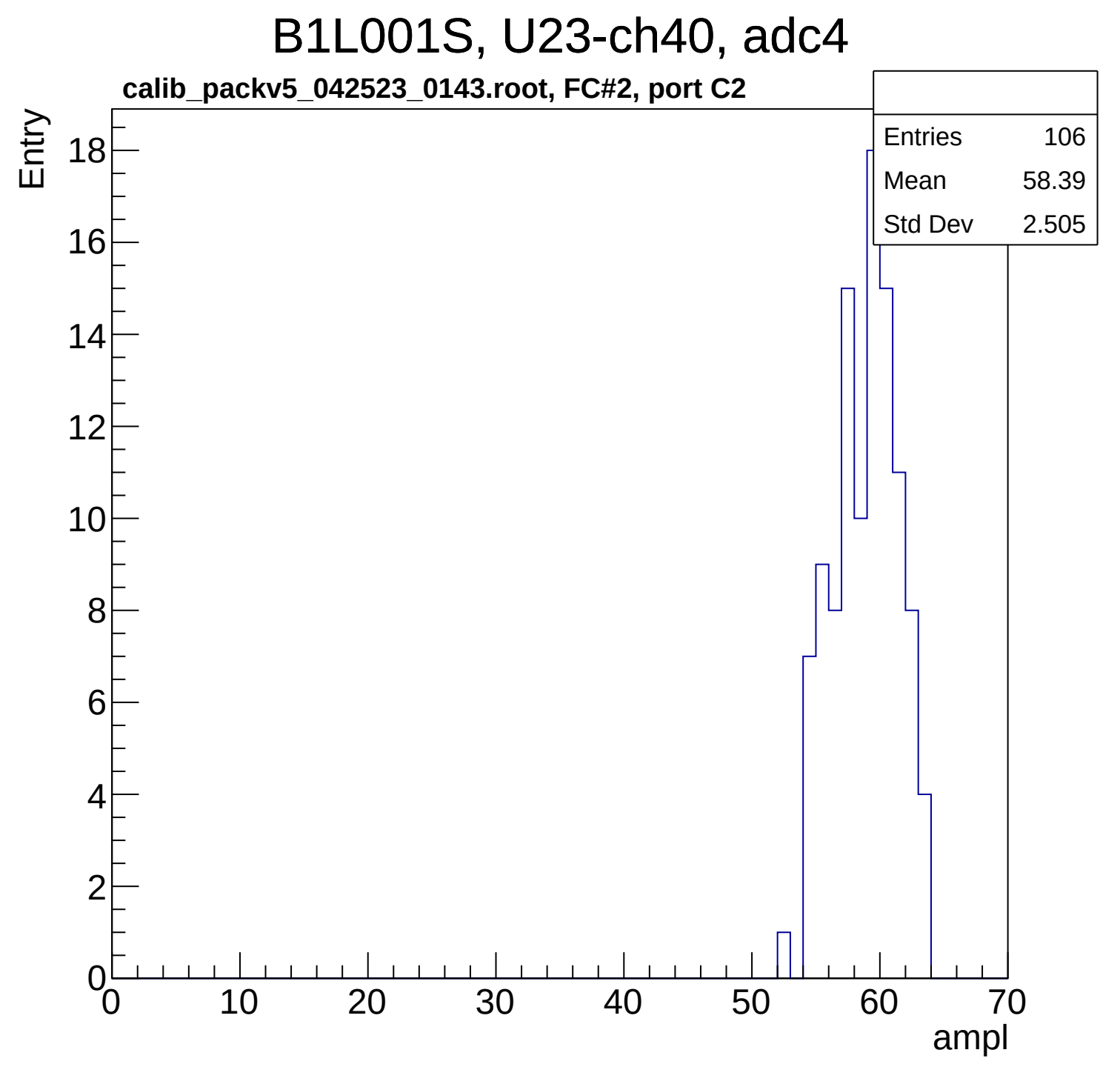
60
70


B1L001S, U23-ch40, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 58.39
Std Dev 2.505
Entry
ampl
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4
6
8
10
12
14
16
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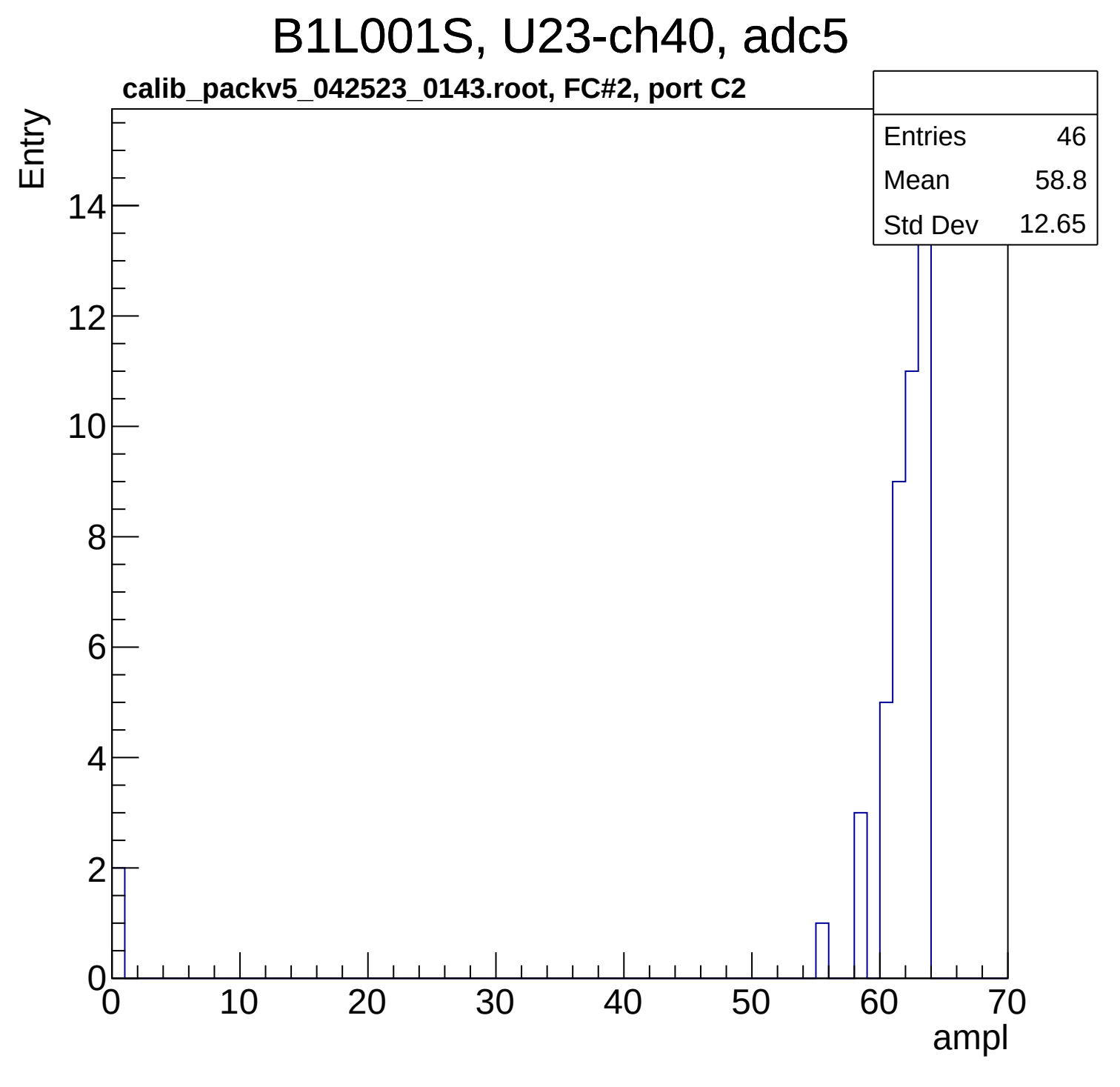
0
10
20
30
40
50
60
70


B1L001S, U23-ch40, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 46
Mean 58.8
Std Dev 12.65
Entry
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6
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10
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10
20
30
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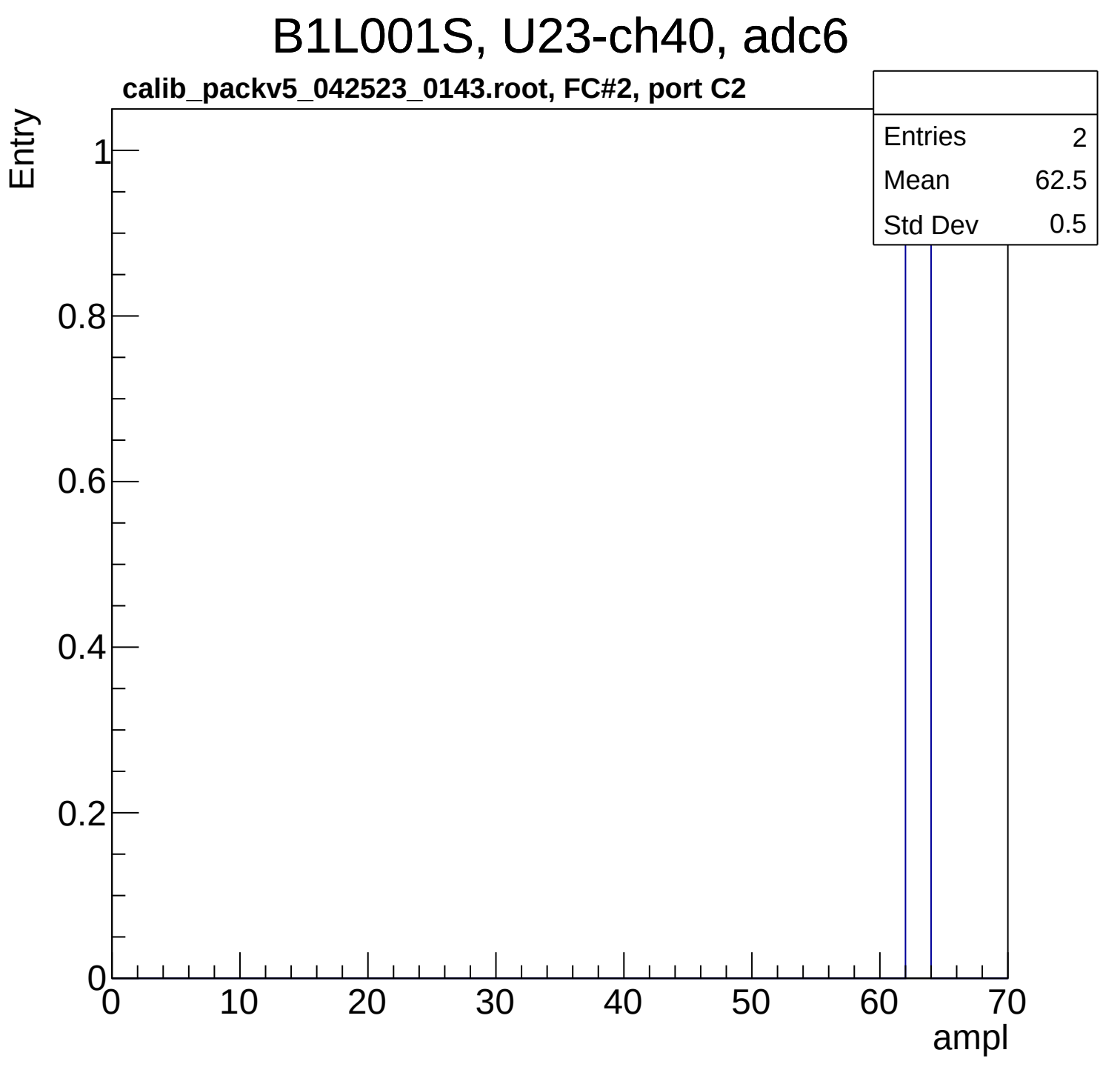
50
60
70
ampl


B1L001S, U23-ch40, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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0.6
0.4
0.2
0
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10
20
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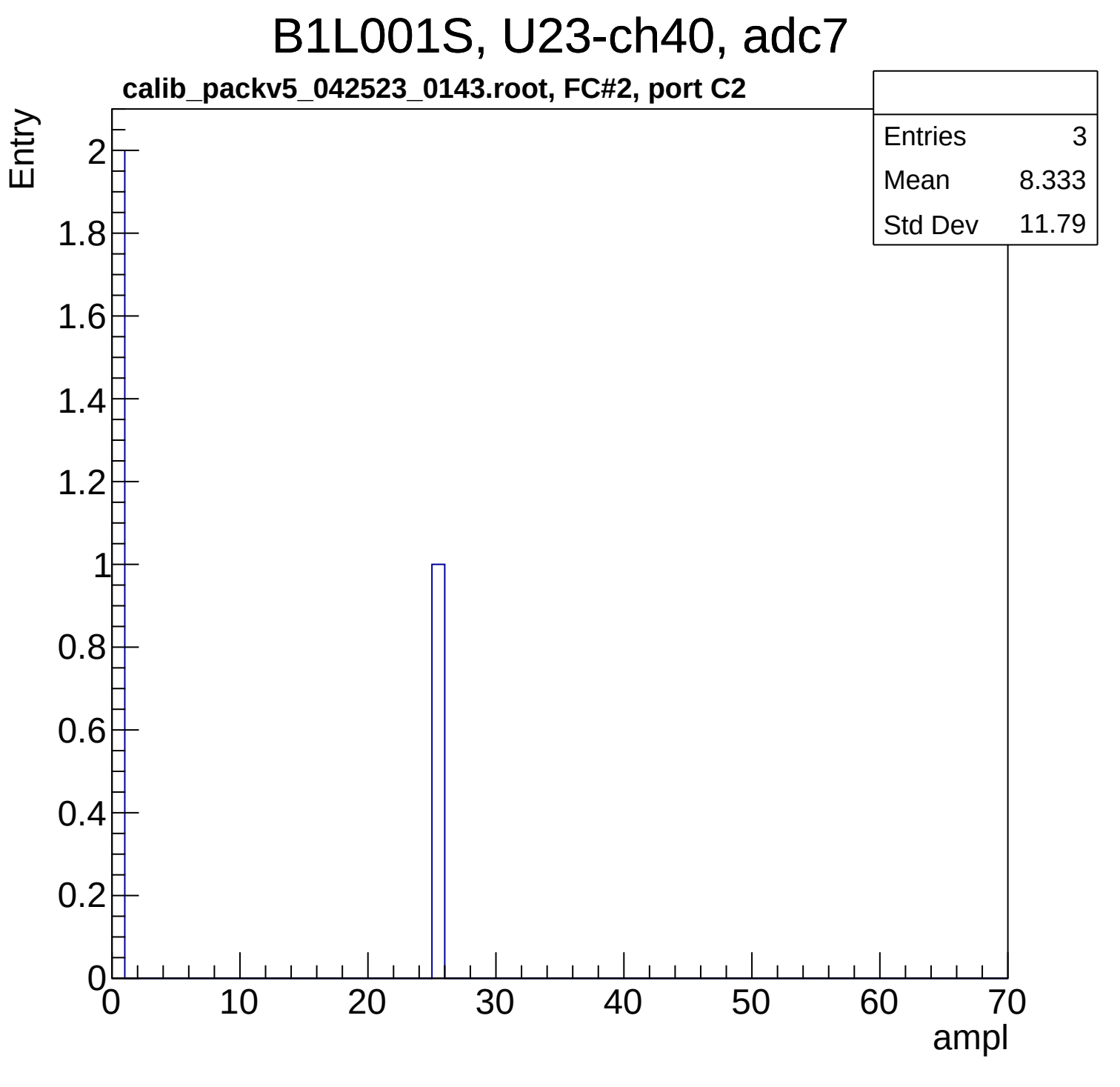
40
50
60
70
ampl


B1L001S, U23-ch40, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 8.333
Std Dev 11.79
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
20
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60
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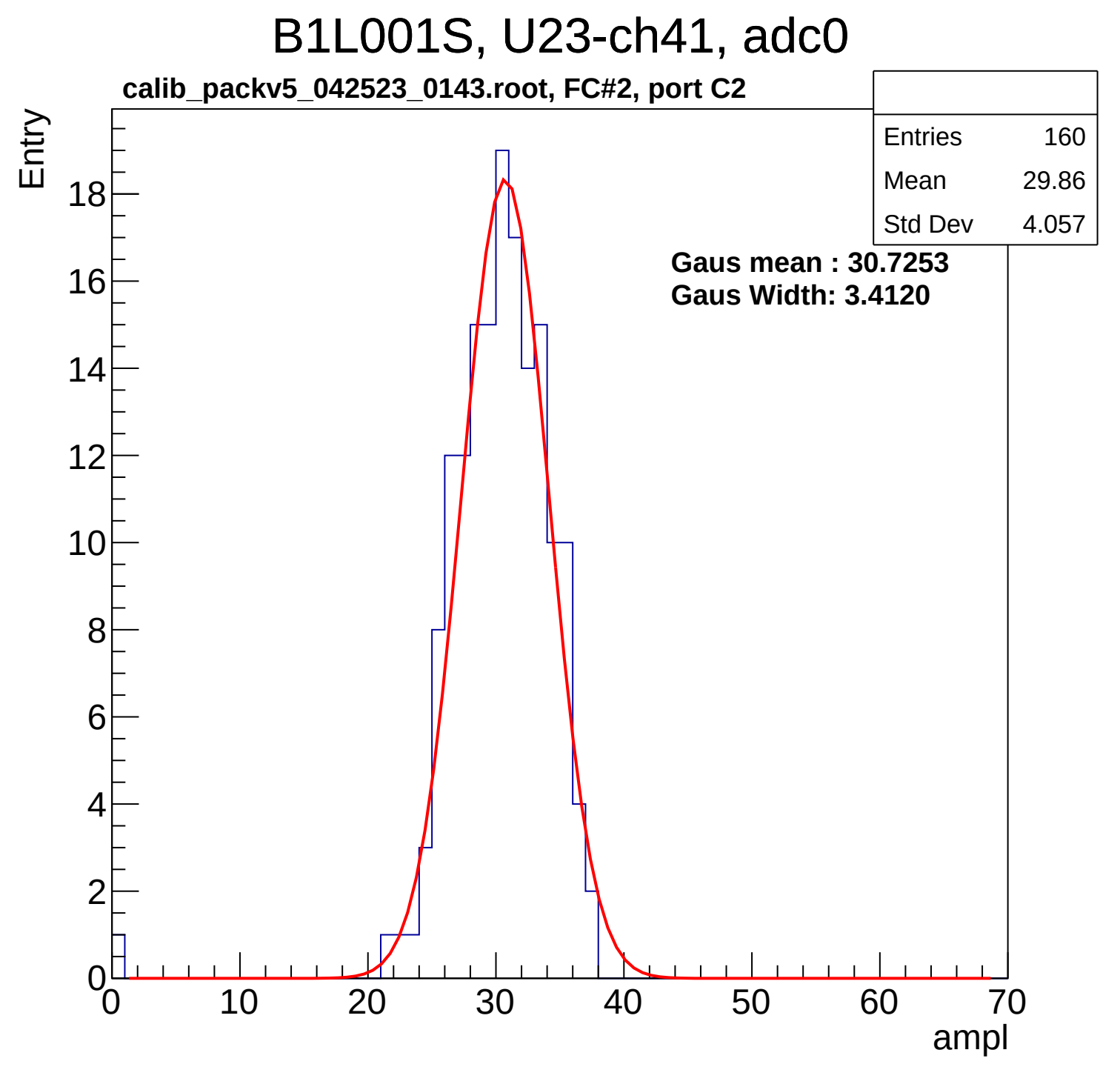
B1L001S, U23-ch41, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 29.86
Std Dev 4.057
Gaus mean : 30.7253
Gaus Width: 3.4120
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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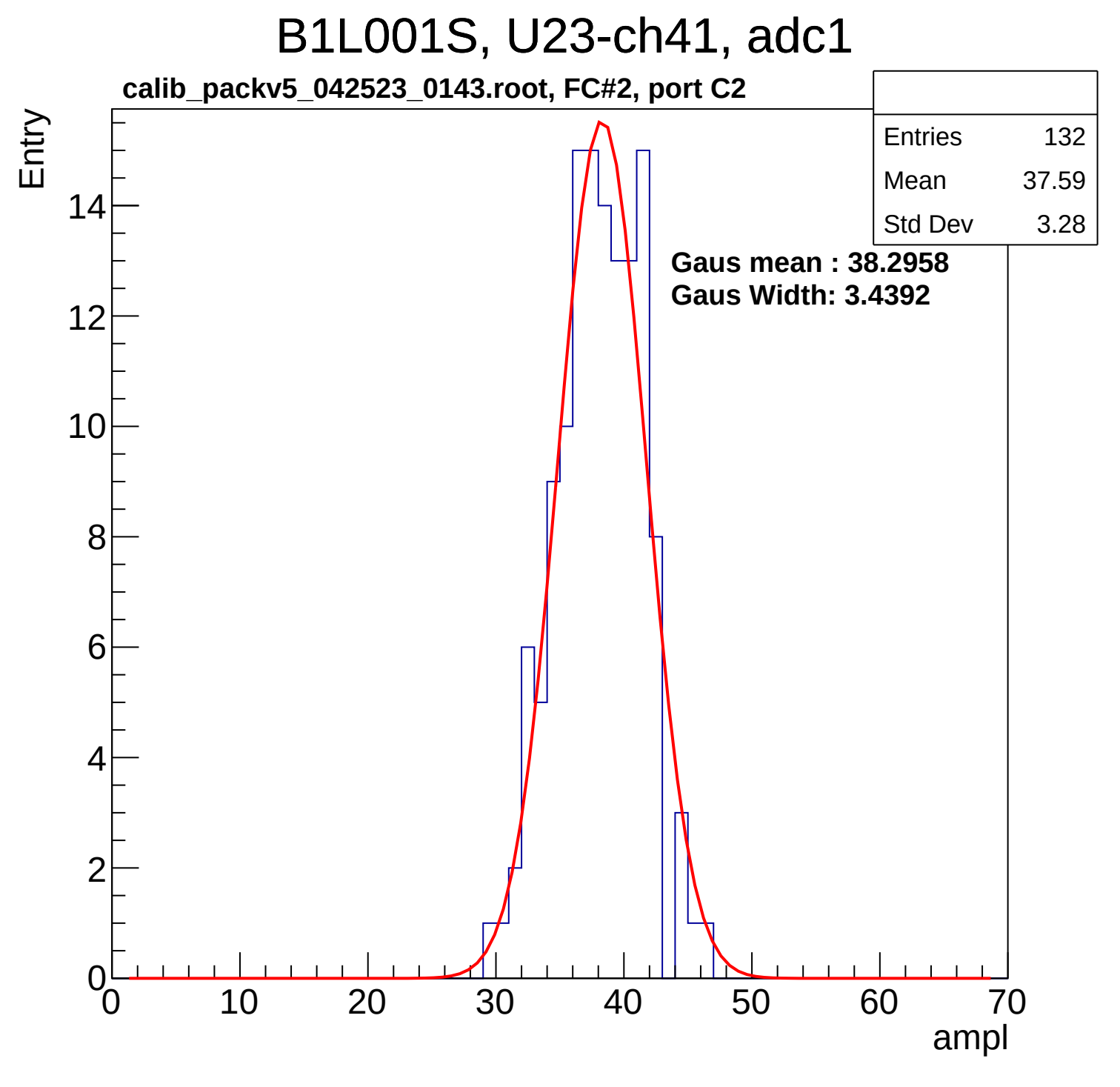
B1L001S, U23-ch41, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 37.59
Std Dev 3.28
Gaus mean : 38.2958
Gaus Width: 3.4392
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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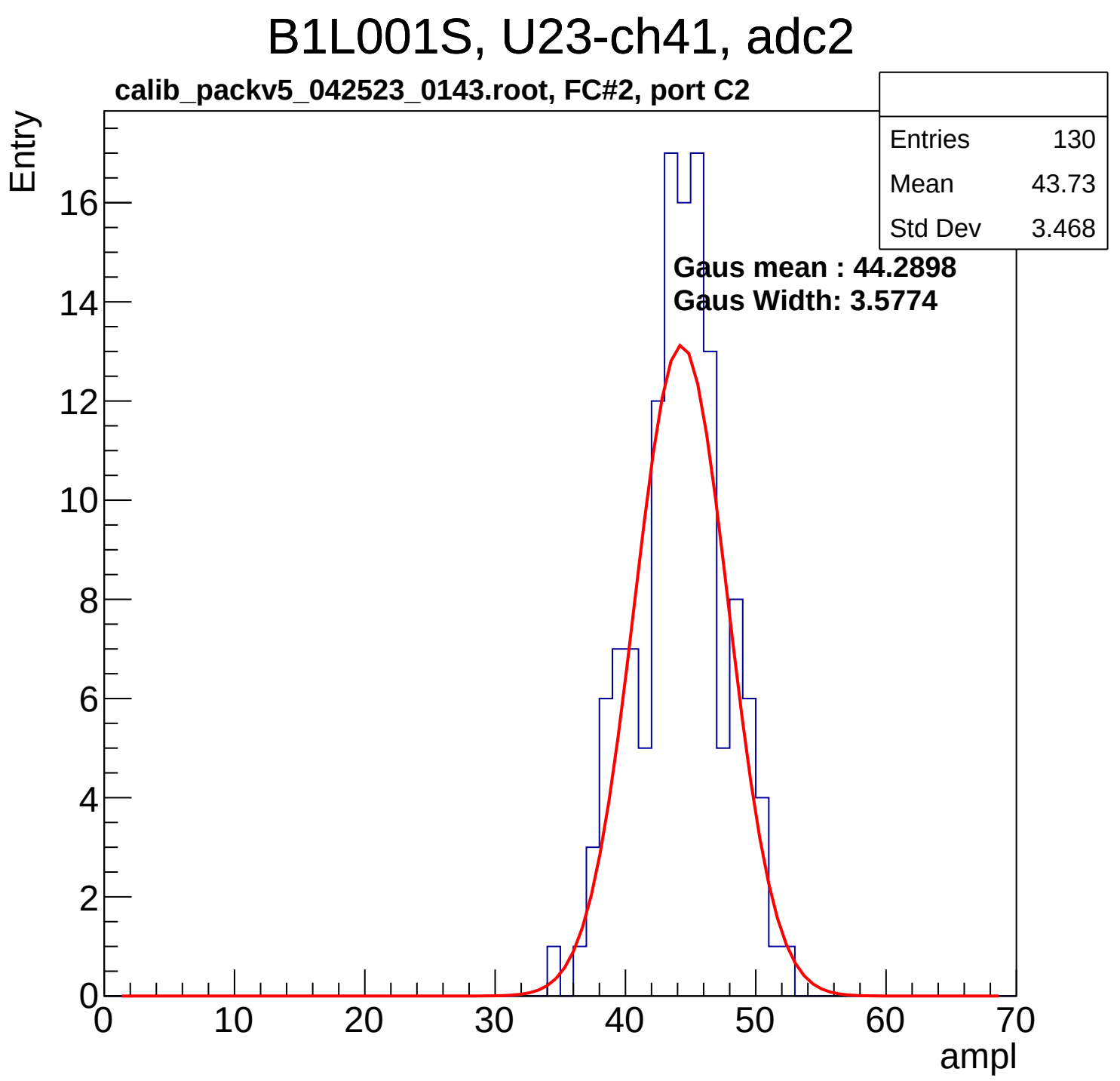
B1L001S, U23-ch41, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 43.73
Std Dev 3.468
Gaus mean : 44.2898
Gaus Width: 3.5774
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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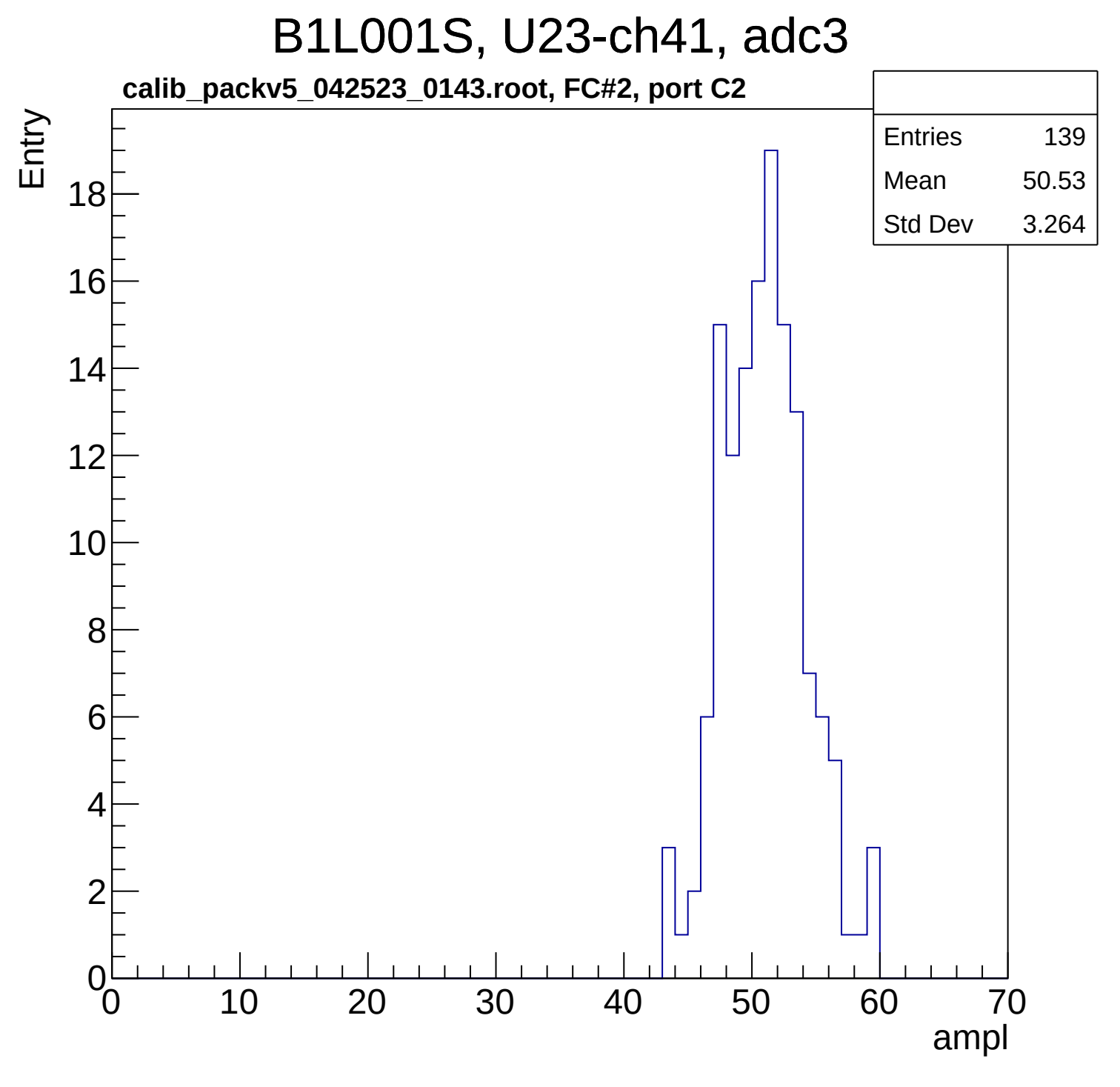
B1L001S, U23-ch41, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 50.53
Std Dev 3.264
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
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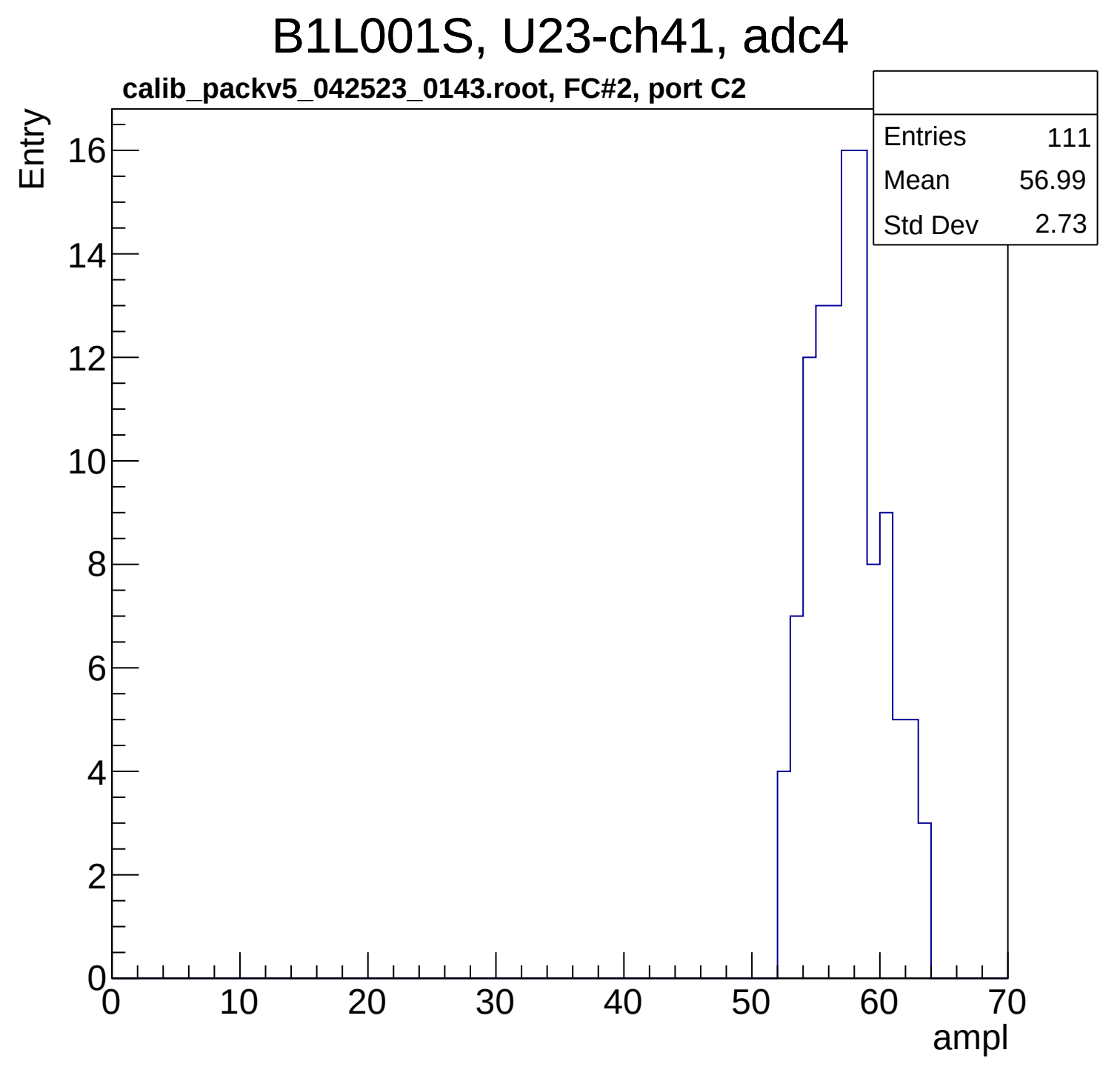
B1L001S, U23-ch41, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 56.99
Std Dev 2.73
Entry
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2
4
6
8
10
12
14
16
0
10
20
30
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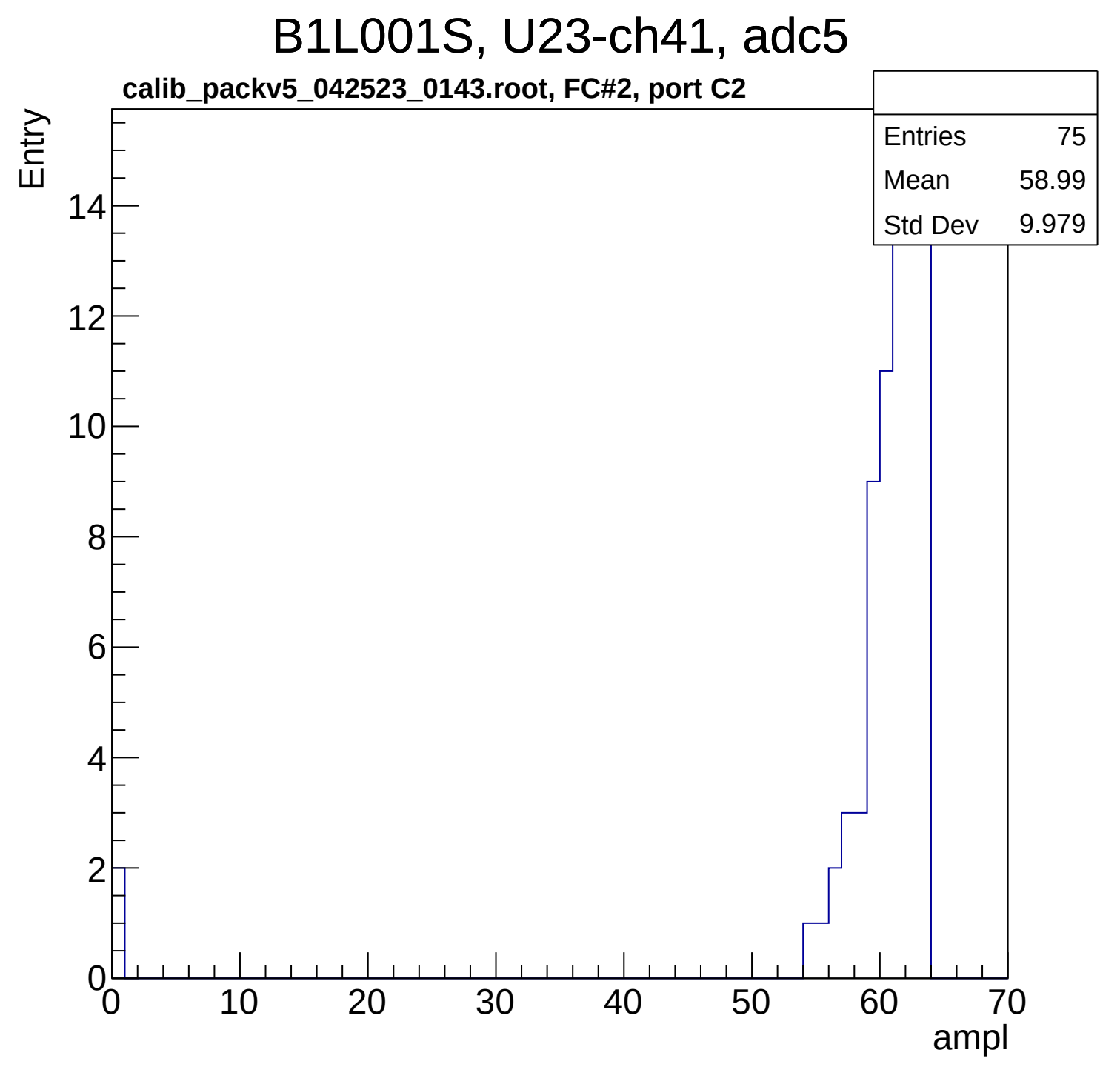
50
60
70
ampl


B1L001S, U23-ch41, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 58.99
Std Dev 9.979
Entry
ampl
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4
6
8
10
12
14
0
10
20
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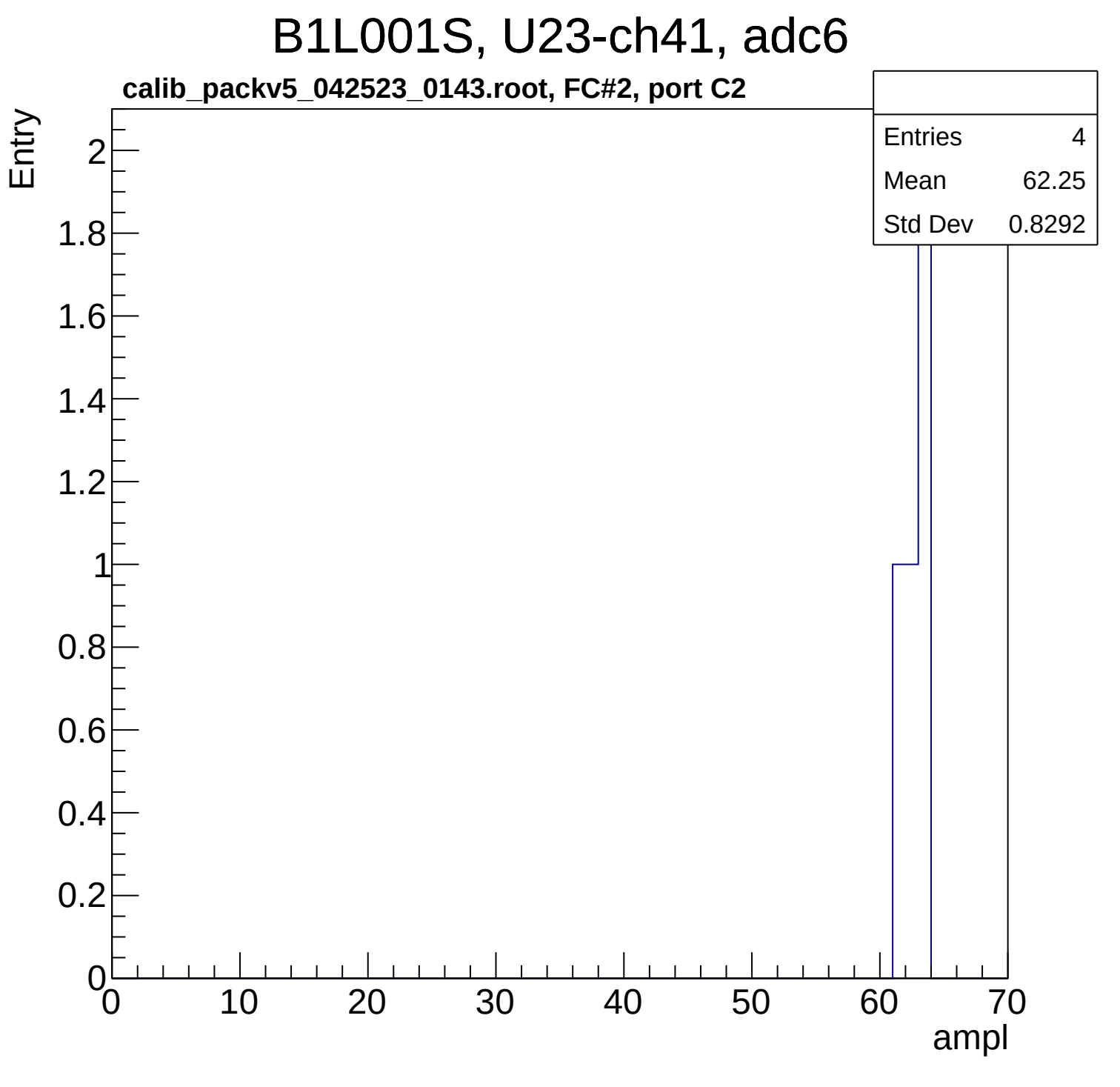
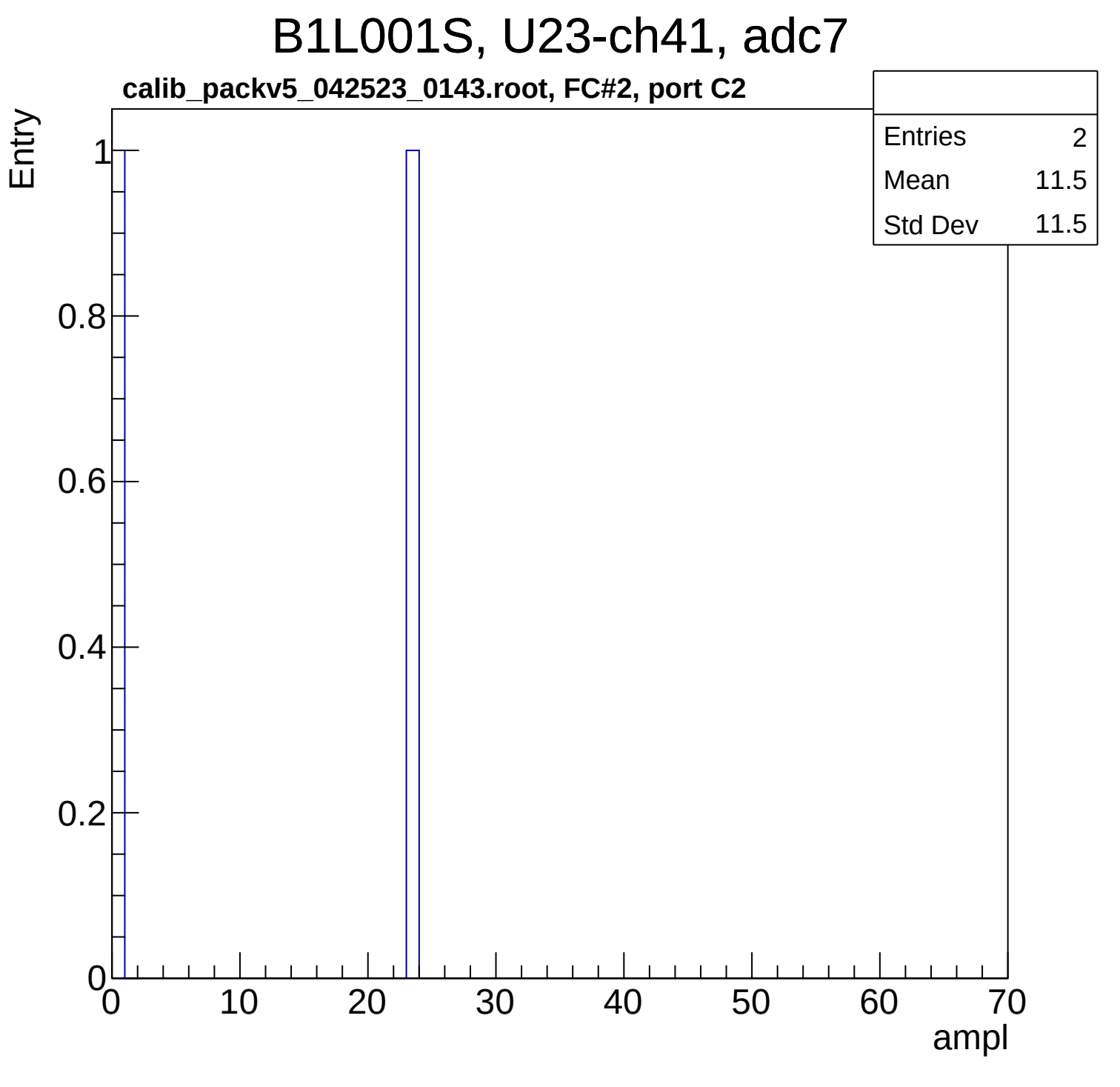
40
50
60
70


B1L001S, U23-ch41, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch41, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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0.6
0.4
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0
0
10
20
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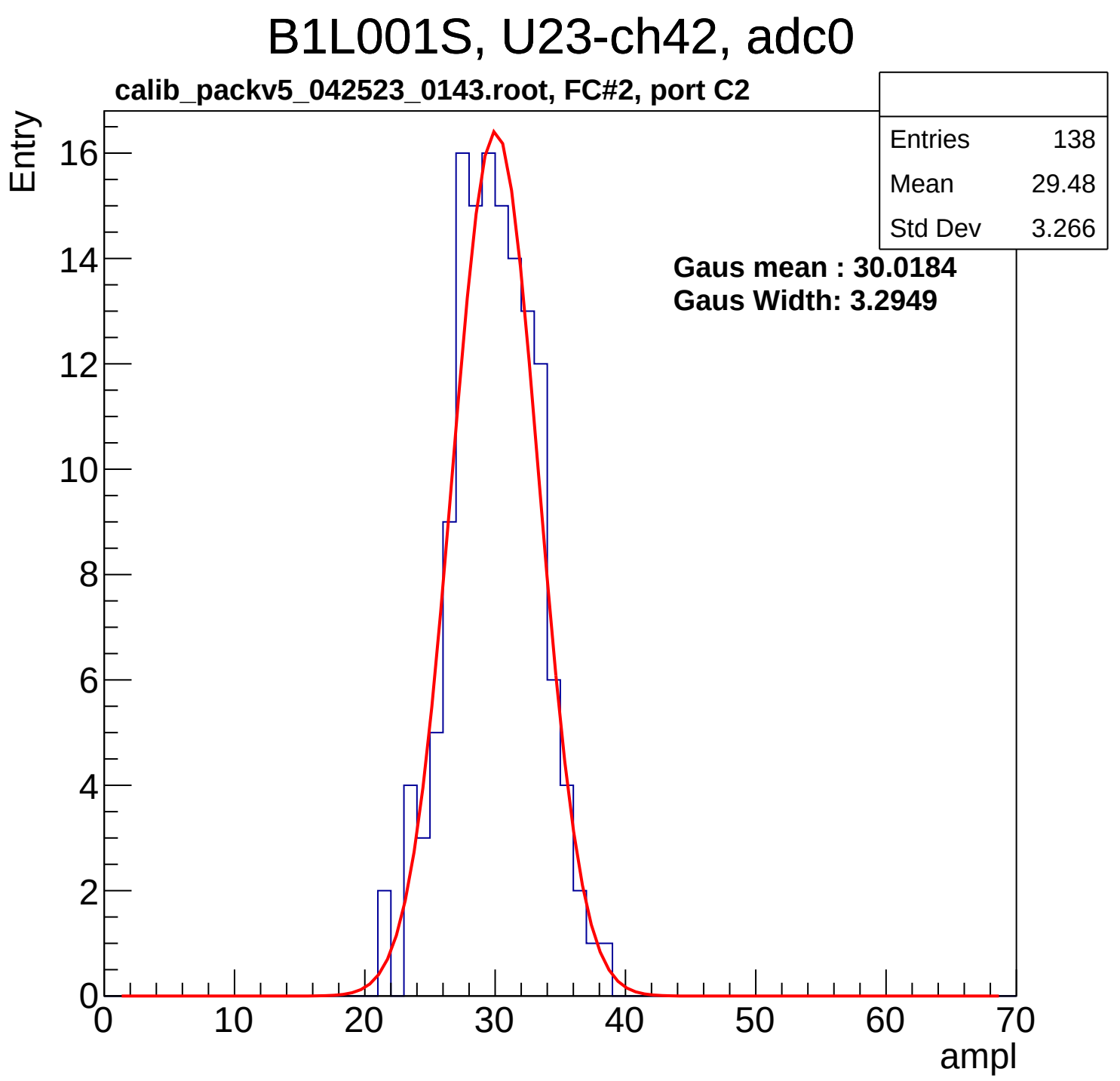
40
50
60
70
ampl


B1L001S, U23-ch42, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 29.48
Std Dev 3.266
Gaus mean : 30.0184
Gaus Width: 3.2949
Entry
ampl
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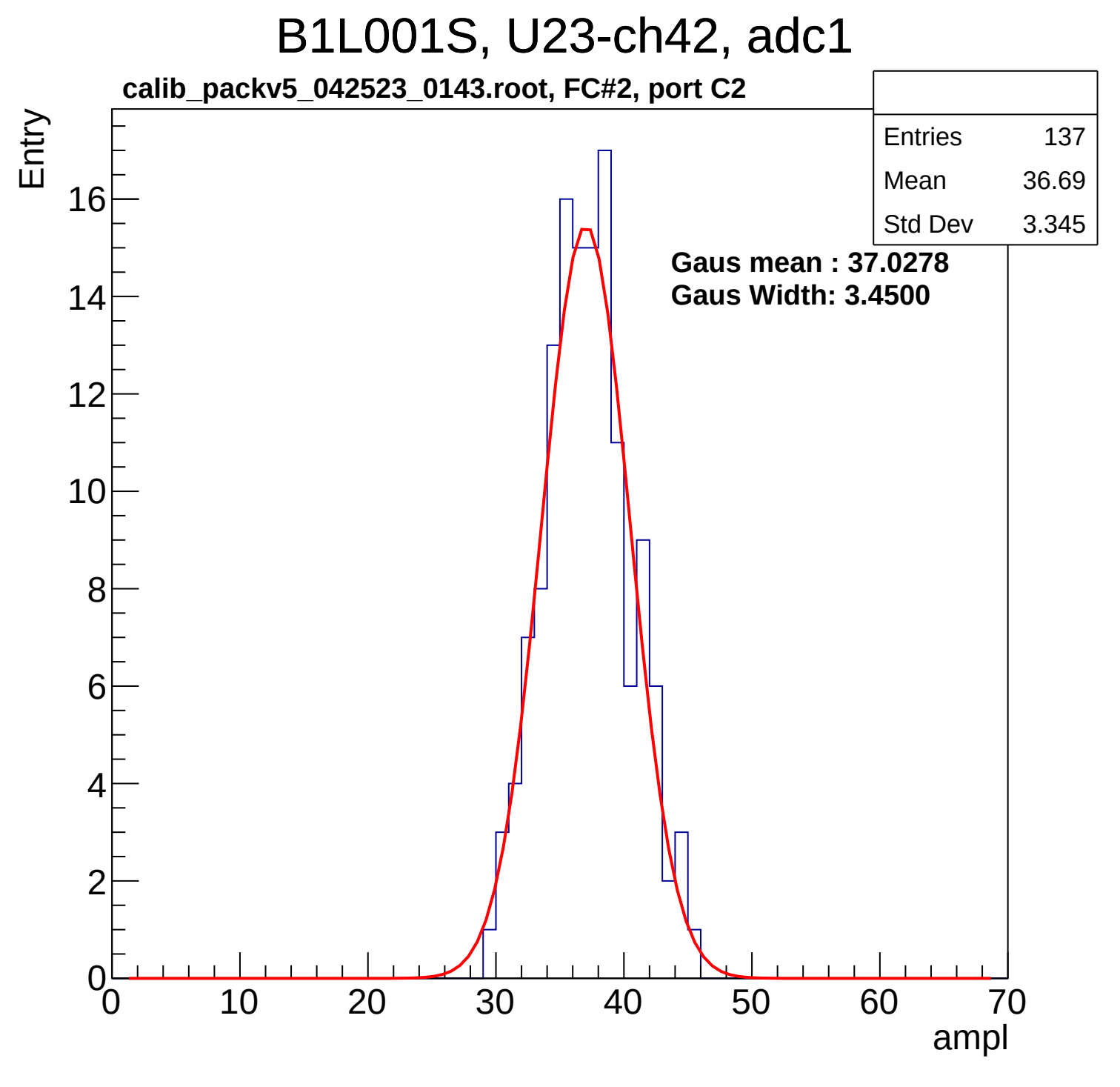
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch42, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 36.69
Std Dev 3.345
Gaus mean : 37.0278
Gaus Width: 3.4500
Entry
ampl
0
2
4
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10
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60
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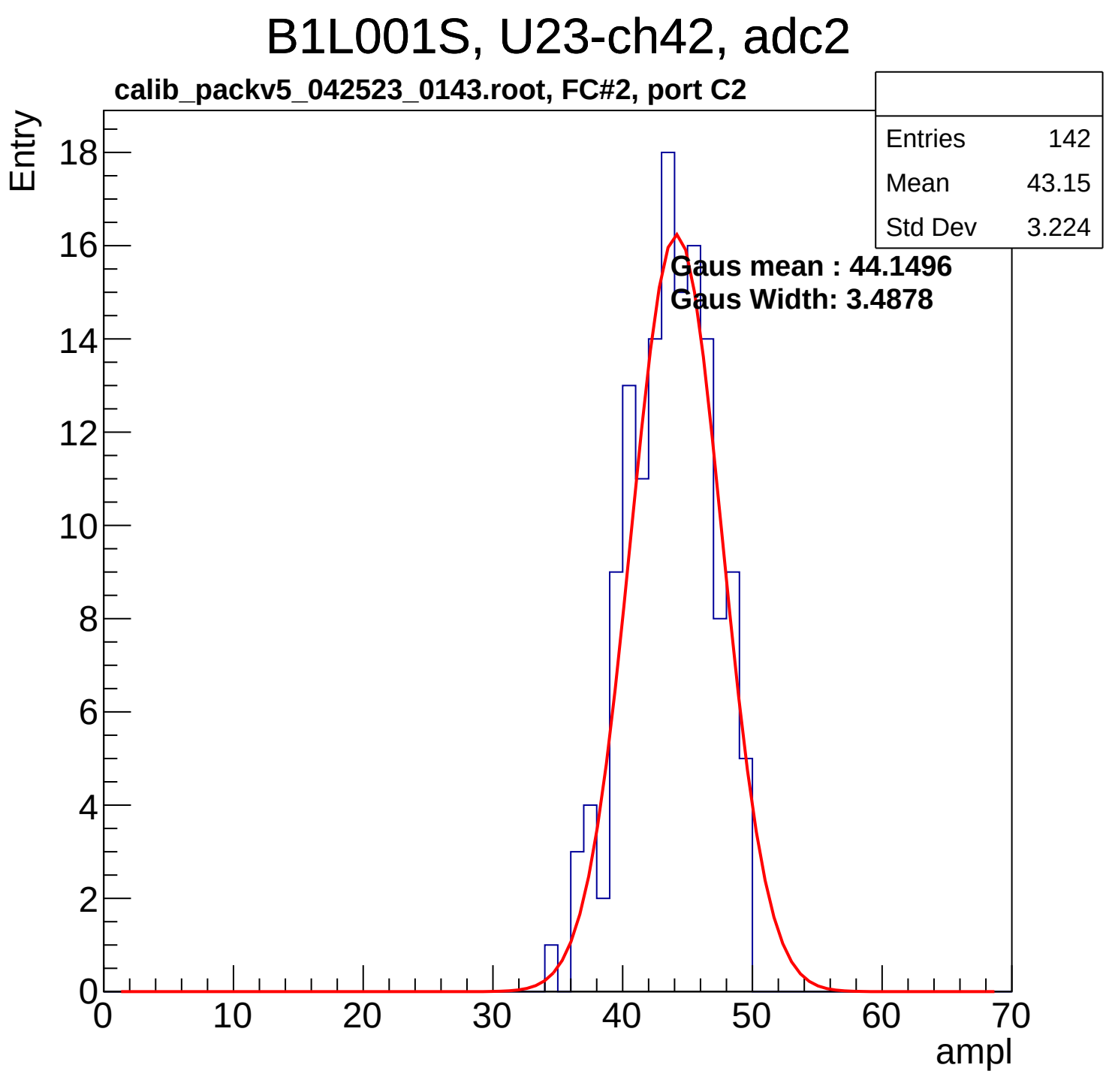
B1L001S, U23-ch42, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 43.15
Std Dev 3.224
Gaus mean : 44.1496
Gaus Width: 3.4878
Entry
ampl
0
2
4
6
8
10
12
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16
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0
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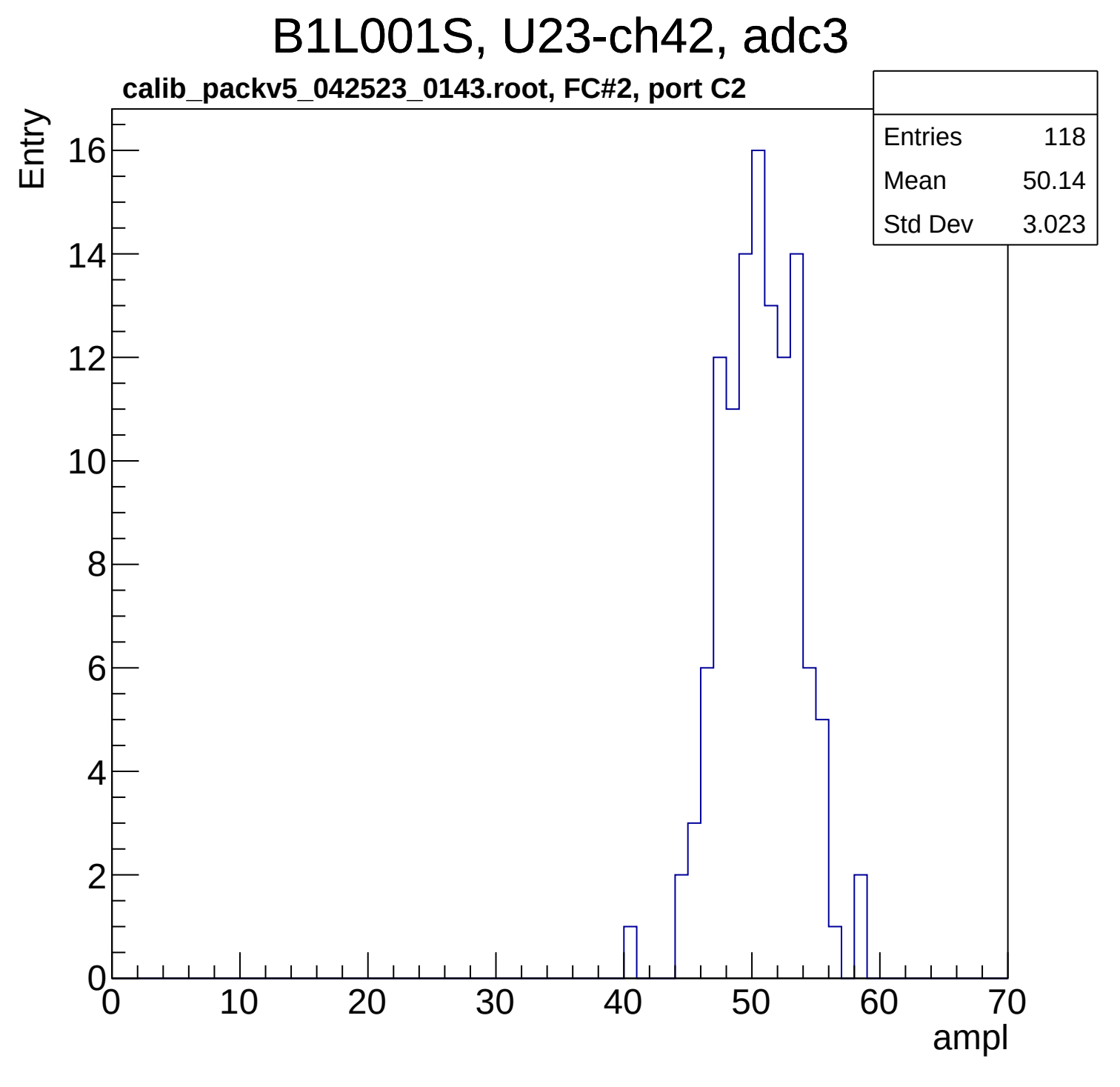
B1L001S, U23-ch42, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 50.14
Std Dev 3.023
Entry
0
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4
6
8
10
12
14
16
0
10
20
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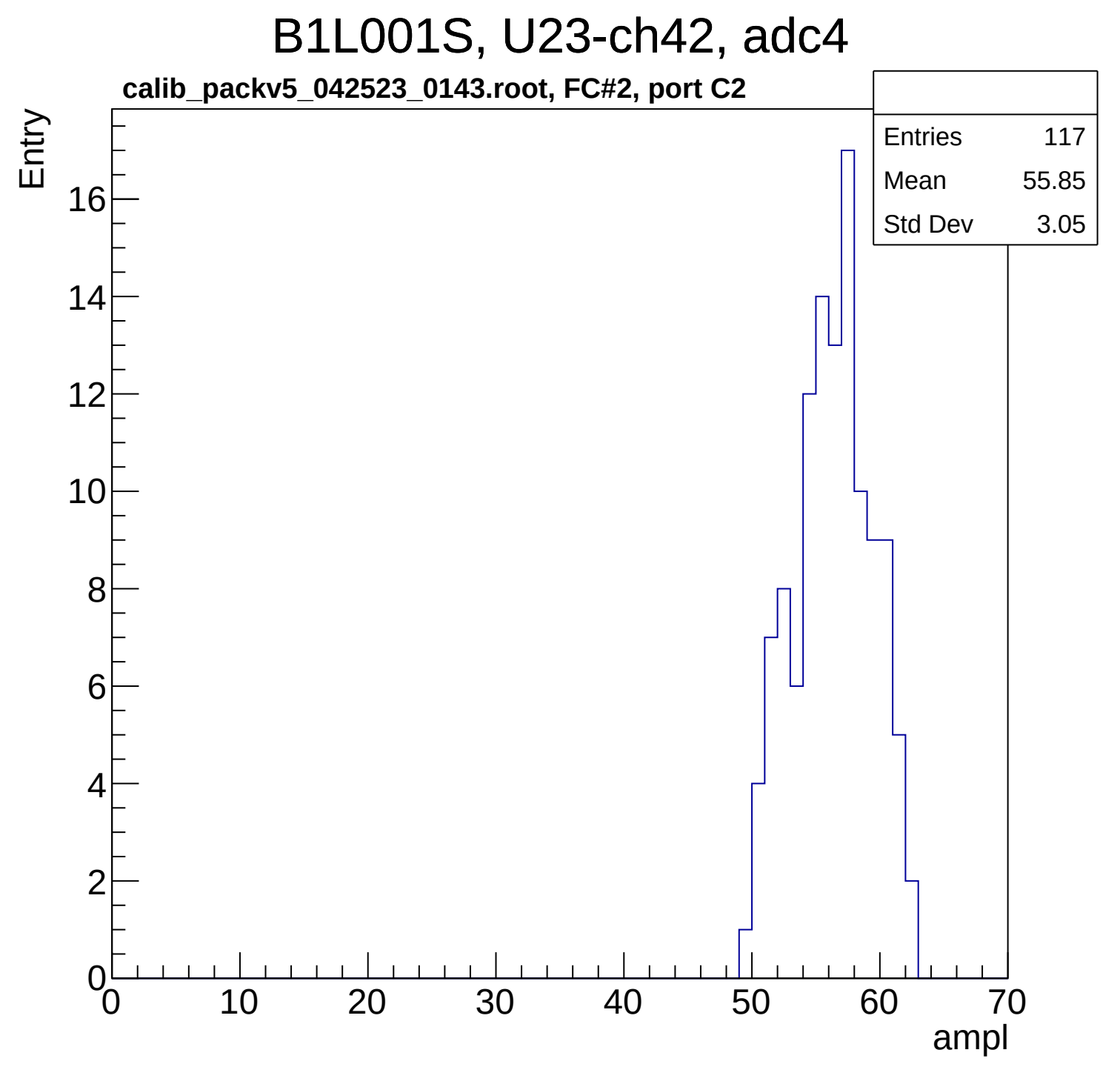
50
60
70
ampl


B1L001S, U23-ch42, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 55.85
Std Dev 3.05
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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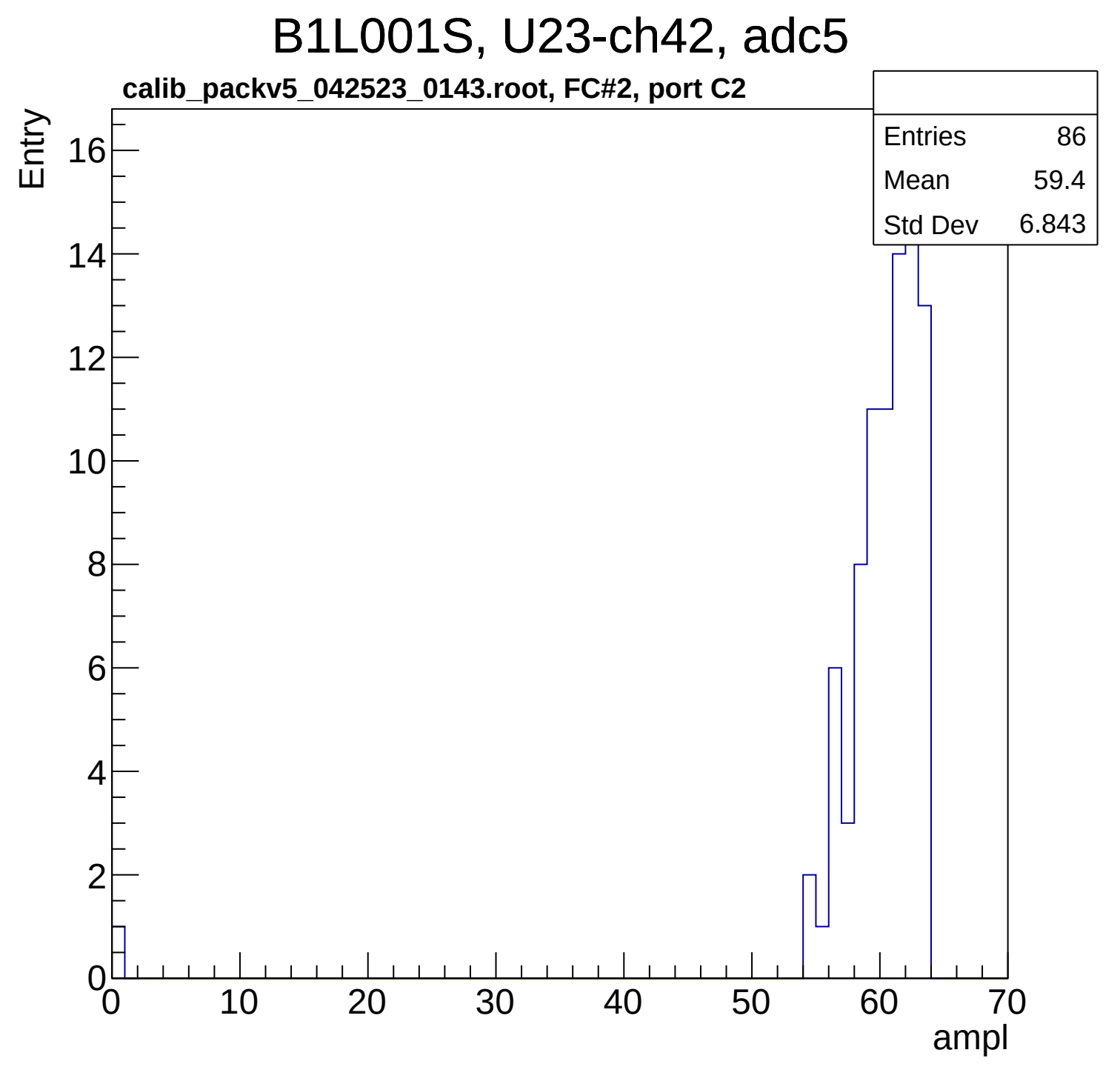
B1L001S, U23-ch42, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 86
Mean 59.4
Std Dev 6.843
Entry
ampl
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2
4
6
8
10
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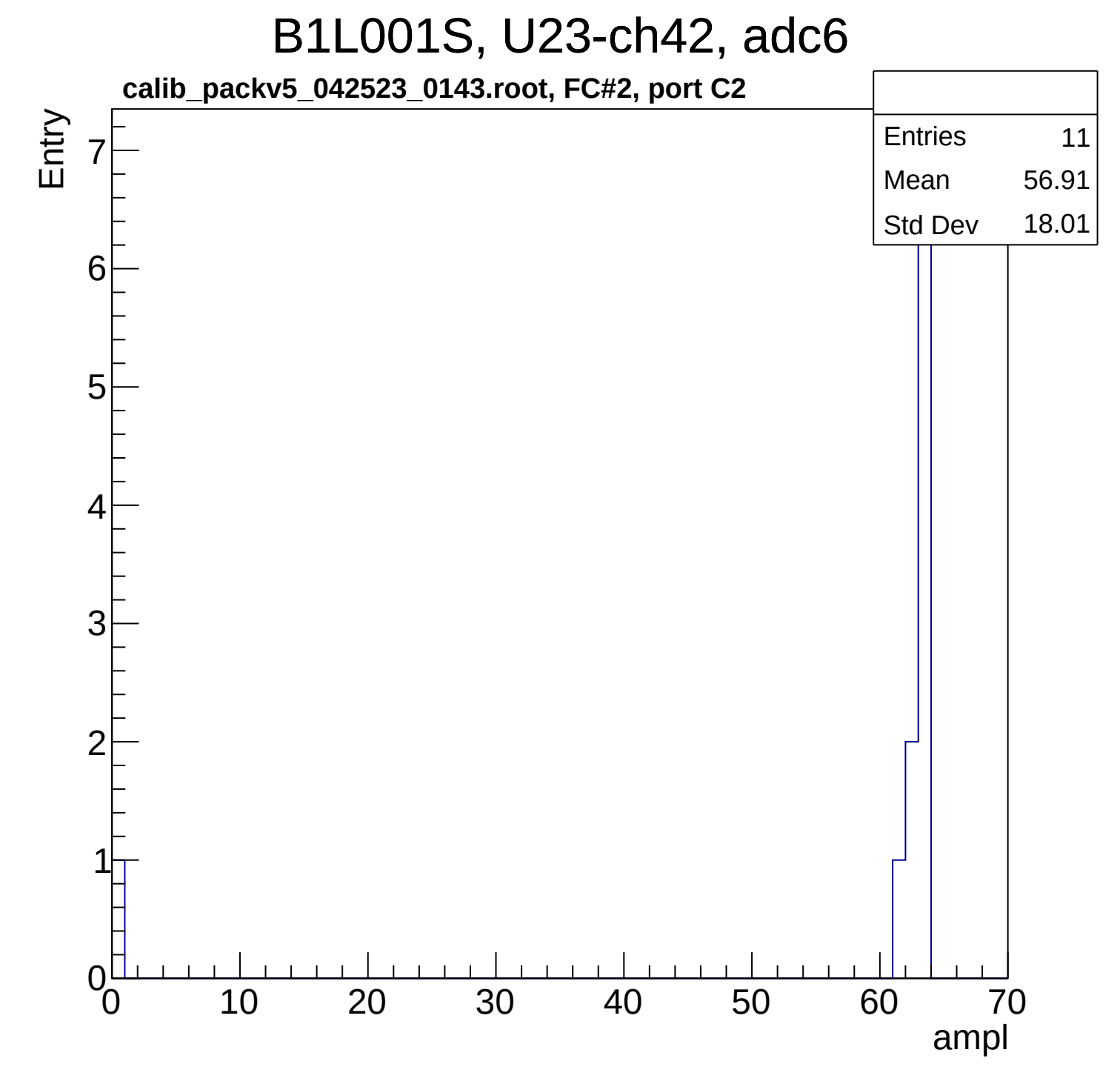
B1L001S, U23-ch42, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 56.91
Std Dev 18.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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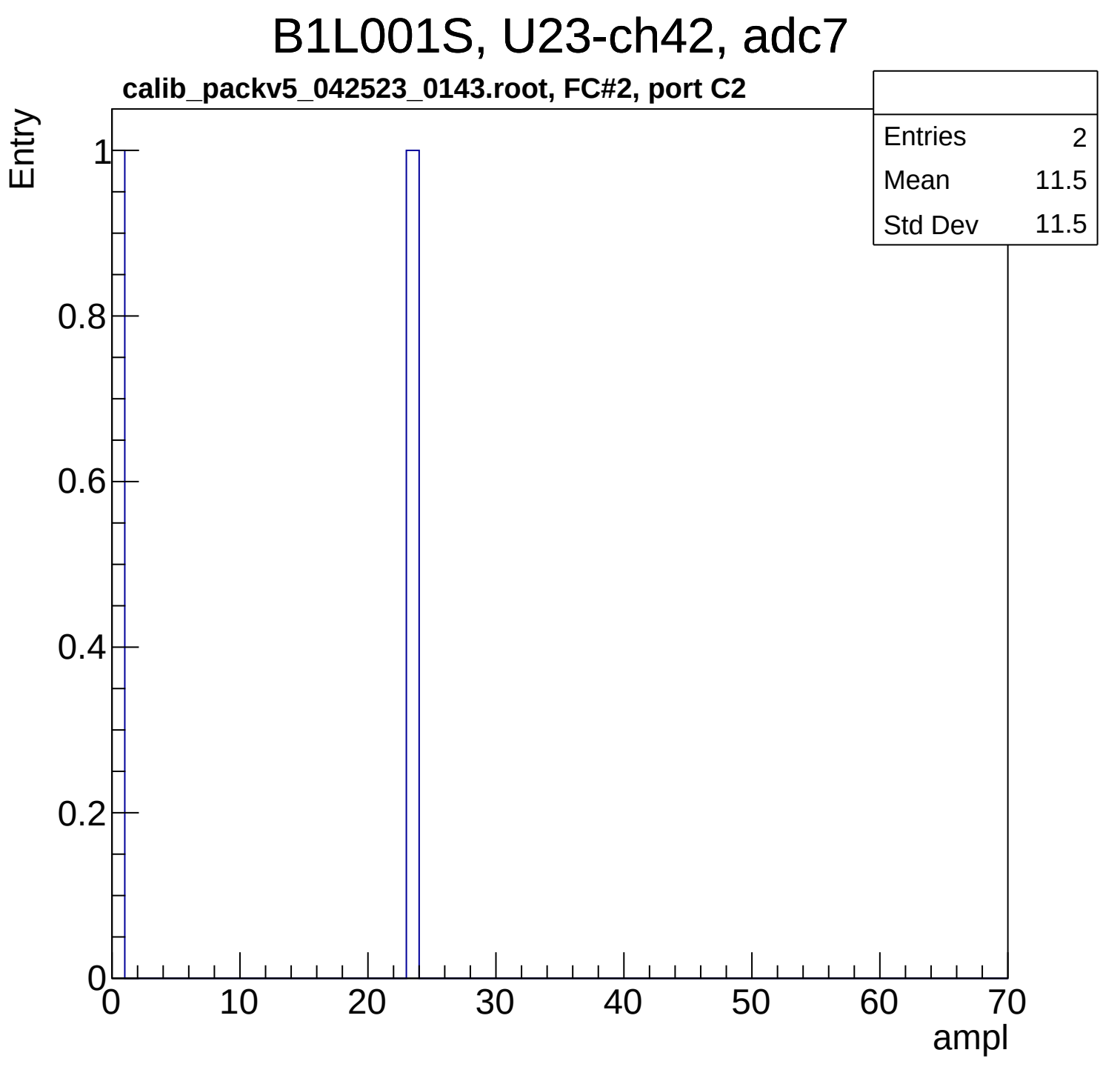
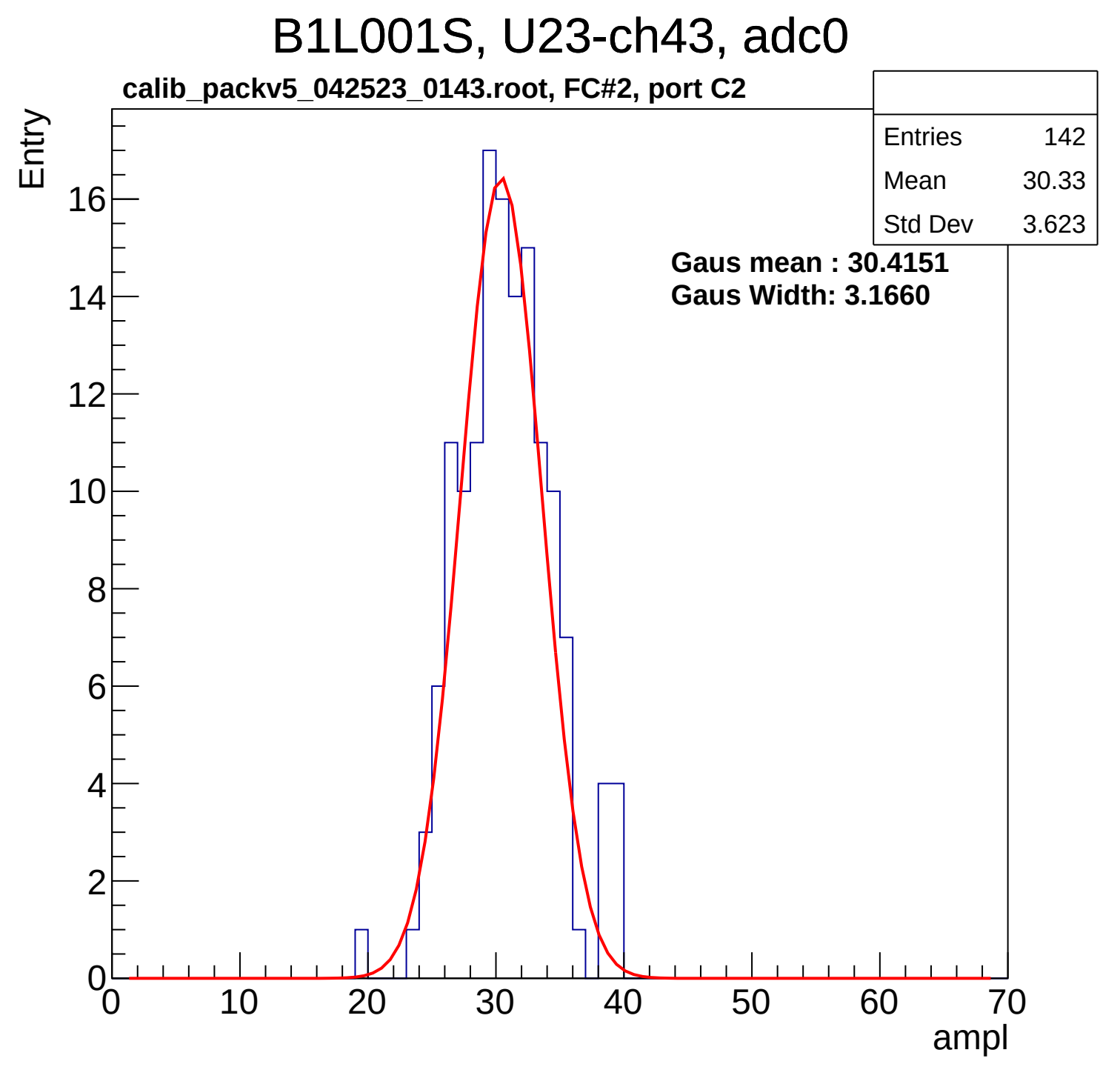

B1L001S, U23-ch42, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch43, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 30.33
Std Dev 3.623
Gaus mean : 30.4151
Gaus Width: 3.1660
Entry
ampl
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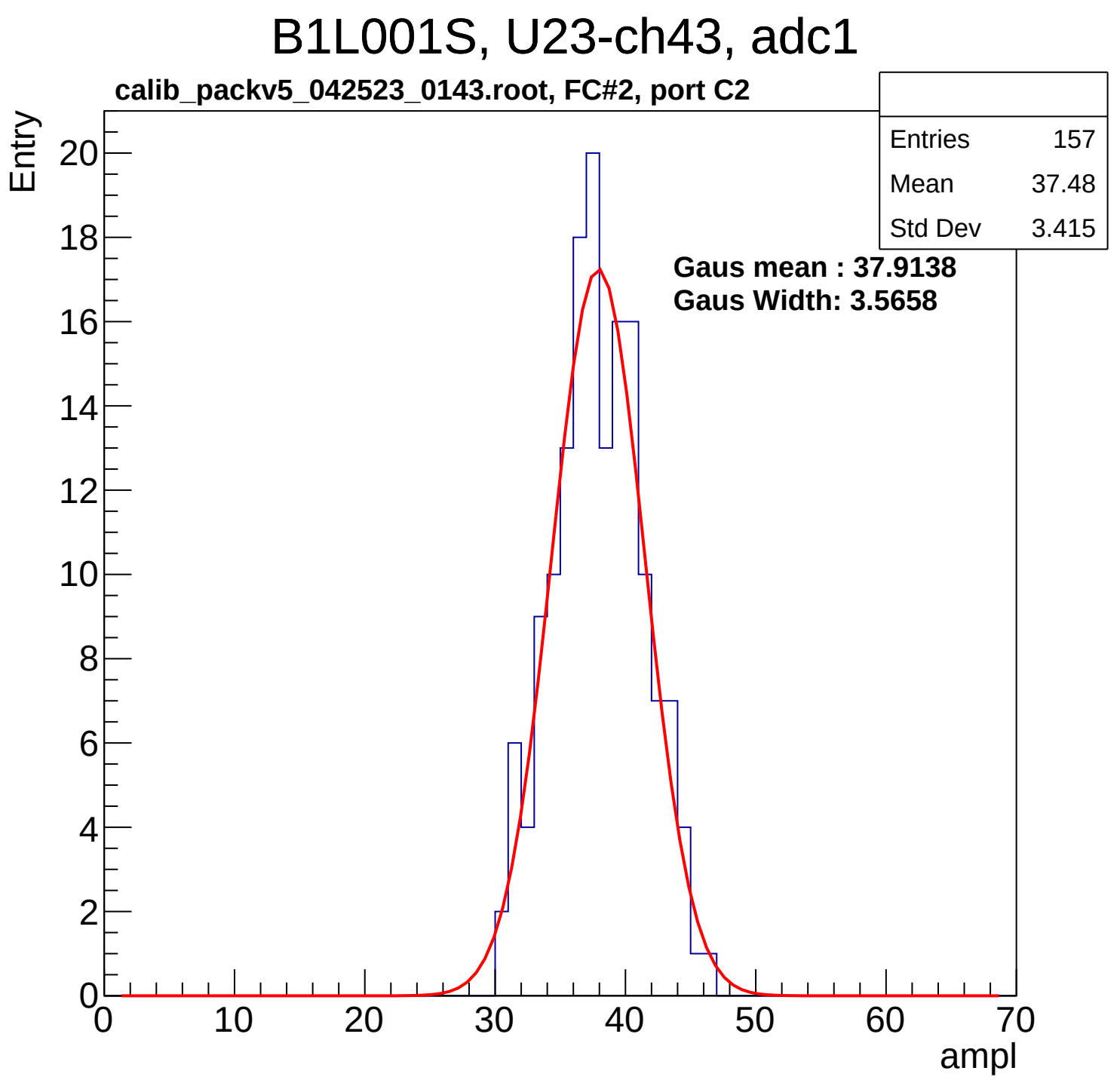
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch43, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 37.48
Std Dev 3.415
Gaus mean : 37.9138
Gaus Width: 3.5658
Entry
ampl
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4
6
8
10
12
14
16
18
20
0
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20
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60
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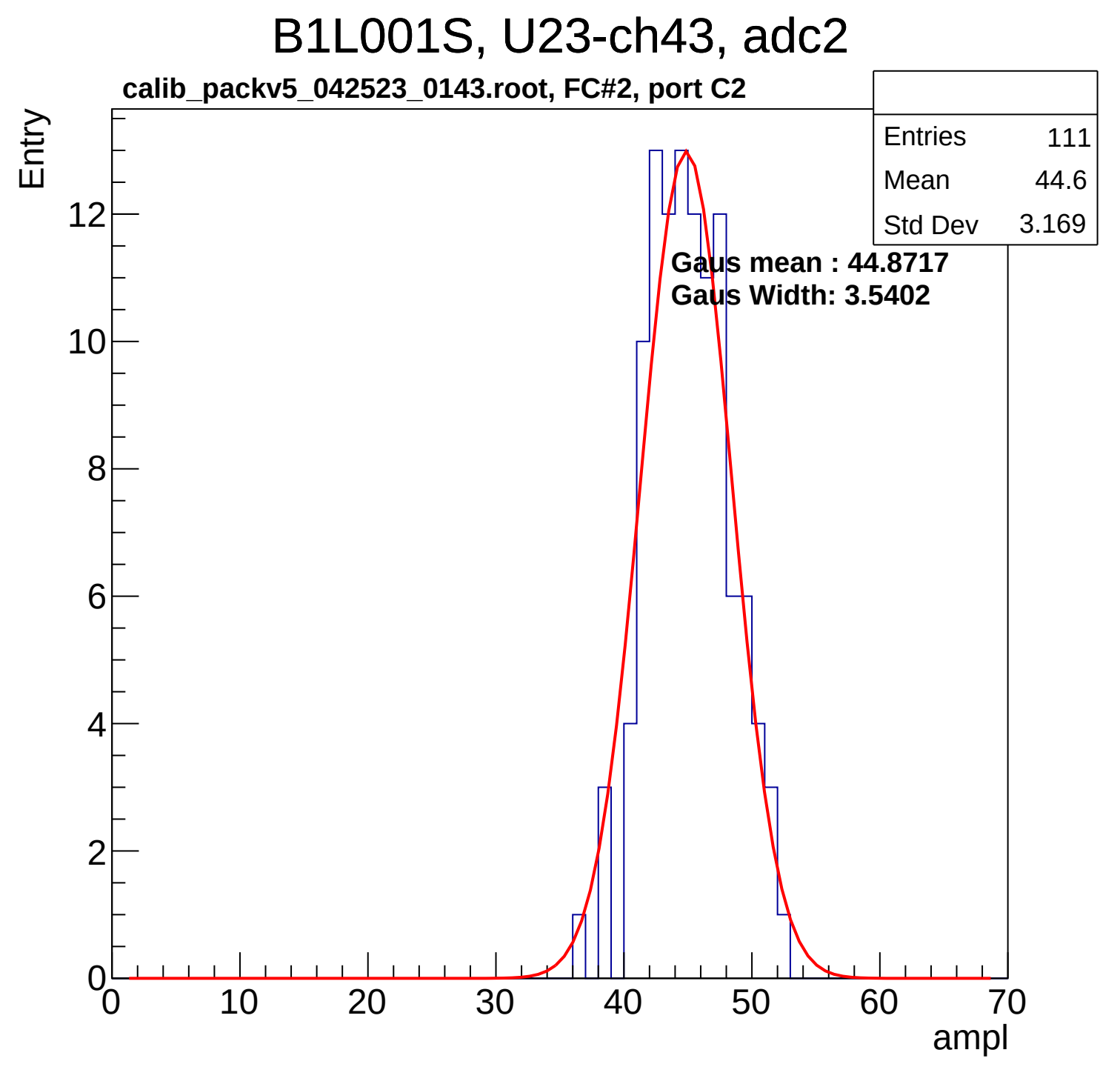
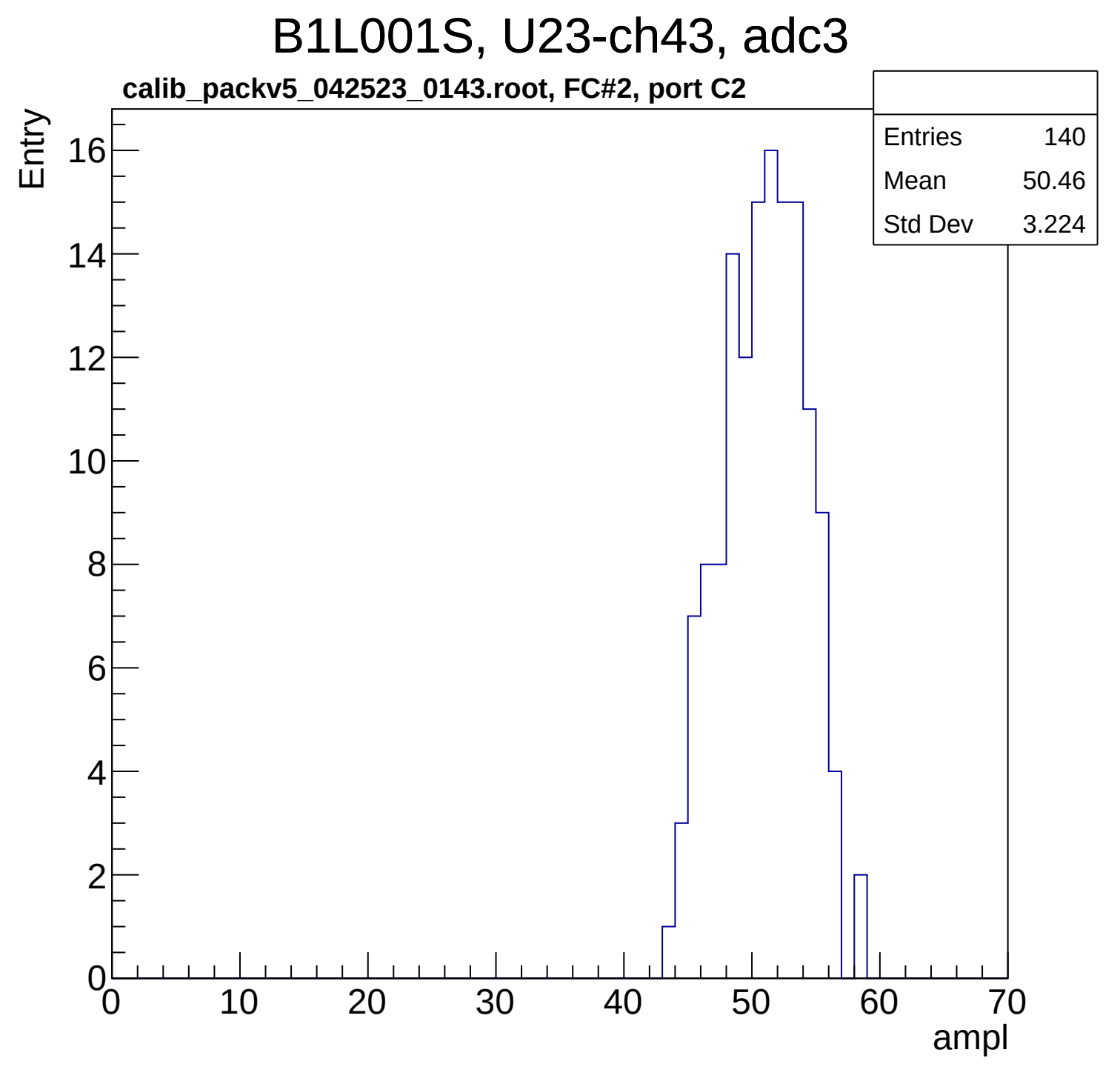

B1L001S, U23-ch43, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 44.6
Std Dev 3.169
Gaus mean : 44.8717
Gaus Width: 3.5402
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U23-ch43, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 50.46
Std Dev 3.224
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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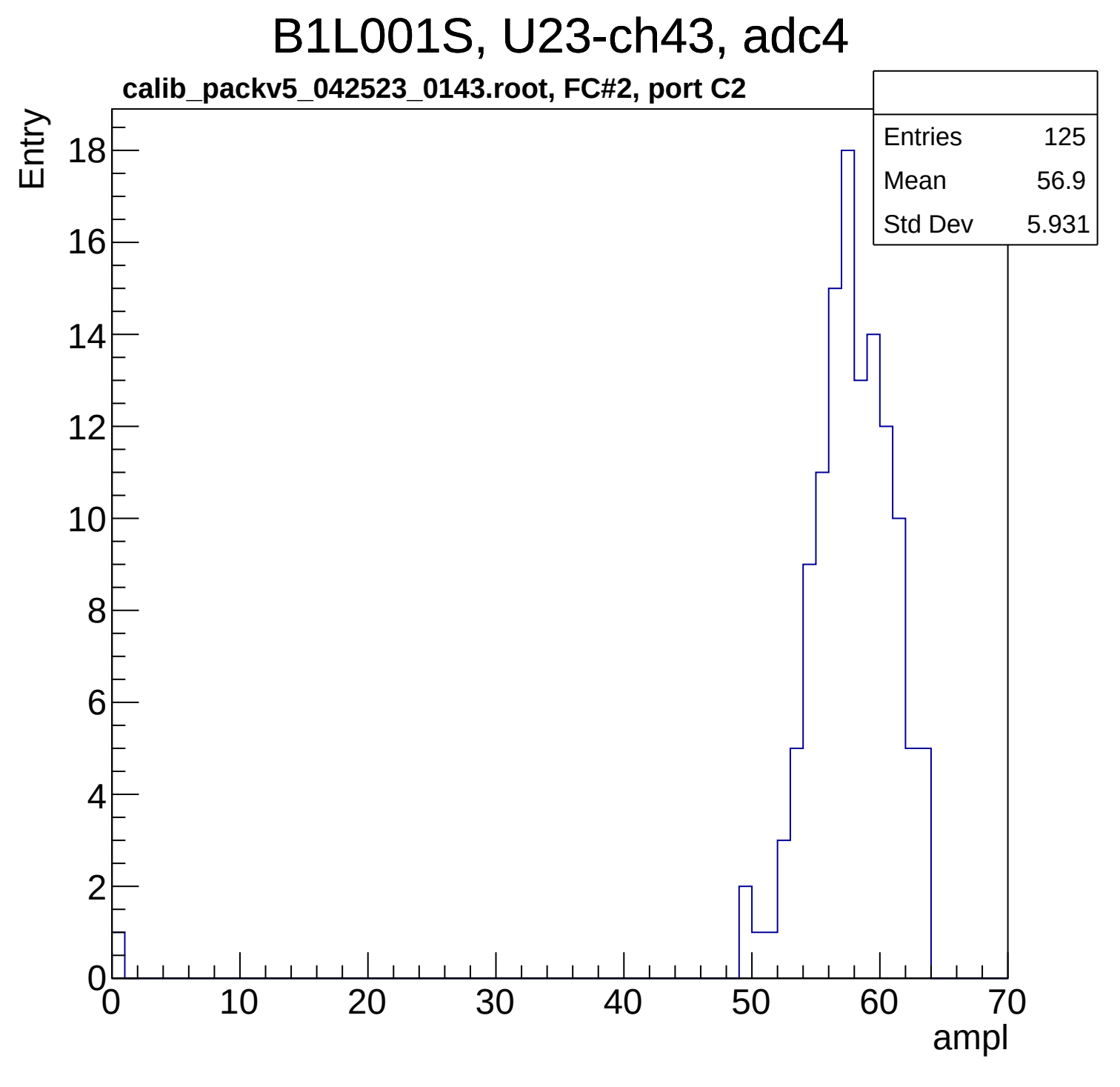
B1L001S, U23-ch43, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 56.9
Std Dev 5.931
Entry
ampl
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4
6
8
10
12
14
16
18
0
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40
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60
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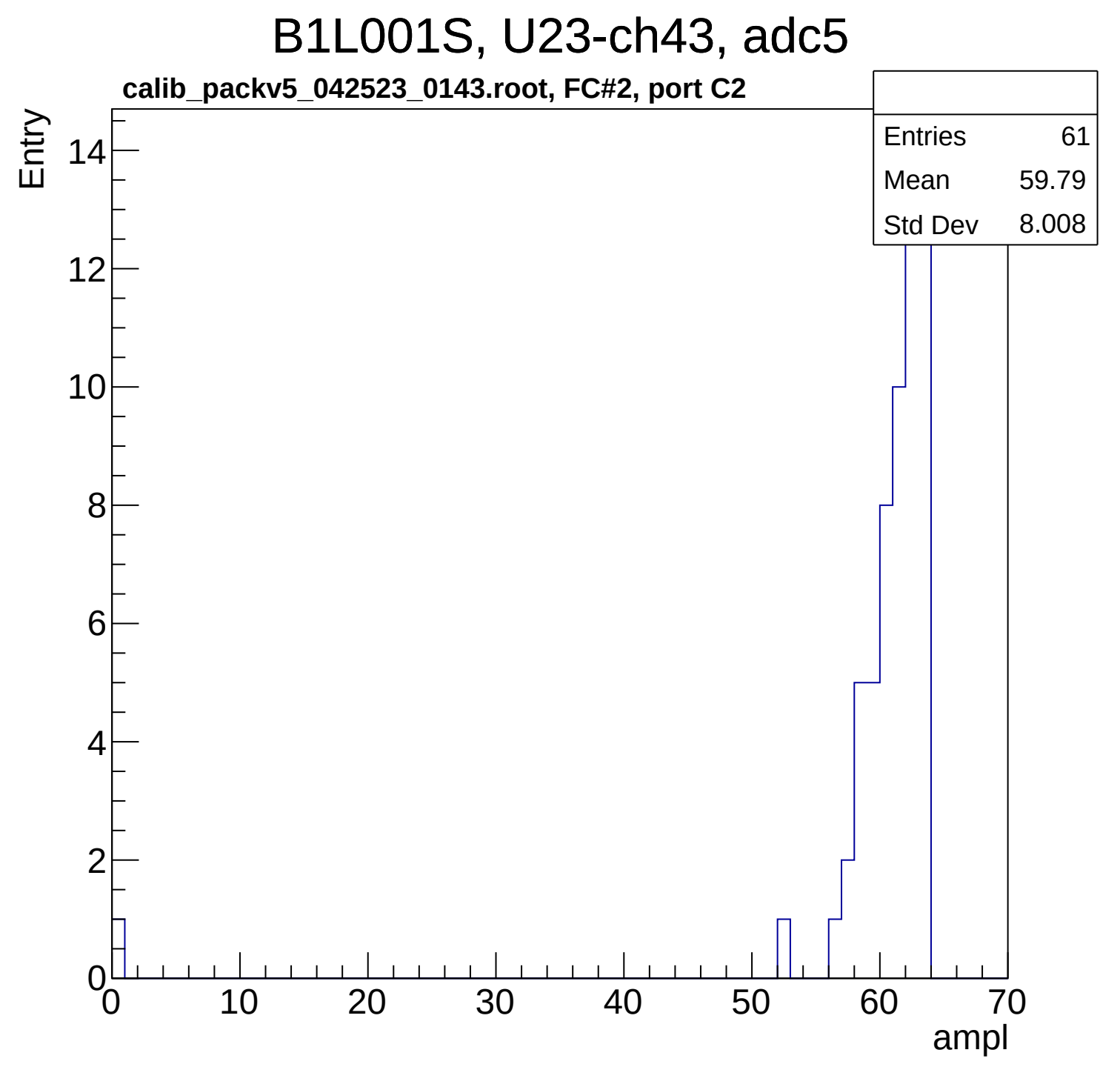
B1L001S, U23-ch43, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 59.79
Std Dev 8.008
Entry
ampl
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6
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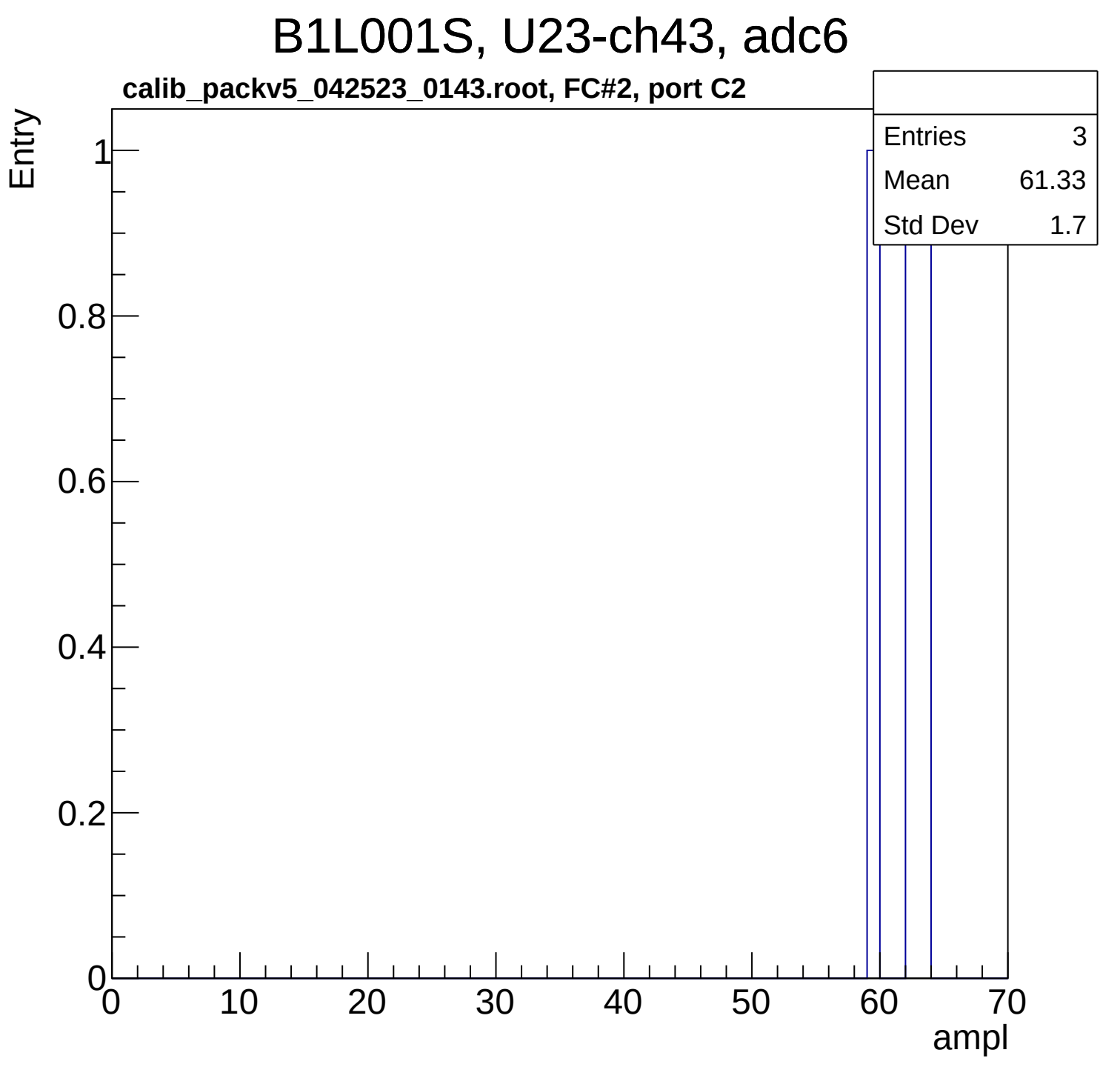
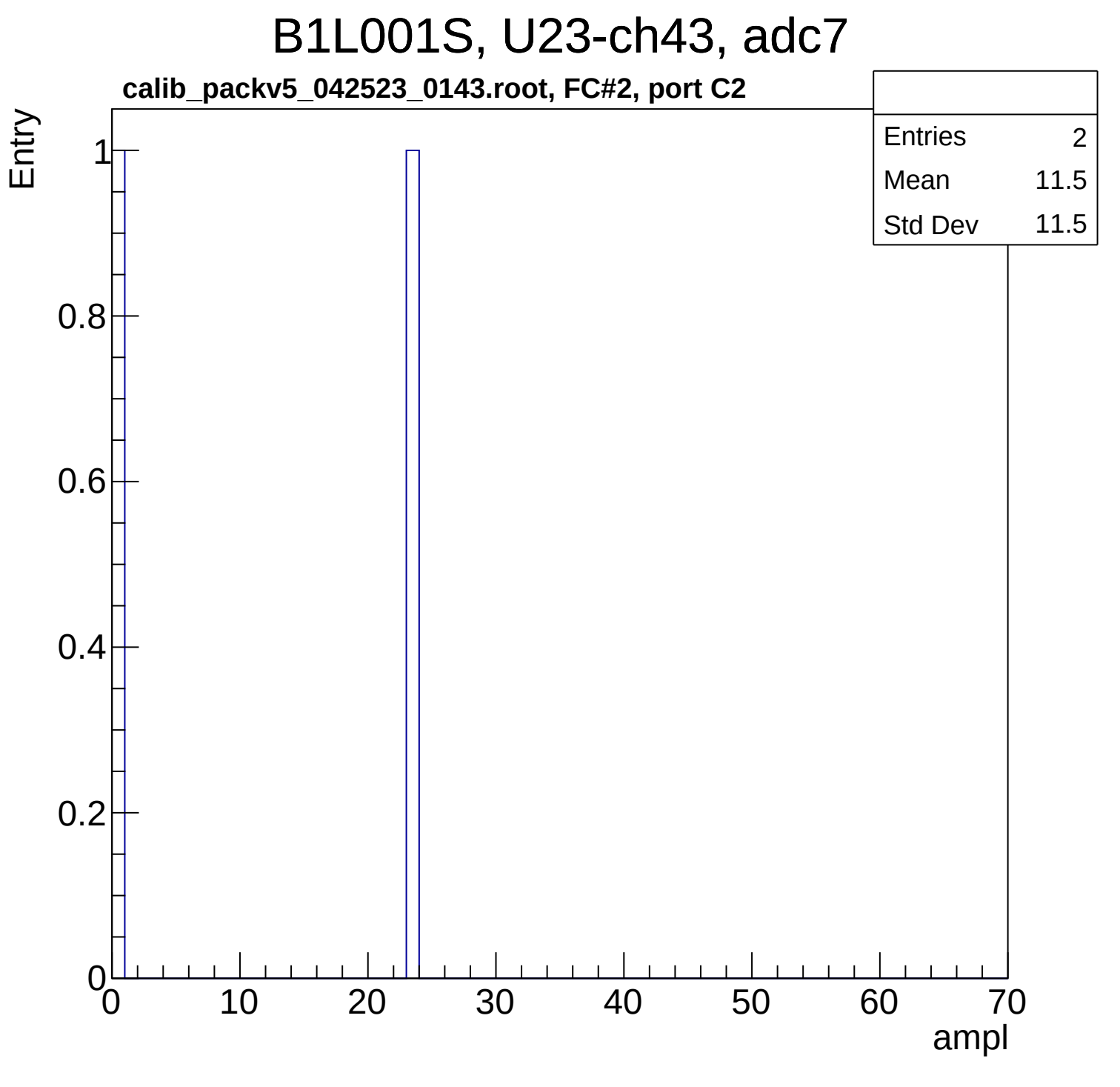
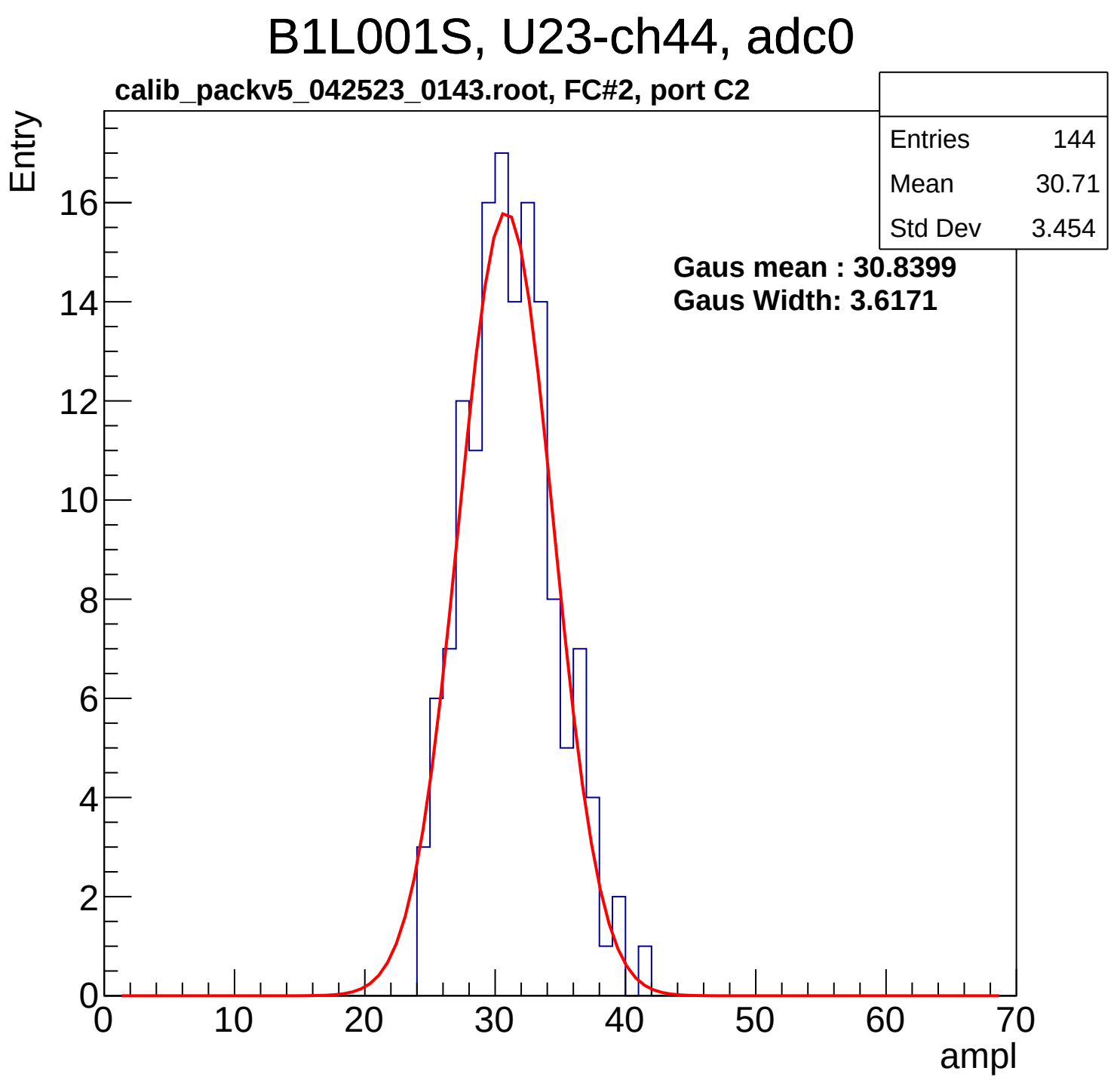
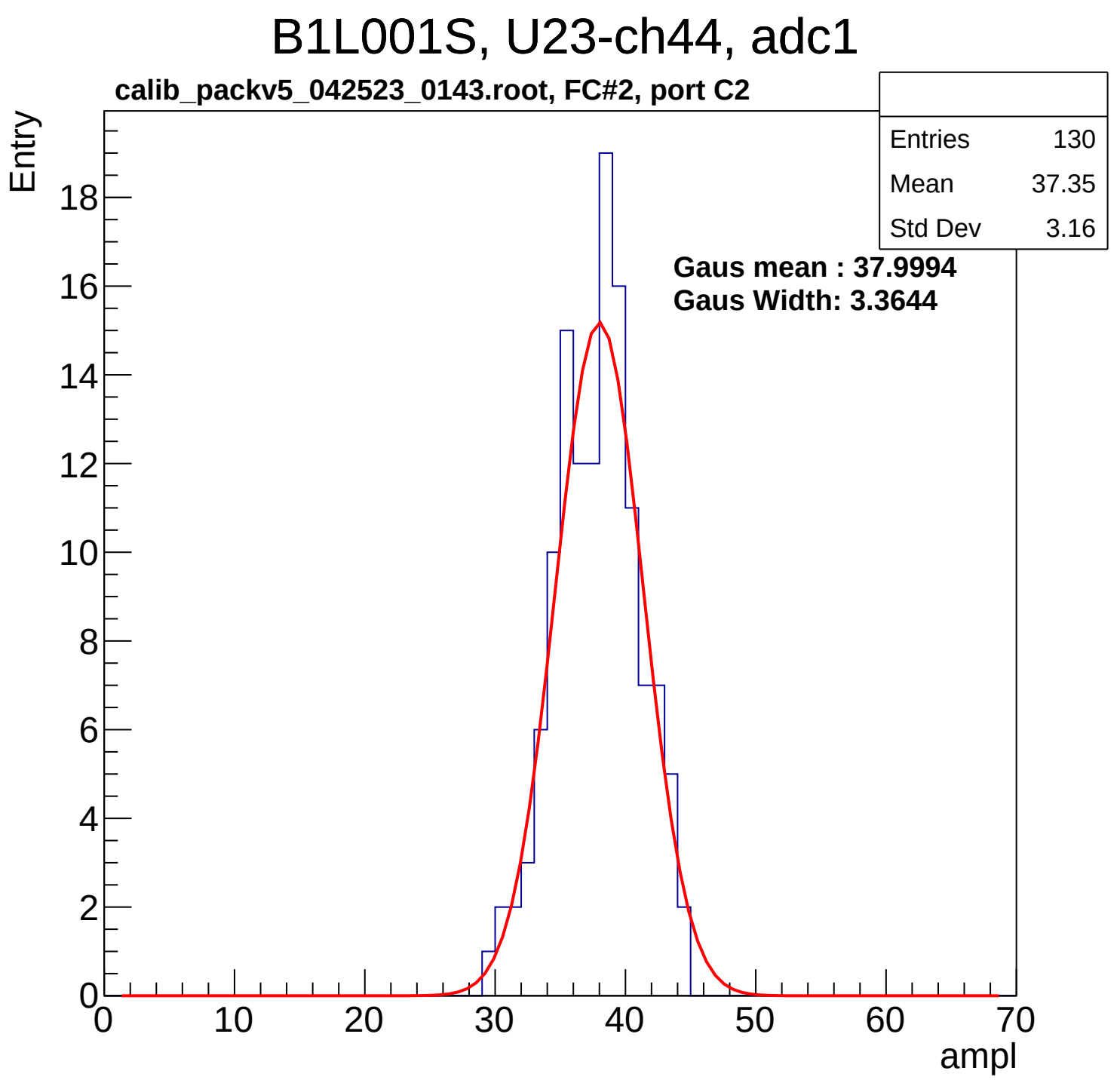

B1L001S, U23-ch43, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 61.33
Std Dev 1.7
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch43, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch44, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 30.71
Std Dev 3.454
Gaus mean : 30.8399
Gaus Width: 3.6171
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch44, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 37.35
Std Dev 3.16
Gaus mean : 37.9994
Gaus Width: 3.3644
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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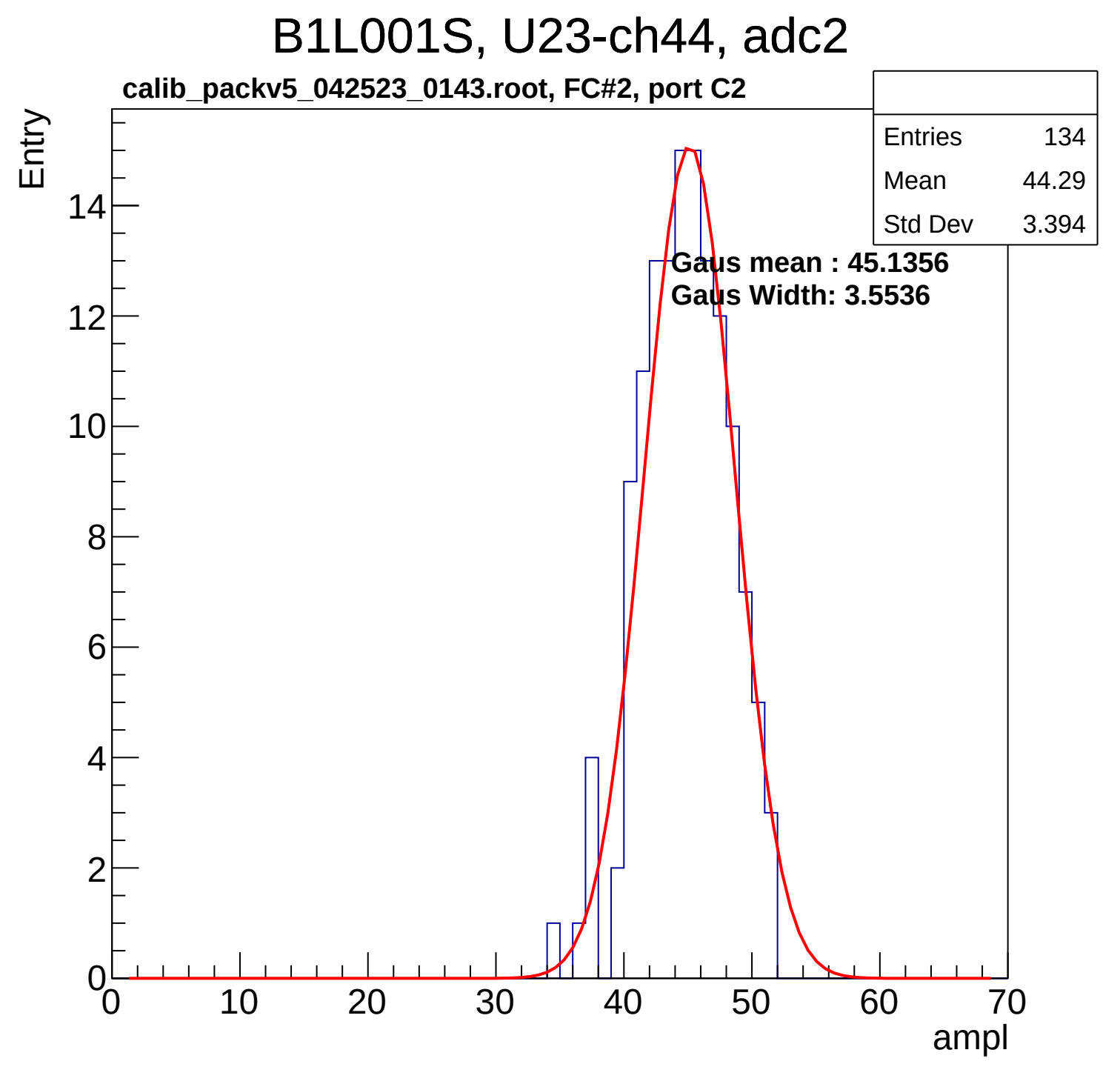
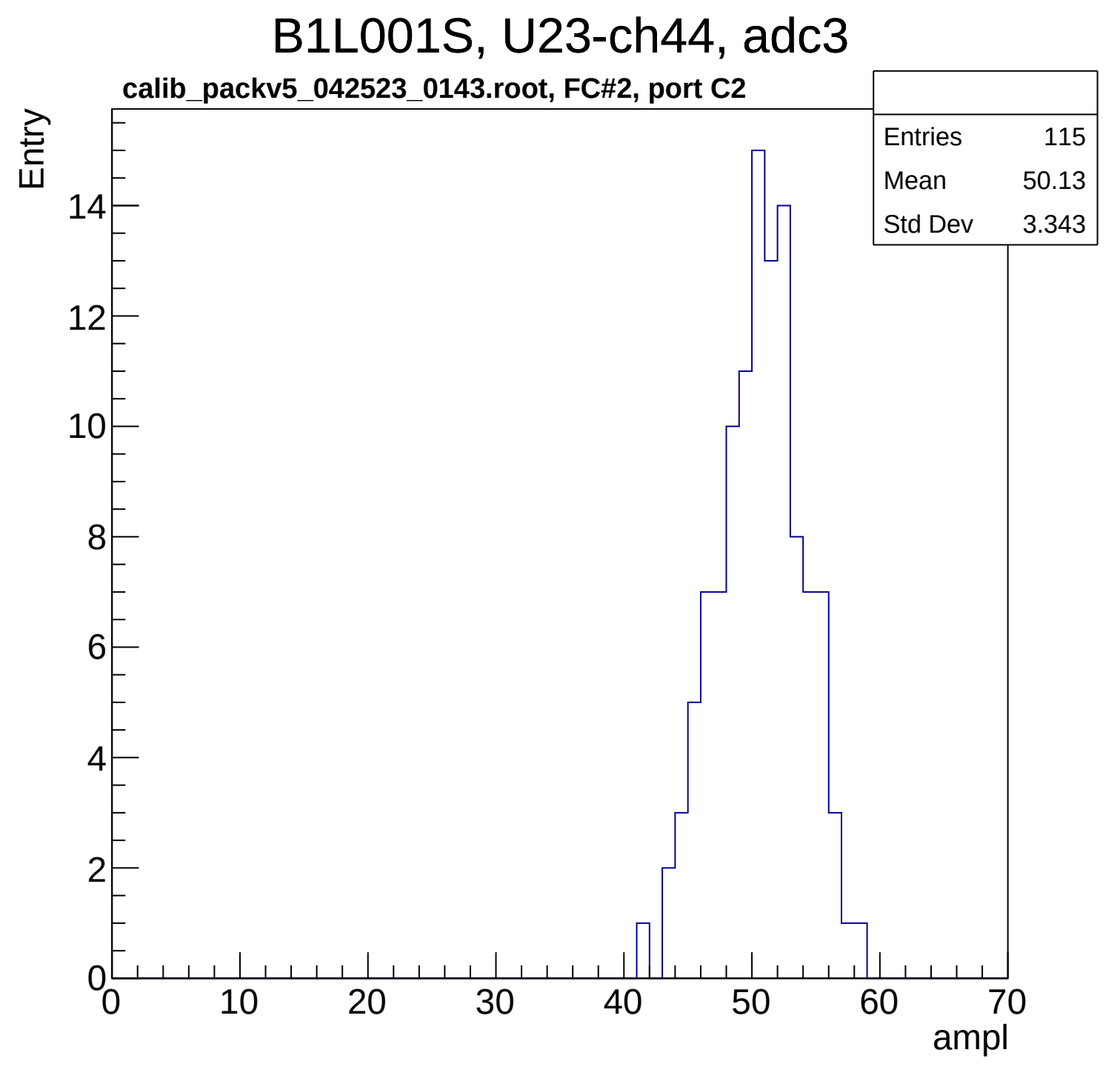
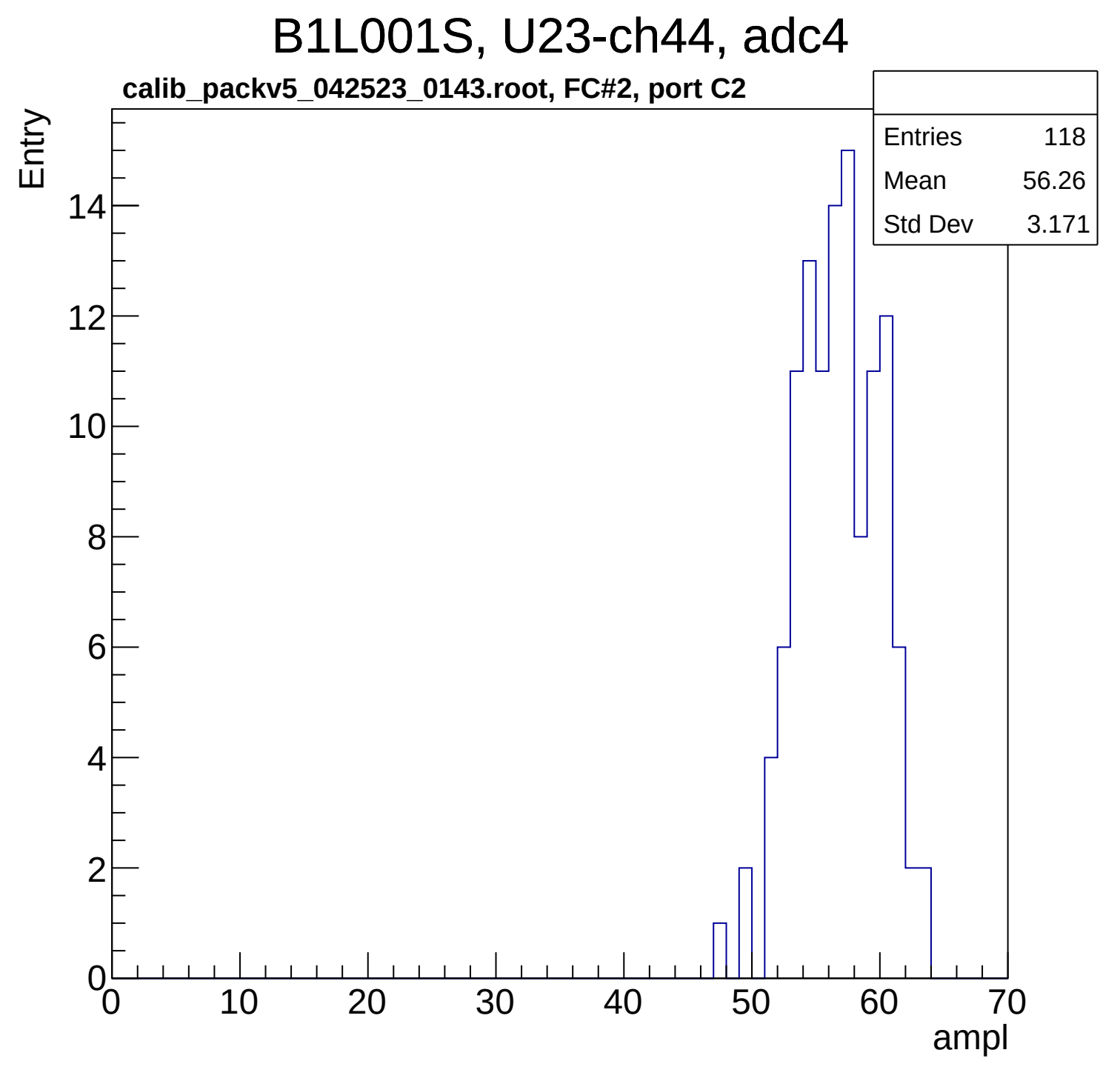

B1L001S, U23-ch44, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 44.29
Std Dev 3.394
Gaus mean : 45.1356
Gaus Width: 3.5536
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch44, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 50.13
Std Dev 3.343
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch44, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 56.26
Std Dev 3.171
Entry
ampl
0
2
4
6
8
10
12
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0
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20
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60
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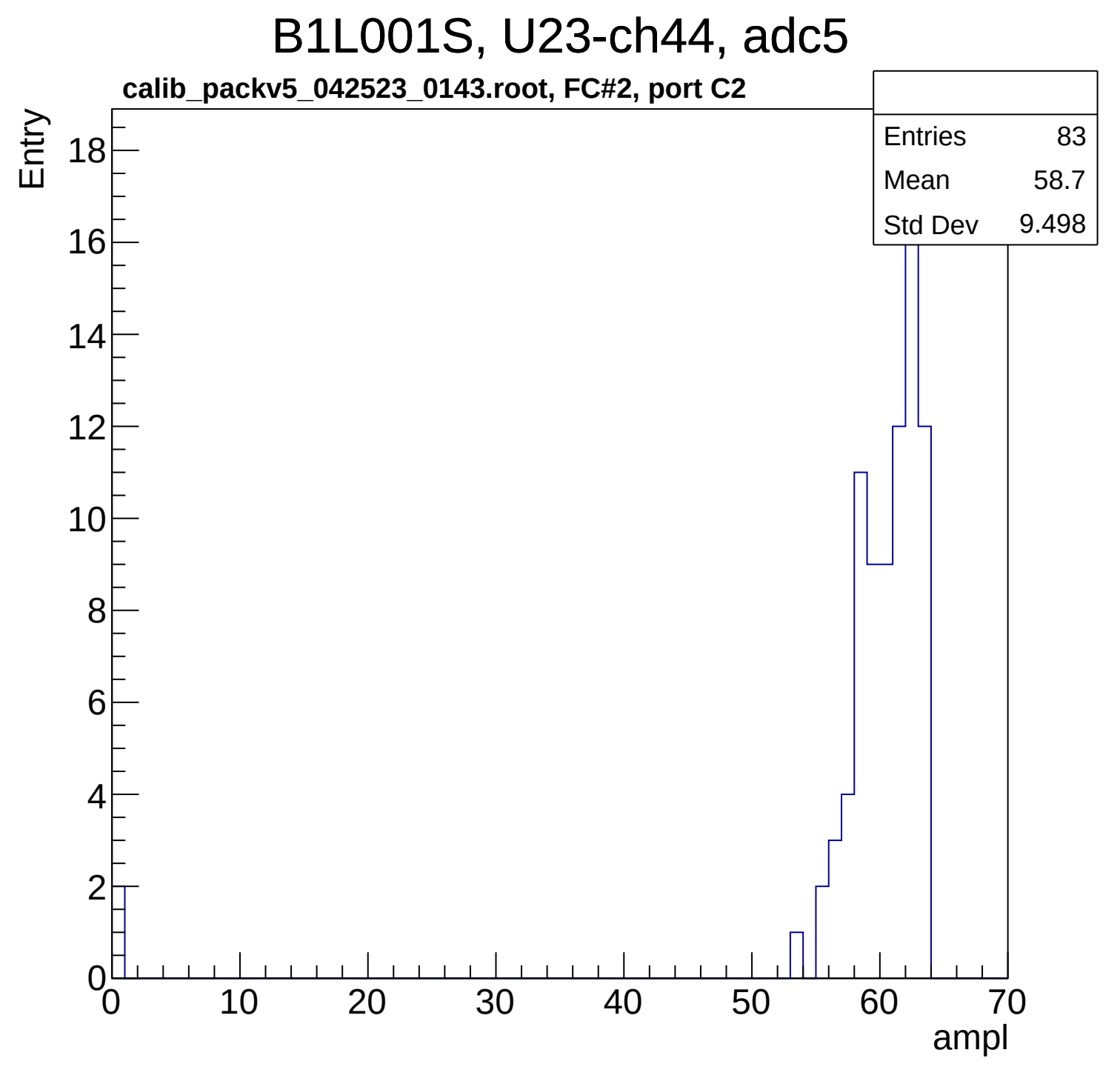
B1L001S, U23-ch44, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 58.7
Std Dev 9.498
Entry
ampl
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4
6
8
10
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14
16
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0
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20
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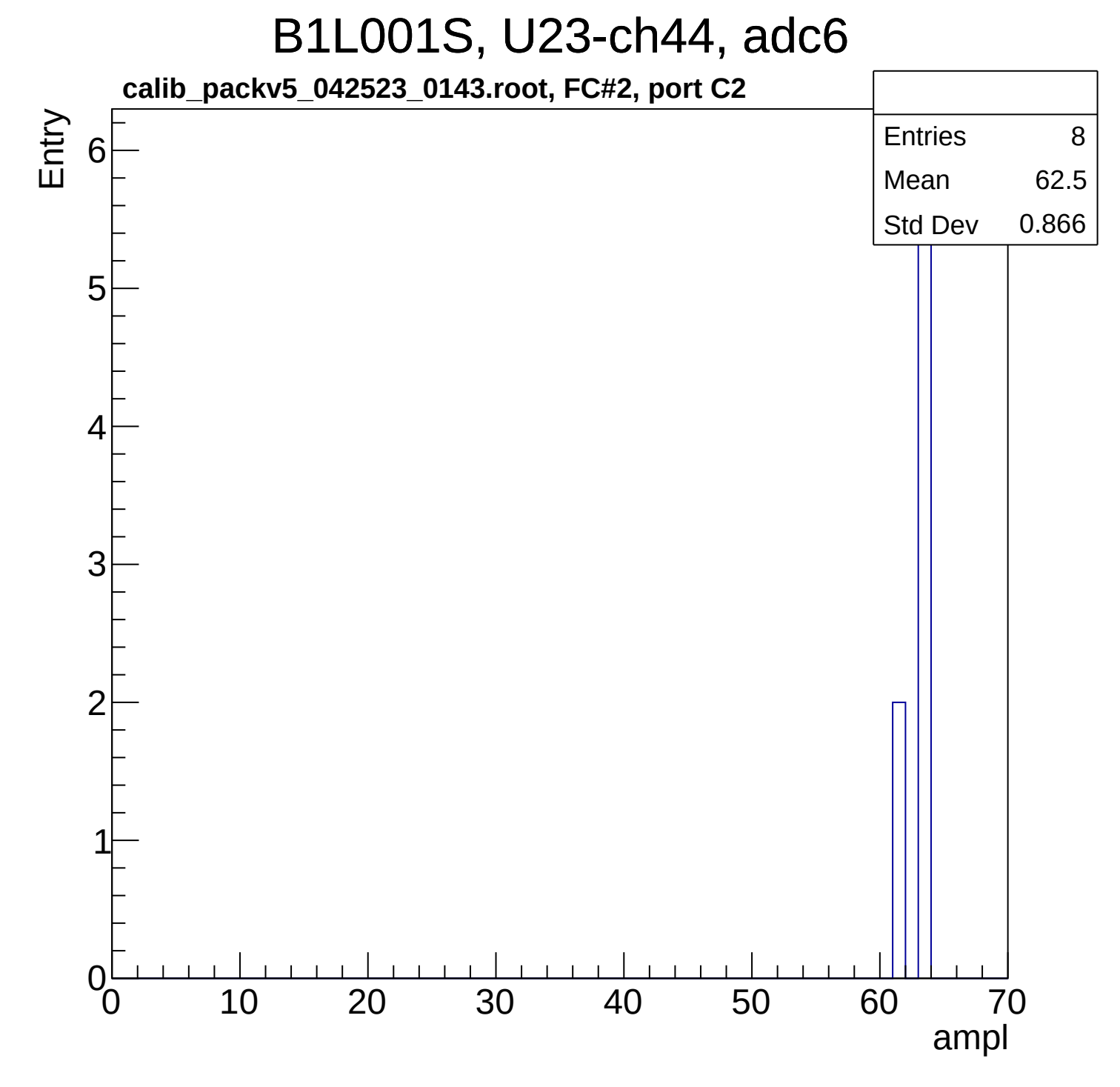
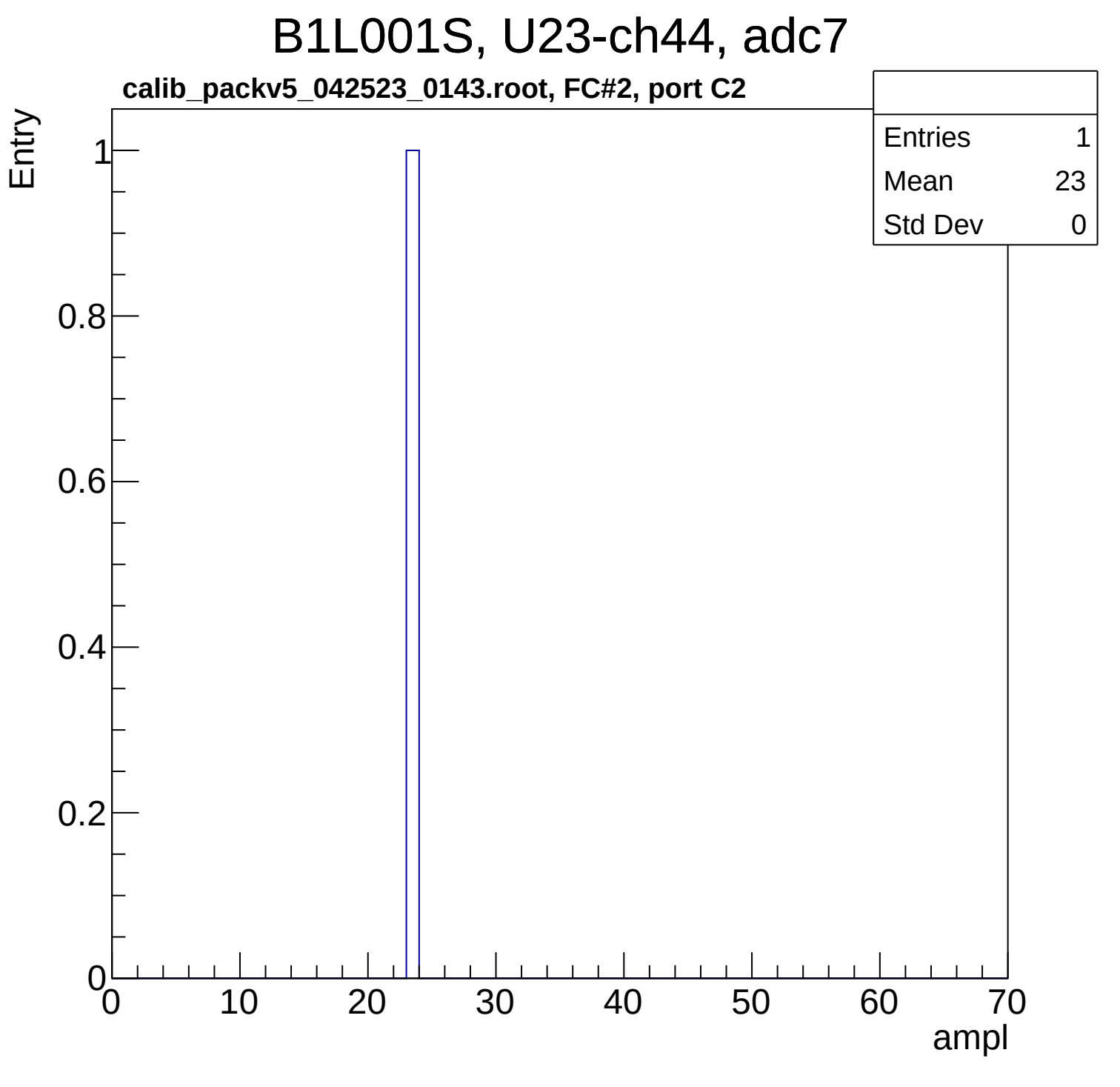

B1L001S, U23-ch44, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 62.5
Std Dev 0.866
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L001S, U23-ch44, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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0.4
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10
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30
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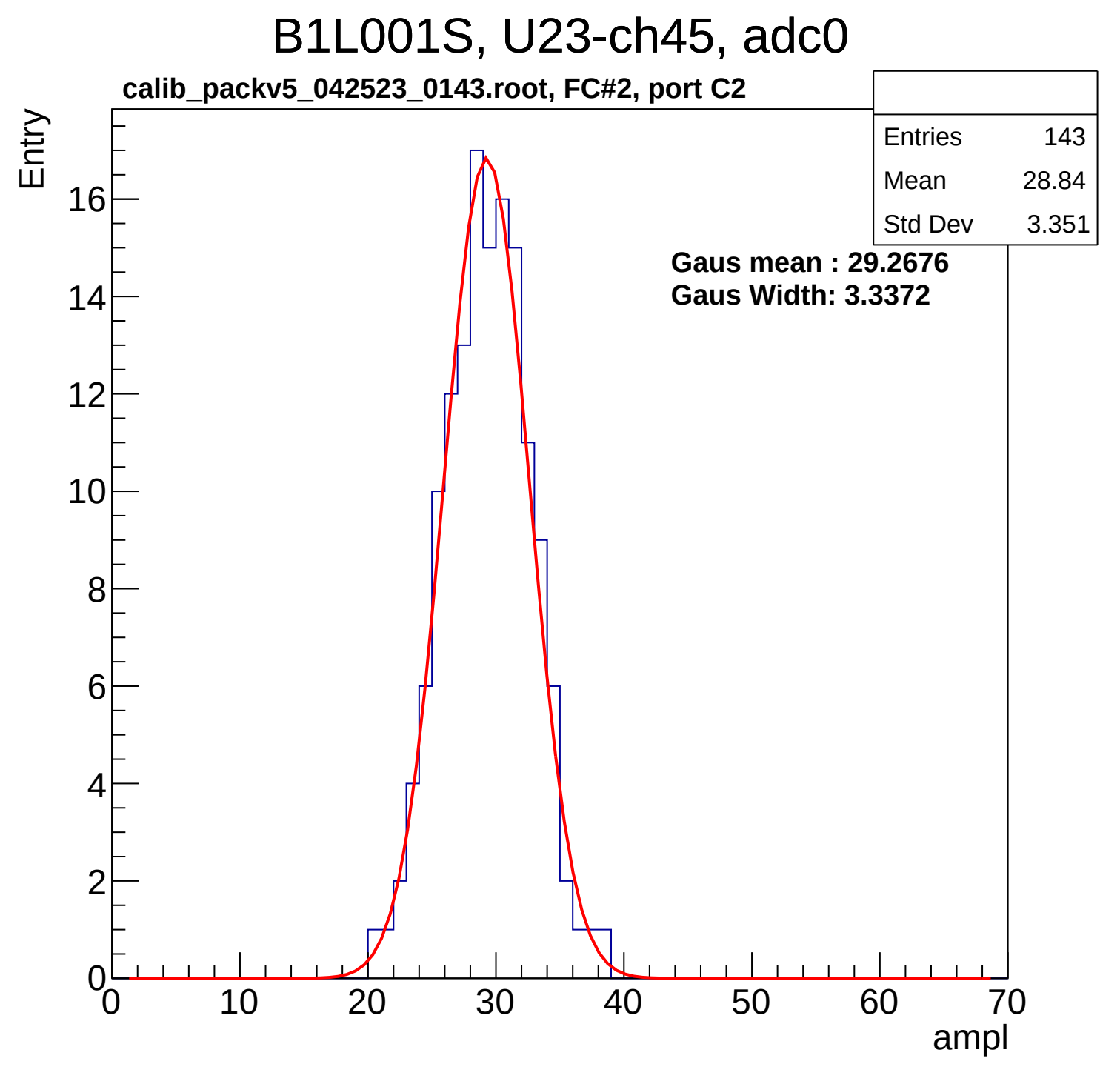
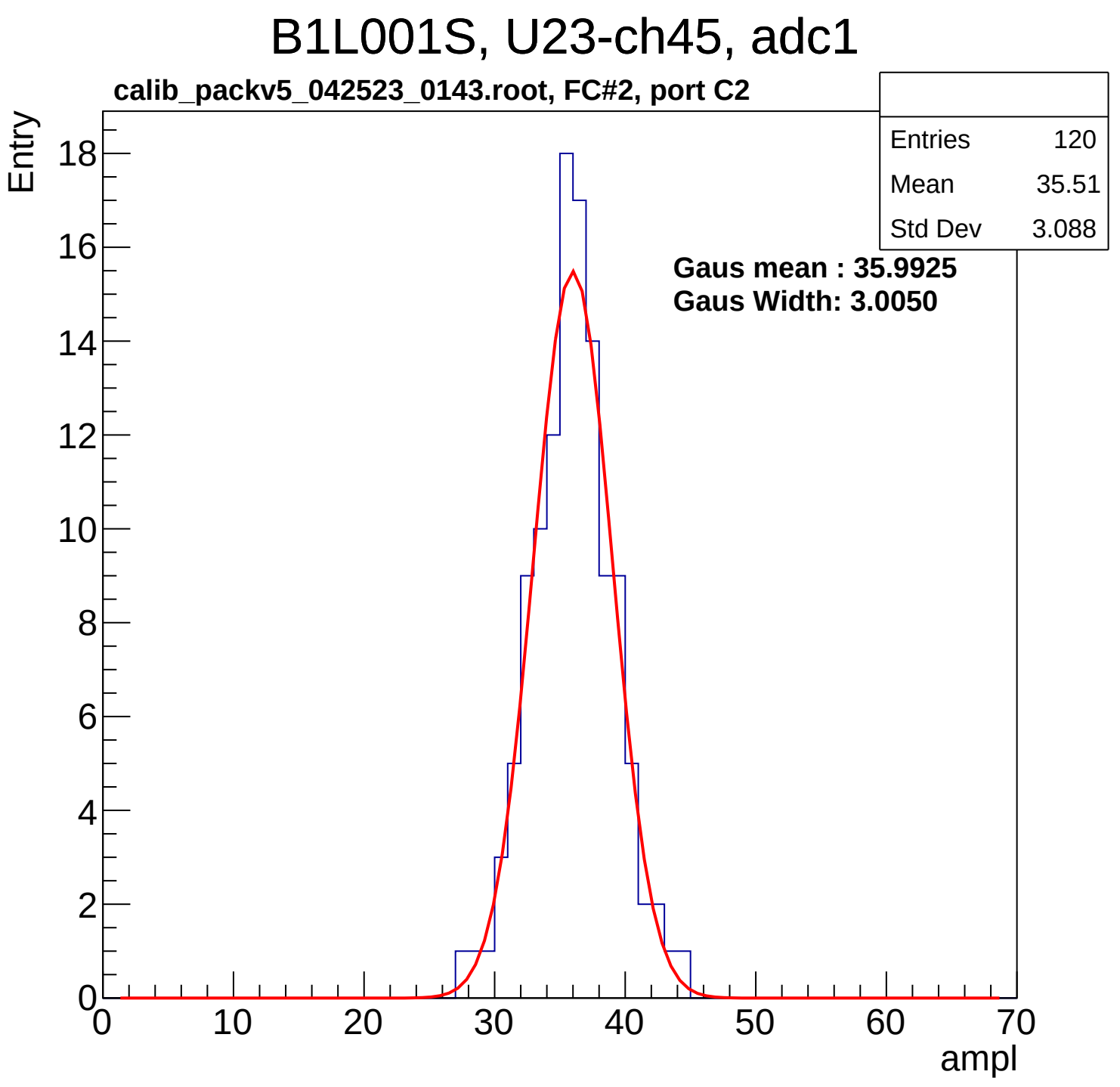
50
60
70
ampl


B1L001S, U23-ch45, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 28.84
Std Dev 3.351
Gaus mean : 29.2676
Gaus Width: 3.3372
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch45, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 35.51
Std Dev 3.088
Gaus mean : 35.9925
Gaus Width: 3.0050
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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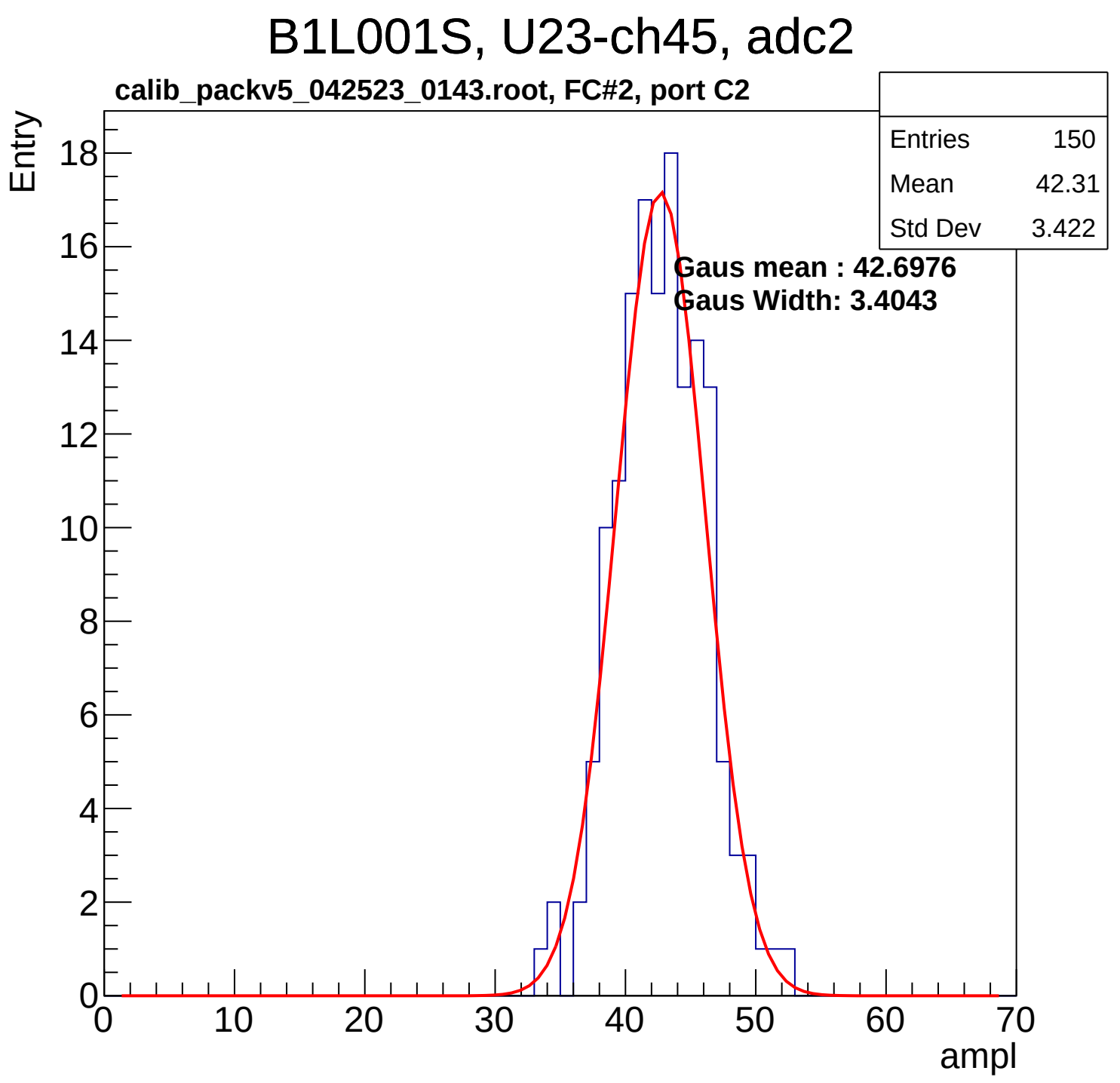
B1L001S, U23-ch45, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 42.31
Std Dev 3.422
Gaus mean : 42.6976
Gaus Width: 3.4043
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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60
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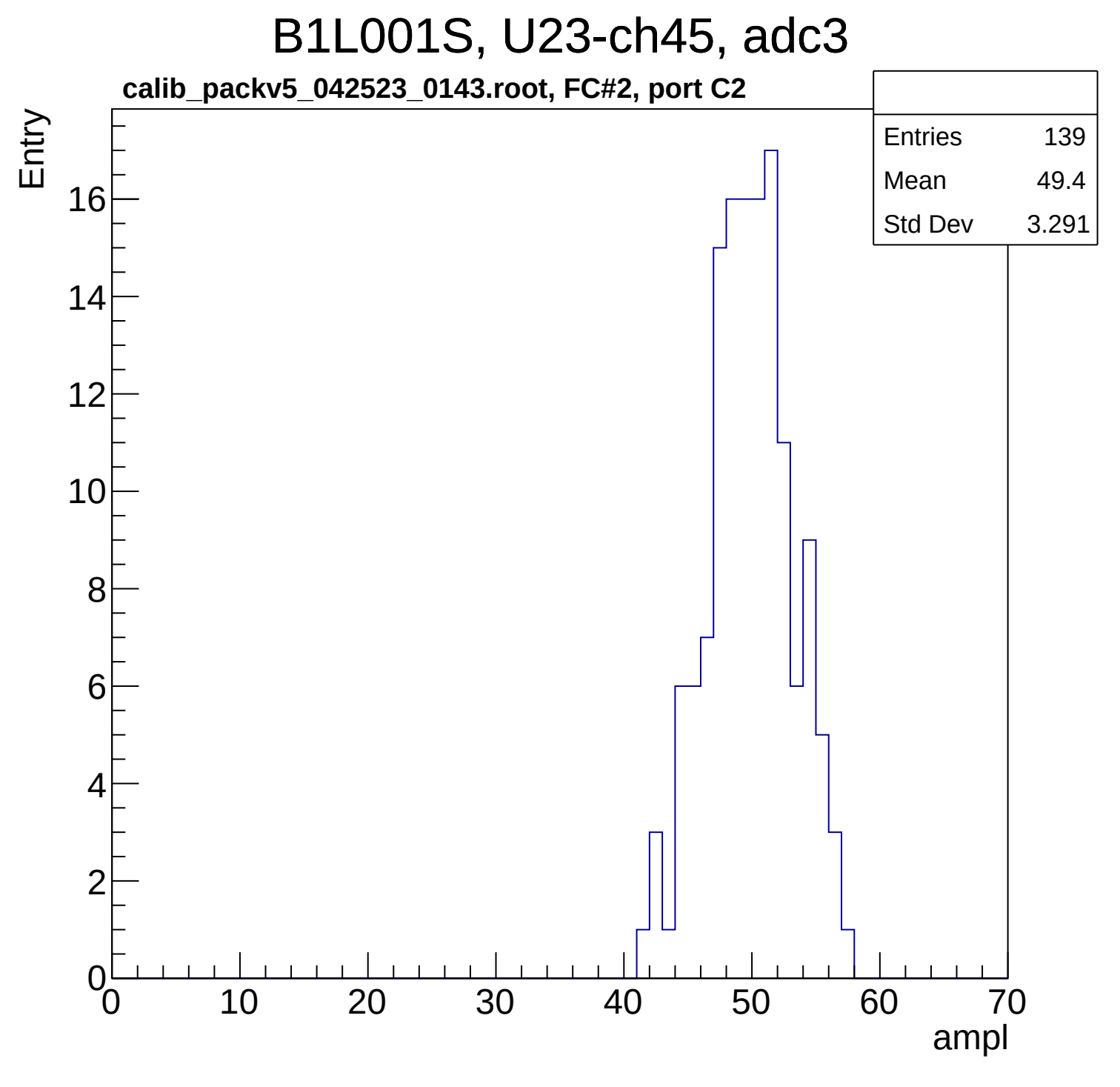
B1L001S, U23-ch45, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 49.4
Std Dev 3.291
Entry
ampl
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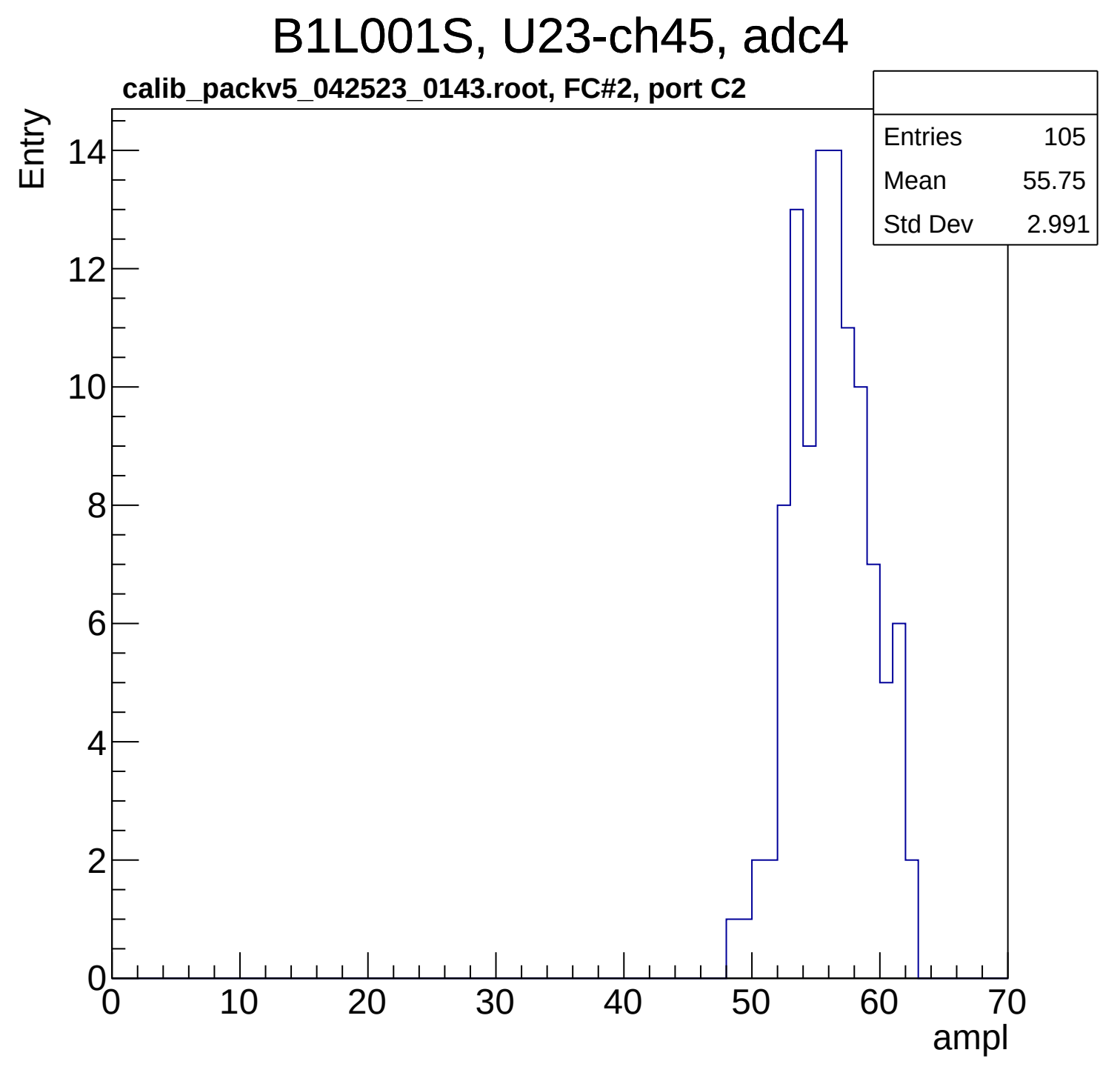
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch45, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 55.75
Std Dev 2.991
Entry
ampl
0
2
4
6
8
10
12
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0
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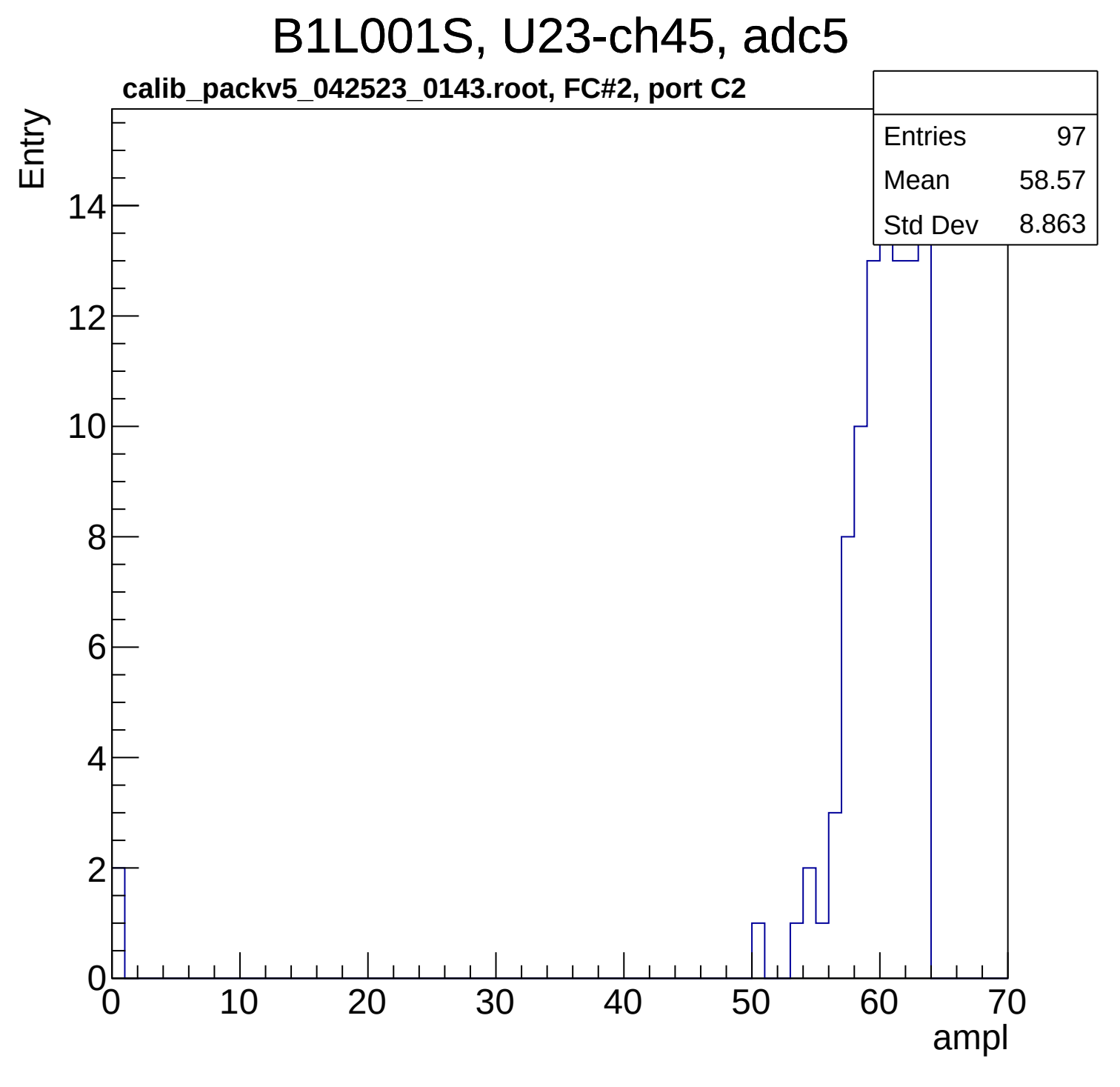
30
40
50
60
70


B1L001S, U23-ch45, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 58.57
Std Dev 8.863
Entry
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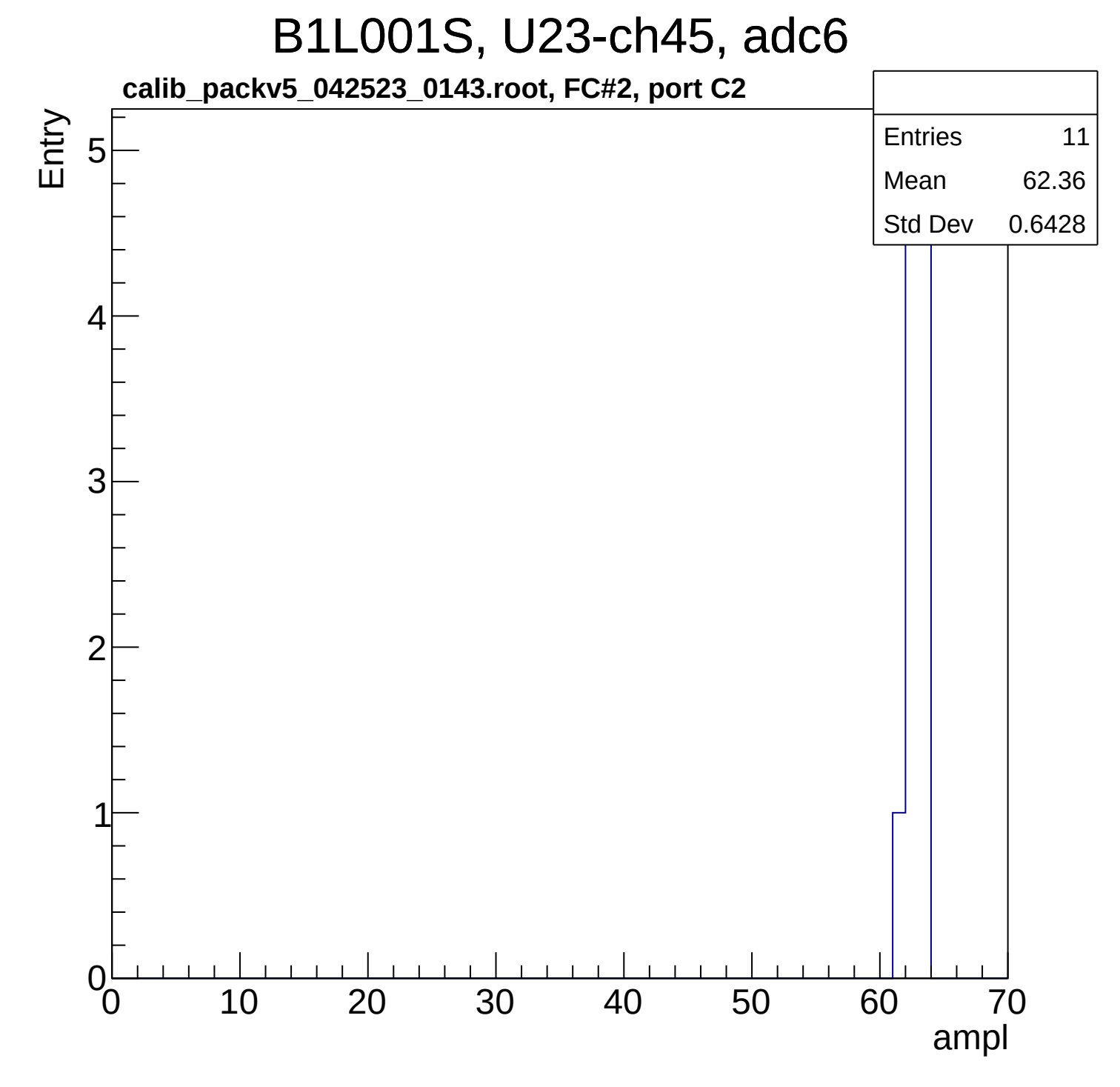
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch45, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 62.36
Std Dev 0.6428
Entry
ampl
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1
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3
4
5
0
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20
30
40
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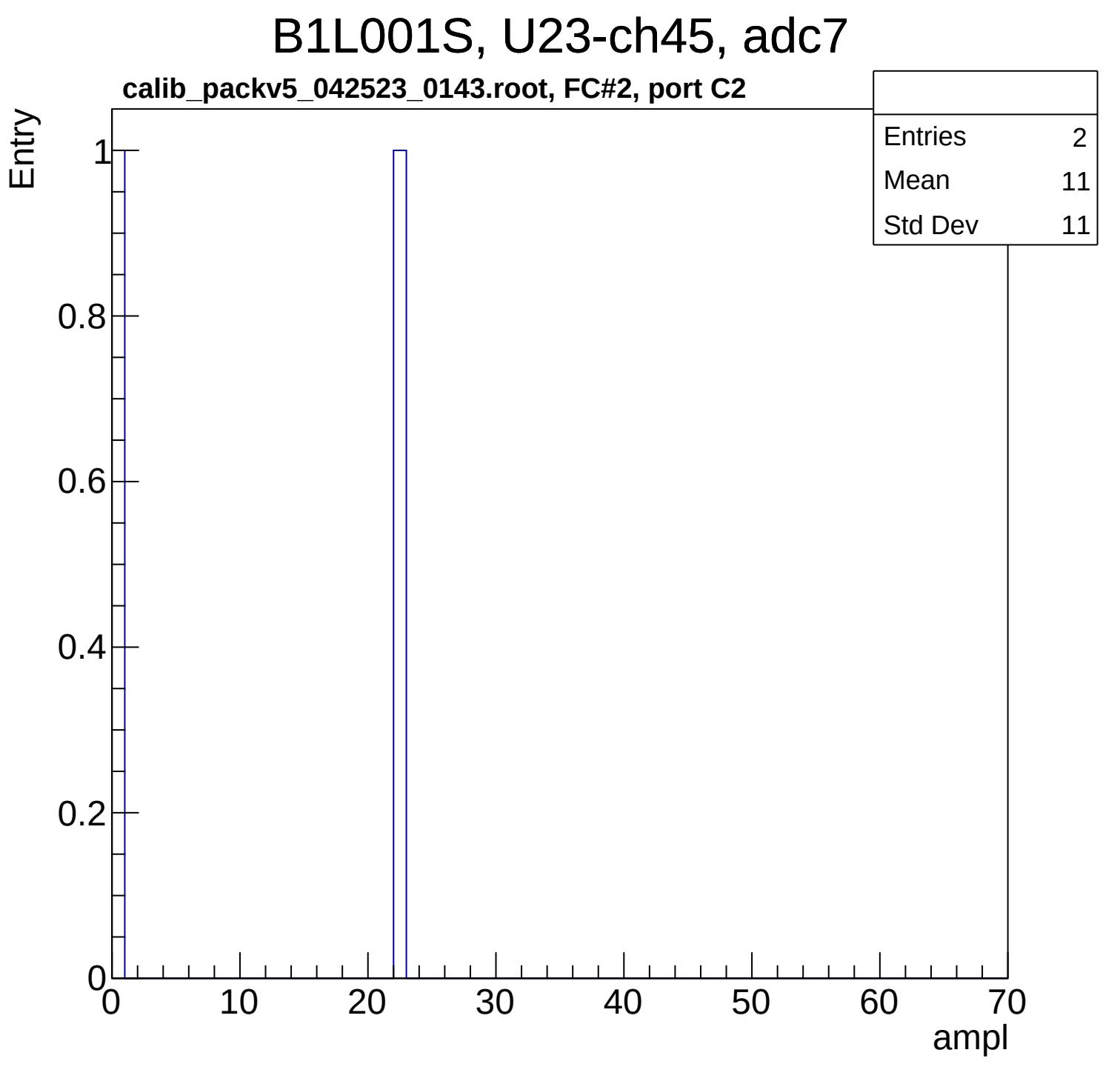
60
70


B1L001S, U23-ch45, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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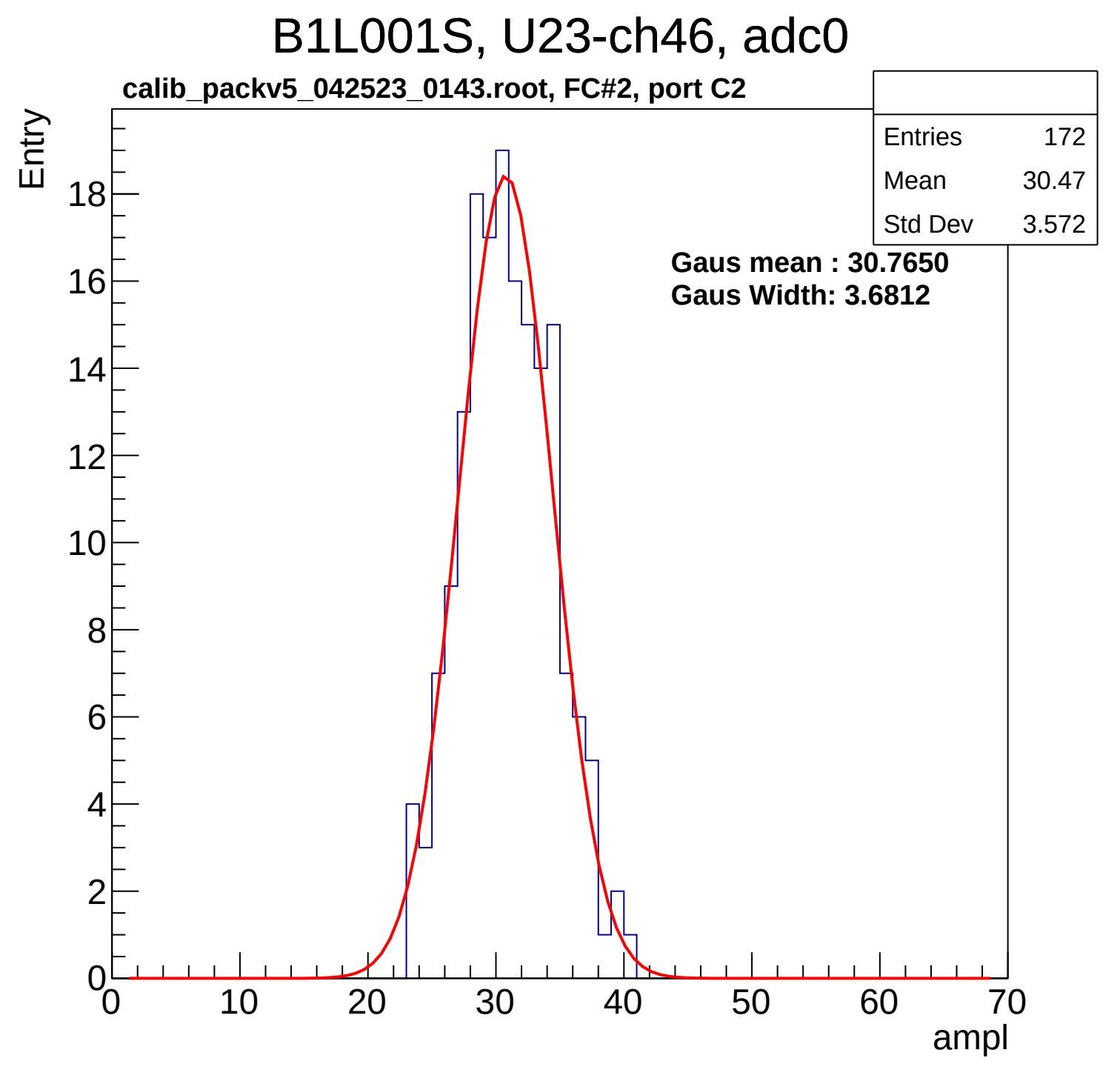
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch46, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 172
Mean 30.47
Std Dev 3.572
Gaus mean : 30.7650
Gaus Width: 3.6812
Entry
ampl
0
2
4
6
8
10
12
14
16
18
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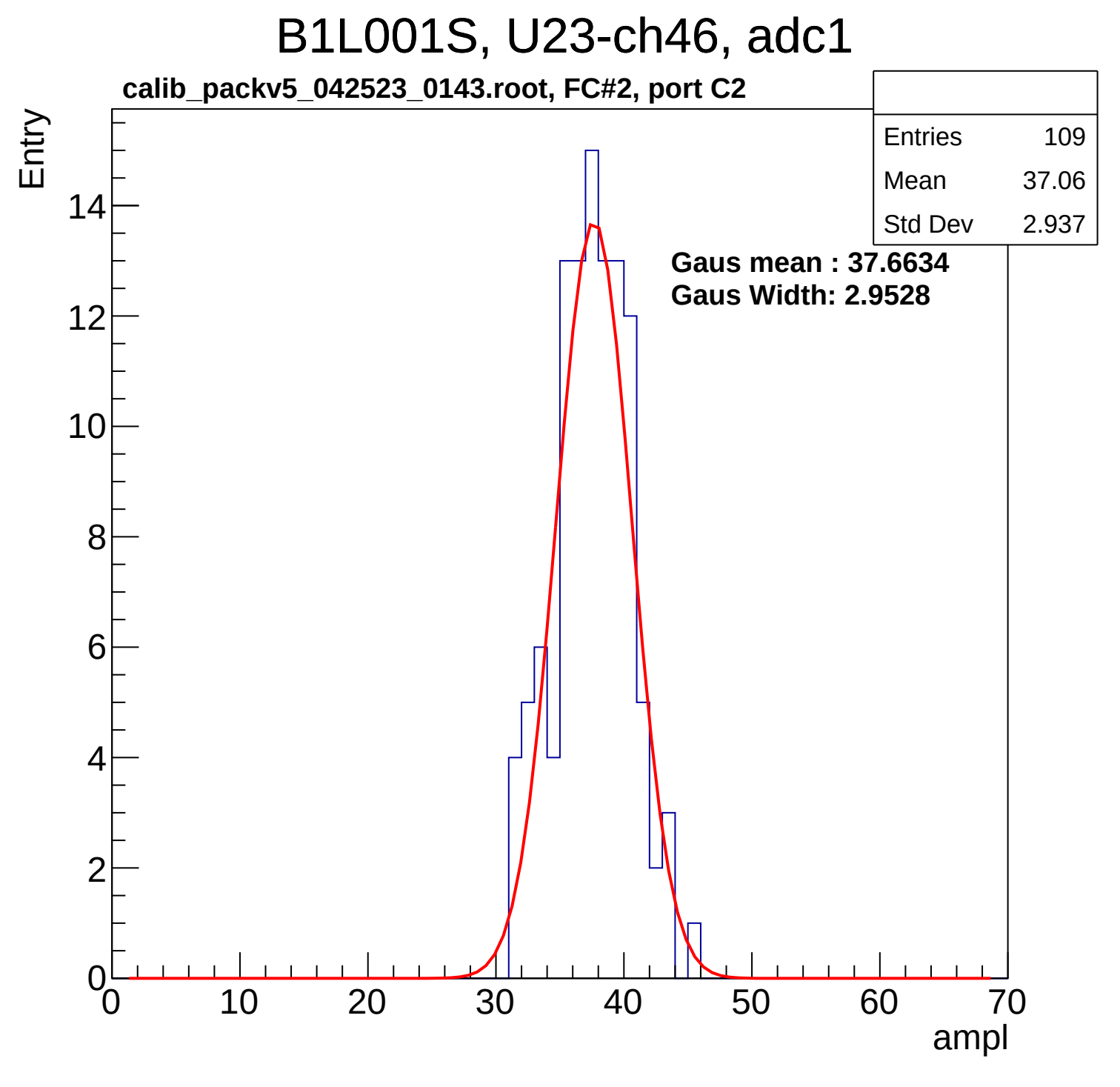
B1L001S, U23-ch46, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 37.06
Std Dev 2.937
Gaus mean : 37.6634
Gaus Width: 2.9528
Entry
ampl
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4
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8
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0
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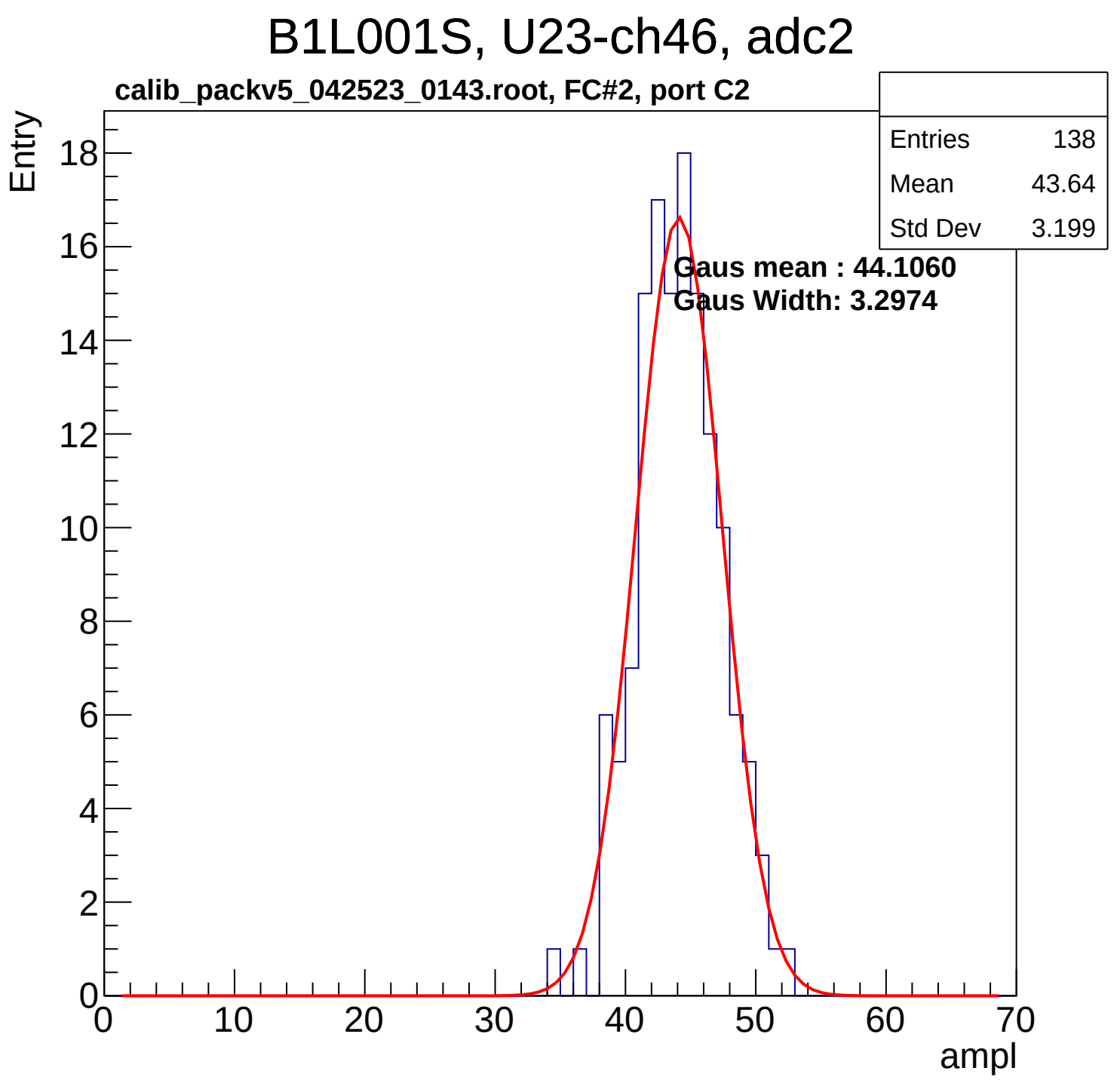
B1L001S, U23-ch46, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.64
Std Dev 3.199
Gaus mean : 44.1060
Gaus Width: 3.2974
Entry
ampl
0
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4
6
8
10
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0
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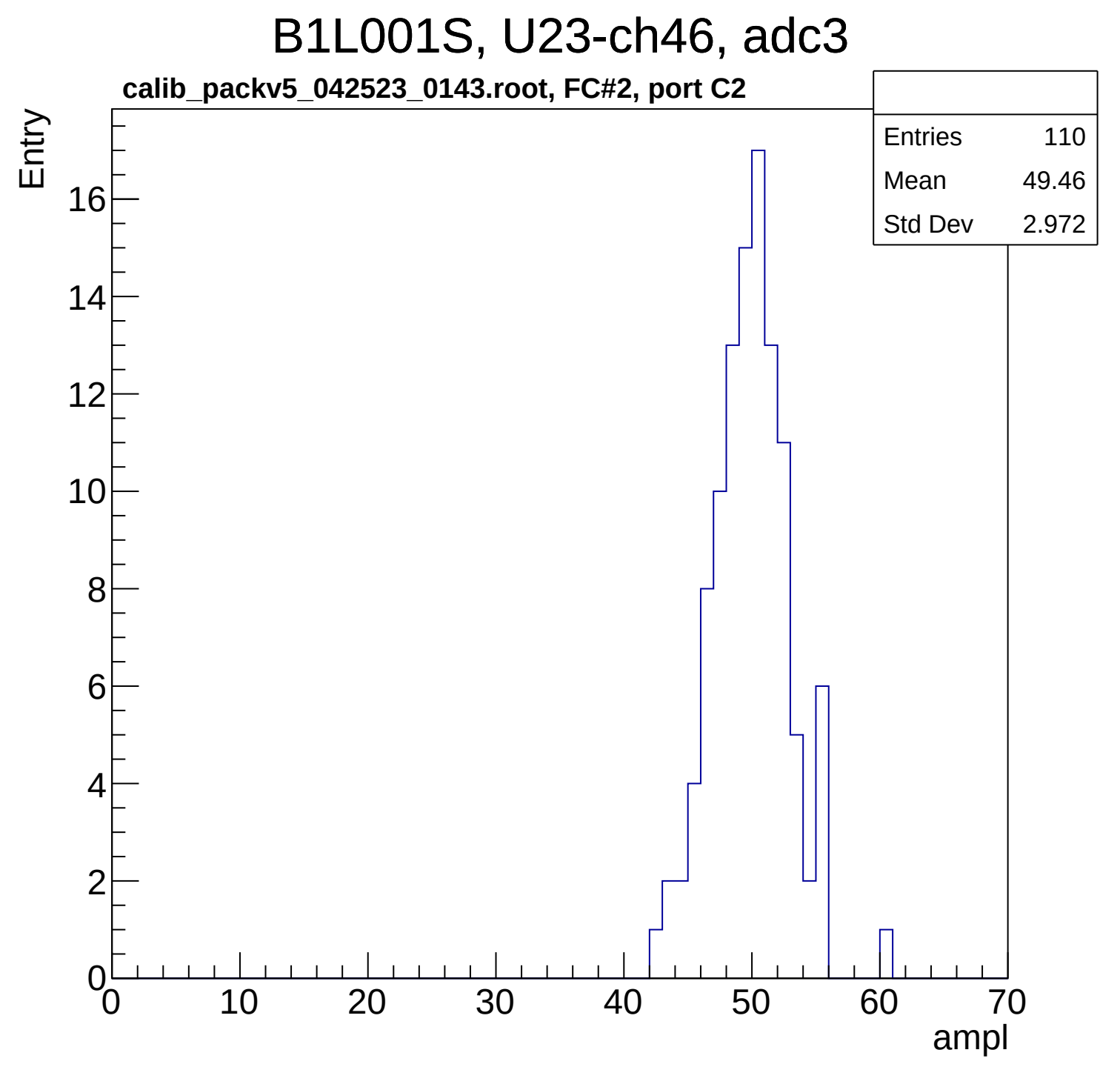
B1L001S, U23-ch46, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.46
Std Dev 2.972
Entry
ampl
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2
4
6
8
10
12
14
16
0
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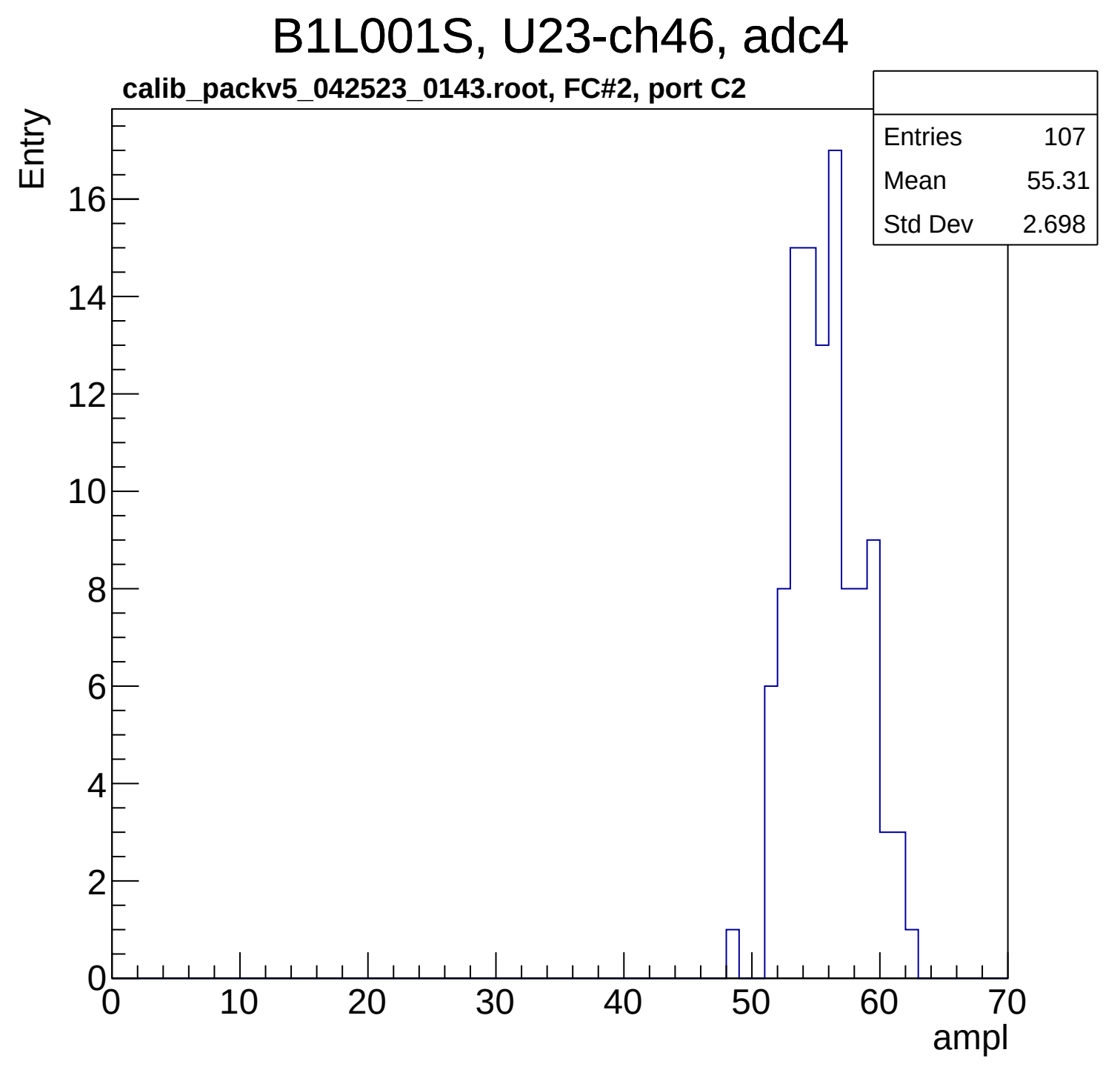
B1L001S, U23-ch46, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 55.31
Std Dev 2.698
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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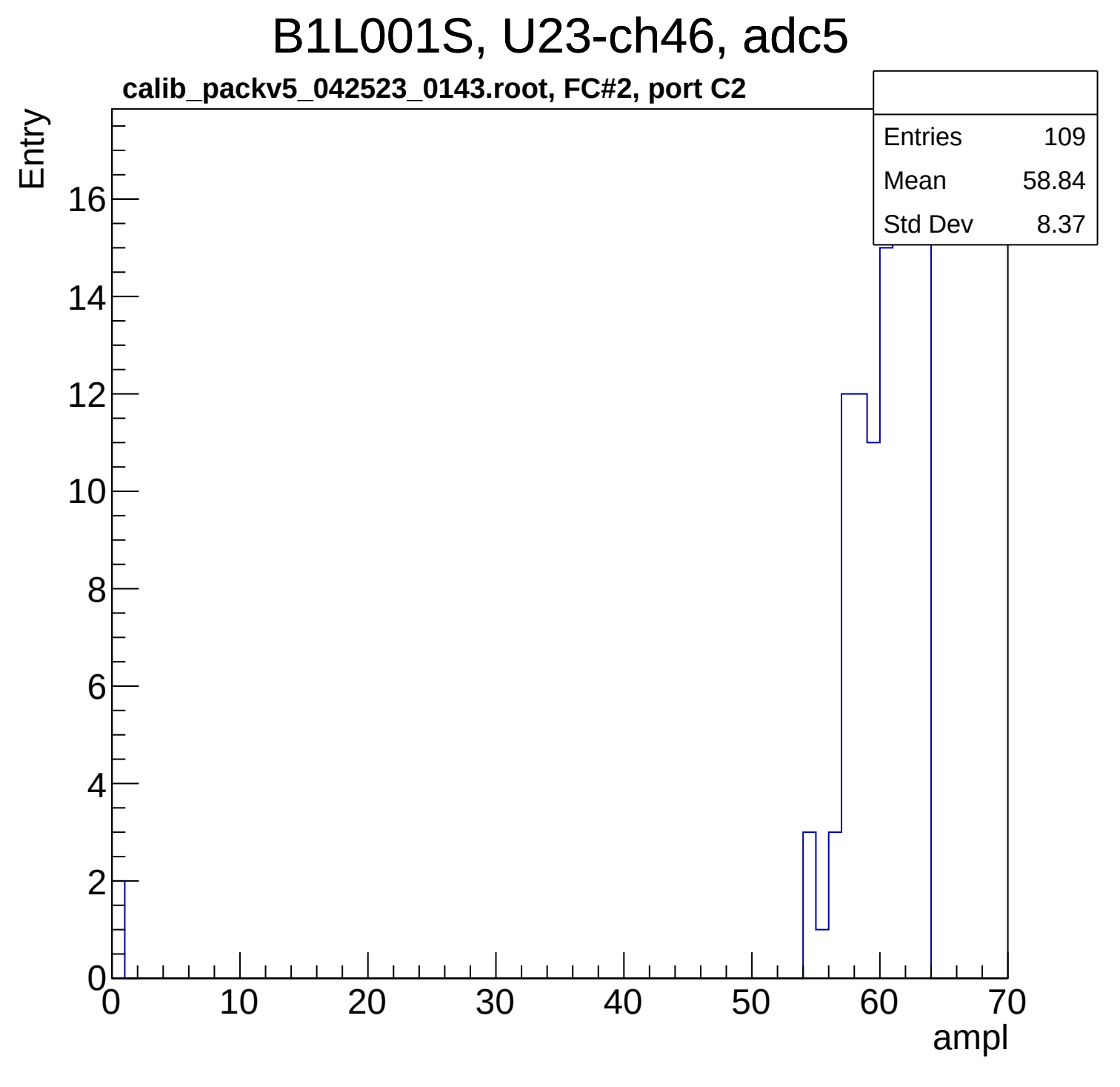
B1L001S, U23-ch46, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 58.84
Std Dev 8.37
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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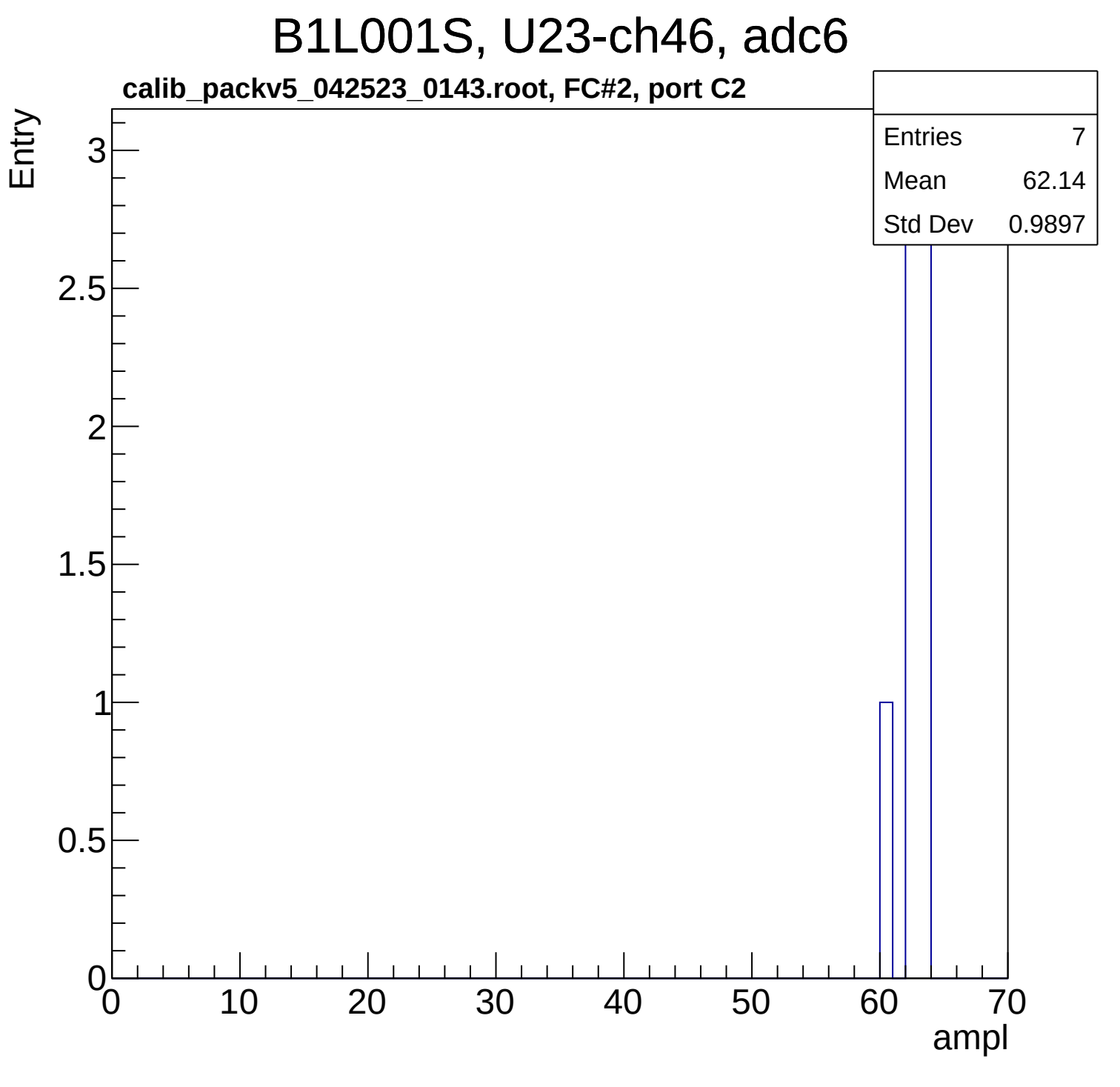
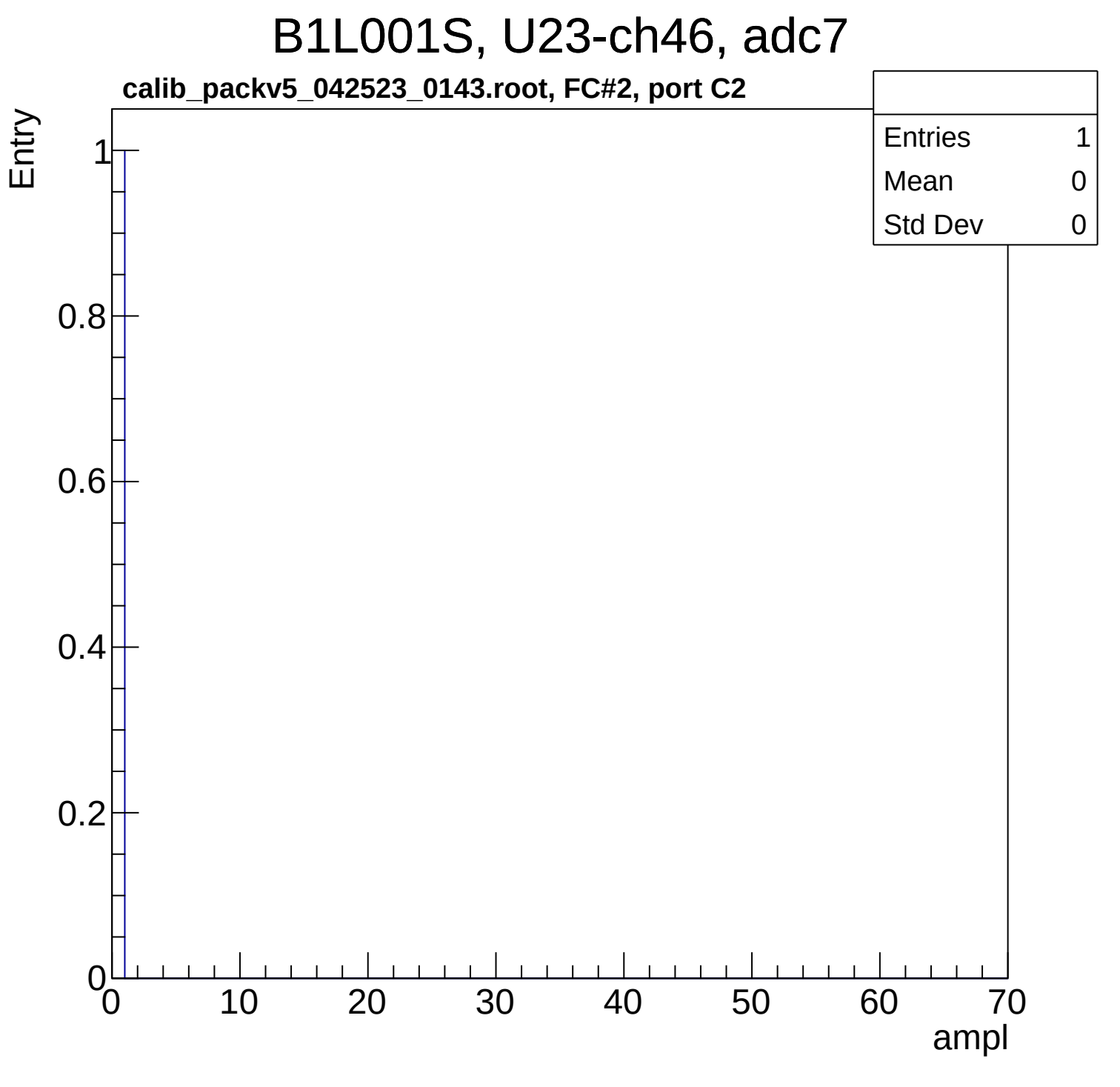

B1L001S, U23-ch46, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 62.14
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch46, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
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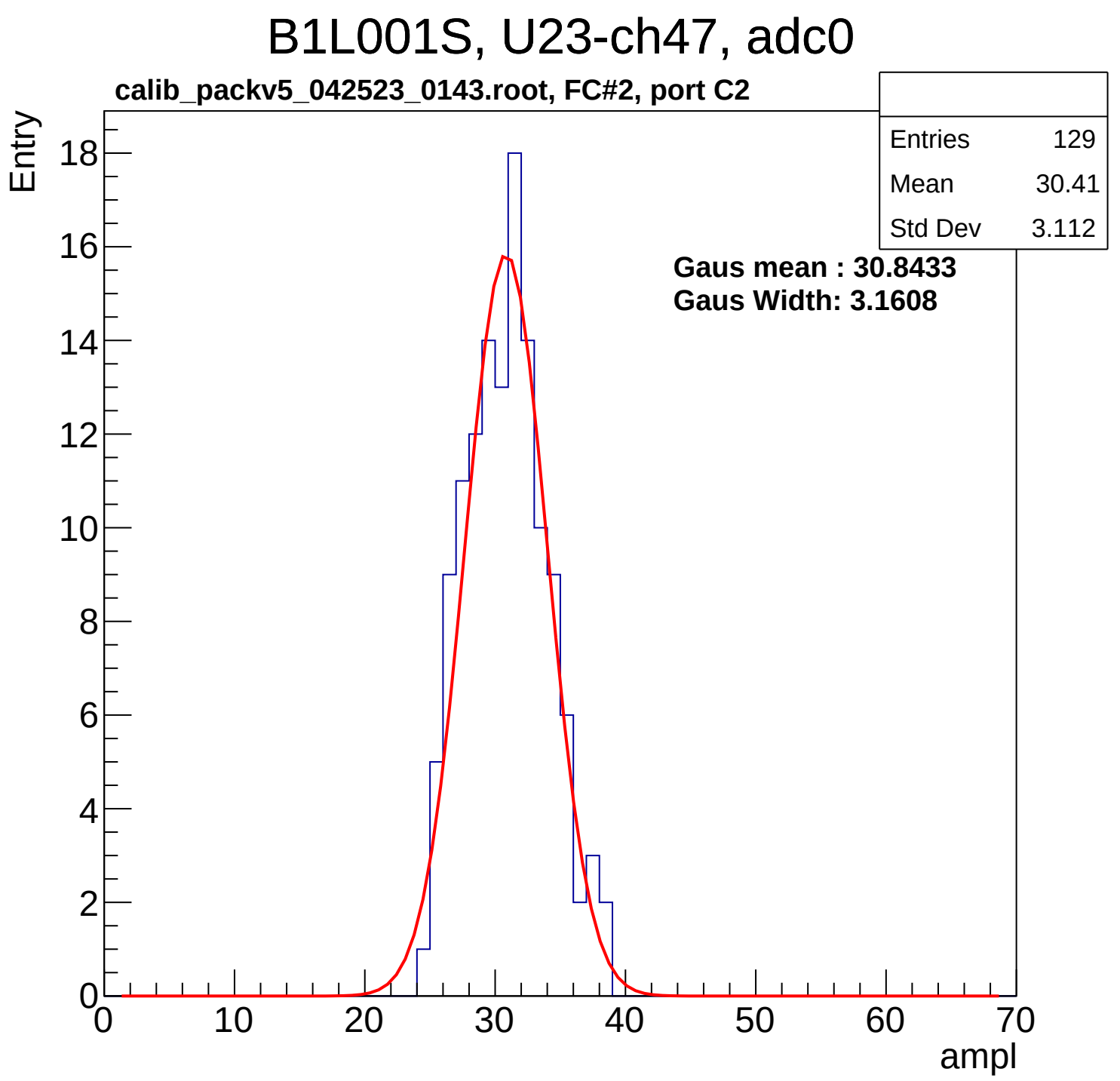
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch47, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 30.41
Std Dev 3.112
Gaus mean : 30.8433
Gaus Width: 3.1608
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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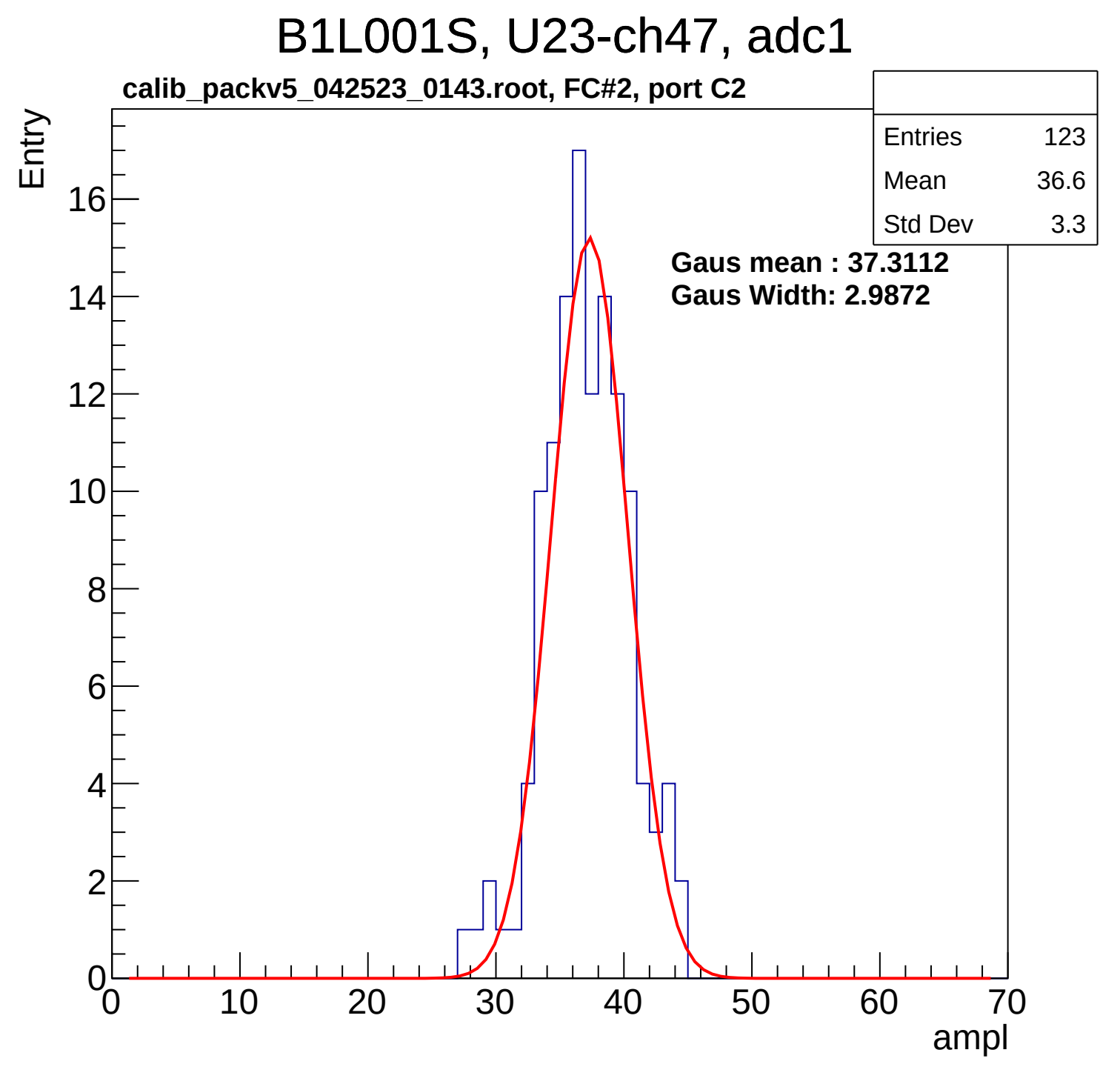
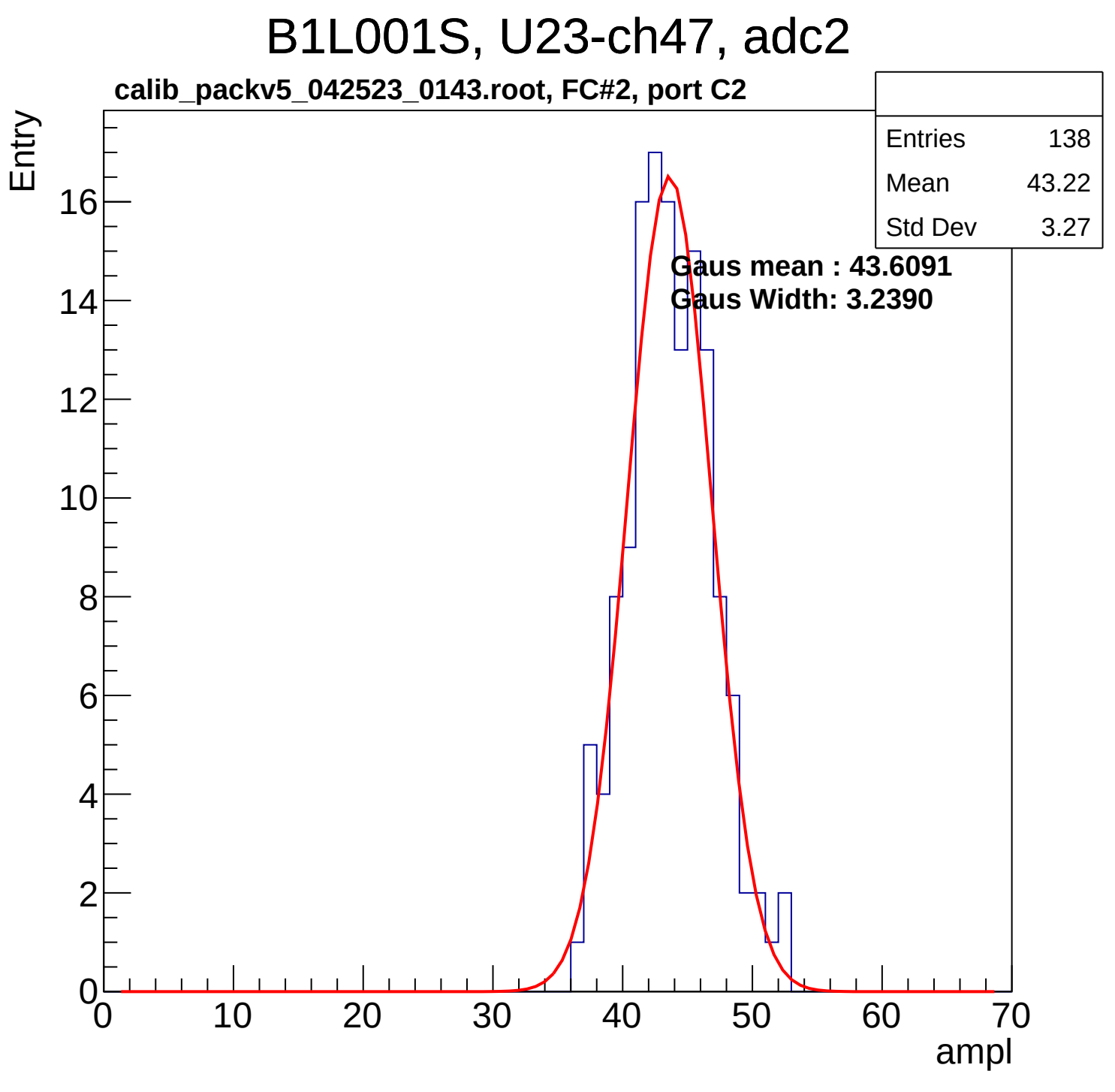

B1L001S, U23-ch47, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 36.6
Std Dev 3.3
Gaus mean : 37.3112
Gaus Width: 2.9872
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch47, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.22
Std Dev 3.27
Gaus mean : 43.6091
Gaus Width: 3.2390
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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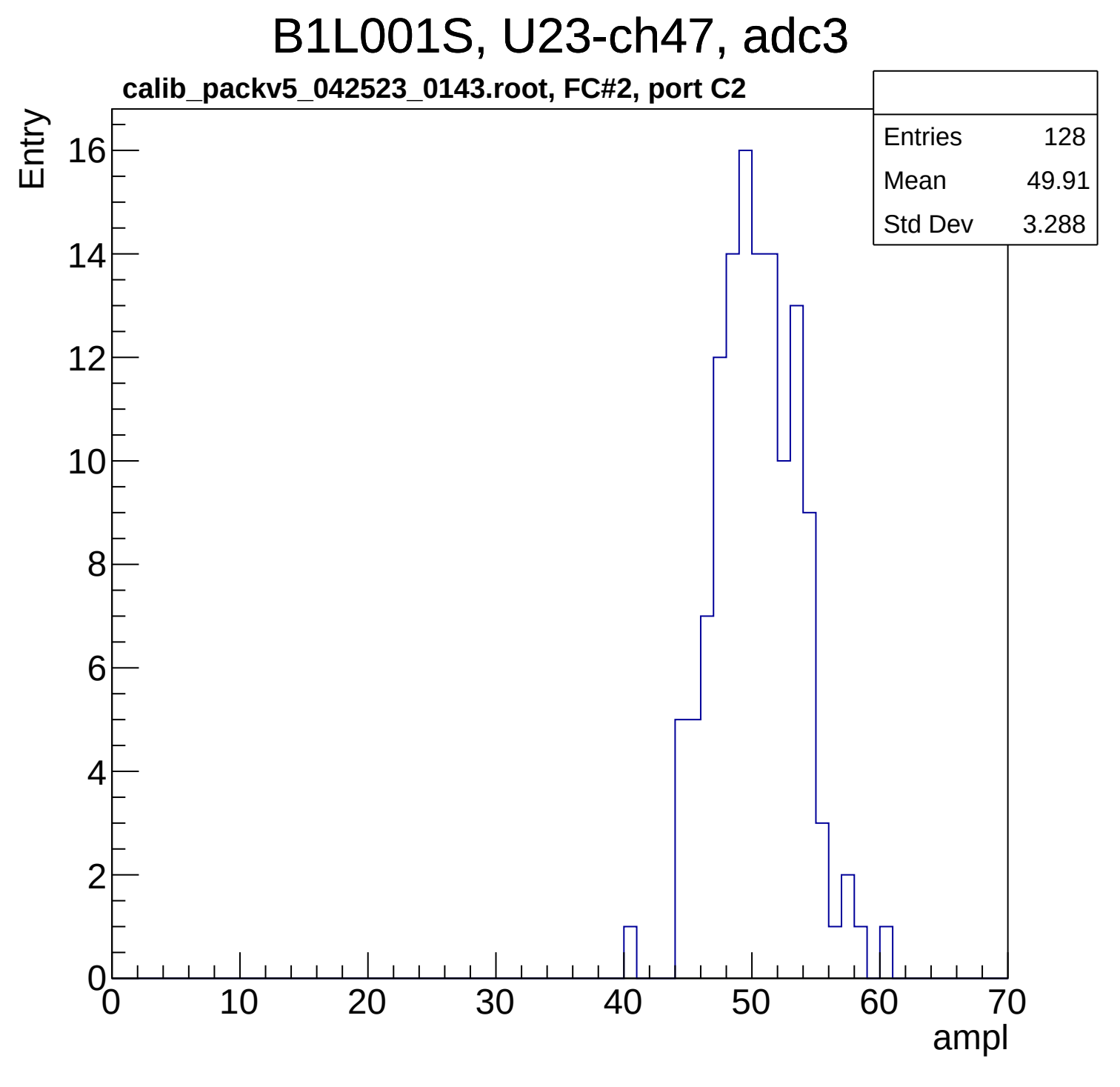
B1L001S, U23-ch47, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 49.91
Std Dev 3.288
Entry
ampl
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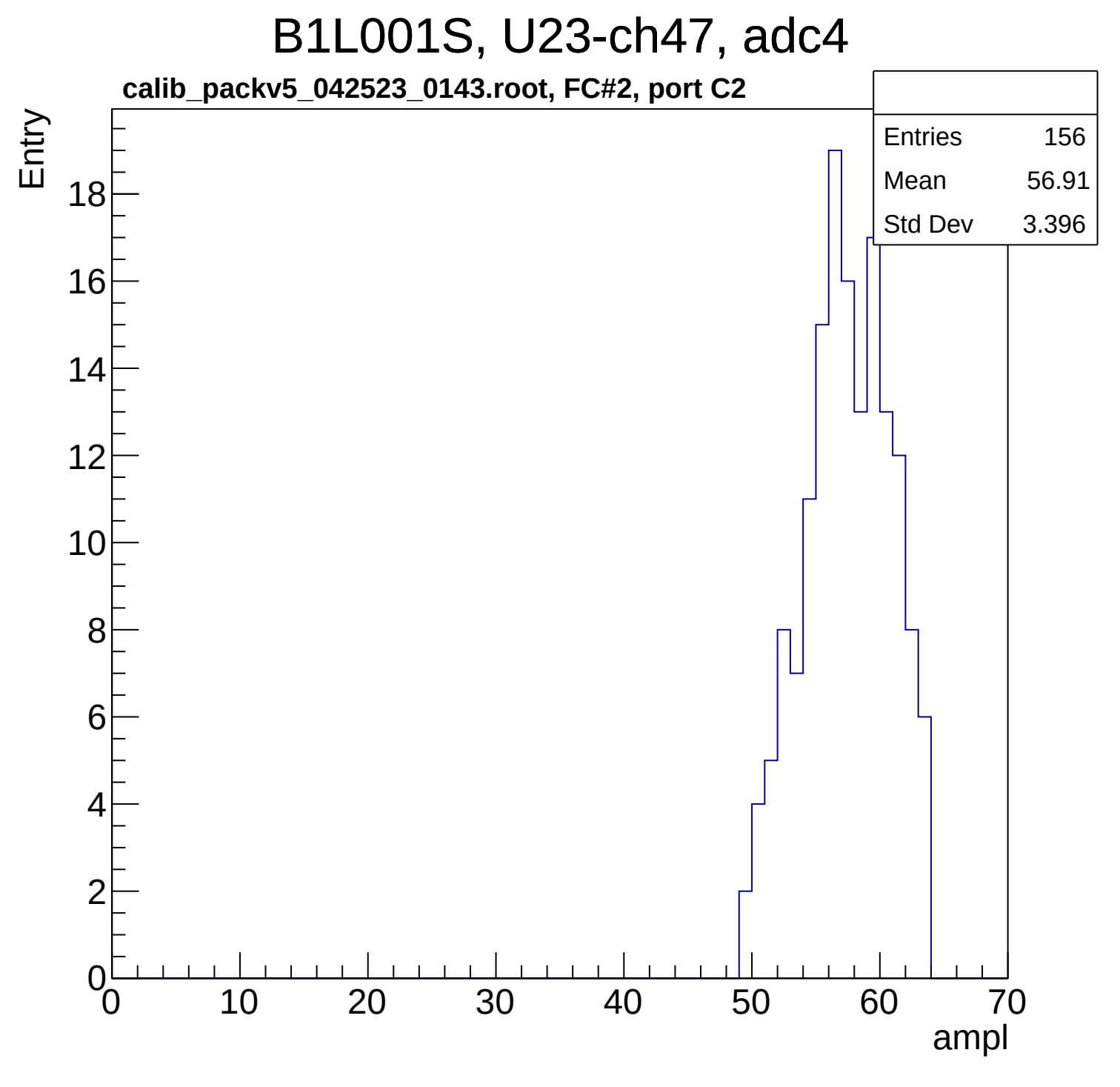
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch47, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 56.91
Std Dev 3.396
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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60
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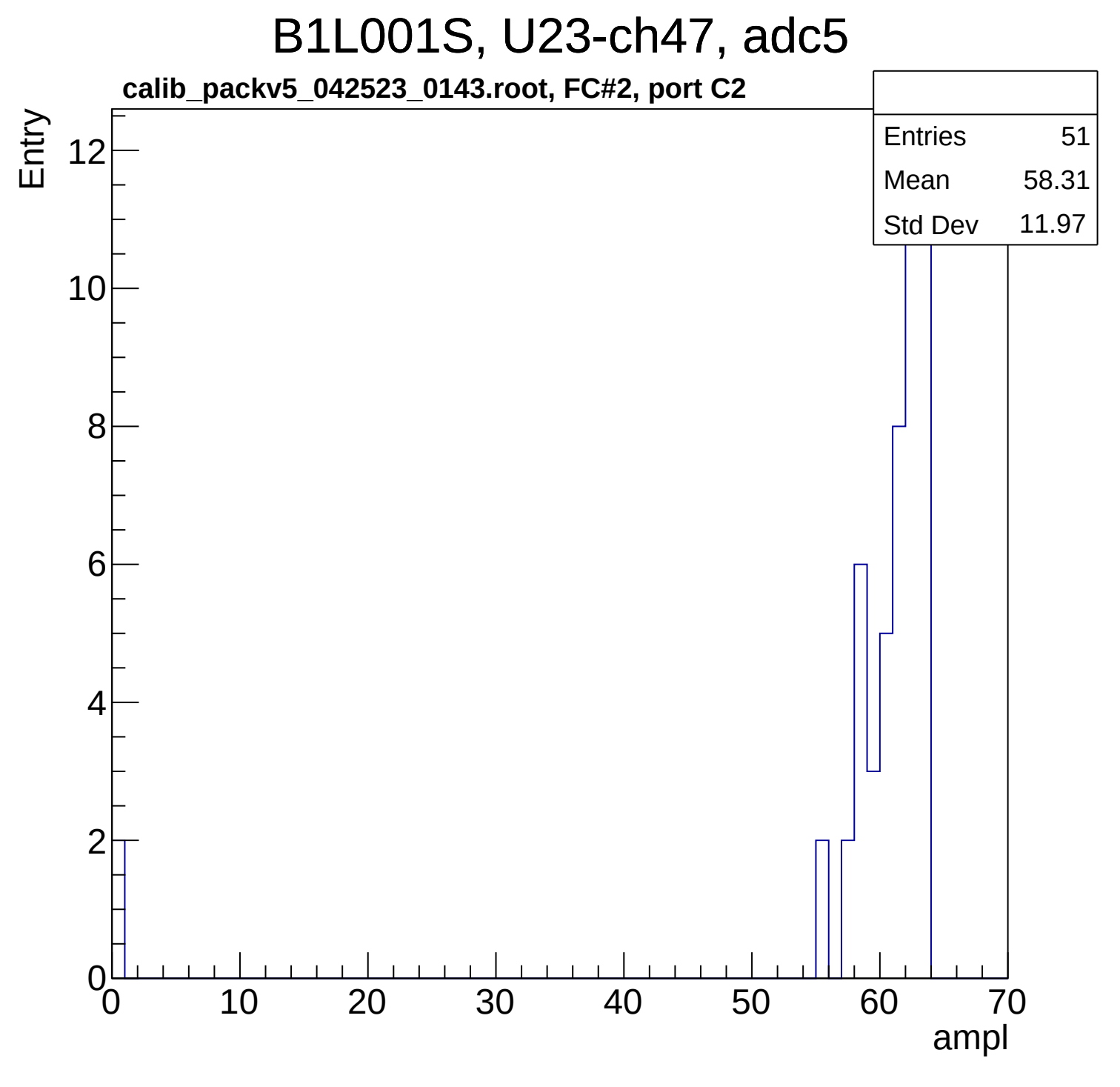
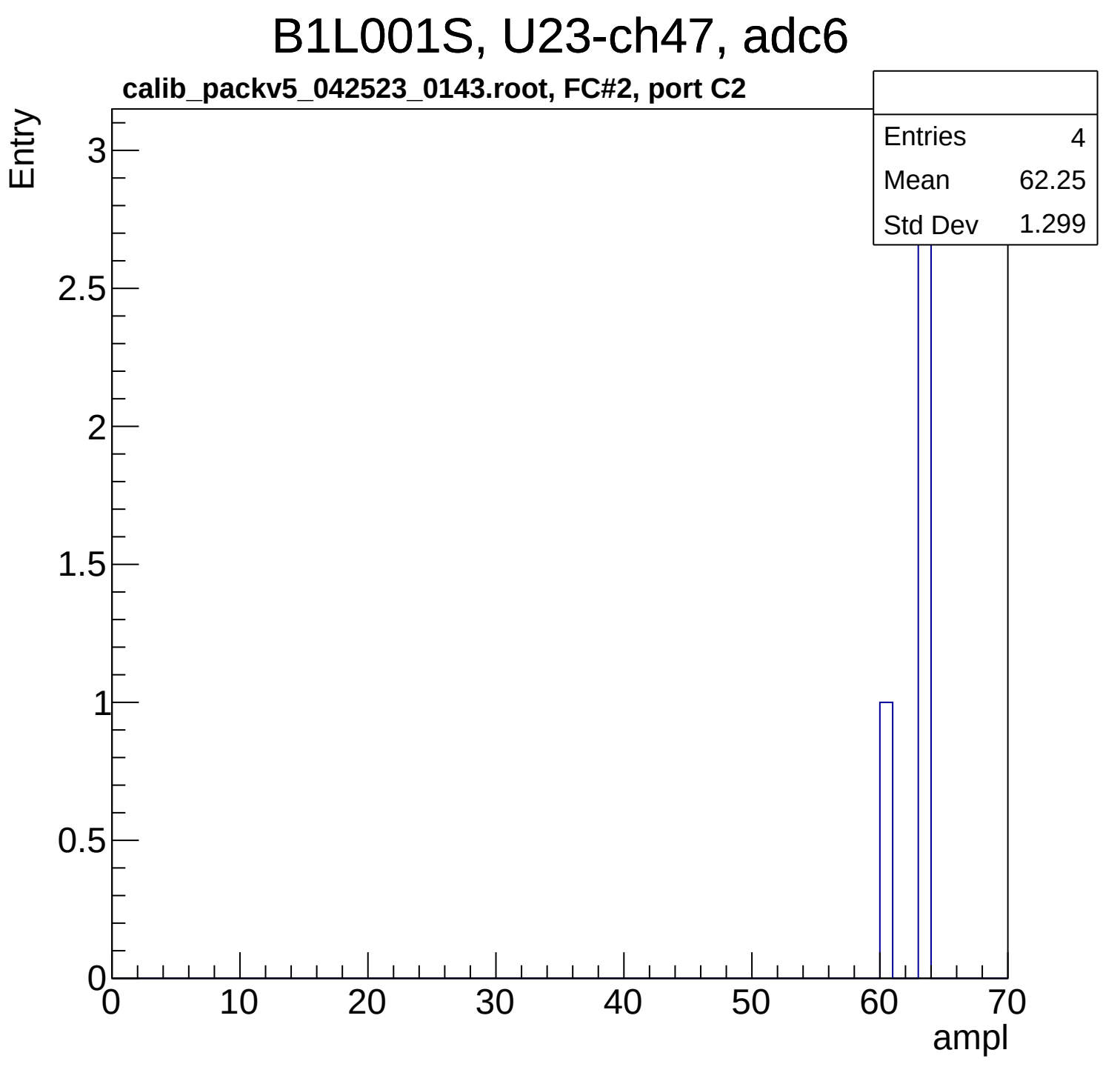

B1L001S, U23-ch47, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 51
Mean 58.31
Std Dev 11.97
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch47, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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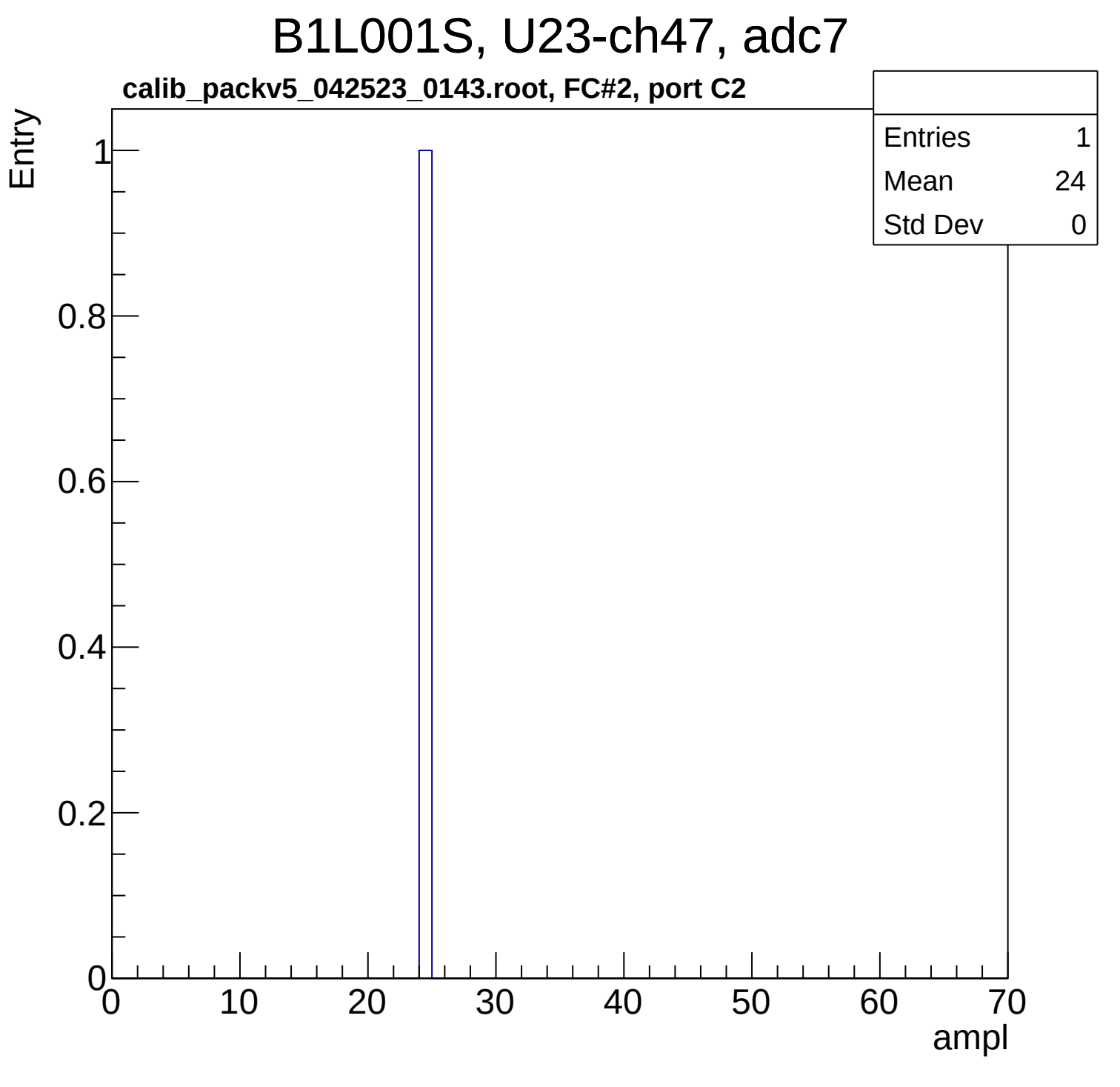
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch47, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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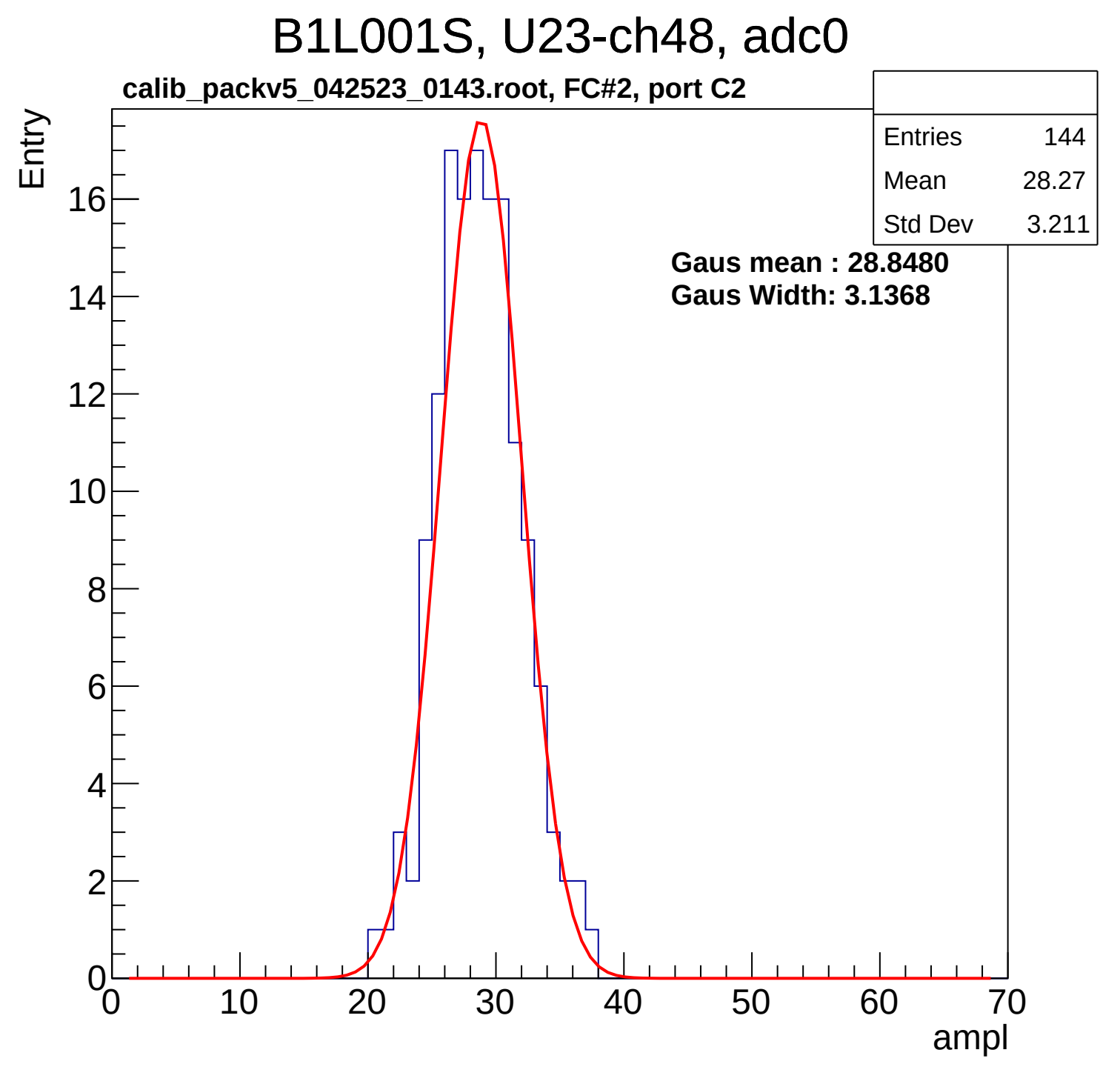
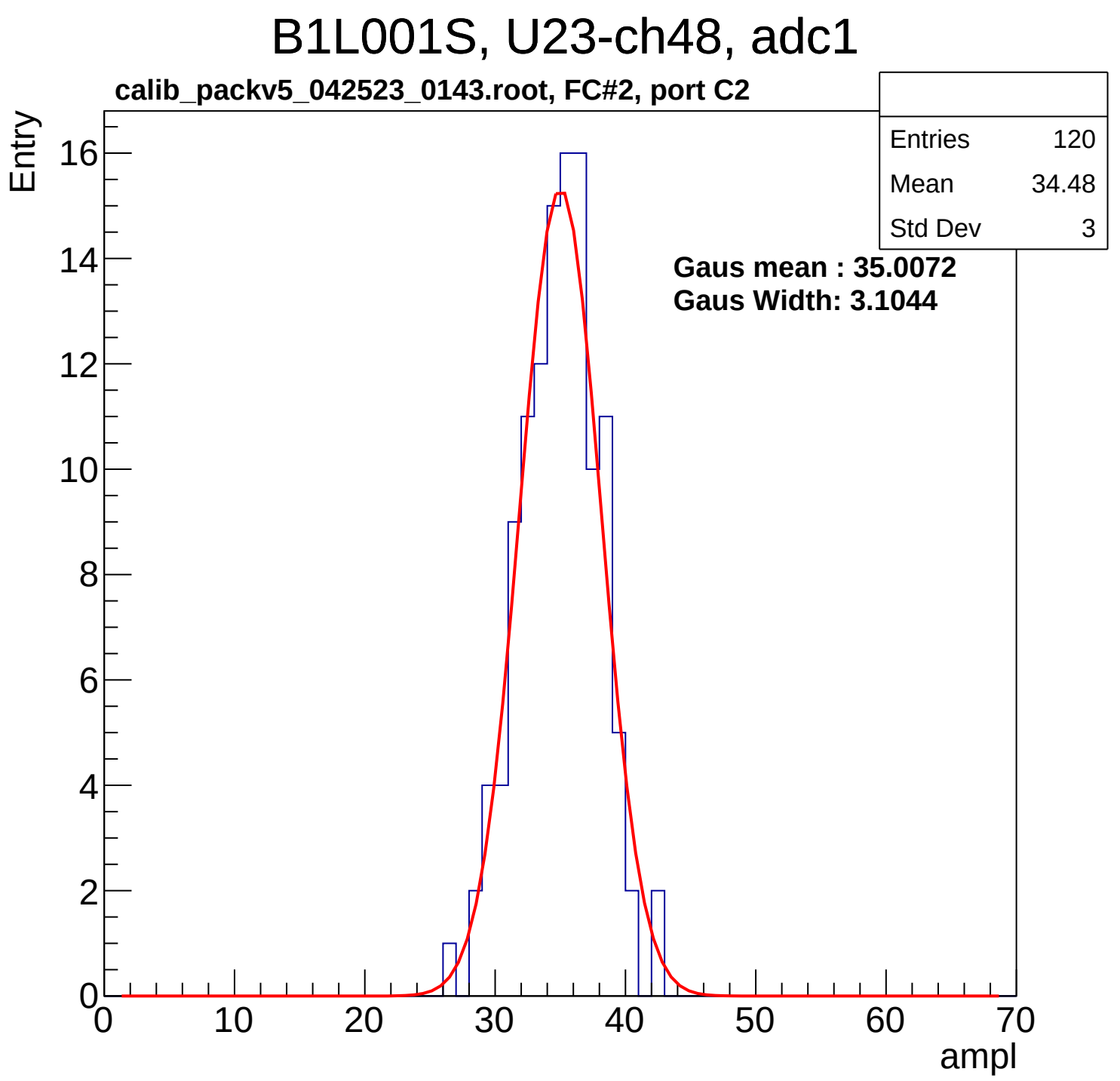
50
60
70
ampl


B1L001S, U23-ch48, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 28.27
Std Dev 3.211
Gaus mean : 28.8480
Gaus Width: 3.1368
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch48, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 34.48
Std Dev 3
Gaus mean : 35.0072
Gaus Width: 3.1044
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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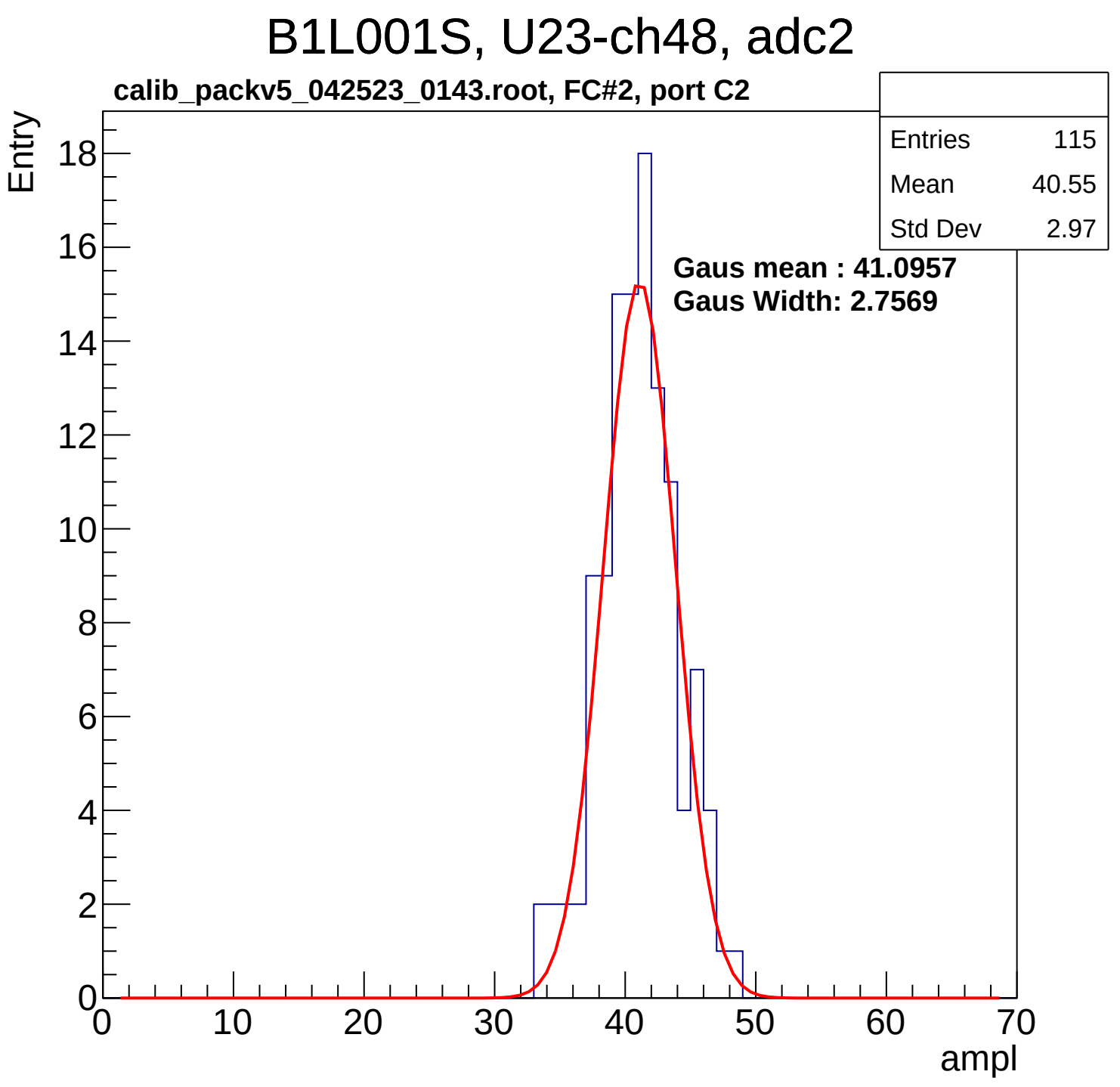
B1L001S, U23-ch48, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 40.55
Std Dev 2.97
Gaus mean : 41.0957
Gaus Width: 2.7569
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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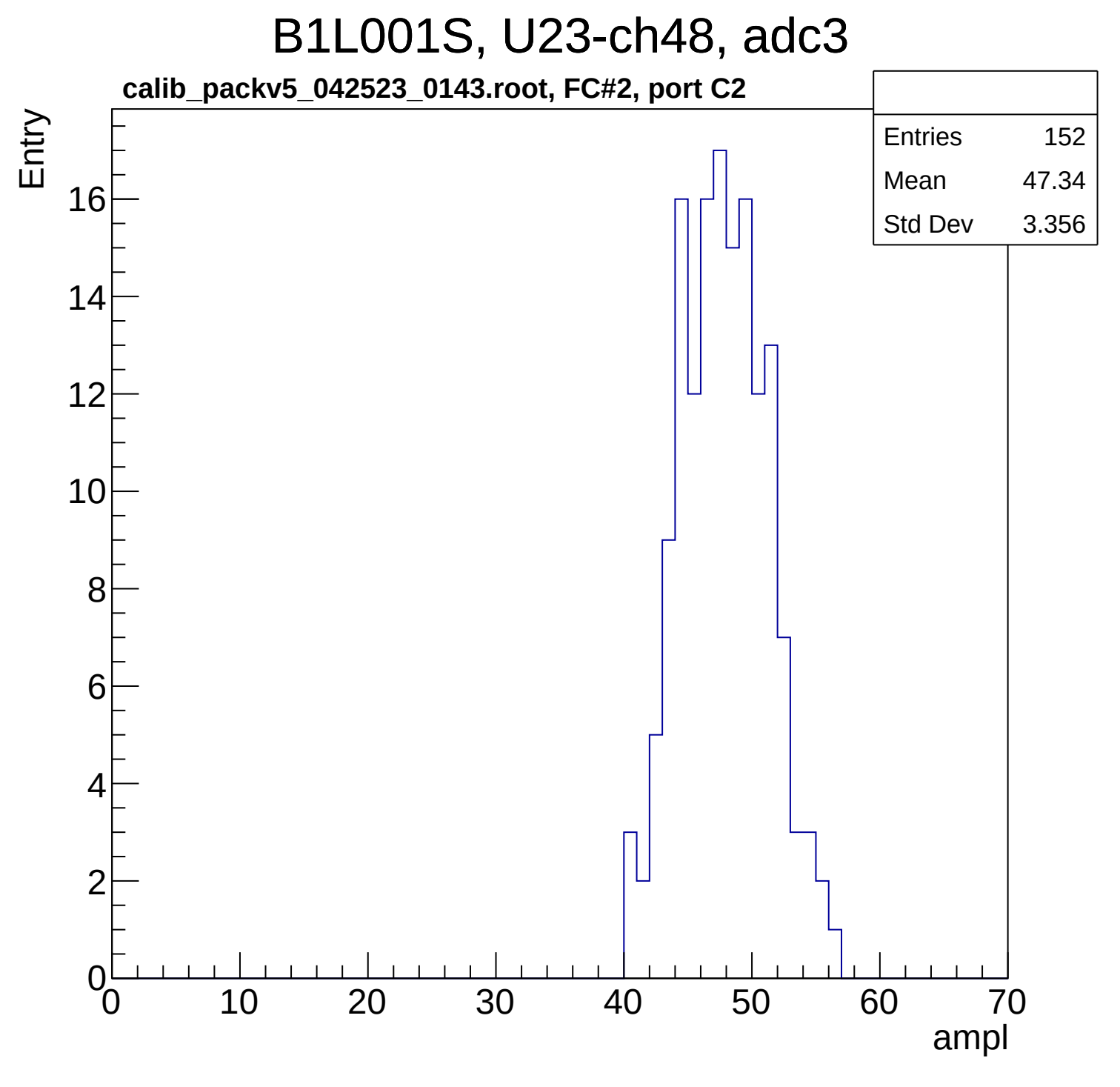
B1L001S, U23-ch48, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 47.34
Std Dev 3.356
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
50
60
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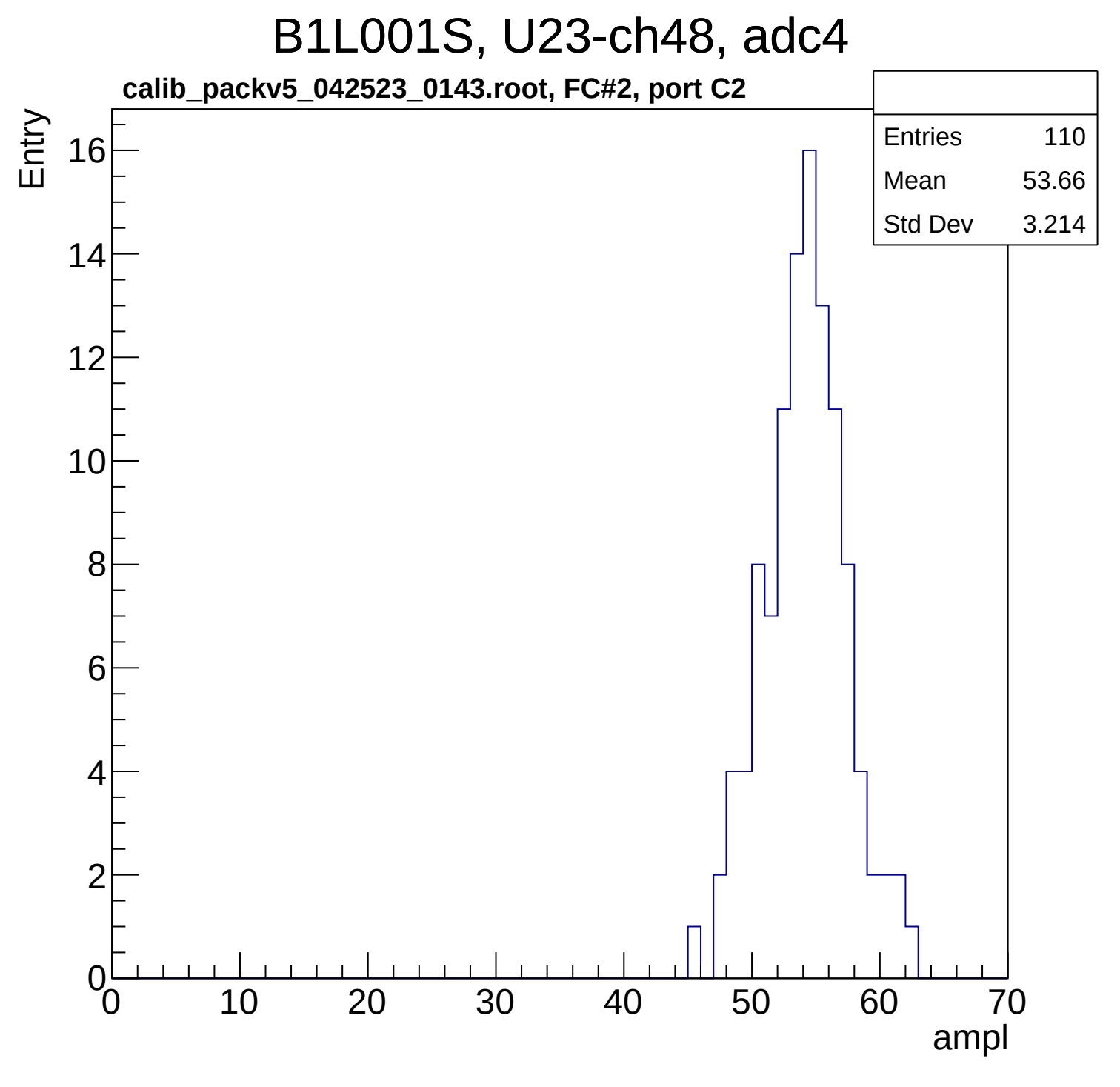
B1L001S, U23-ch48, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 53.66
Std Dev 3.214
Entry
ampl
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6
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10
12
14
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0
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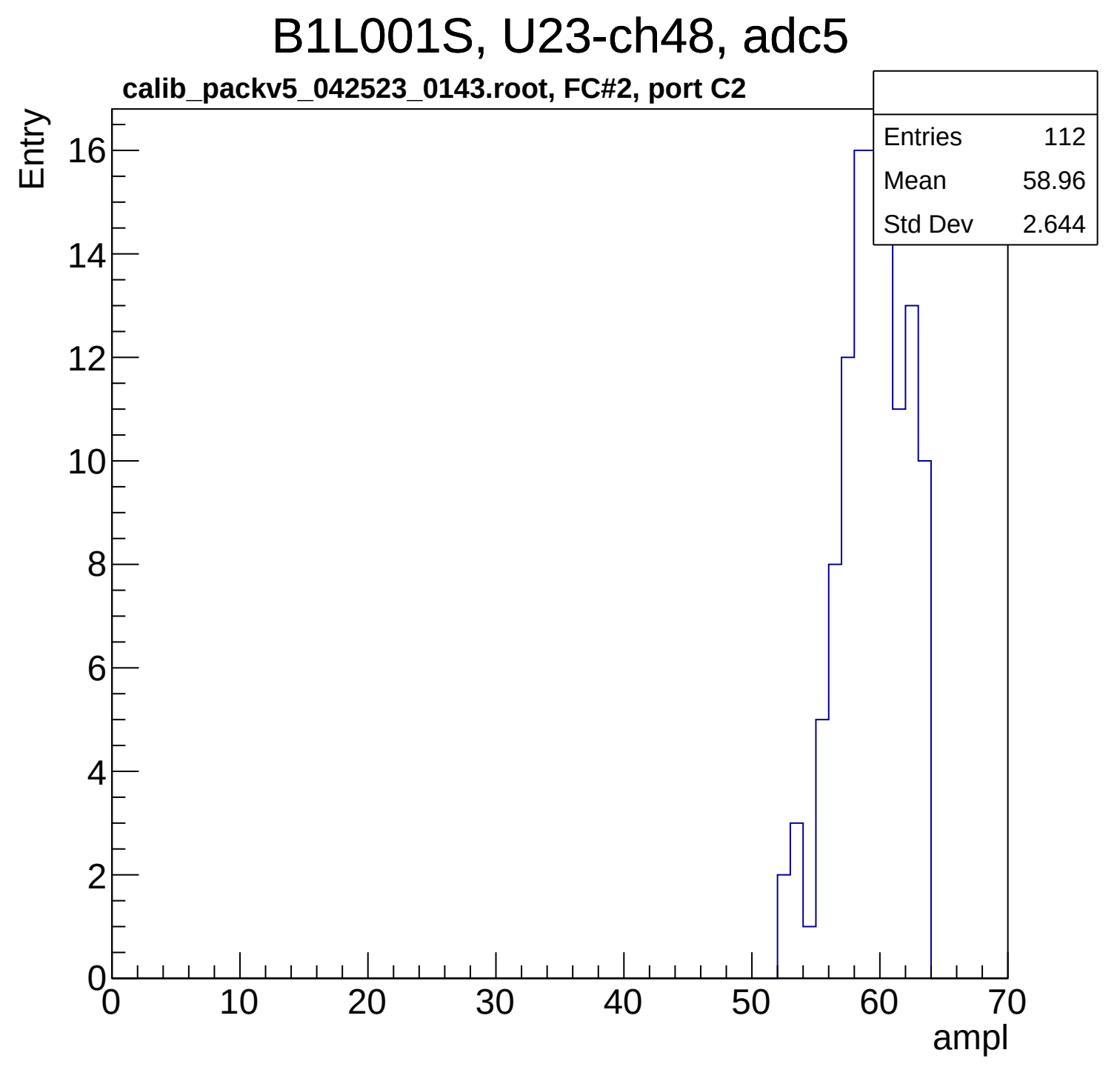
B1L001S, U23-ch48, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 58.96
Std Dev 2.644
Entry
ampl
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4
6
8
10
12
14
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0
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40
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60
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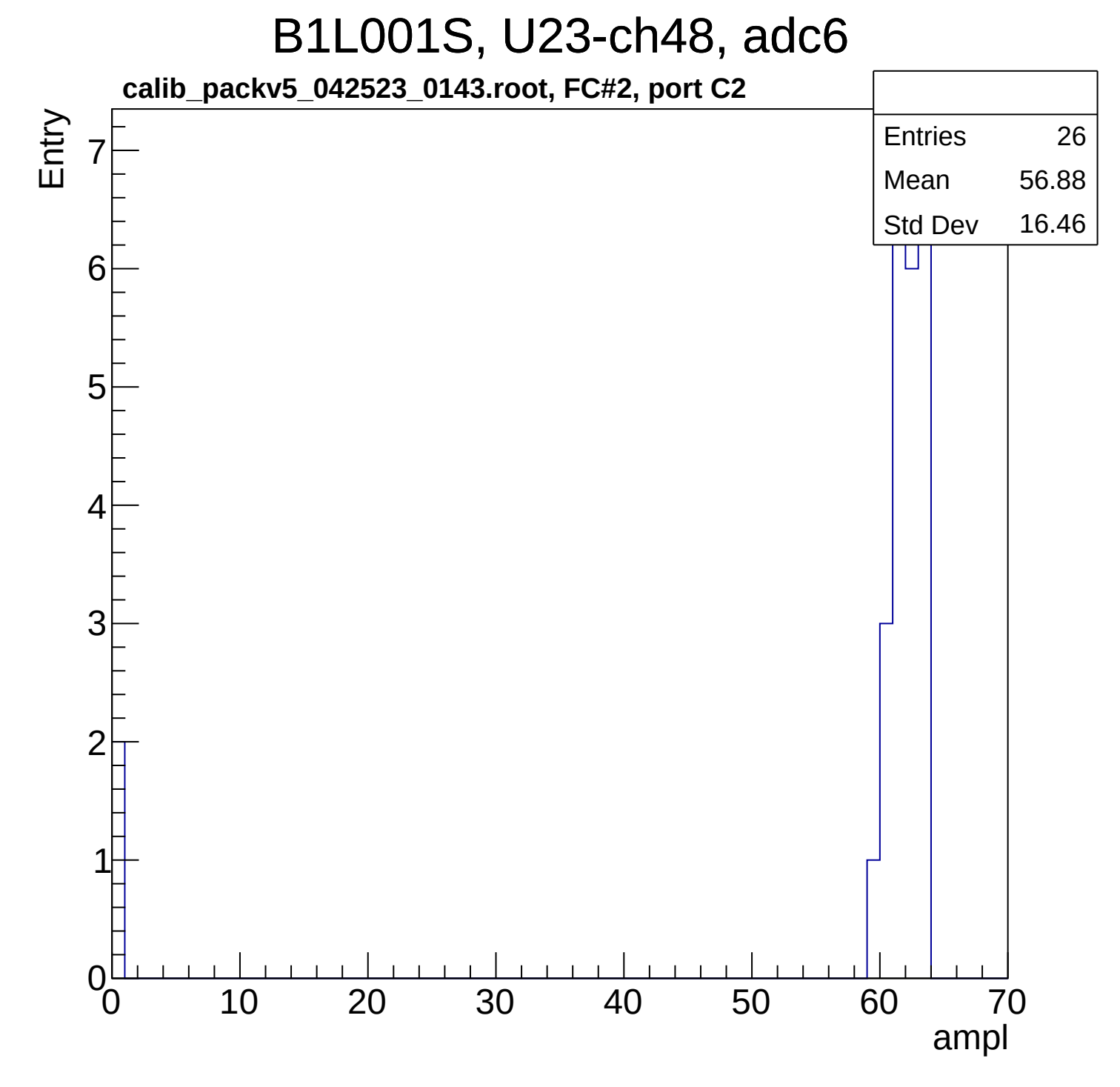
B1L001S, U23-ch48, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 56.88
Std Dev 16.46
Entry
ampl
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1
2
3
4
5
6
7
0
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20
30
40
50
60
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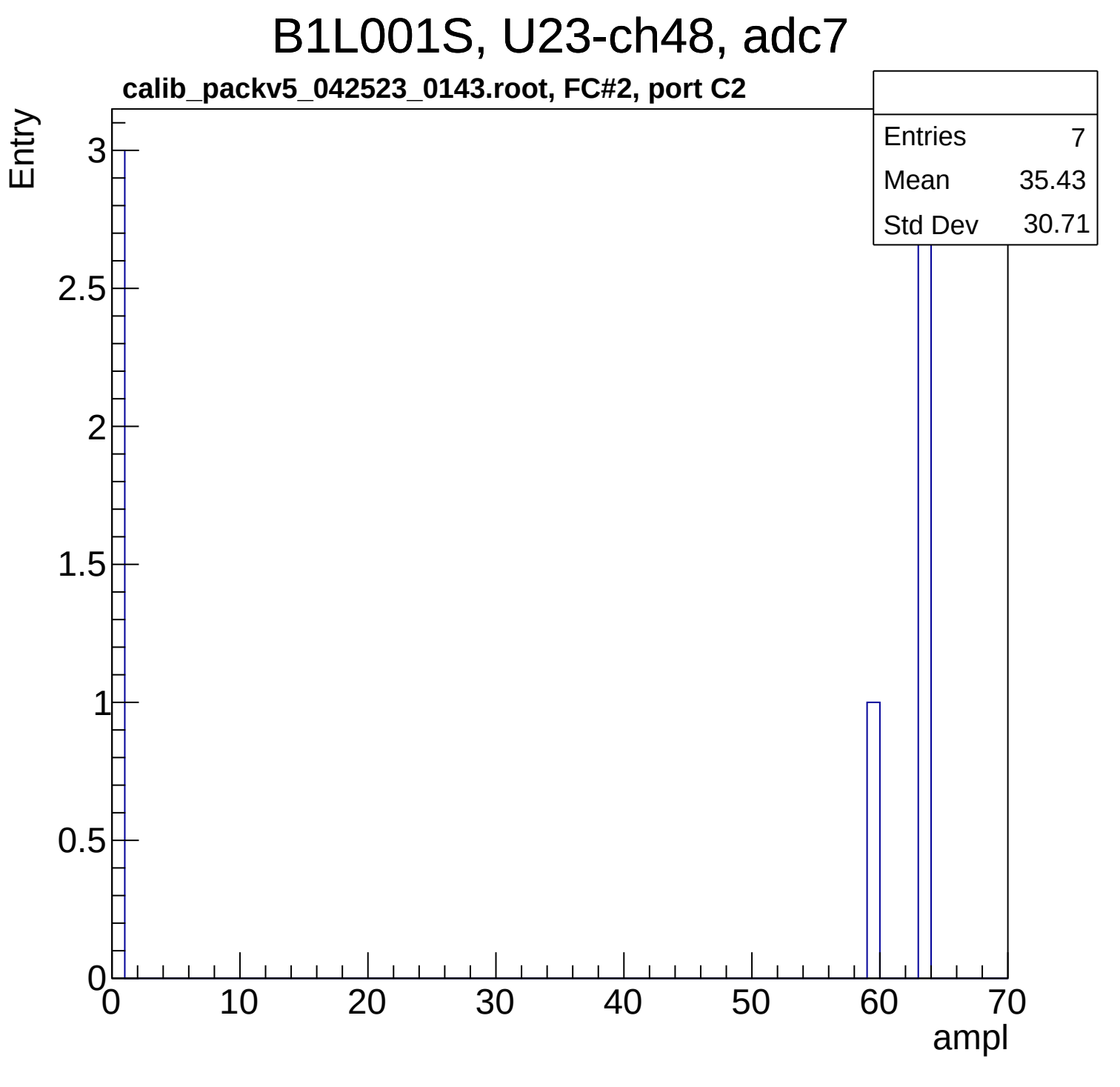
B1L001S, U23-ch48, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 35.43
Std Dev 30.71
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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40
50
60
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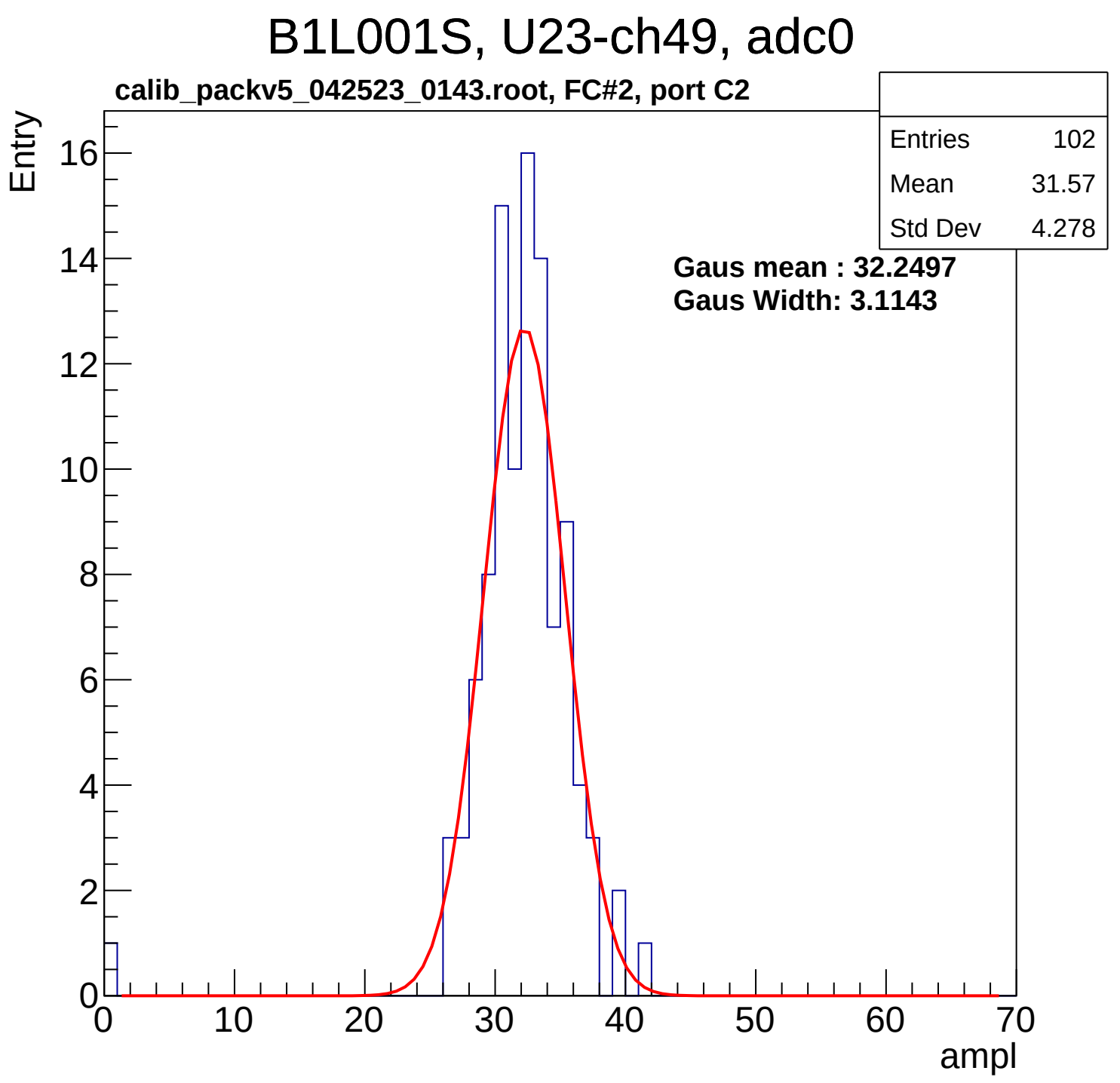
B1L001S, U23-ch49, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 31.57
Std Dev 4.278
Gaus mean : 32.2497
Gaus Width: 3.1143
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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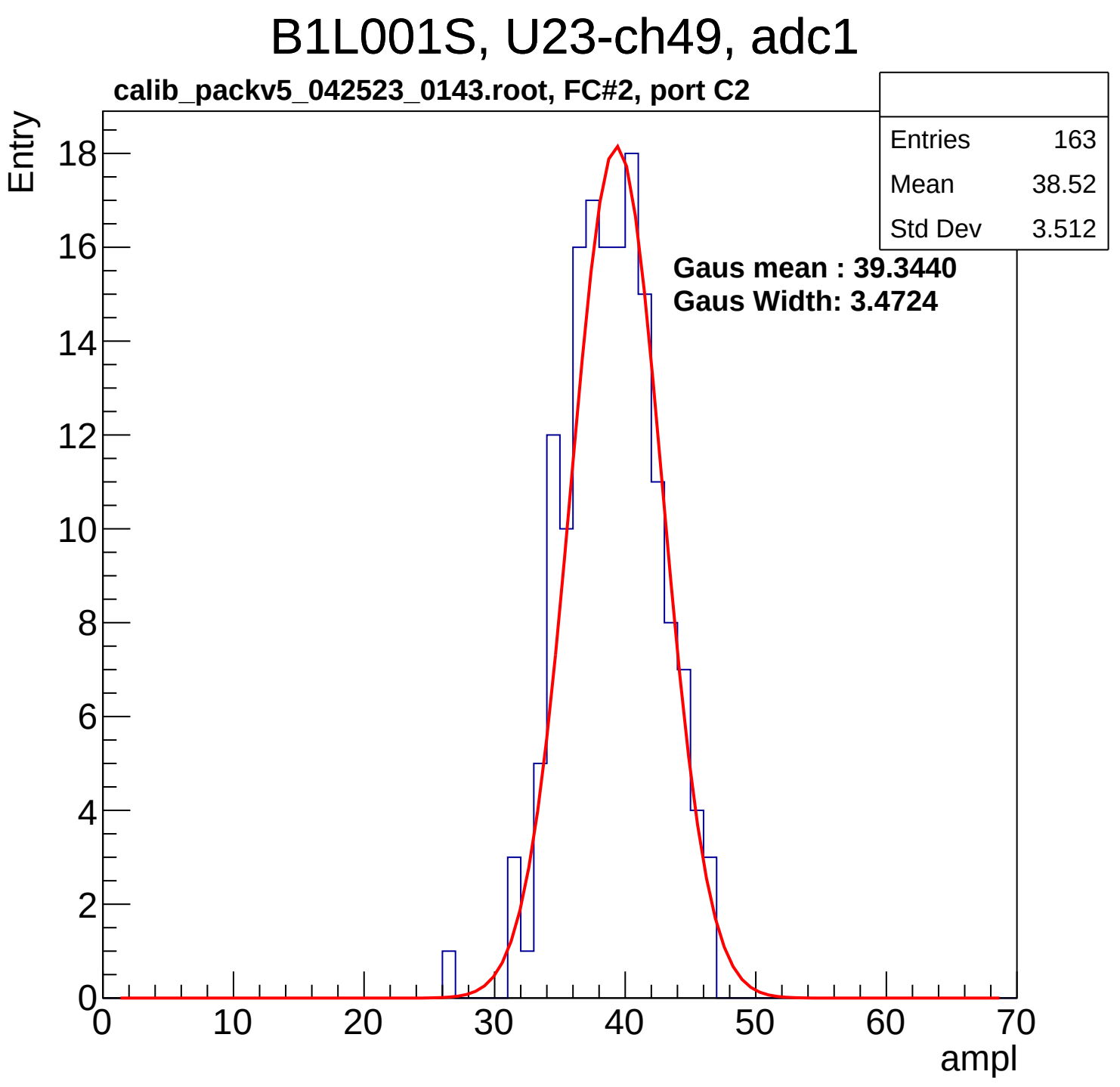
B1L001S, U23-ch49, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 38.52
Std Dev 3.512
Gaus mean : 39.3440
Gaus Width: 3.4724
Entry
ampl
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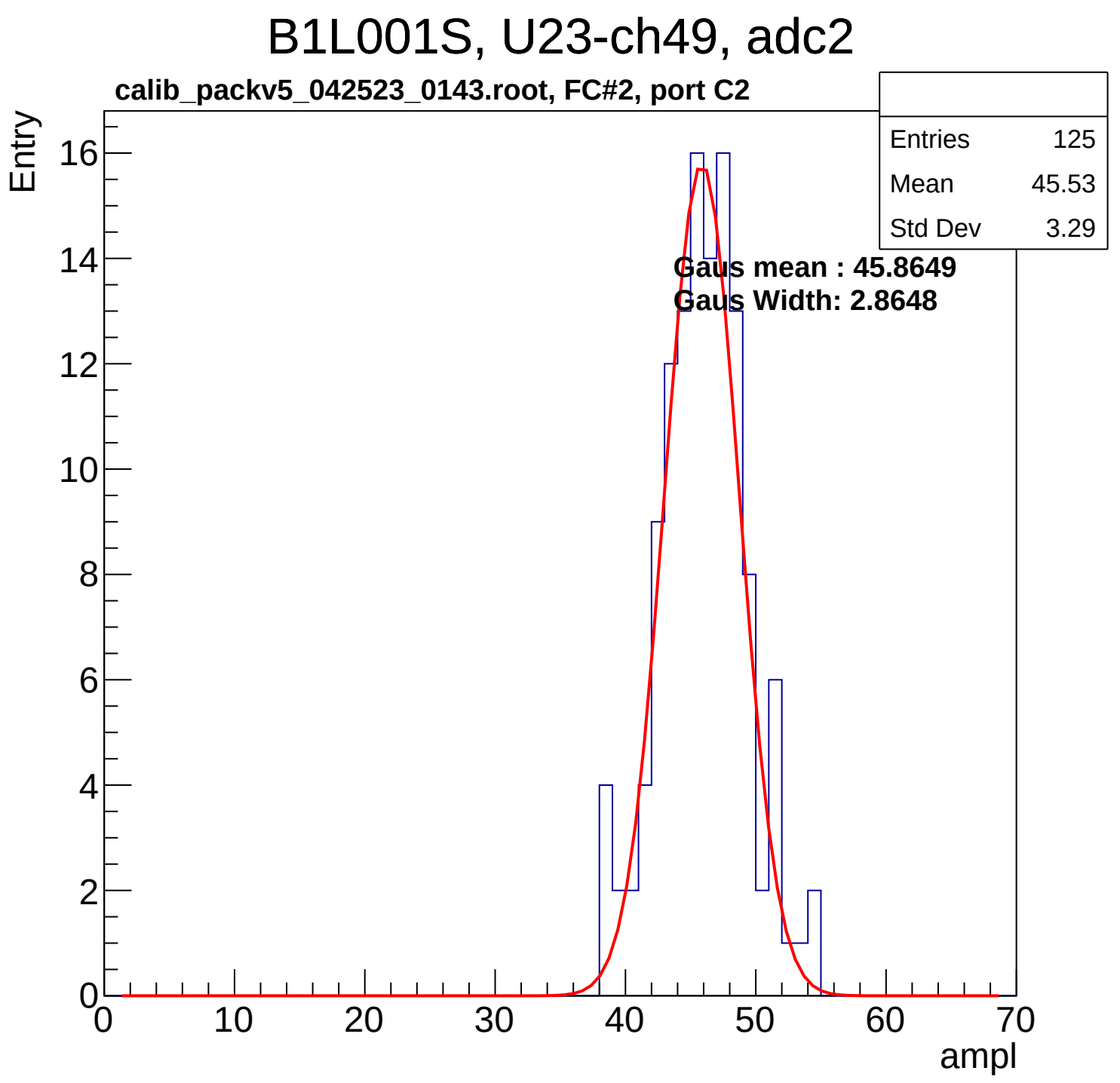
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch49, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 45.53
Std Dev 3.29
Gaus mean : 45.8649
Gaus Width: 2.8648
Entry
ampl
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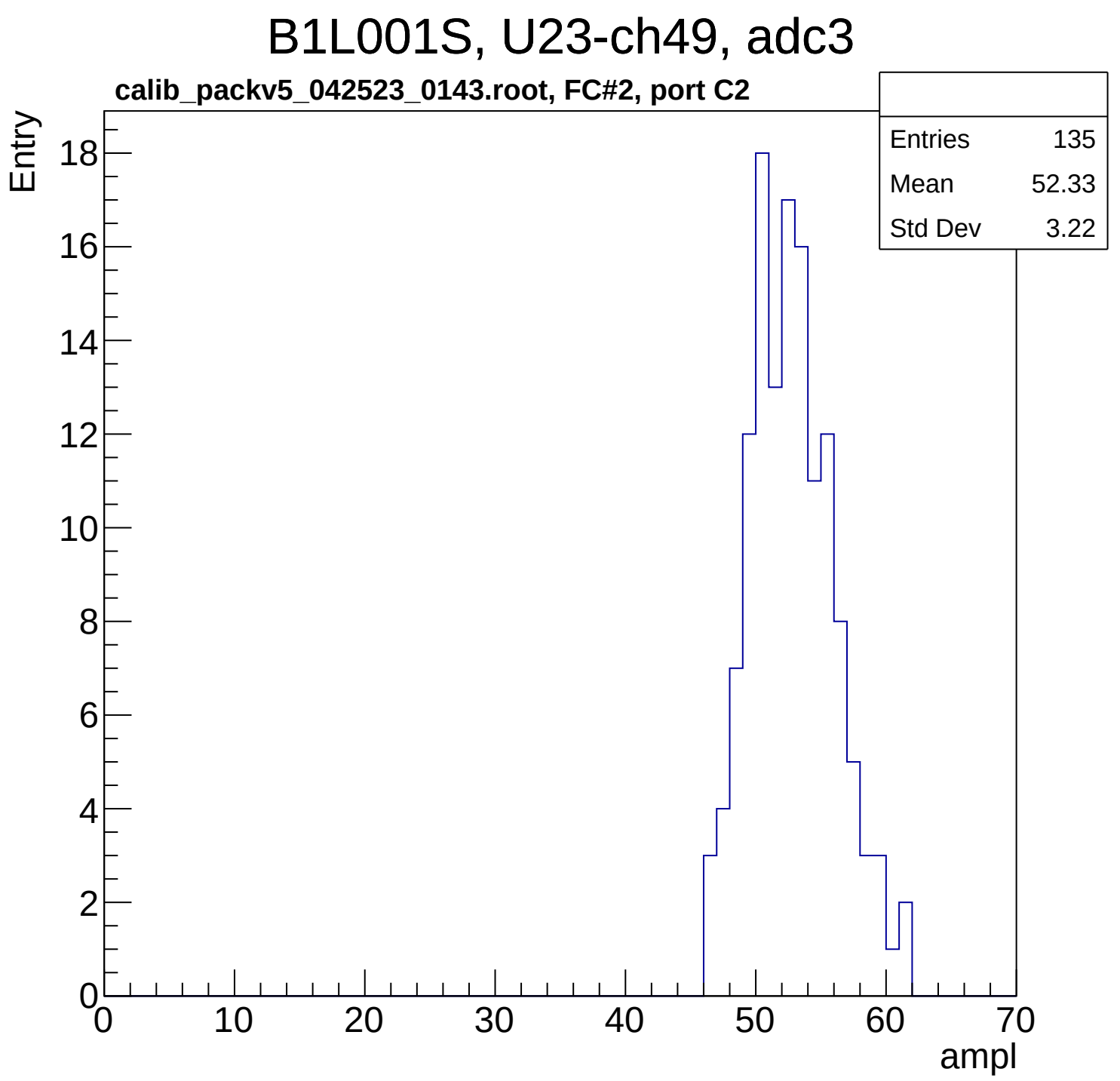
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch49, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 52.33
Std Dev 3.22
Entry
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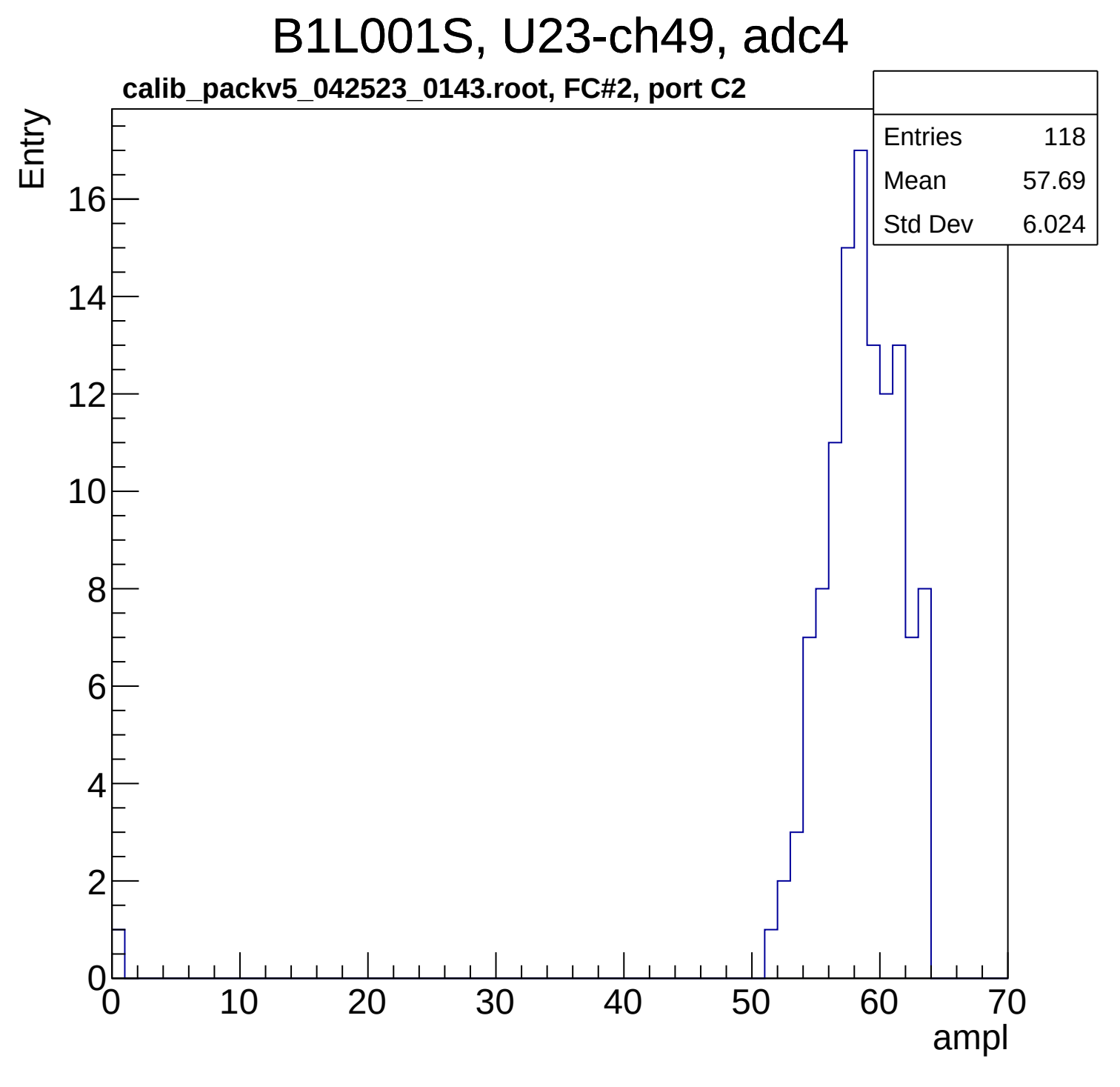
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch49, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 57.69
Std Dev 6.024
Entry
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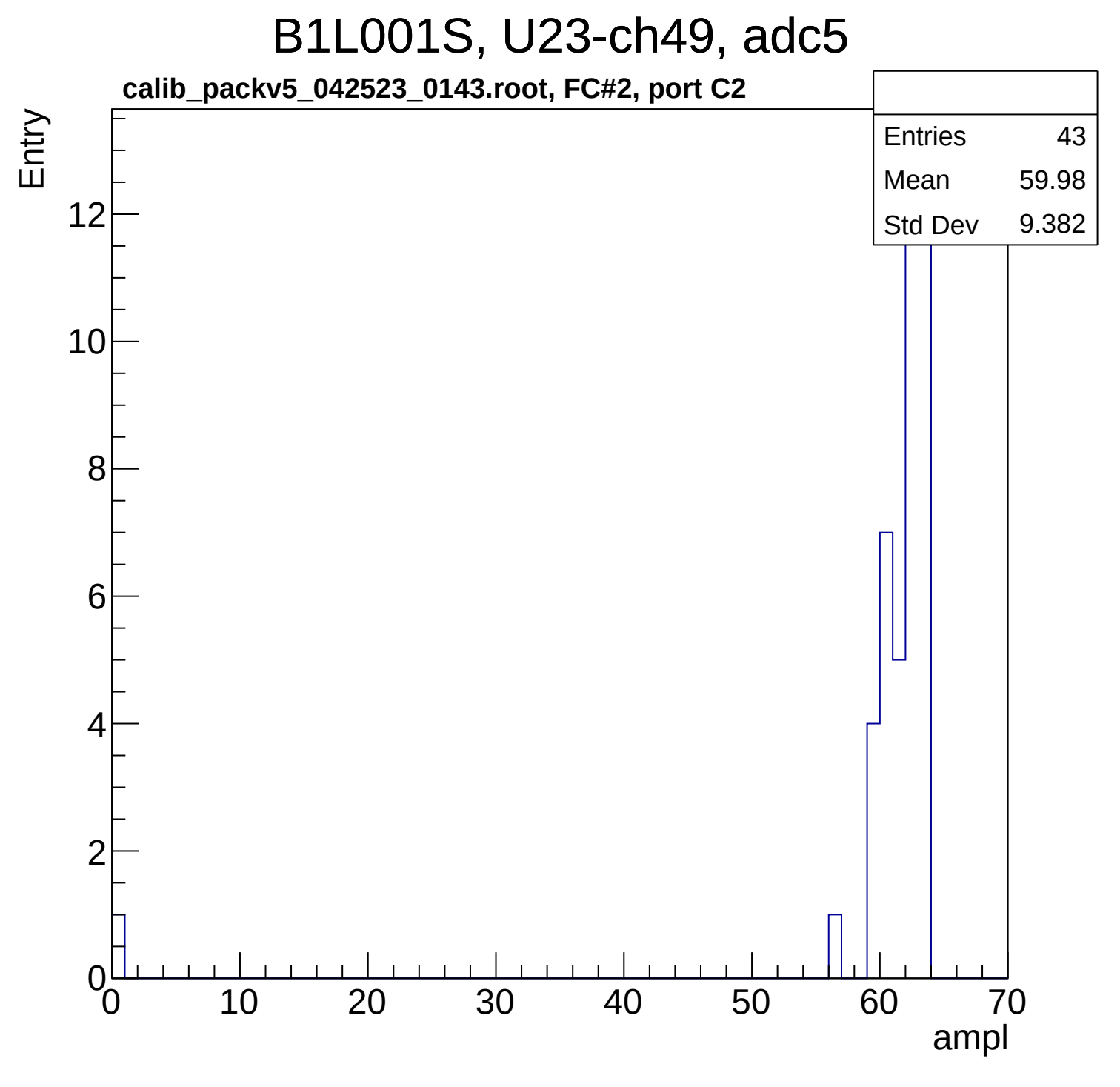
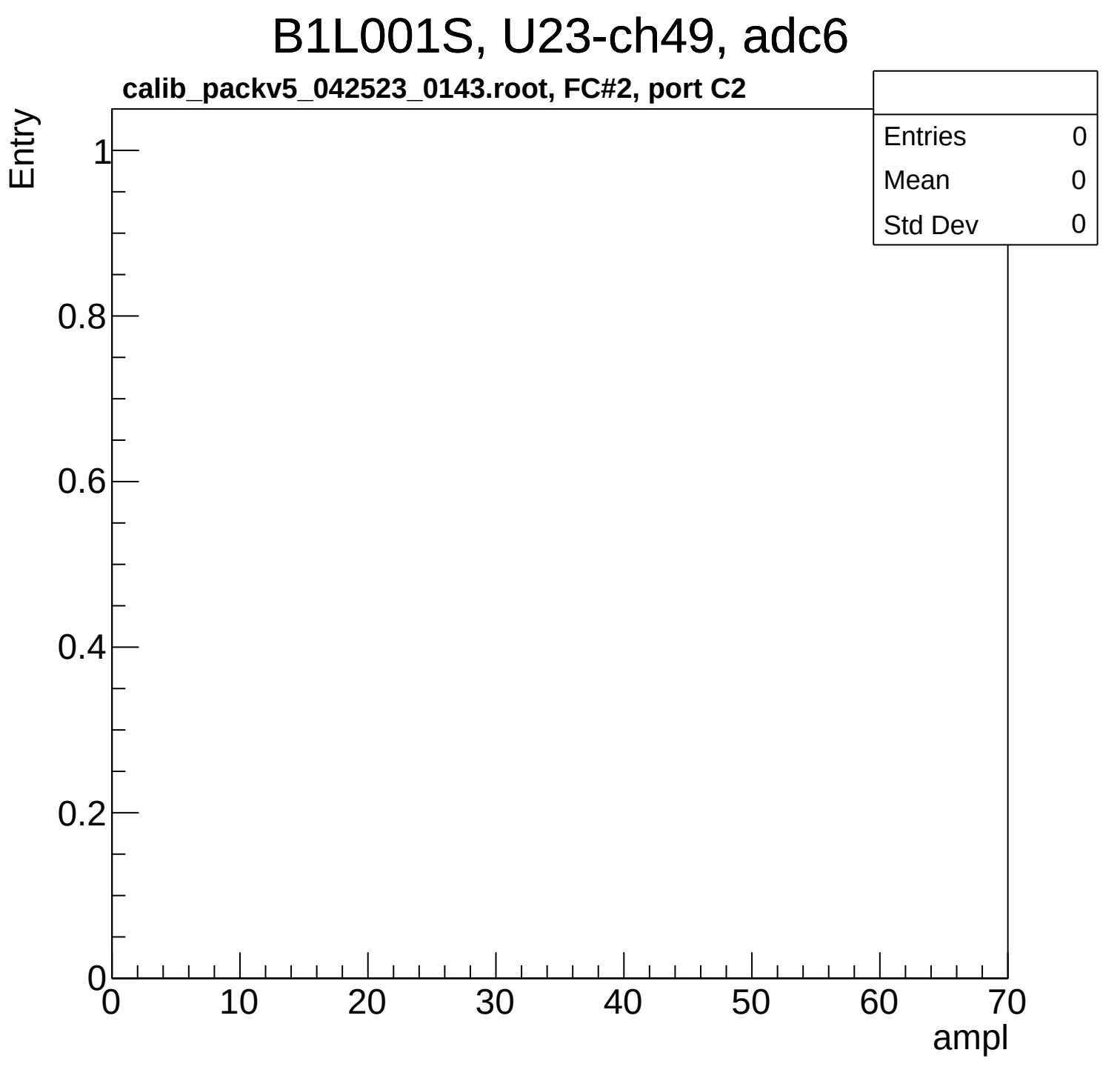
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch49, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 43
Mean 59.98
Std Dev 9.382
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U23-ch49, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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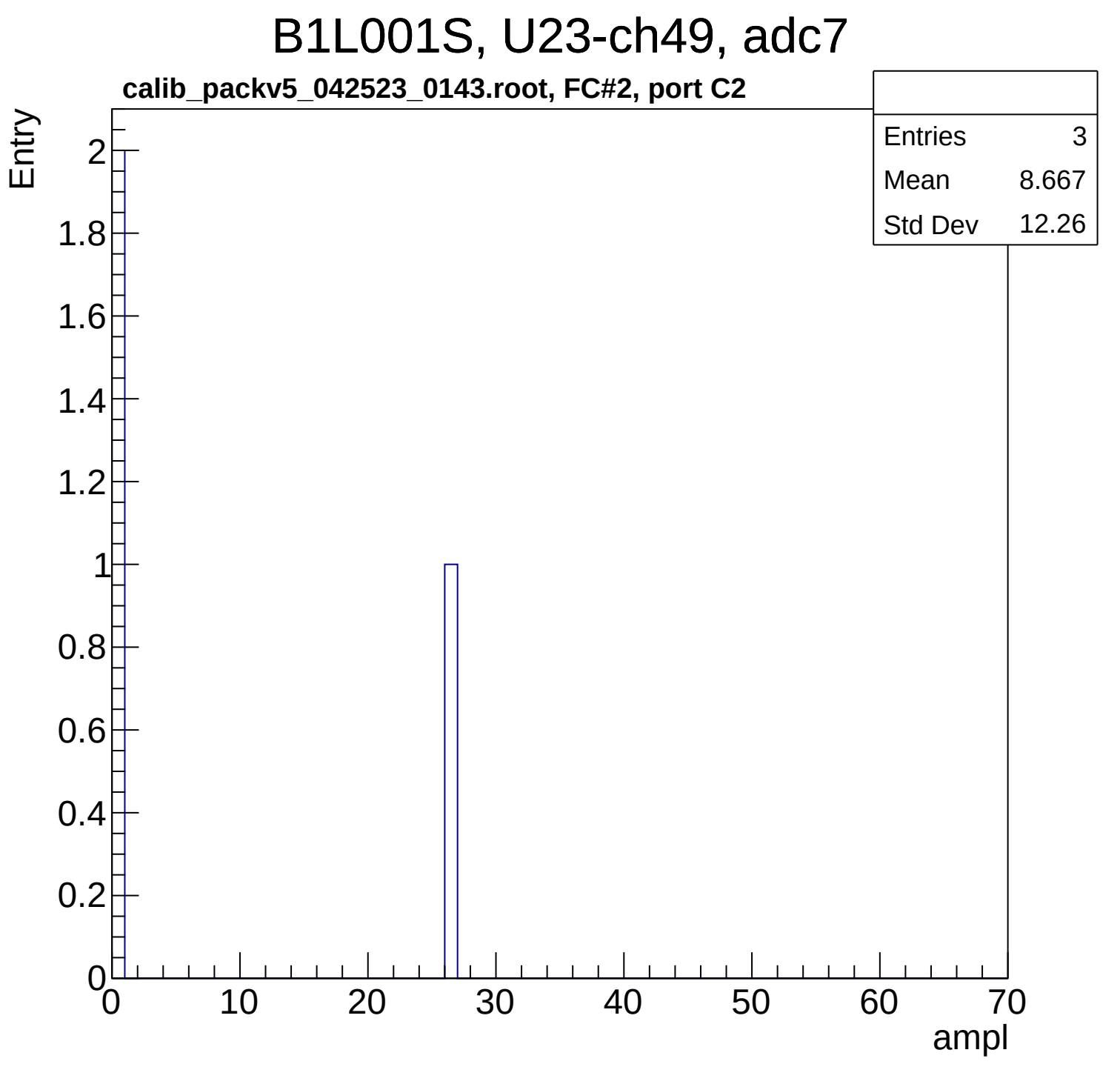
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch49, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 8.667
Std Dev 12.26
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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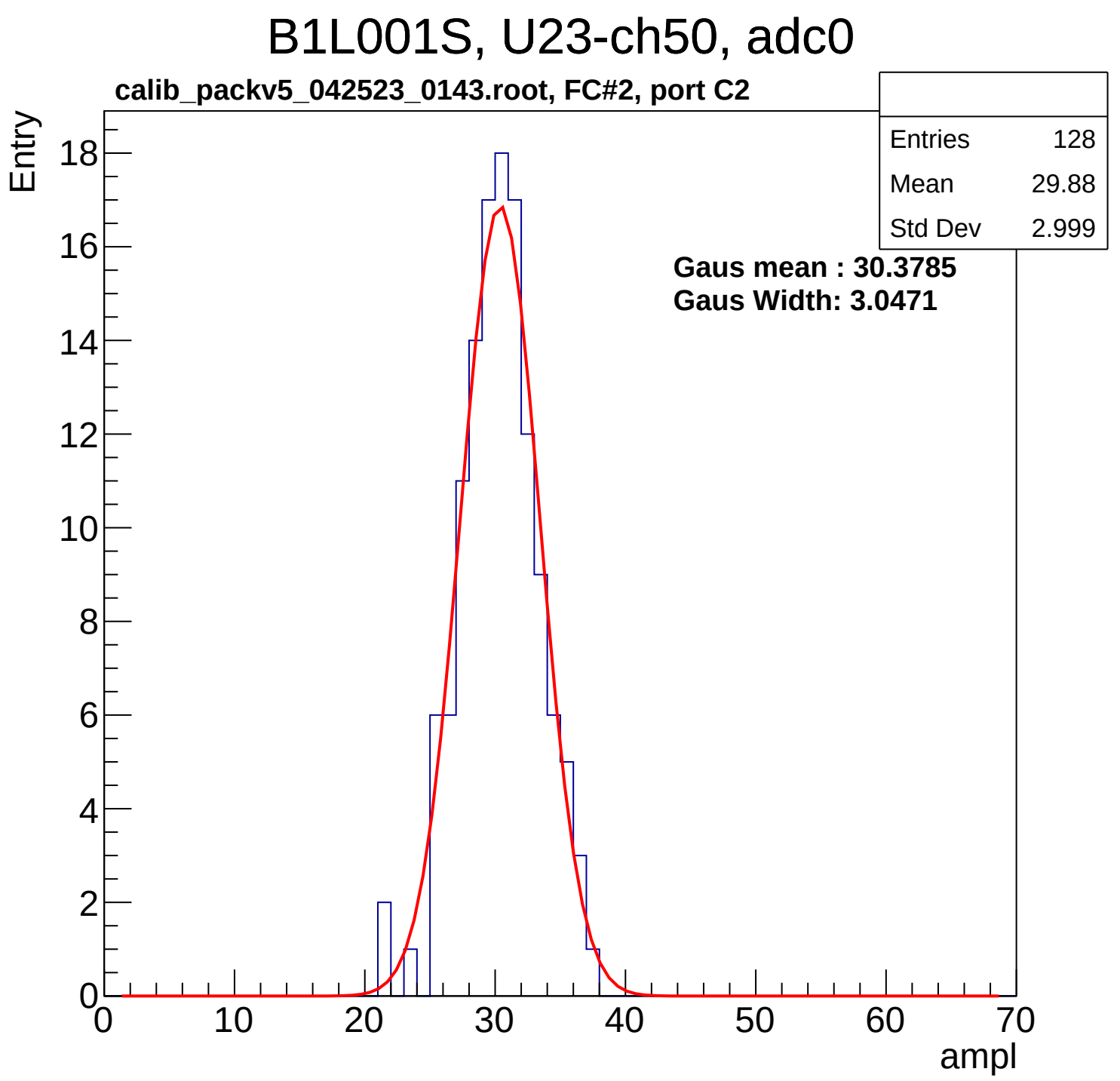
B1L001S, U23-ch50, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 29.88
Std Dev 2.999
Gaus mean : 30.3785
Gaus Width: 3.0471
Entry
ampl
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6
8
10
12
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16
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0
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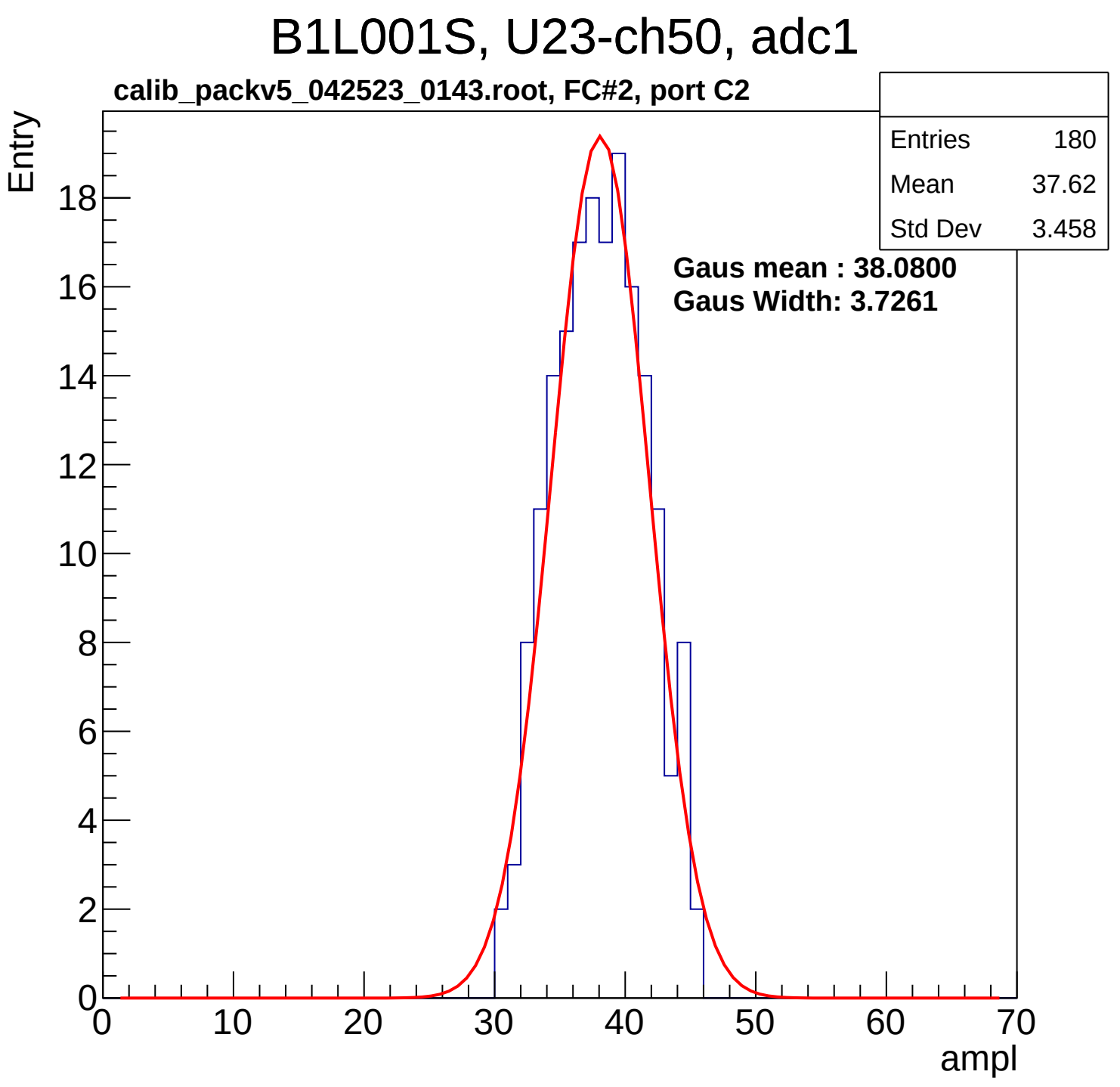
B1L001S, U23-ch50, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 180
Mean 37.62
Std Dev 3.458
Gaus mean : 38.0800
Gaus Width: 3.7261
Entry
ampl
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4
6
8
10
12
14
16
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0
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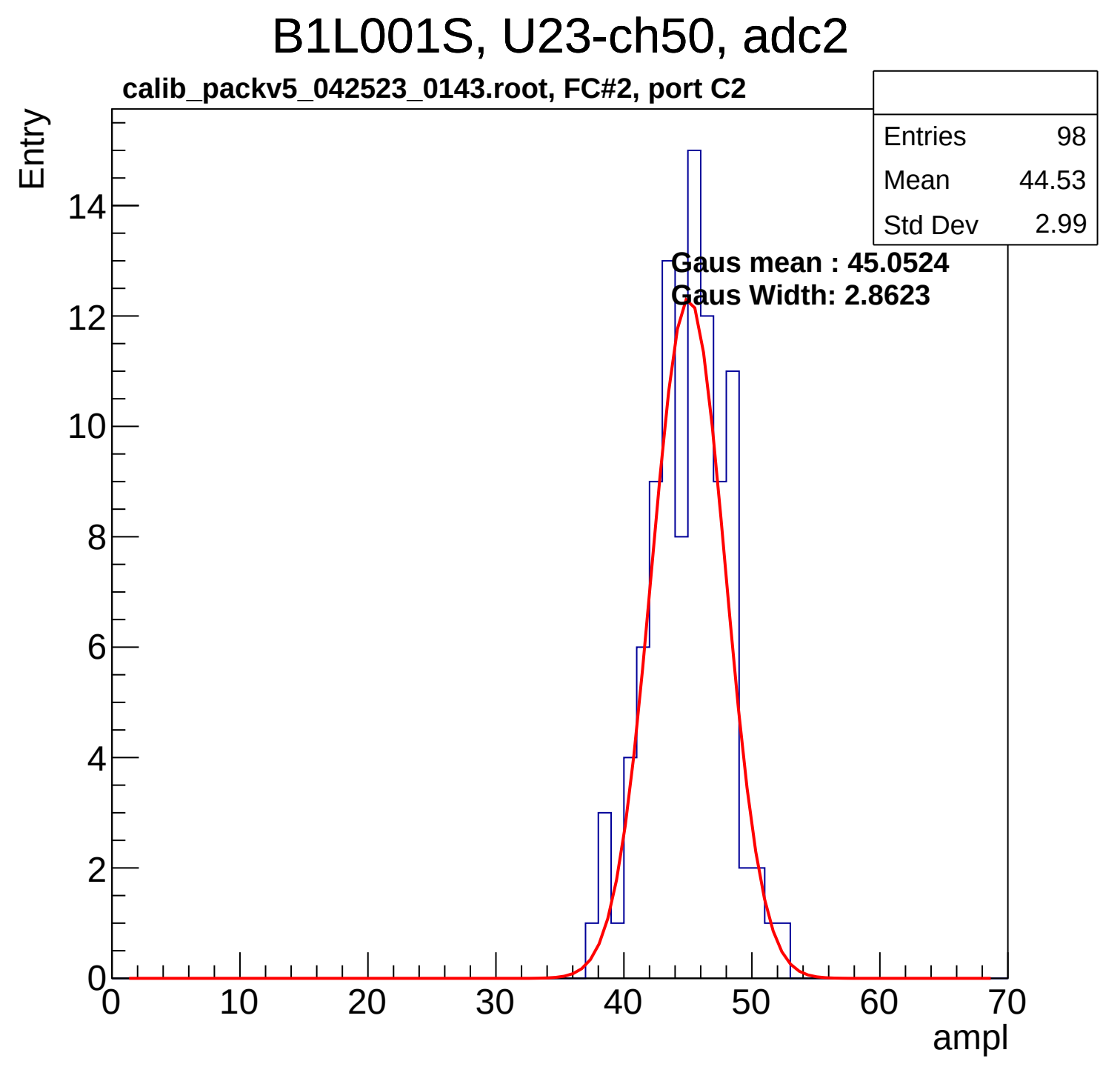
B1L001S, U23-ch50, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 44.53
Std Dev 2.99
Gaus mean : 45.0524
Gaus Width: 2.8623
Entry
ampl
0
2
4
6
8
10
12
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0
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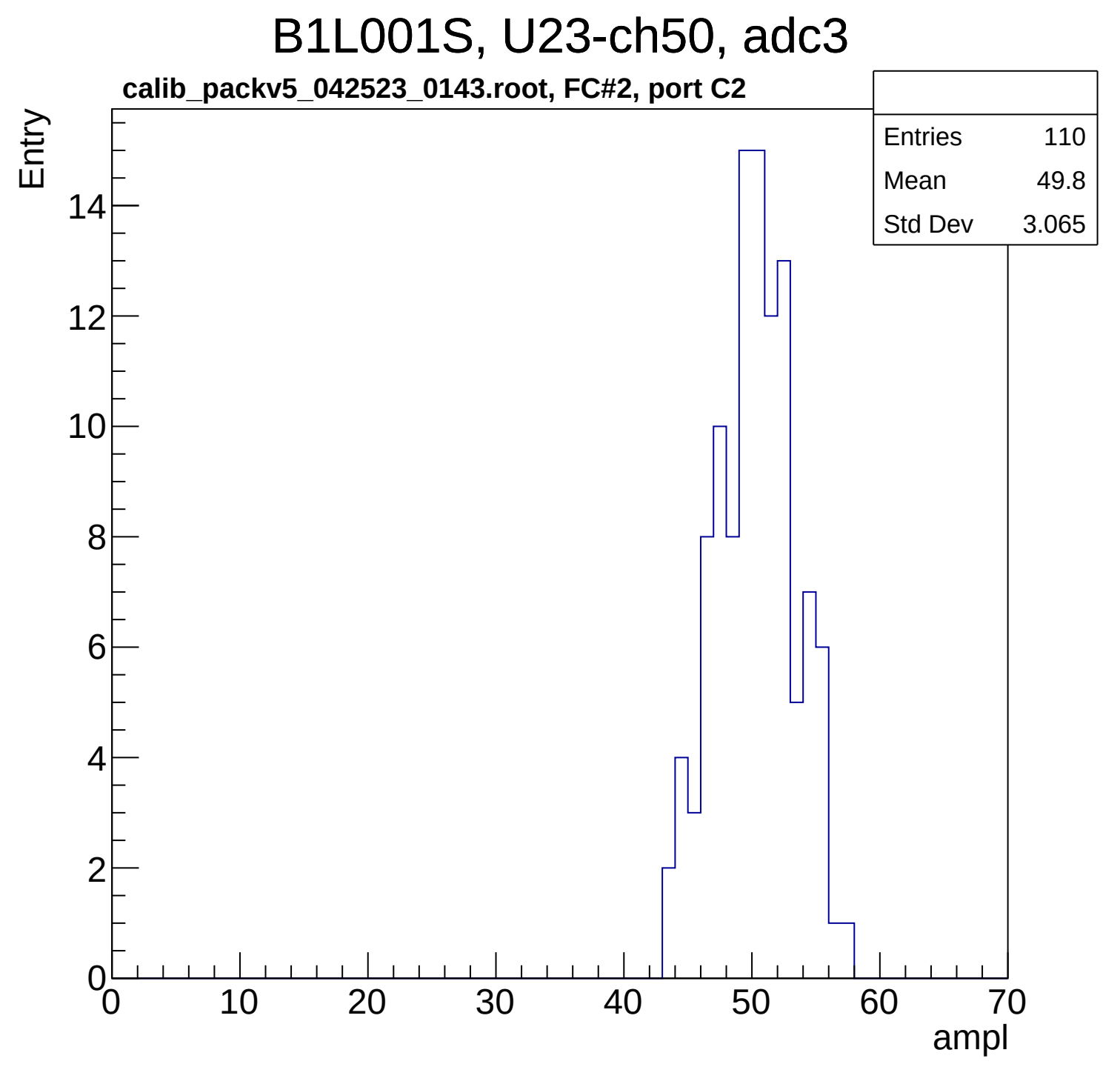
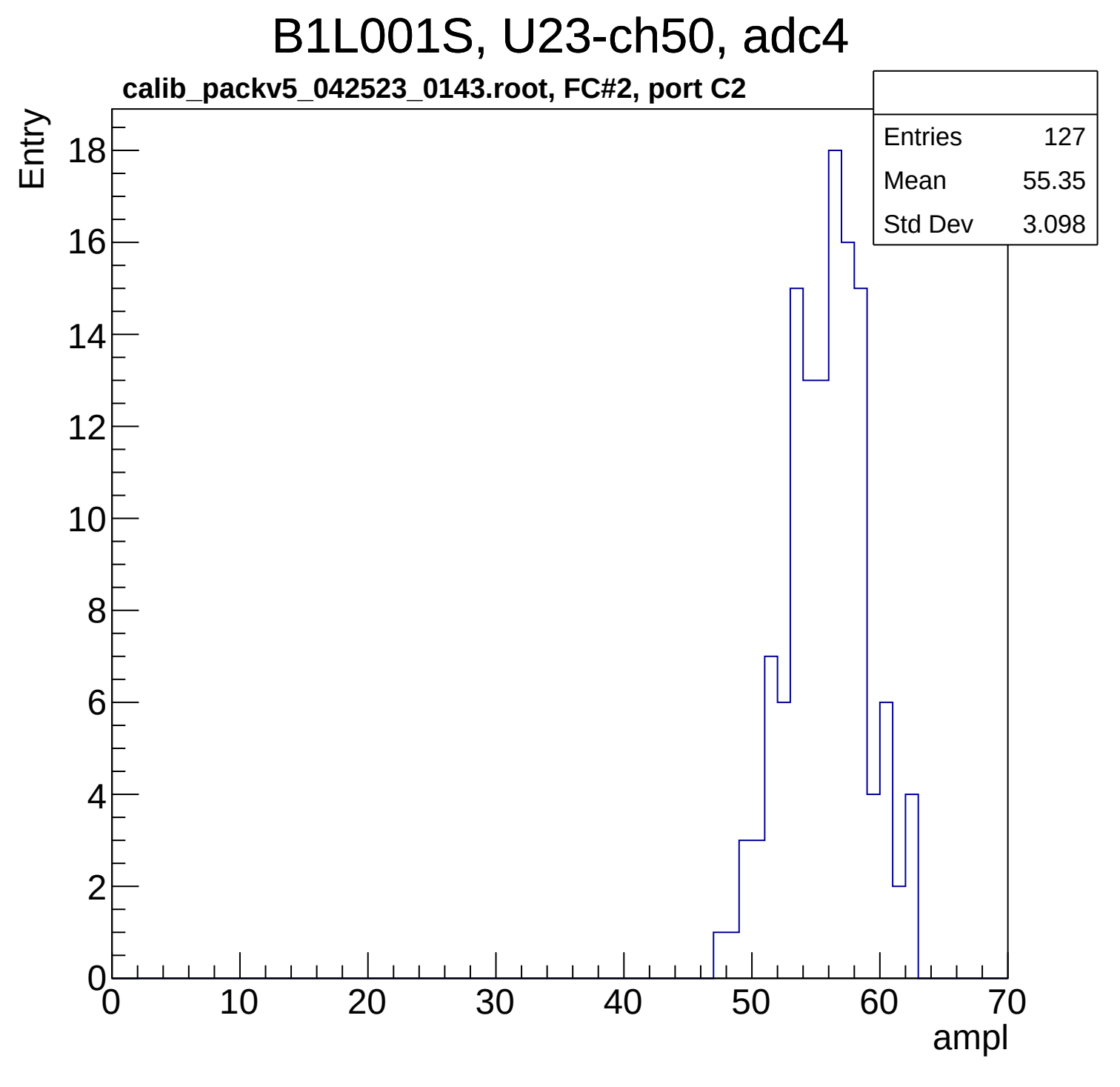

B1L001S, U23-ch50, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.8
Std Dev 3.065
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch50, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 55.35
Std Dev 3.098
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
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60
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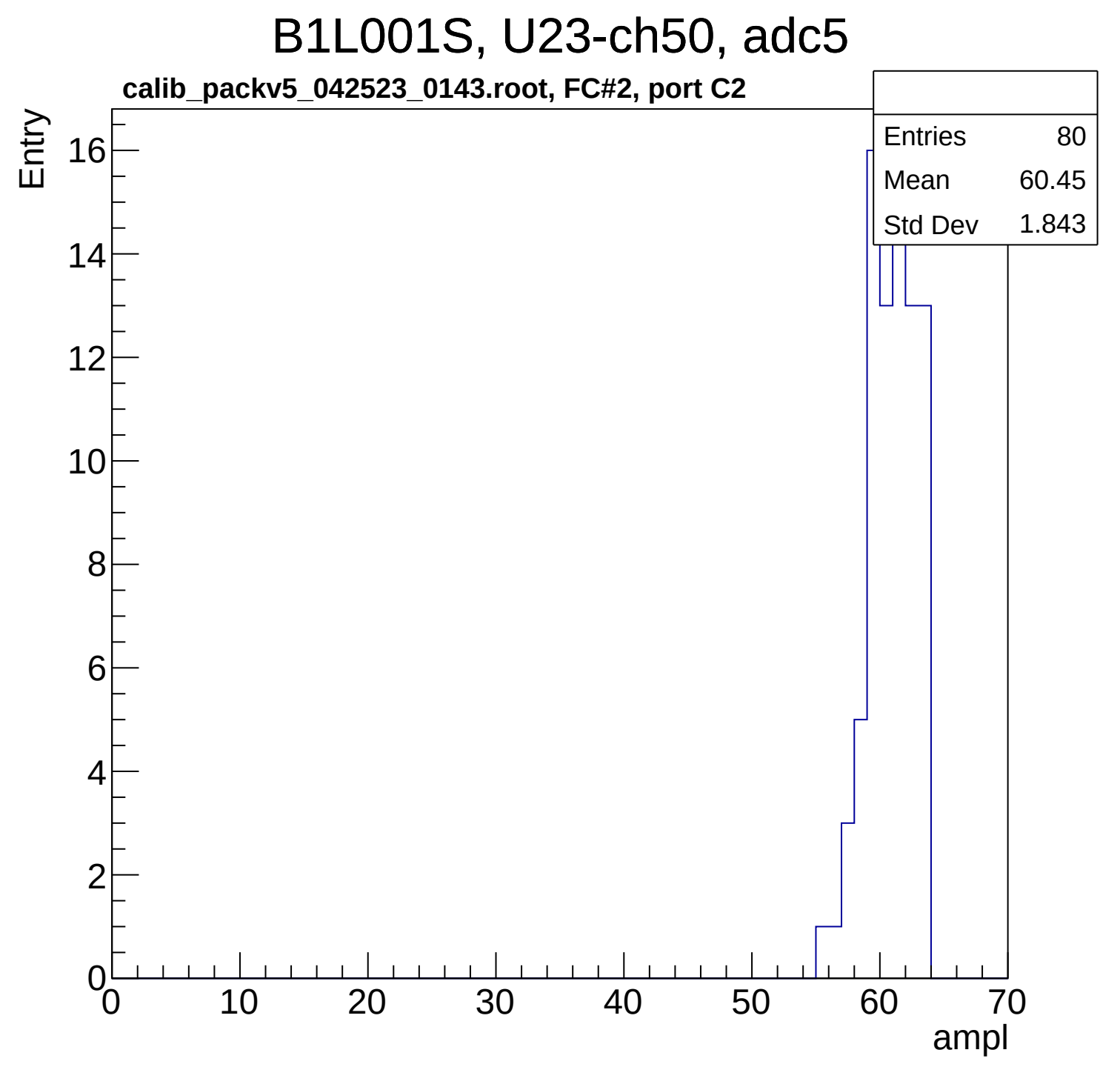
B1L001S, U23-ch50, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 60.45
Std Dev 1.843
Entry
ampl
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2
4
6
8
10
12
14
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0
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20
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40
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60
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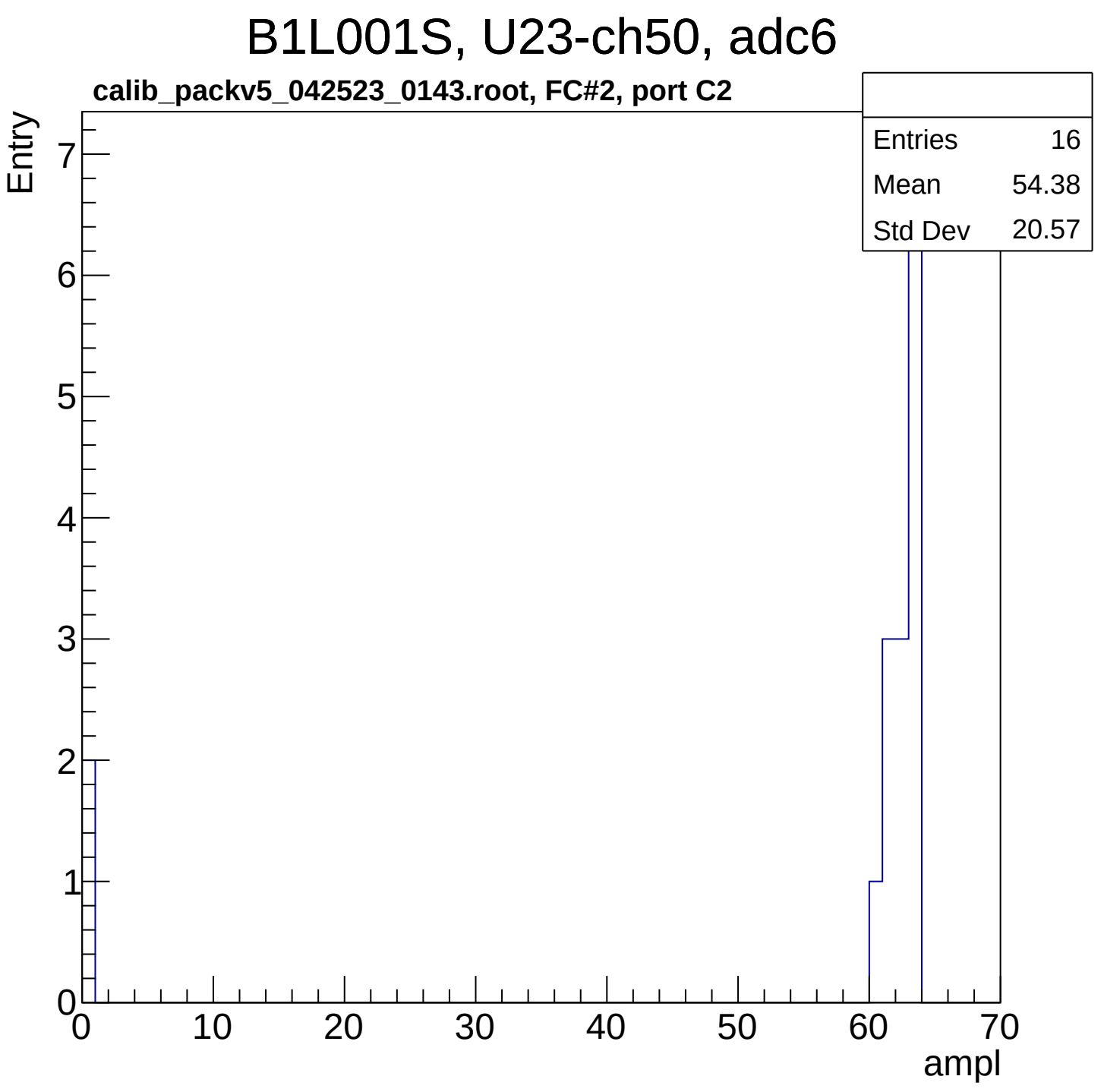
B1L001S, U23-ch50, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 54.38
Std Dev 20.57
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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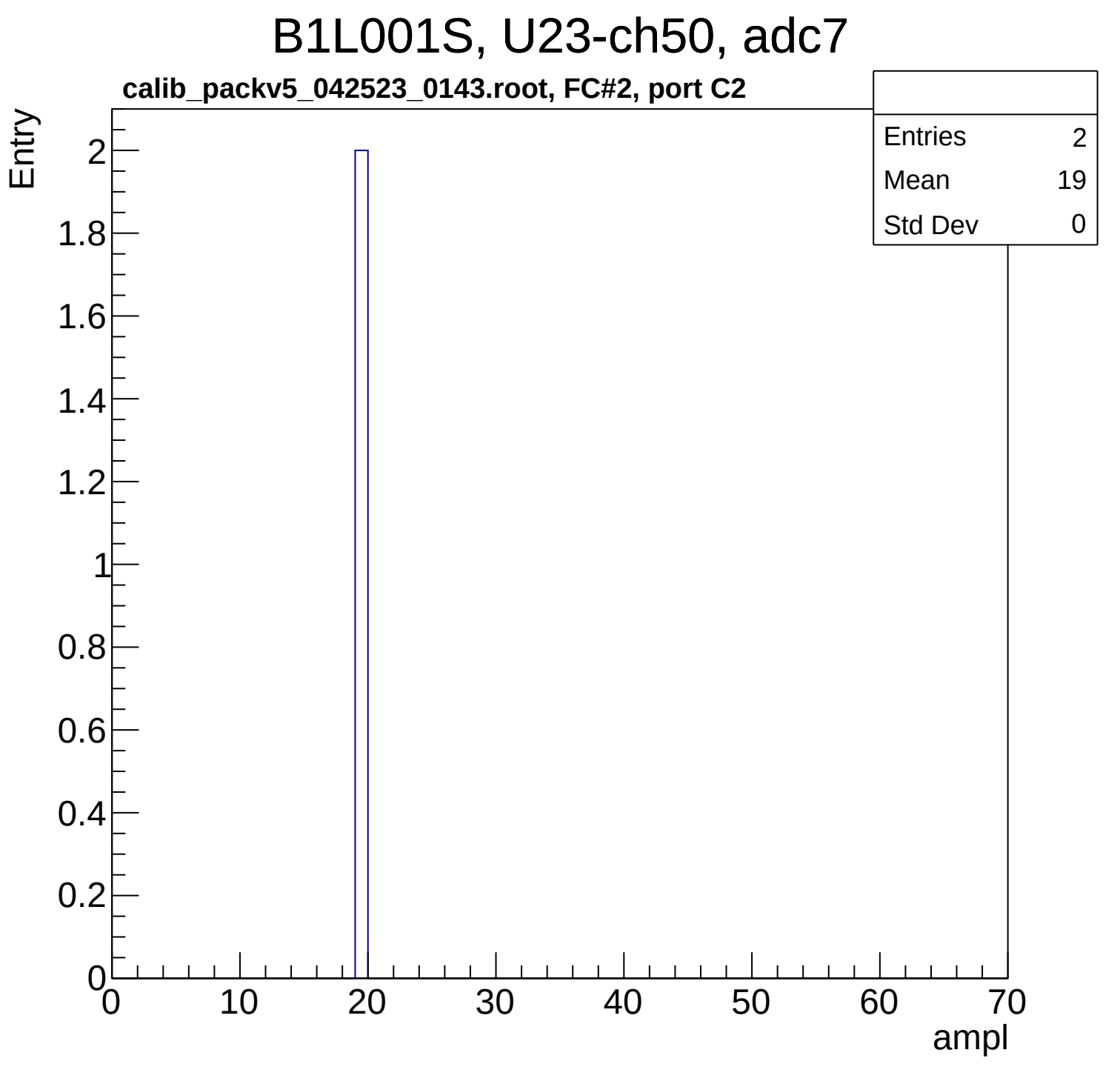
B1L001S, U23-ch50, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 19
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
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40
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60
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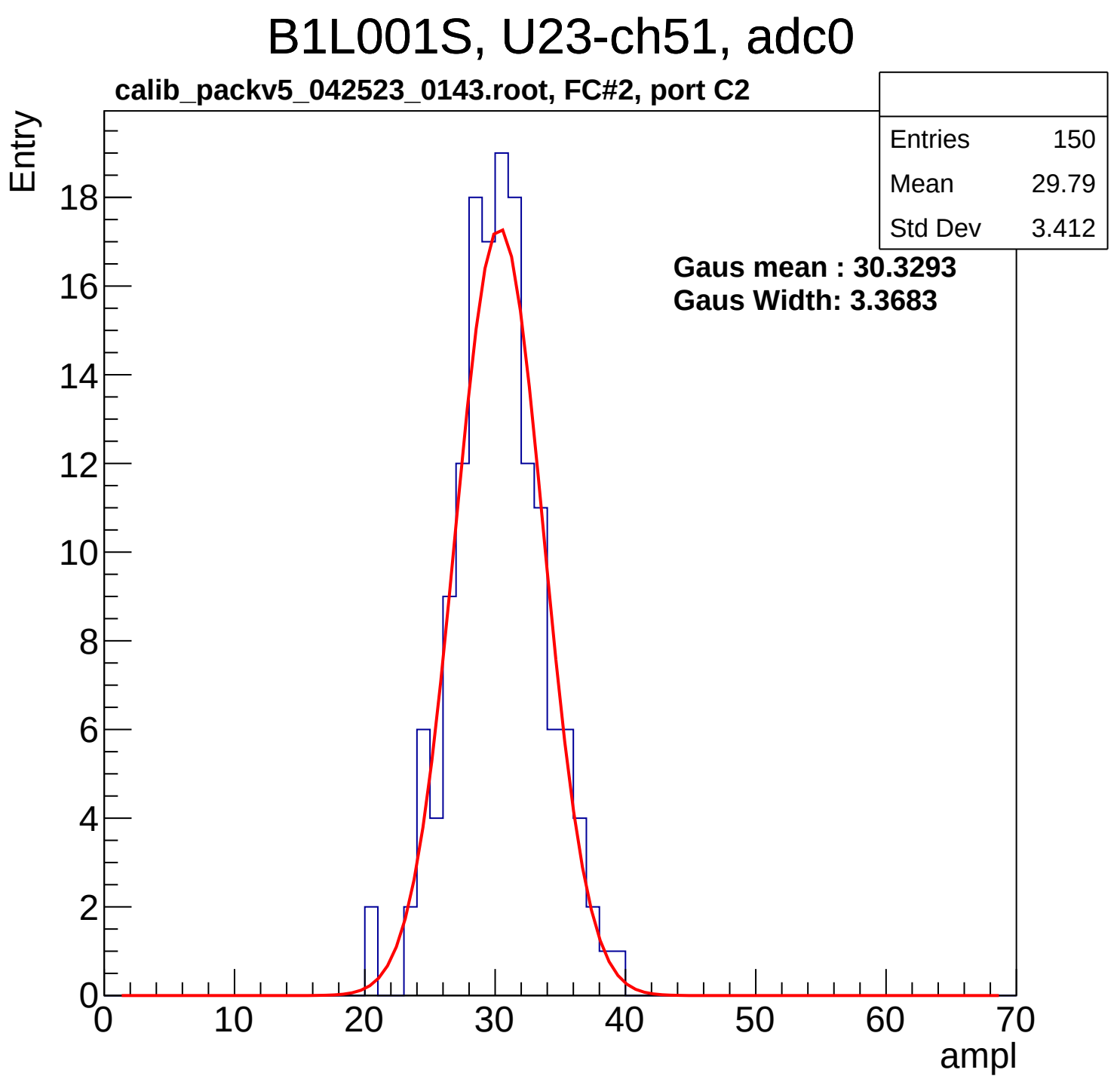
B1L001S, U23-ch51, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 29.79
Std Dev 3.412
Gaus mean : 30.3293
Gaus Width: 3.3683
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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60
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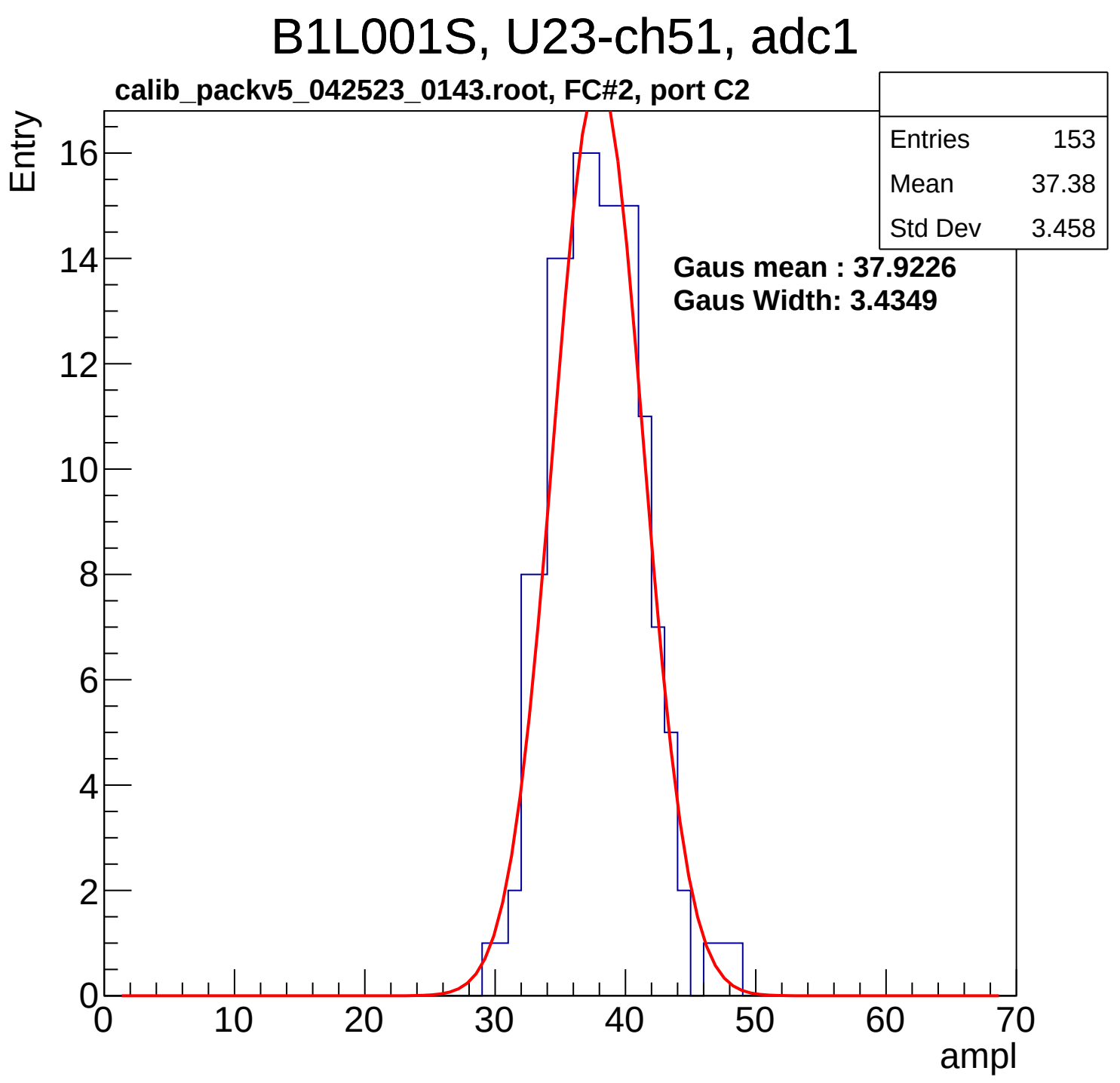
B1L001S, U23-ch51, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 37.38
Std Dev 3.458
Gaus mean : 37.9226
Gaus Width: 3.4349
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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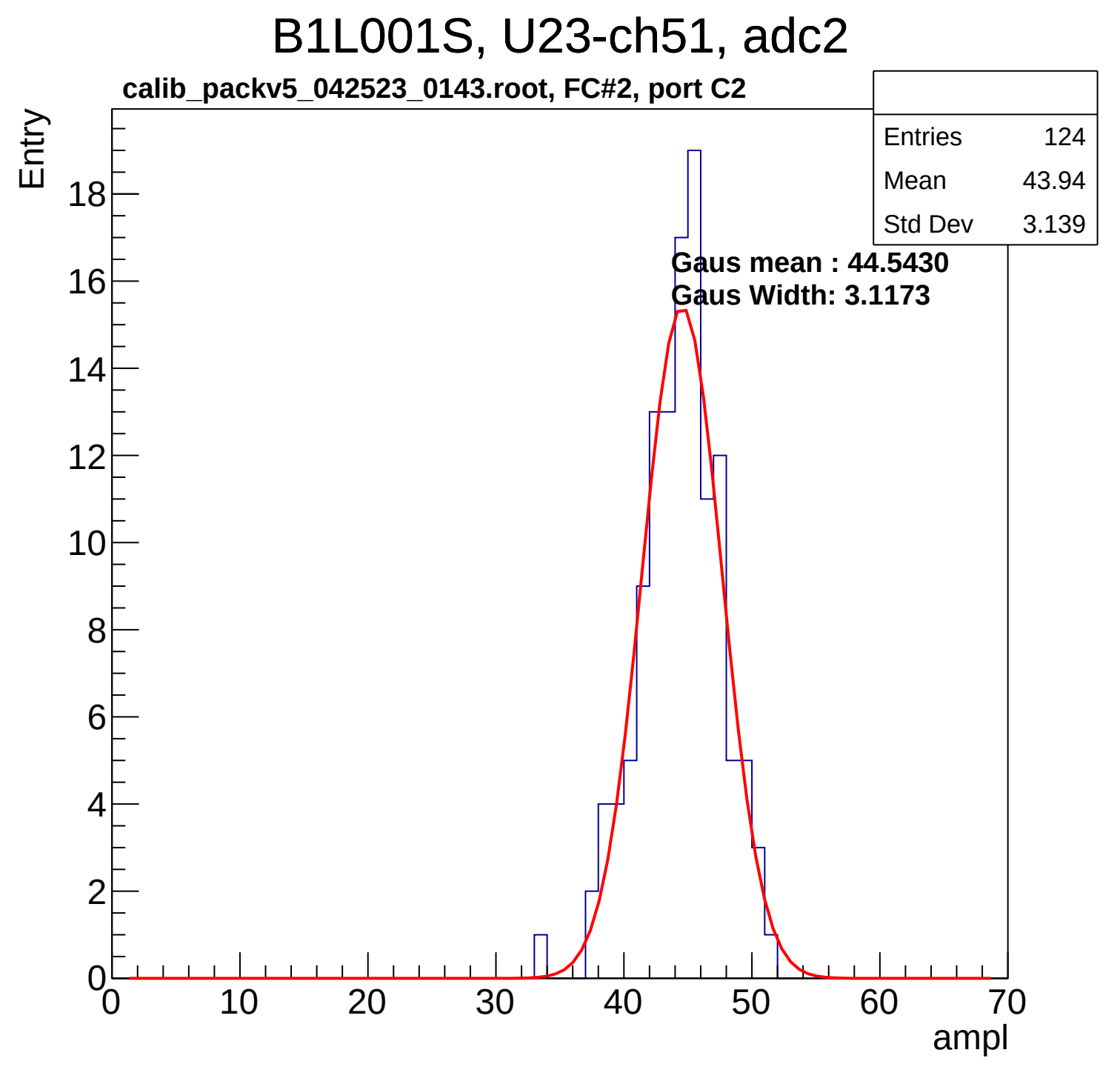
B1L001S, U23-ch51, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 43.94
Std Dev 3.139
Gaus mean : 44.5430
Gaus Width: 3.1173
Entry
ampl
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4
6
8
10
12
14
16
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0
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60
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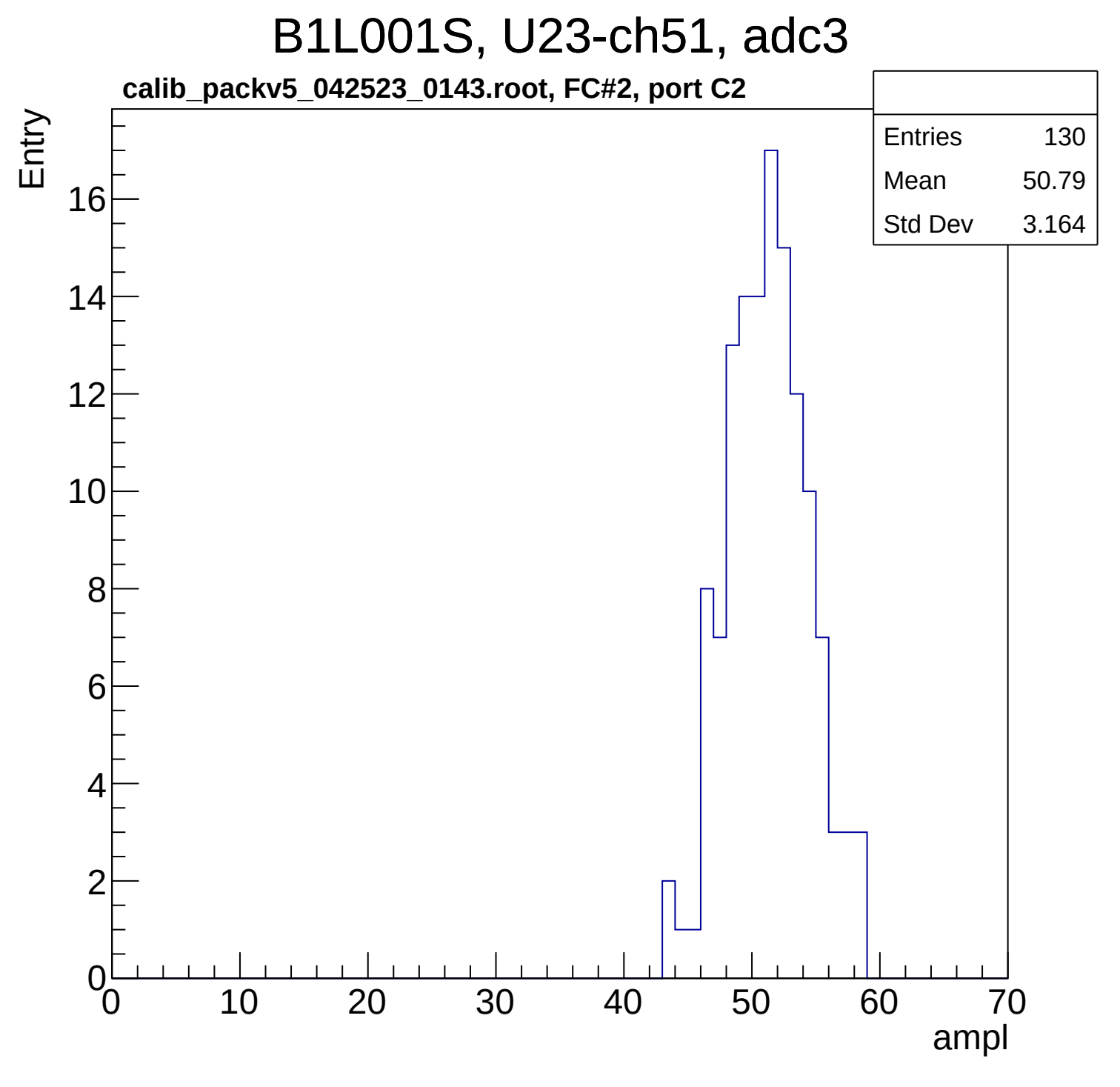
B1L001S, U23-ch51, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 50.79
Std Dev 3.164
Entry
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4
6
8
10
12
14
16
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10
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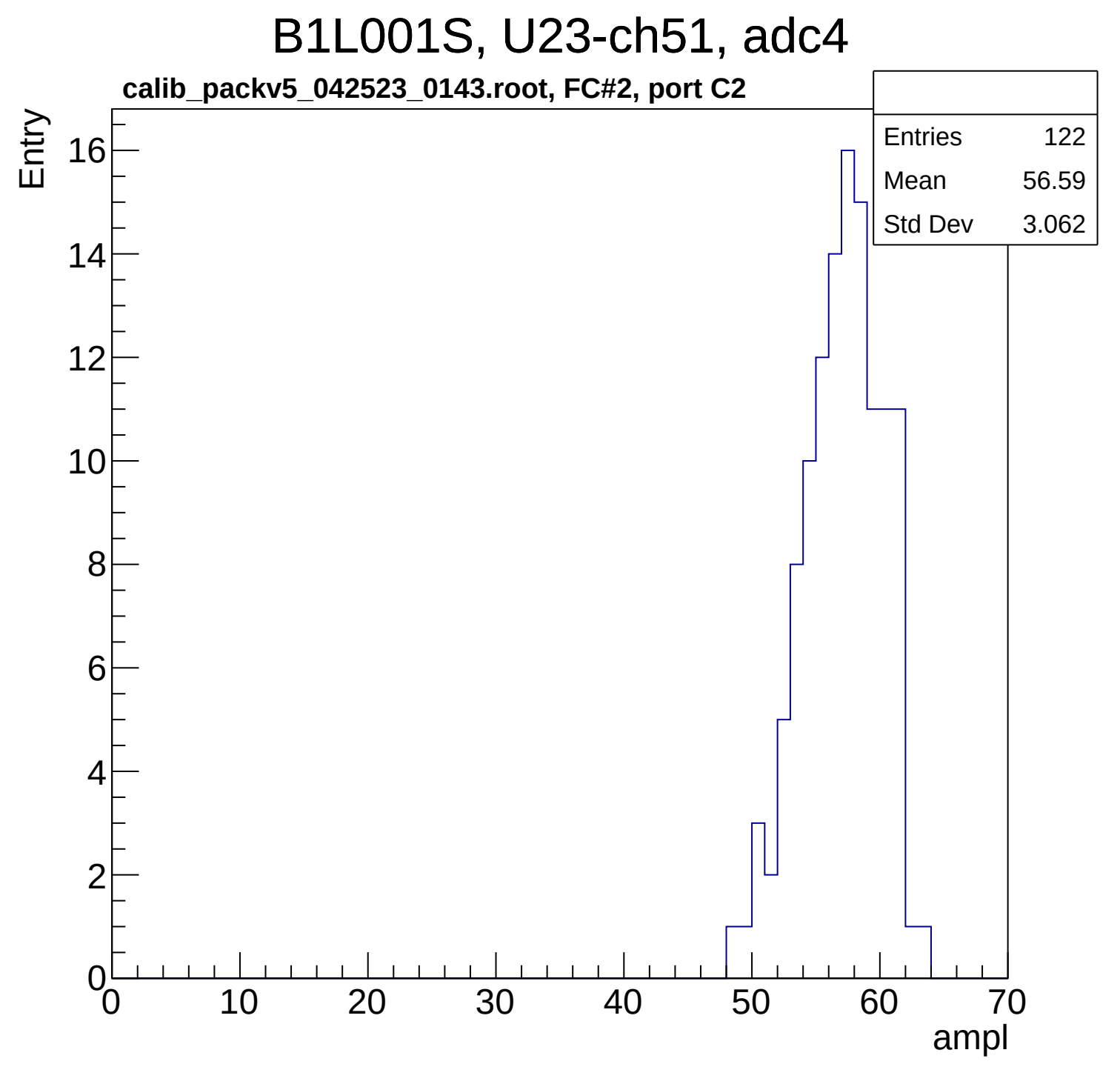
50
60
70
ampl


B1L001S, U23-ch51, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 56.59
Std Dev 3.062
Entry
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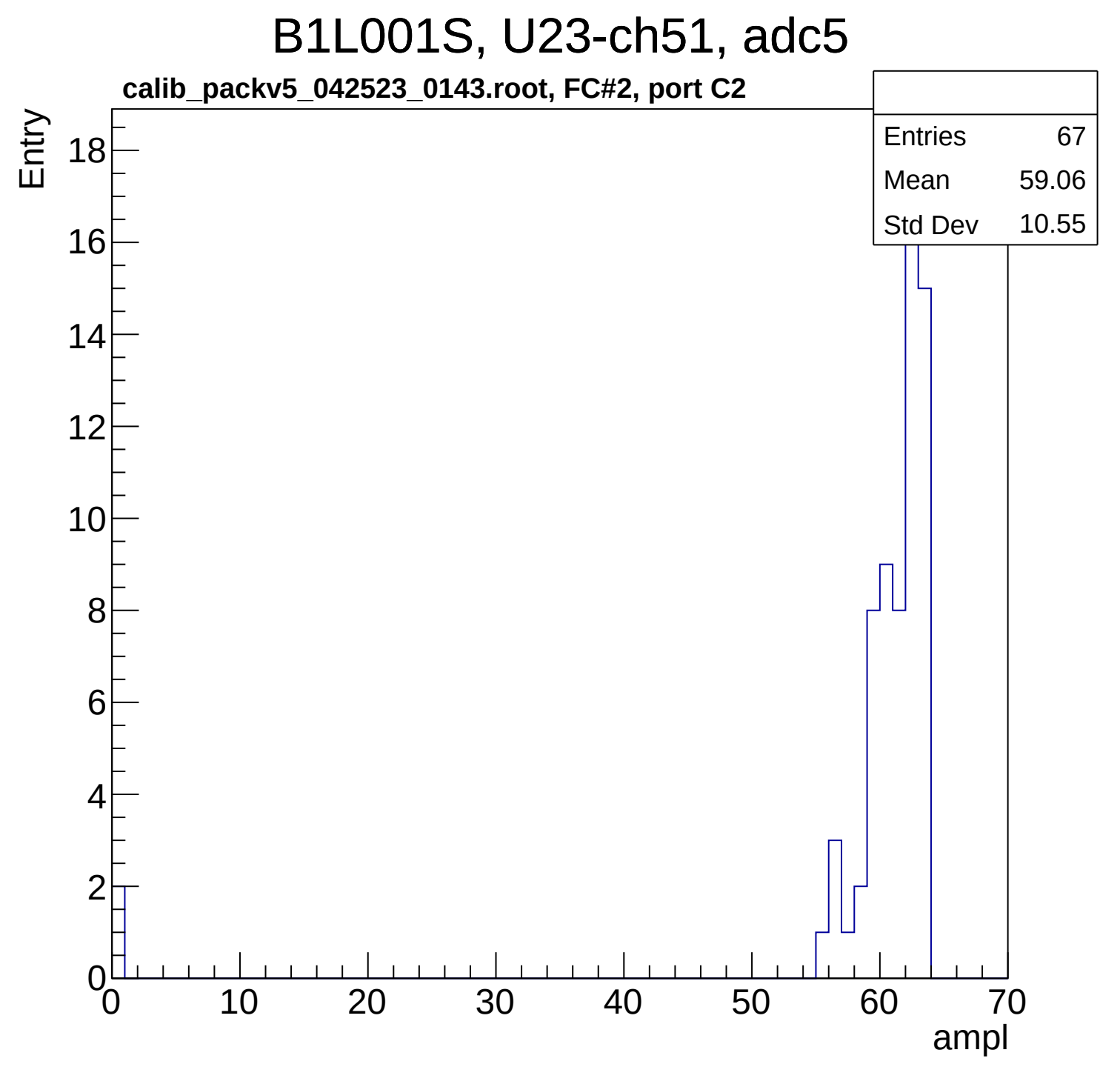
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch51, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 67
Mean 59.06
Std Dev 10.55
Entry
ampl
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4
6
8
10
12
14
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0
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60
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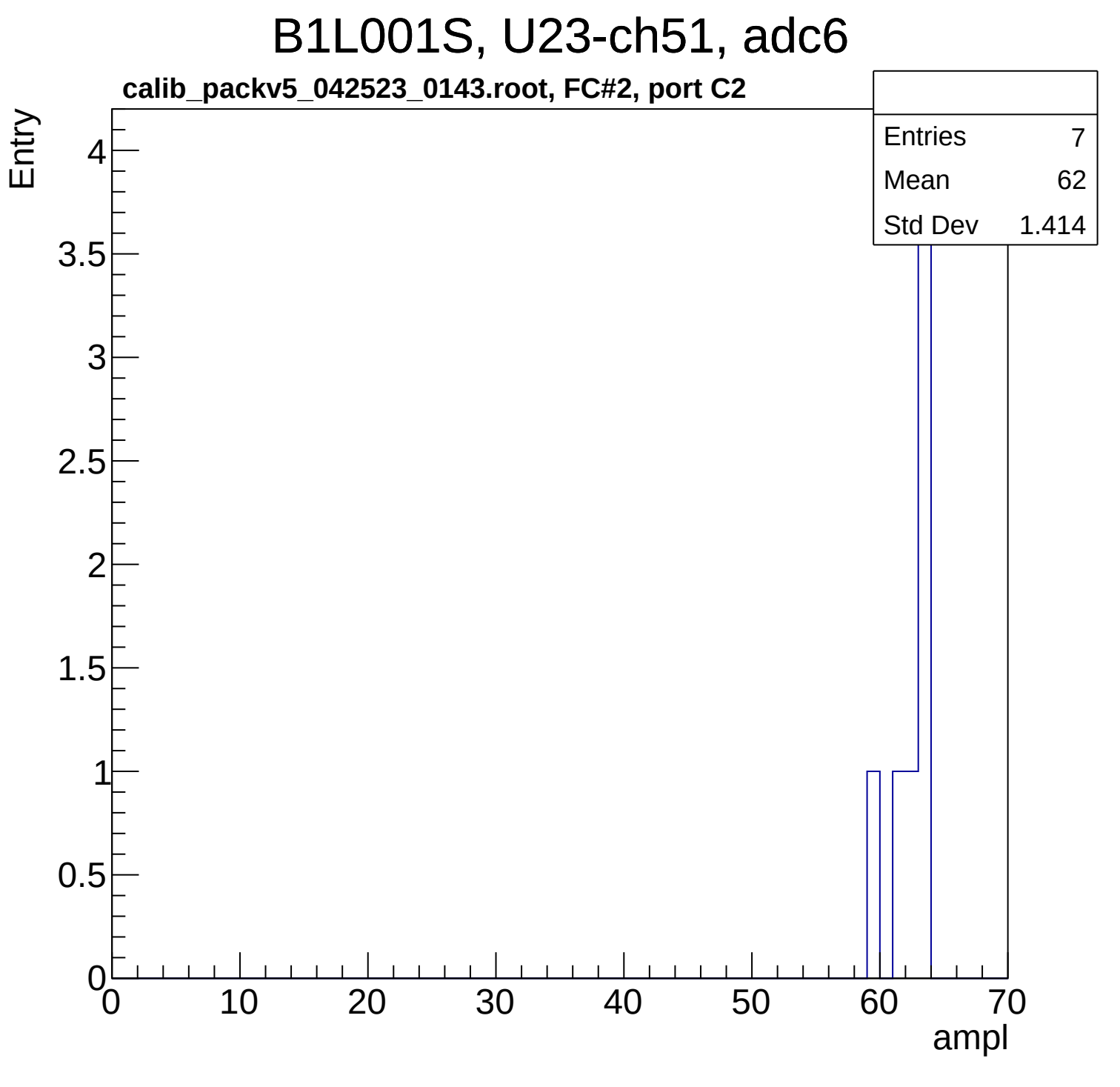
B1L001S, U23-ch51, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 62
Std Dev 1.414
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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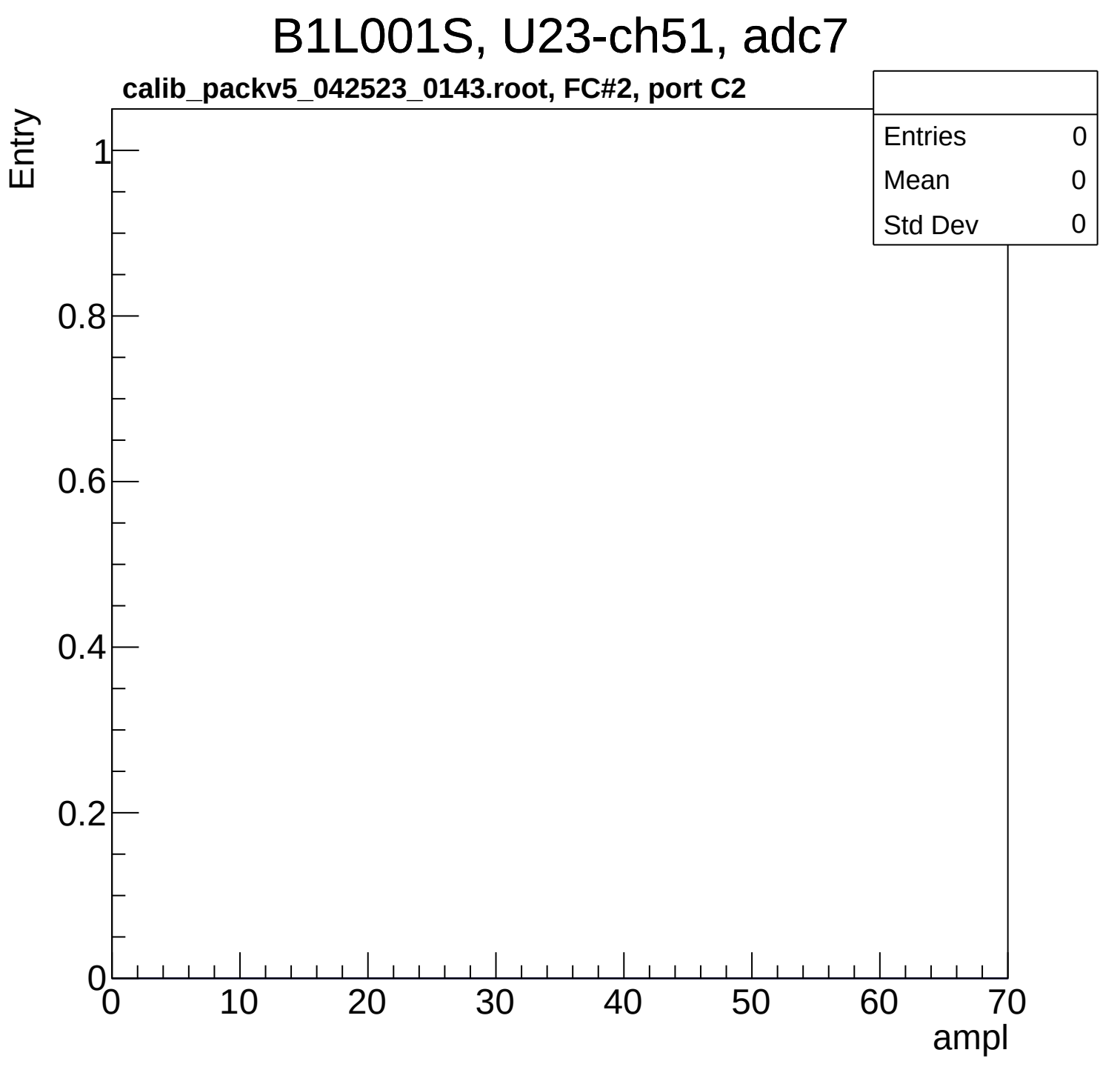
B1L001S, U23-ch51, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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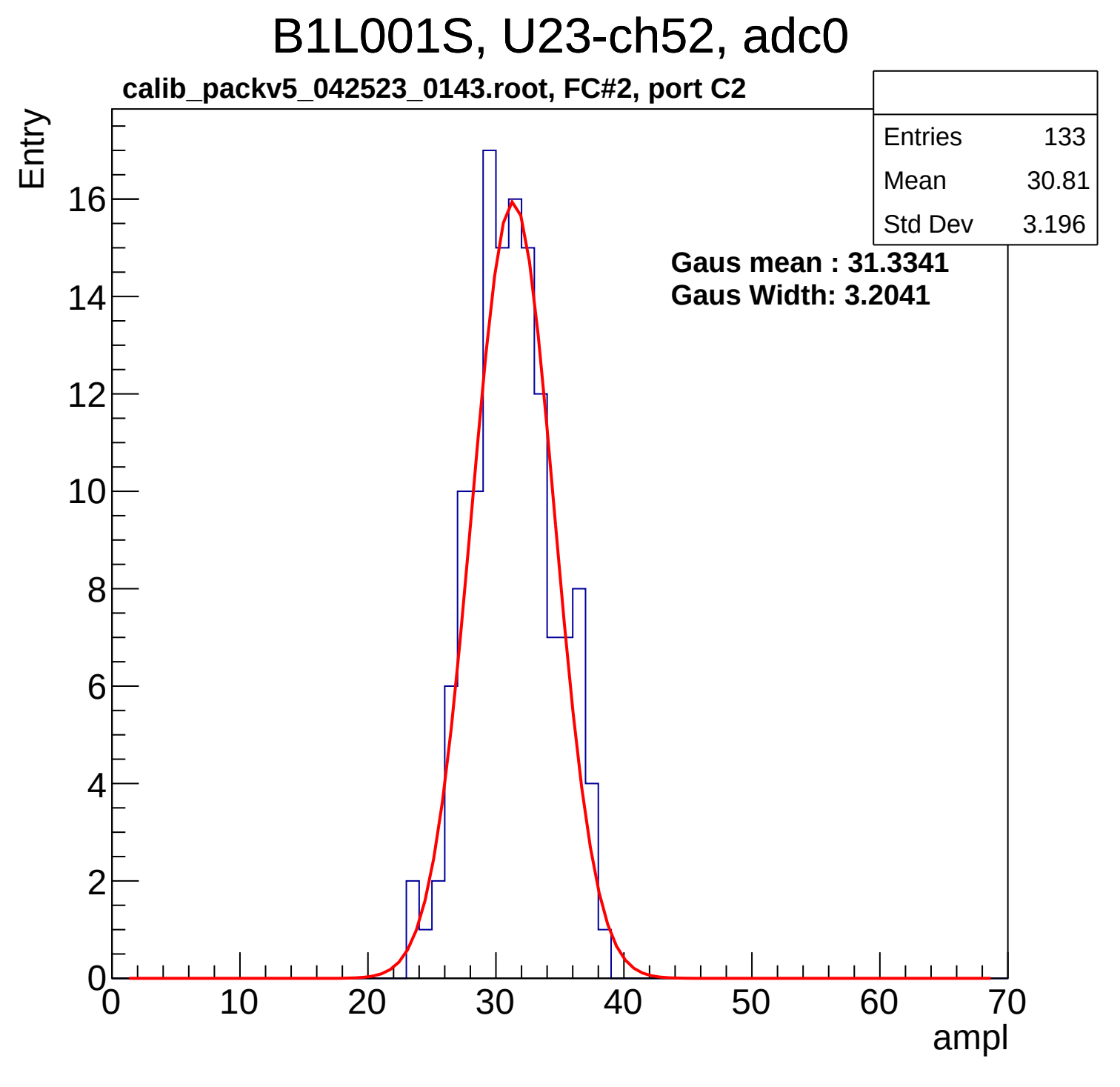
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch52, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 30.81
Std Dev 3.196
Gaus mean : 31.3341
Gaus Width: 3.2041
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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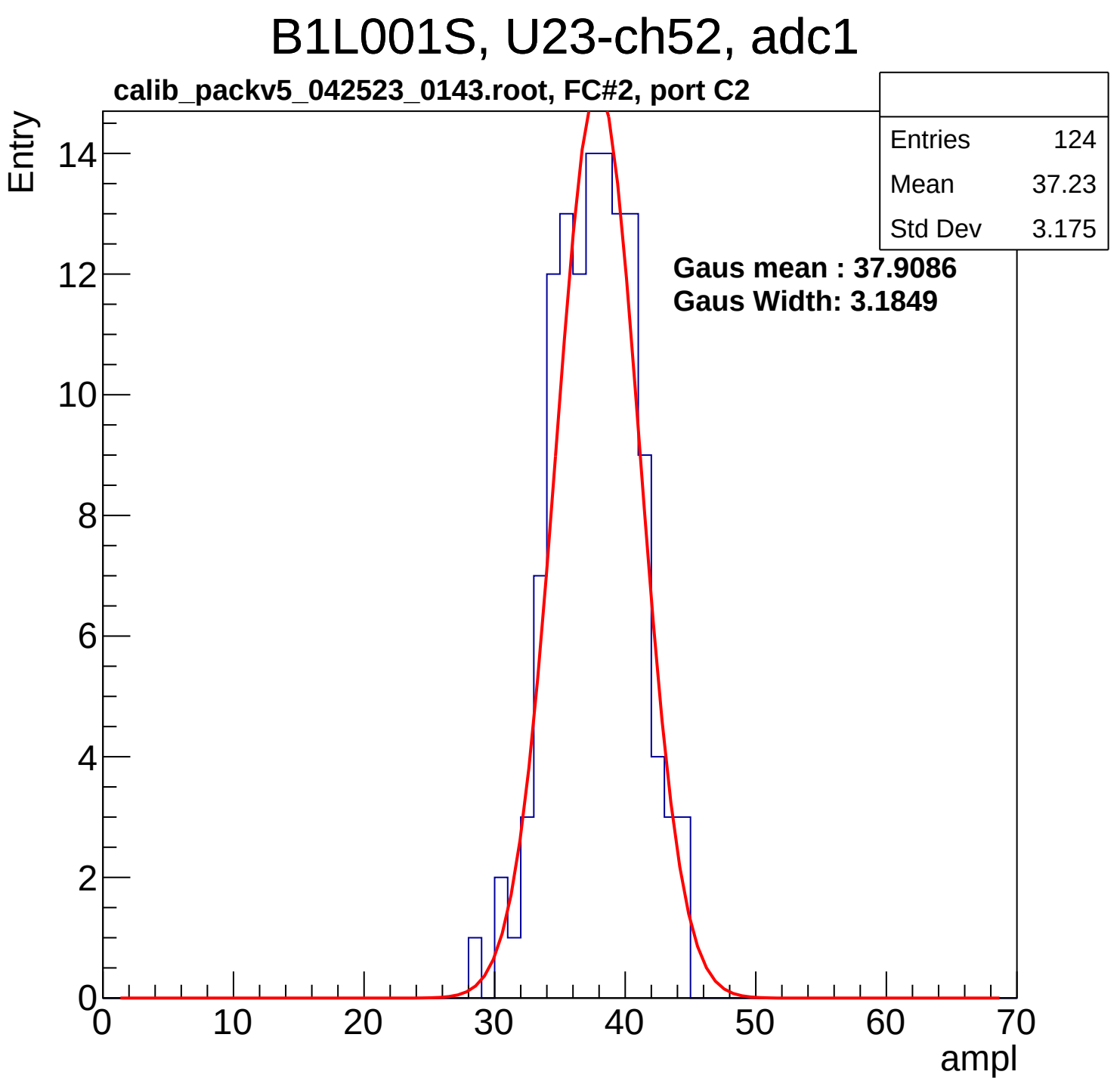
B1L001S, U23-ch52, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 37.23
Std Dev 3.175
Gaus mean : 37.9086
Gaus Width: 3.1849
Entry
ampl
0
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4
6
8
10
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0
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60
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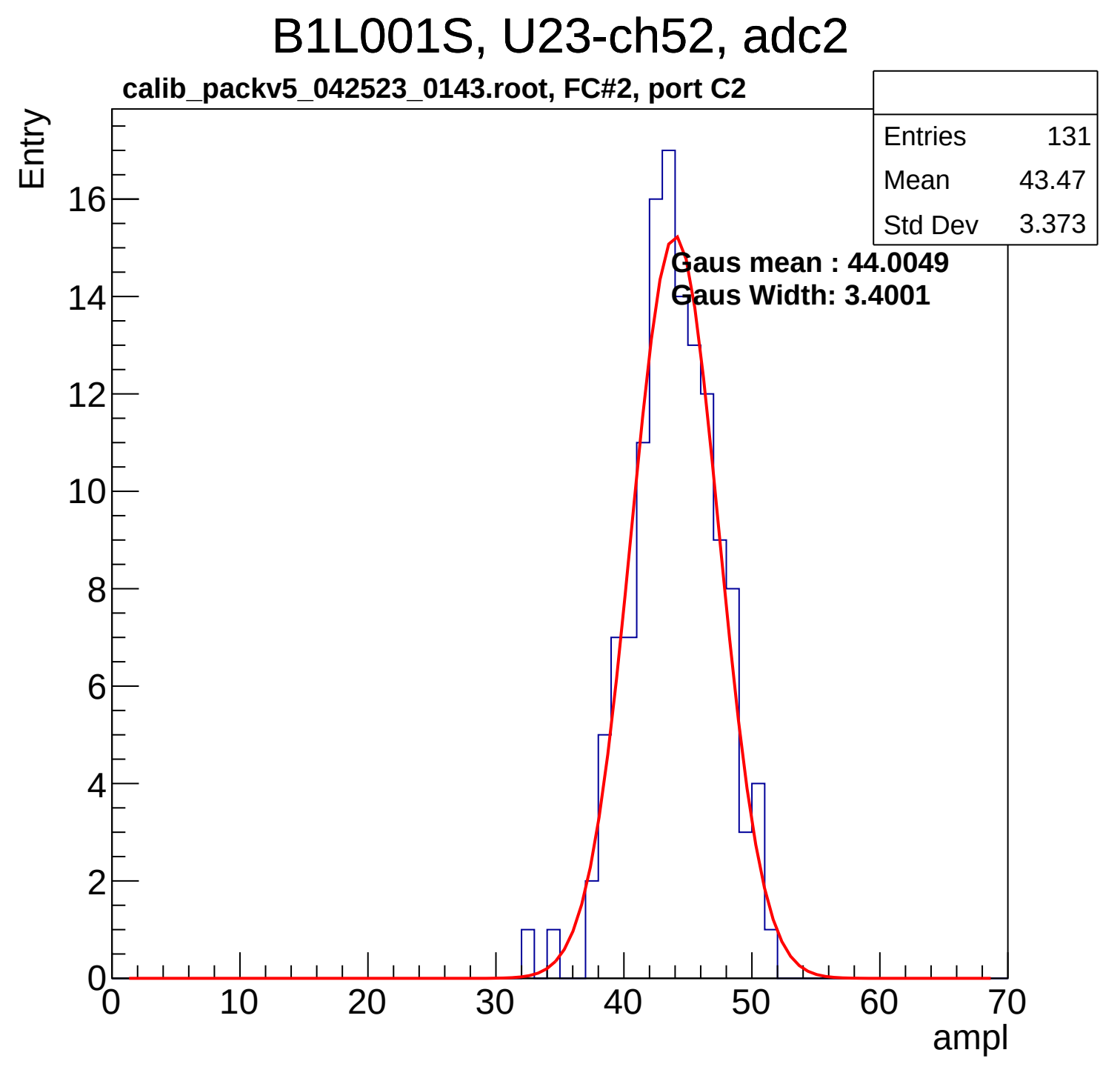
B1L001S, U23-ch52, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 43.47
Std Dev 3.373
Gaus mean : 44.0049
Gaus Width: 3.4001
Entry
ampl
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2
4
6
8
10
12
14
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0
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20
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60
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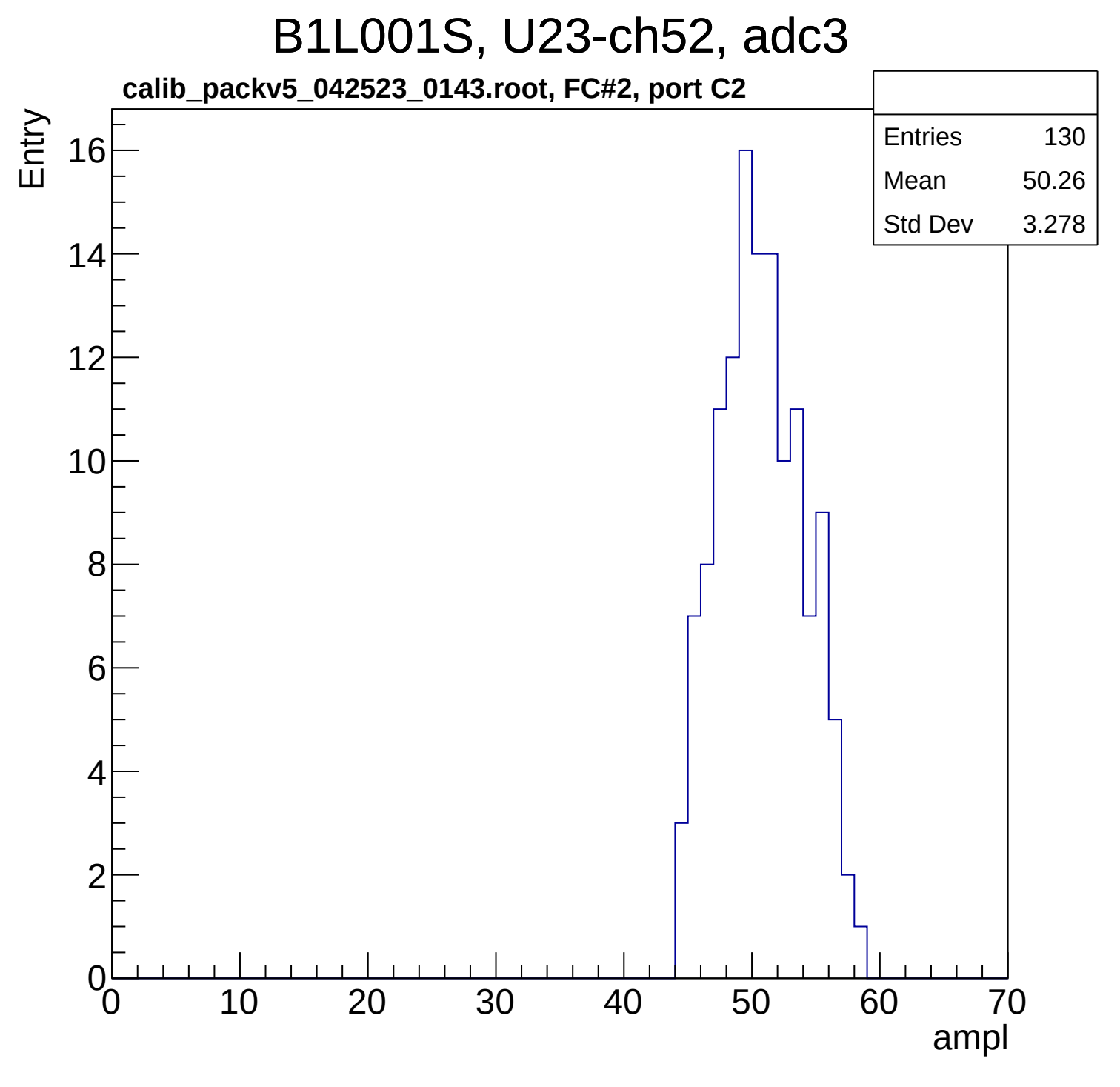
B1L001S, U23-ch52, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 50.26
Std Dev 3.278
Entry
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6
8
10
12
14
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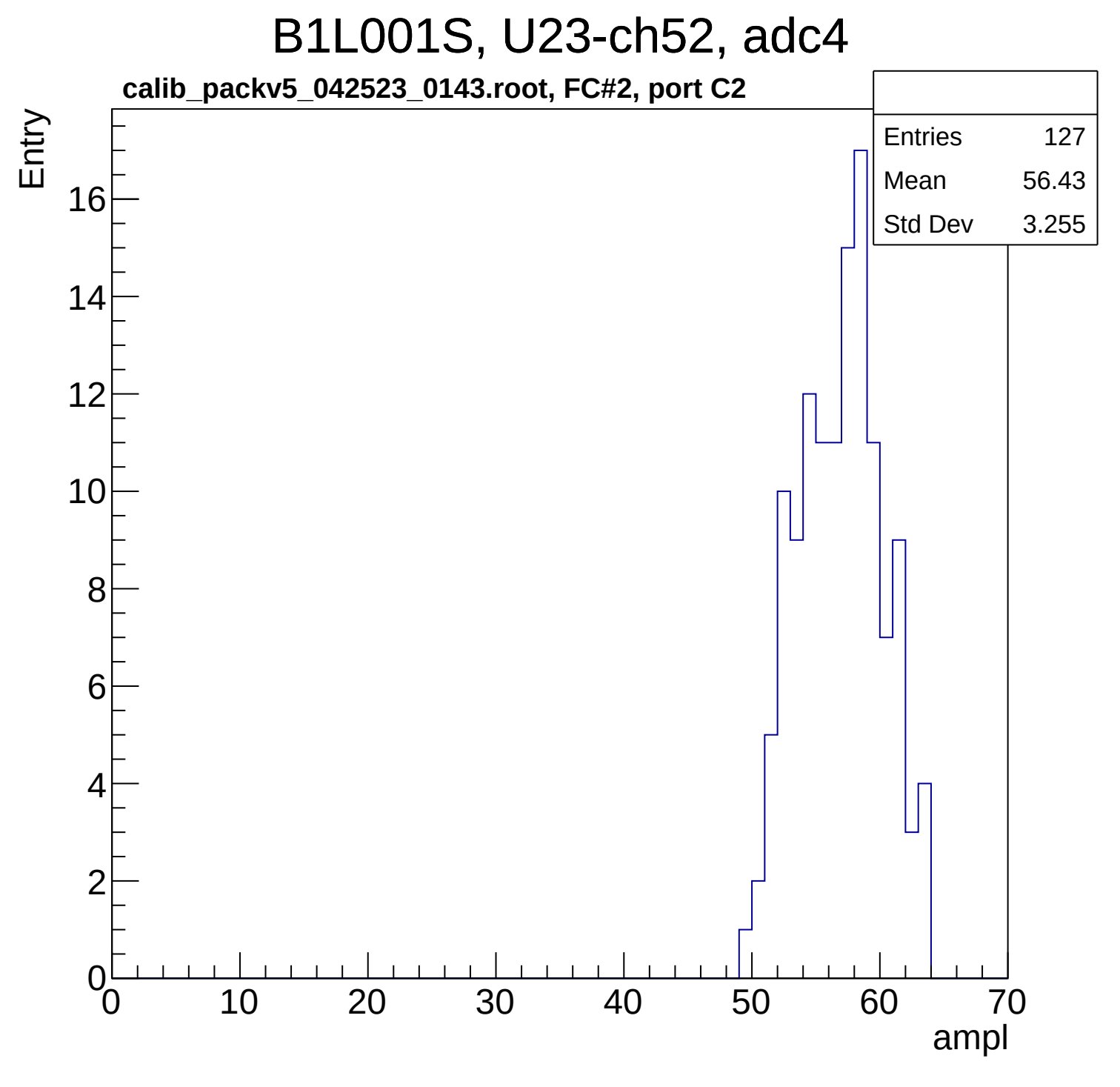
50
60
70
ampl


B1L001S, U23-ch52, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 56.43
Std Dev 3.255
Entry
ampl
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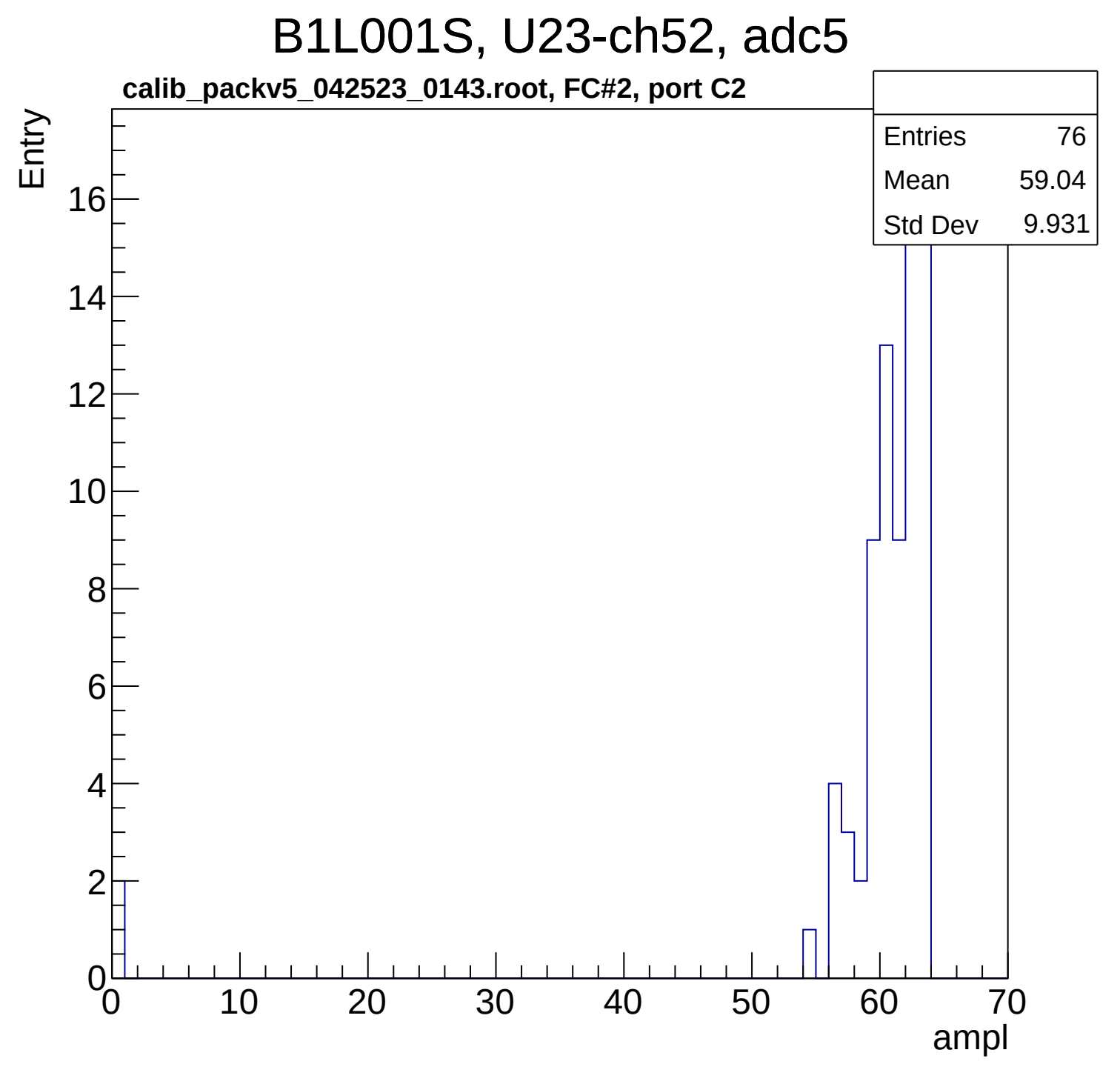
B1L001S, U23-ch52, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 59.04
Std Dev 9.931
Entry
ampl
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0
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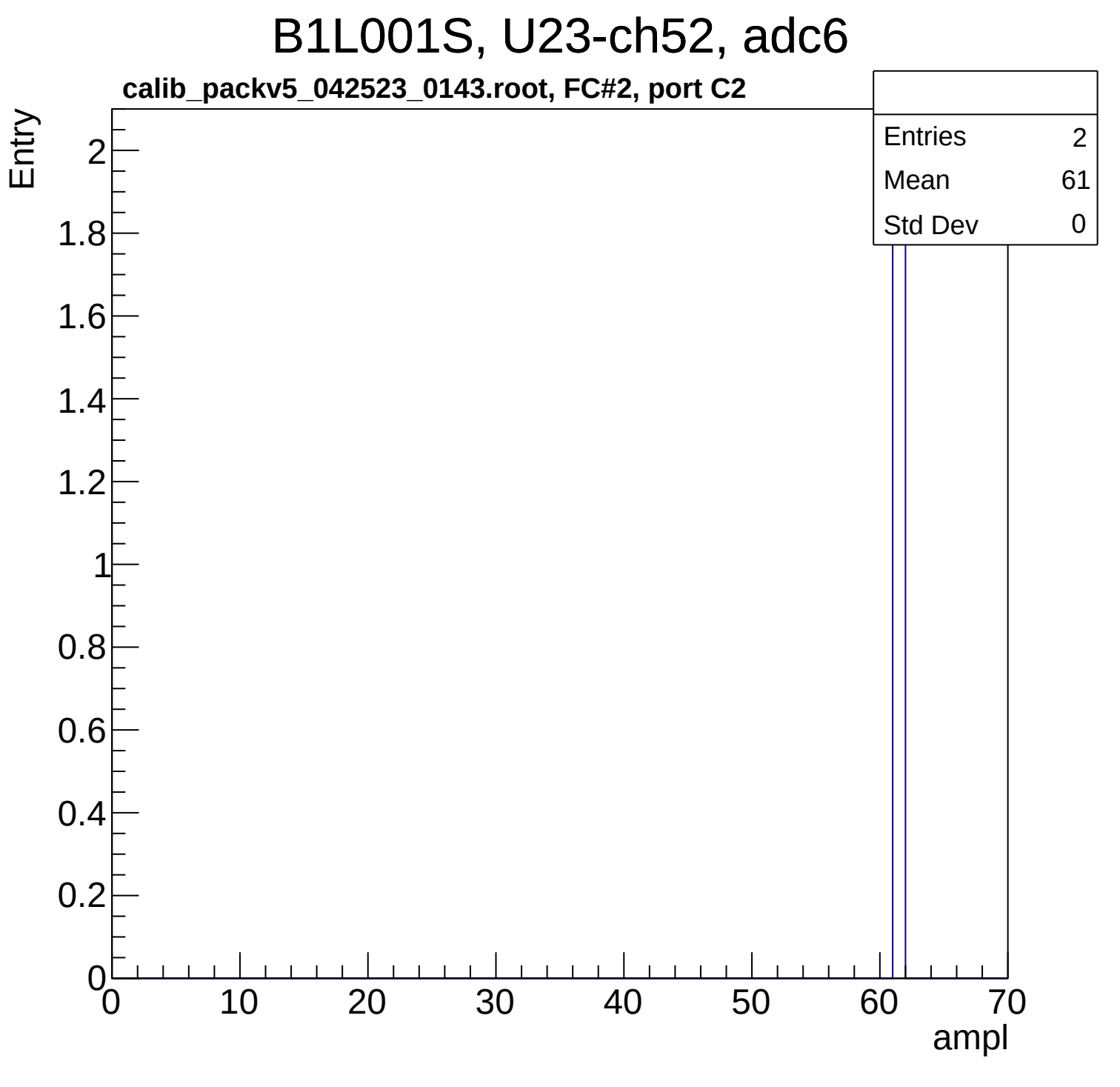
B1L001S, U23-ch52, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 61
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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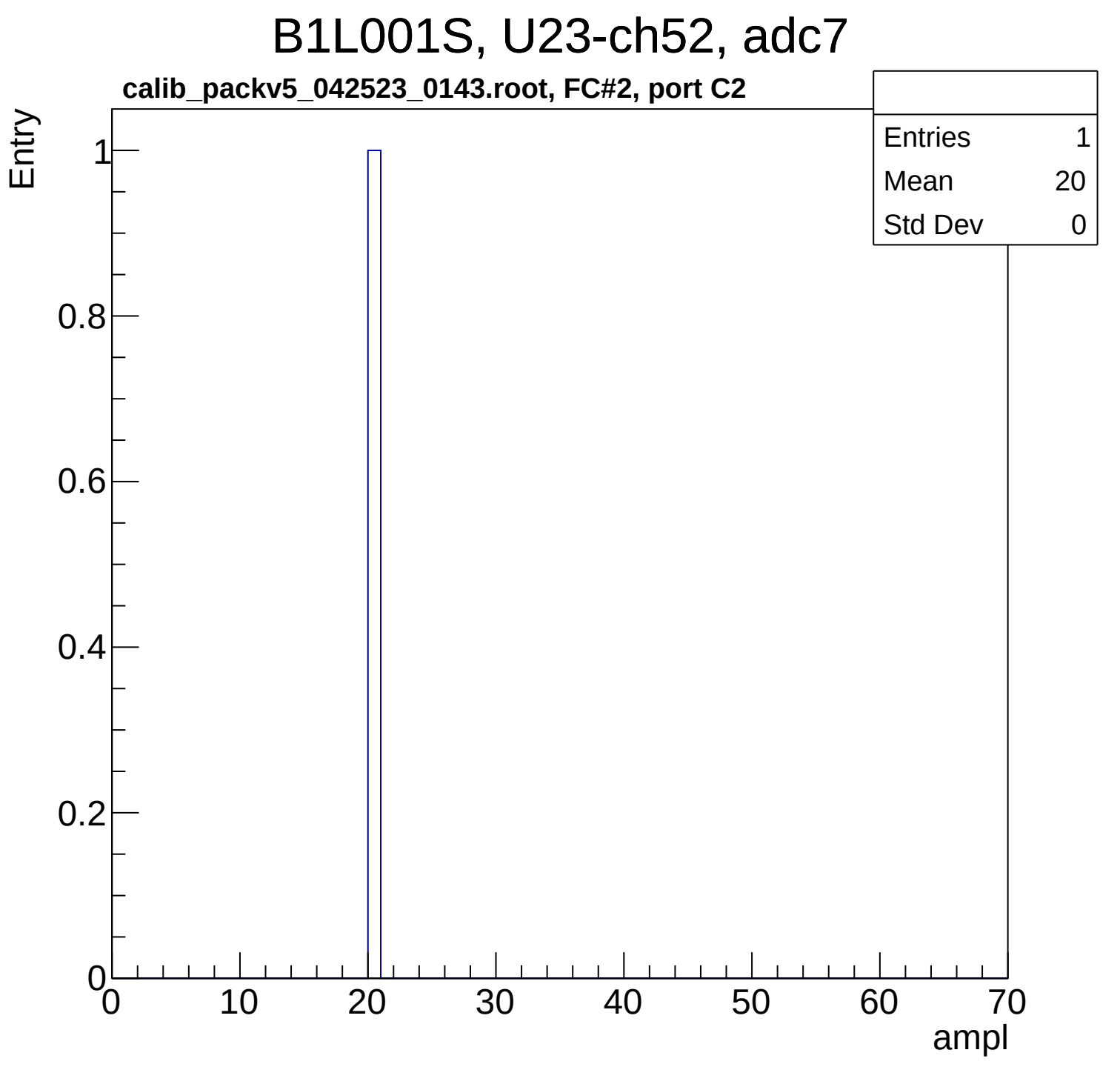
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch52, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 20
Std Dev 0
Entry
ampl
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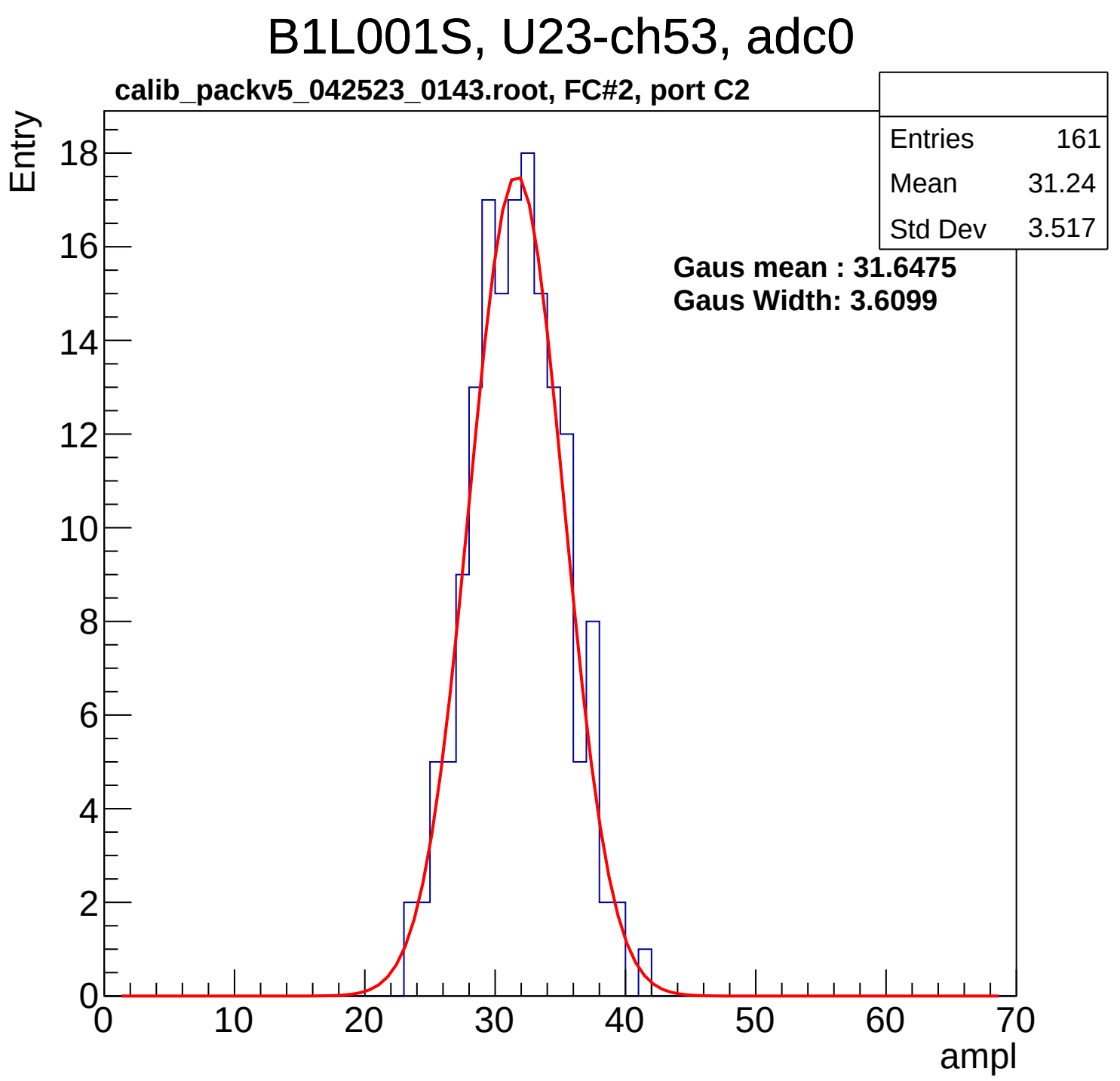
B1L001S, U23-ch53, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 31.24
Std Dev 3.517
Gaus mean : 31.6475
Gaus Width: 3.6099
Entry
ampl
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6
8
10
12
14
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0
10
20
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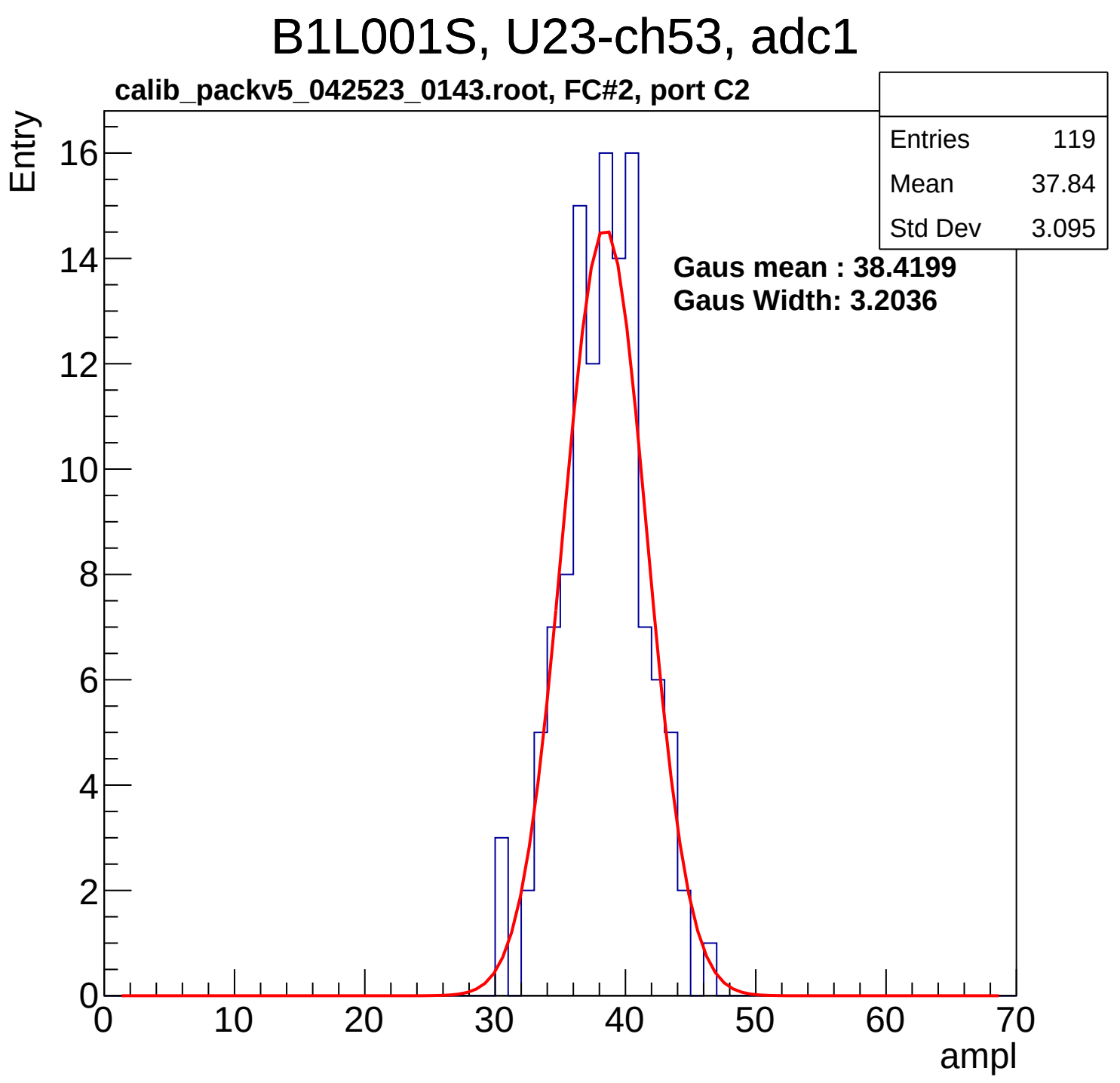
B1L001S, U23-ch53, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 37.84
Std Dev 3.095
Gaus mean : 38.4199
Gaus Width: 3.2036
Entry
ampl
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2
4
6
8
10
12
14
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0
10
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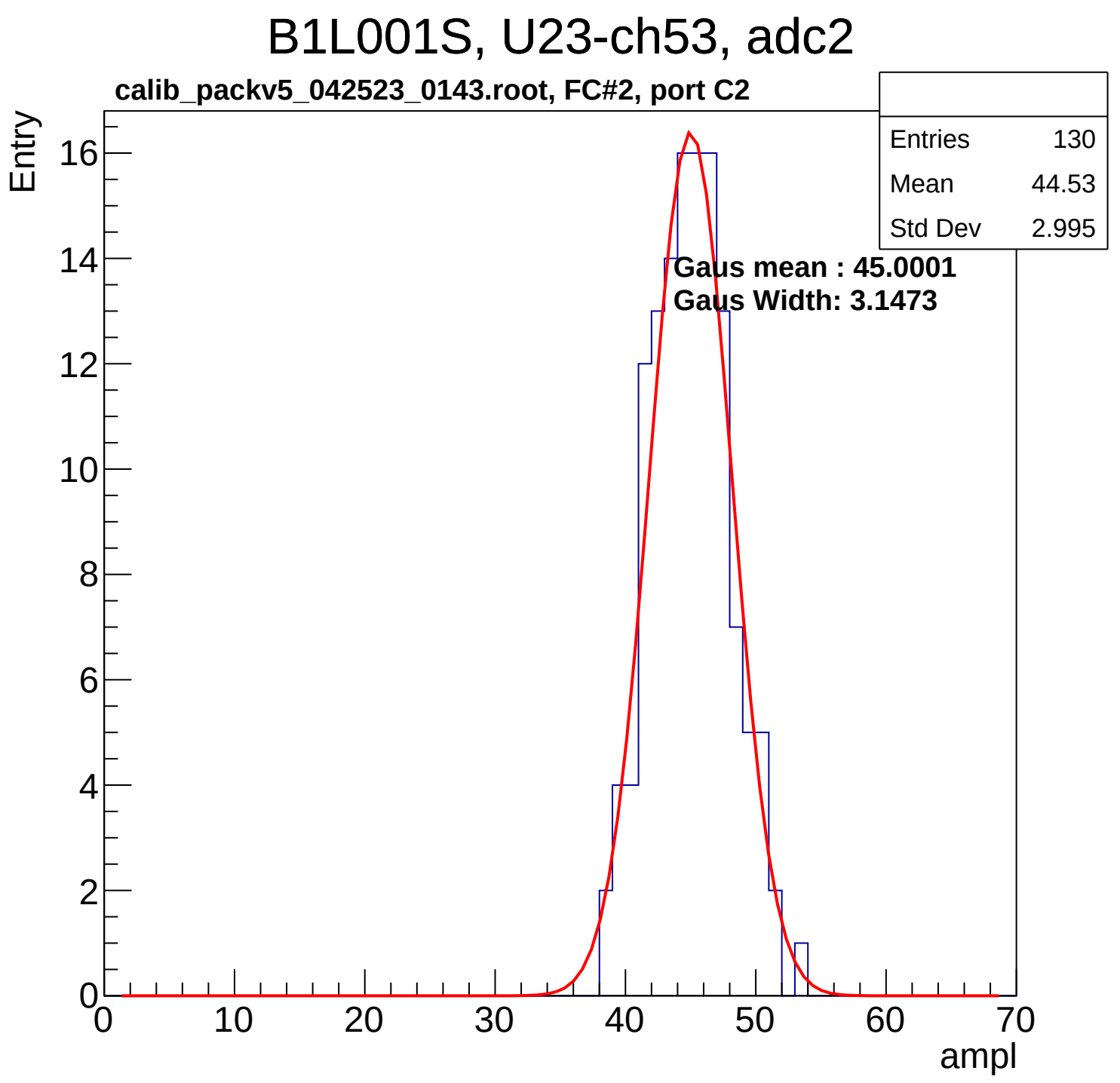
B1L001S, U23-ch53, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 44.53
Std Dev 2.995
Gaus mean : 45.0001
Gaus Width: 3.1473
Entry
ampl
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2
4
6
8
10
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0
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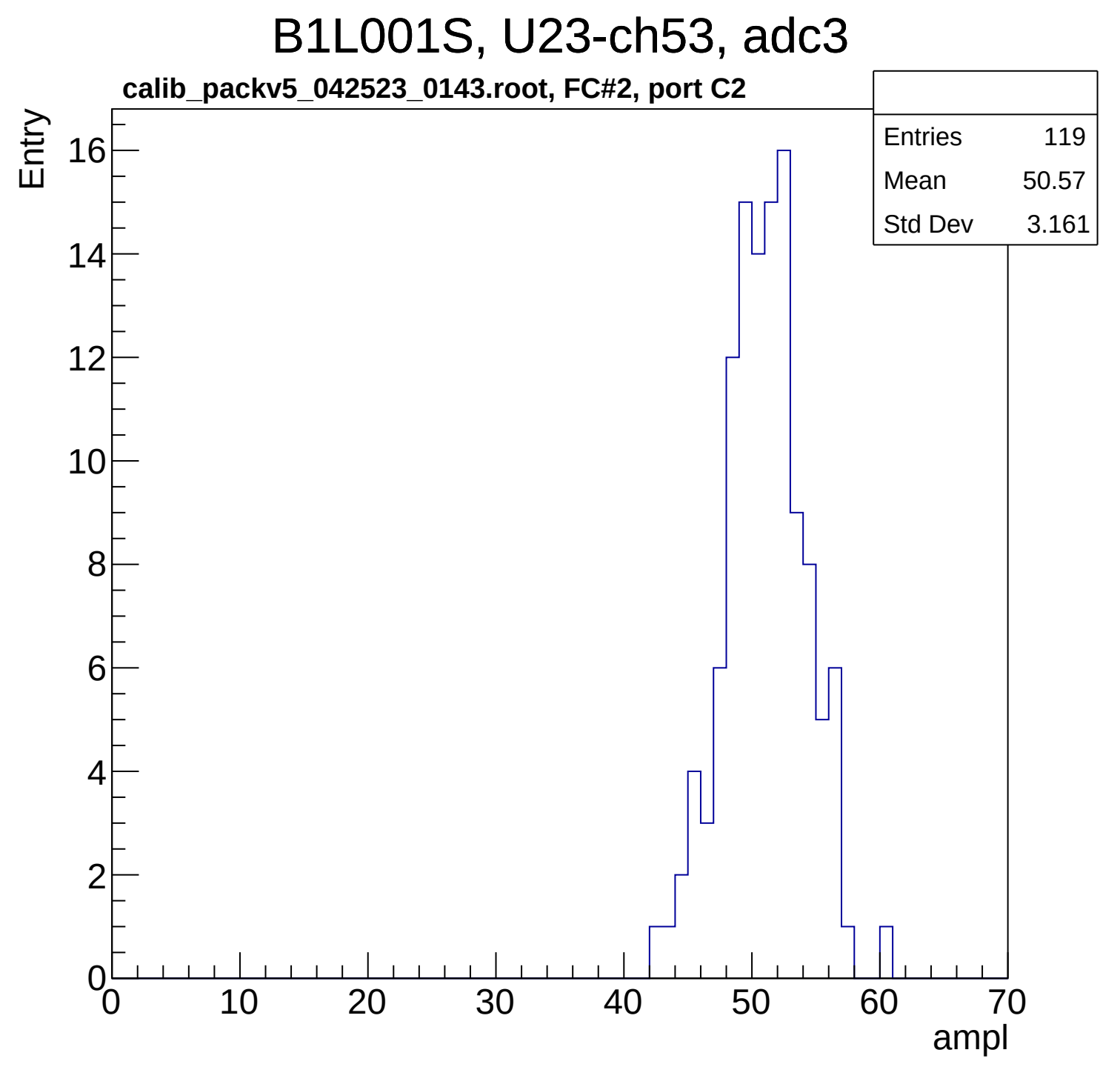
B1L001S, U23-ch53, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 50.57
Std Dev 3.161
Entry
ampl
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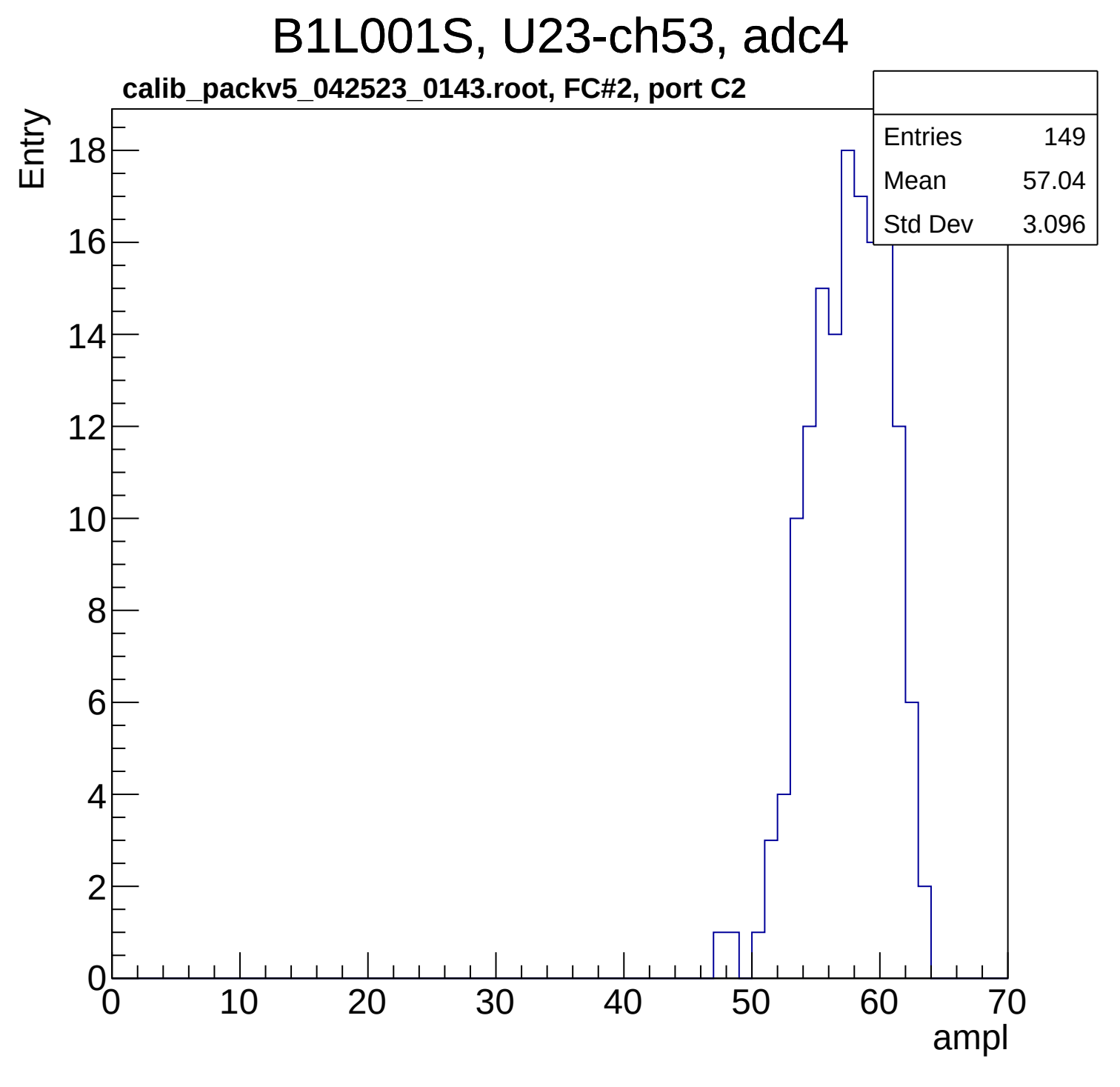
B1L001S, U23-ch53, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 57.04
Std Dev 3.096
Entry
ampl
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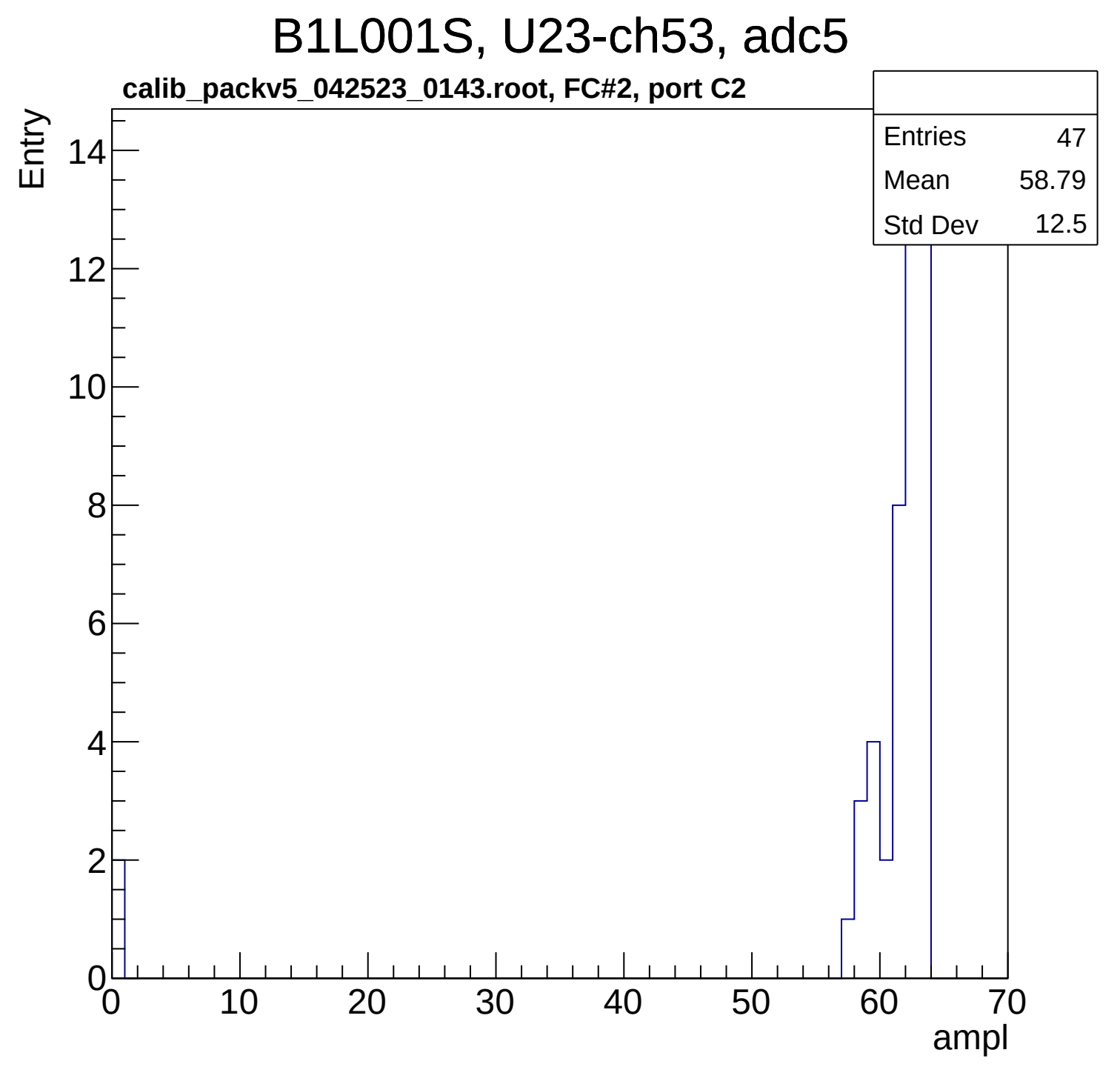
B1L001S, U23-ch53, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 47
Mean 58.79
Std Dev 12.5
Entry
ampl
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2
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6
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60
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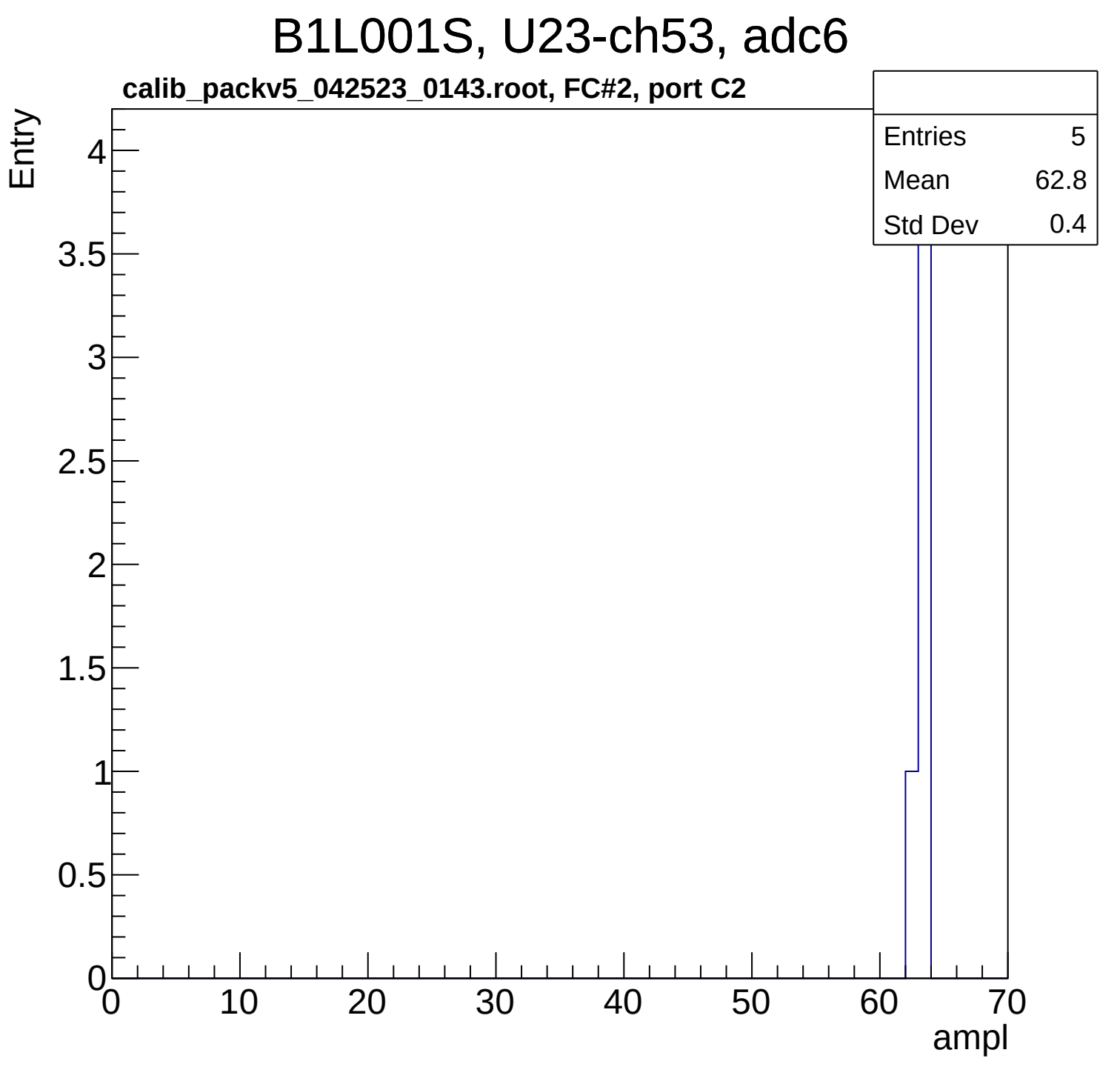
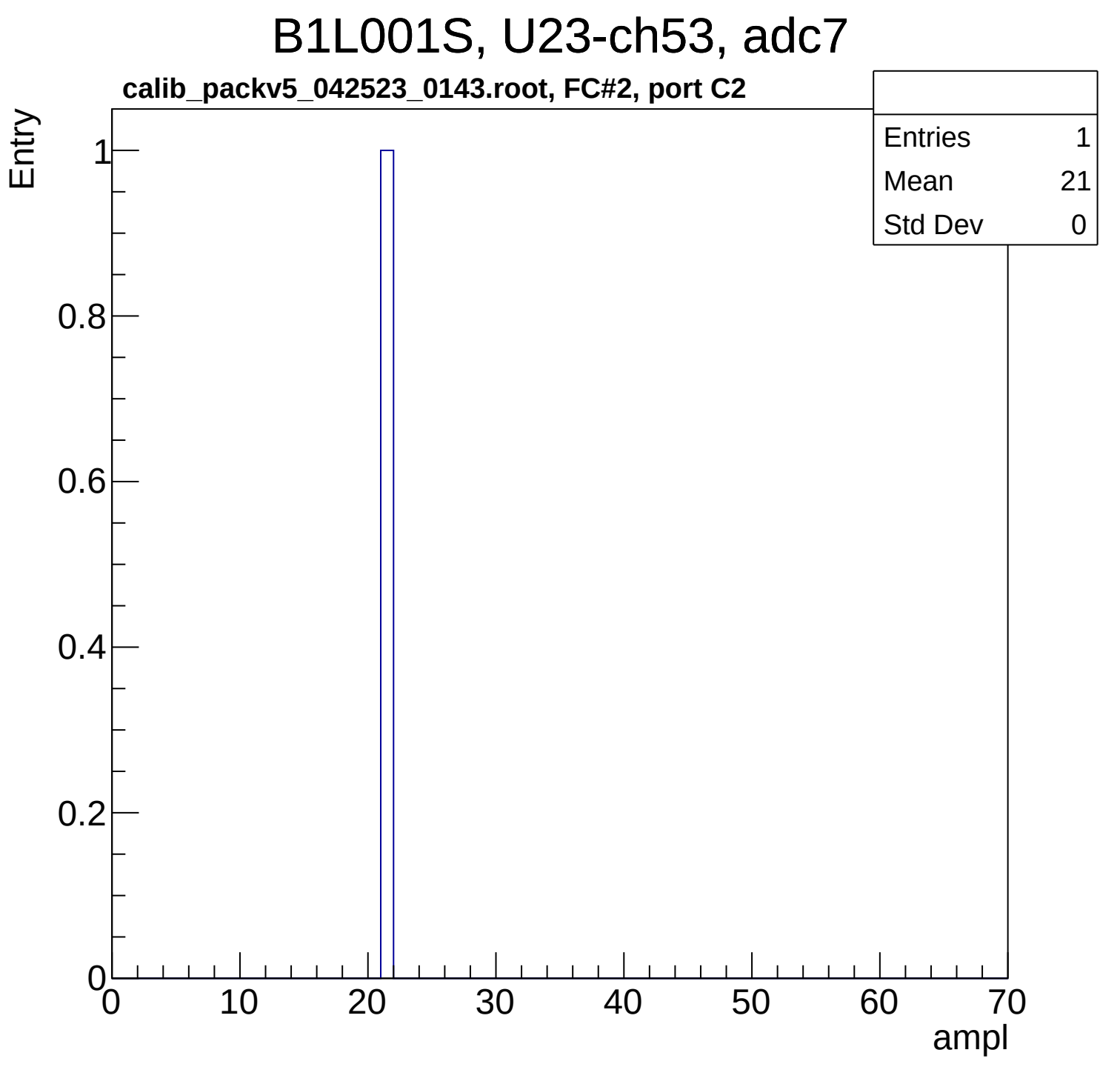
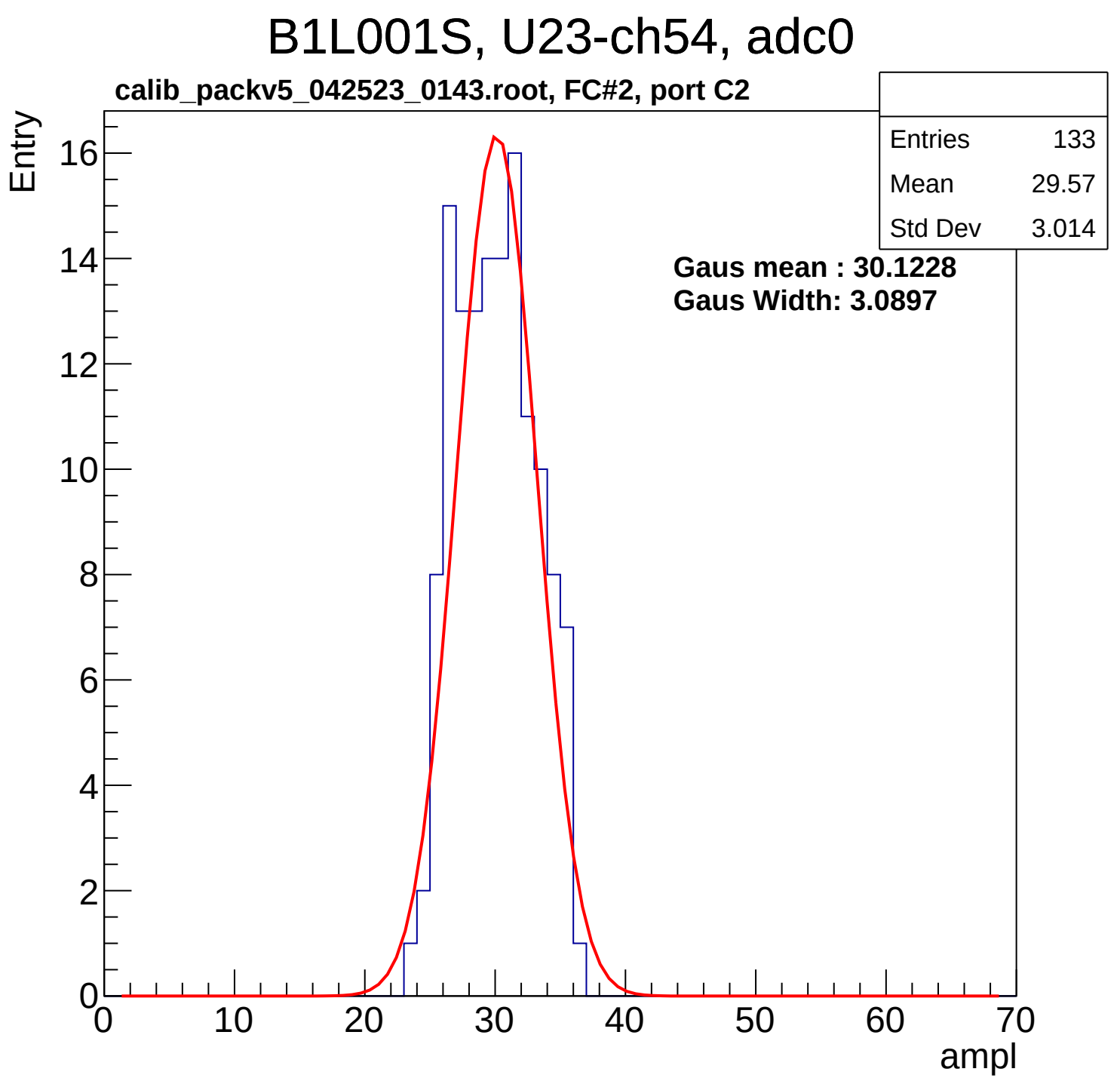
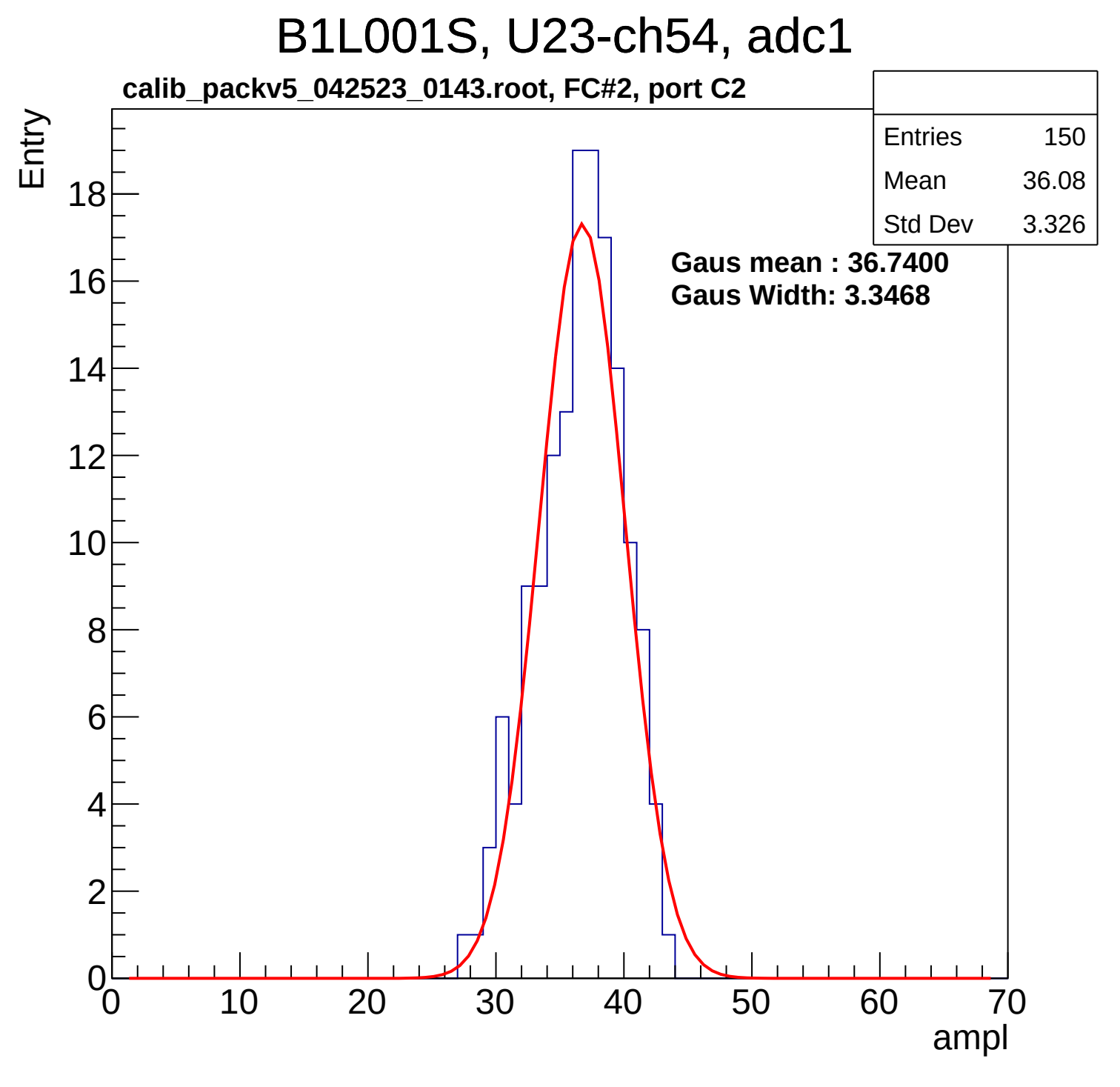

B1L001S, U23-ch53, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch53, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch54, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 29.57
Std Dev 3.014
Gaus mean : 30.1228
Gaus Width: 3.0897
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch54, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 36.08
Std Dev 3.326
Gaus mean : 36.7400
Gaus Width: 3.3468
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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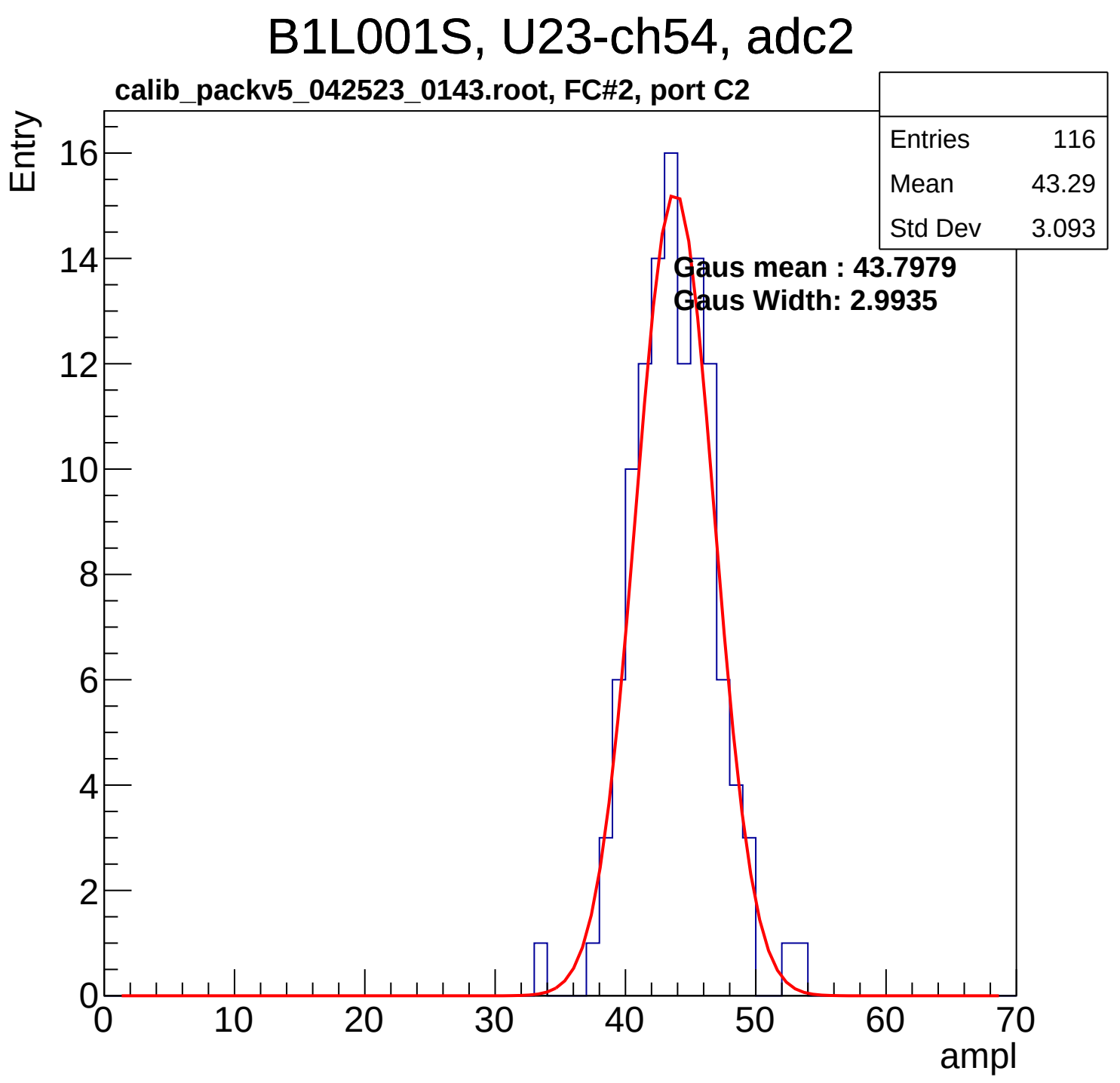
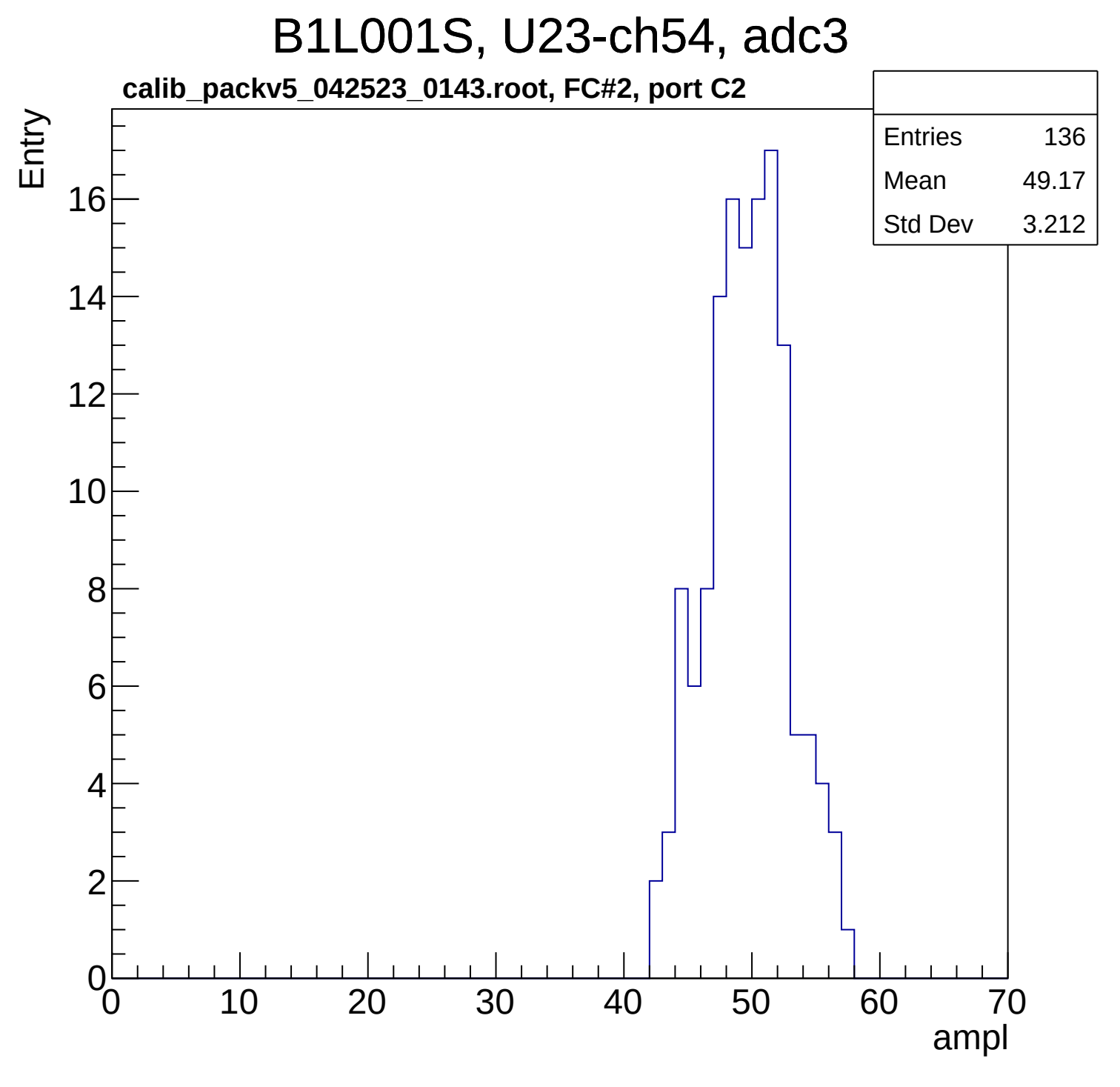
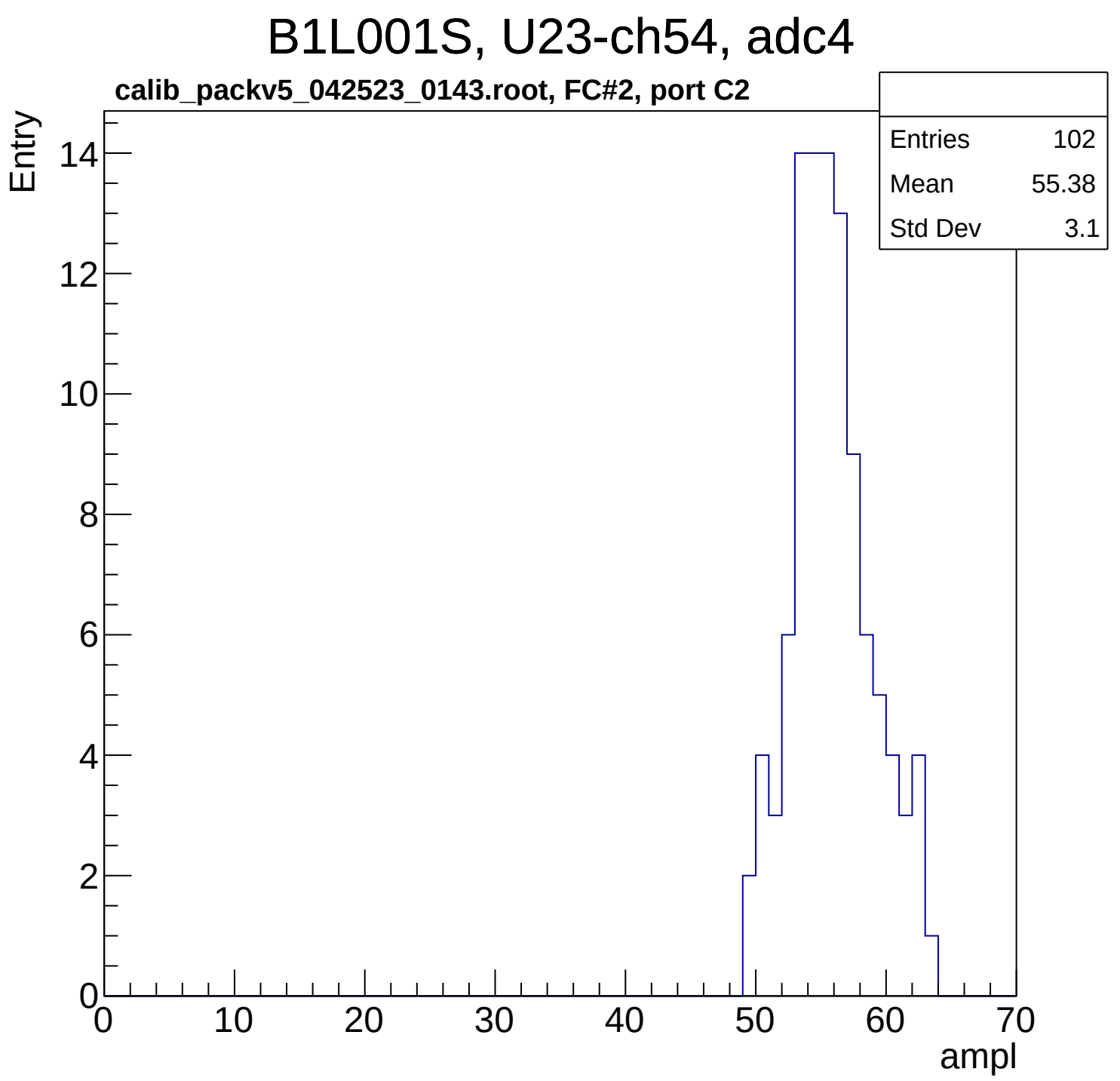

B1L001S, U23-ch54, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 43.29
Std Dev 3.093
Gaus mean : 43.7979
Gaus Width: 2.9935
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch54, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 49.17
Std Dev 3.212
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch54, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 55.38
Std Dev 3.1
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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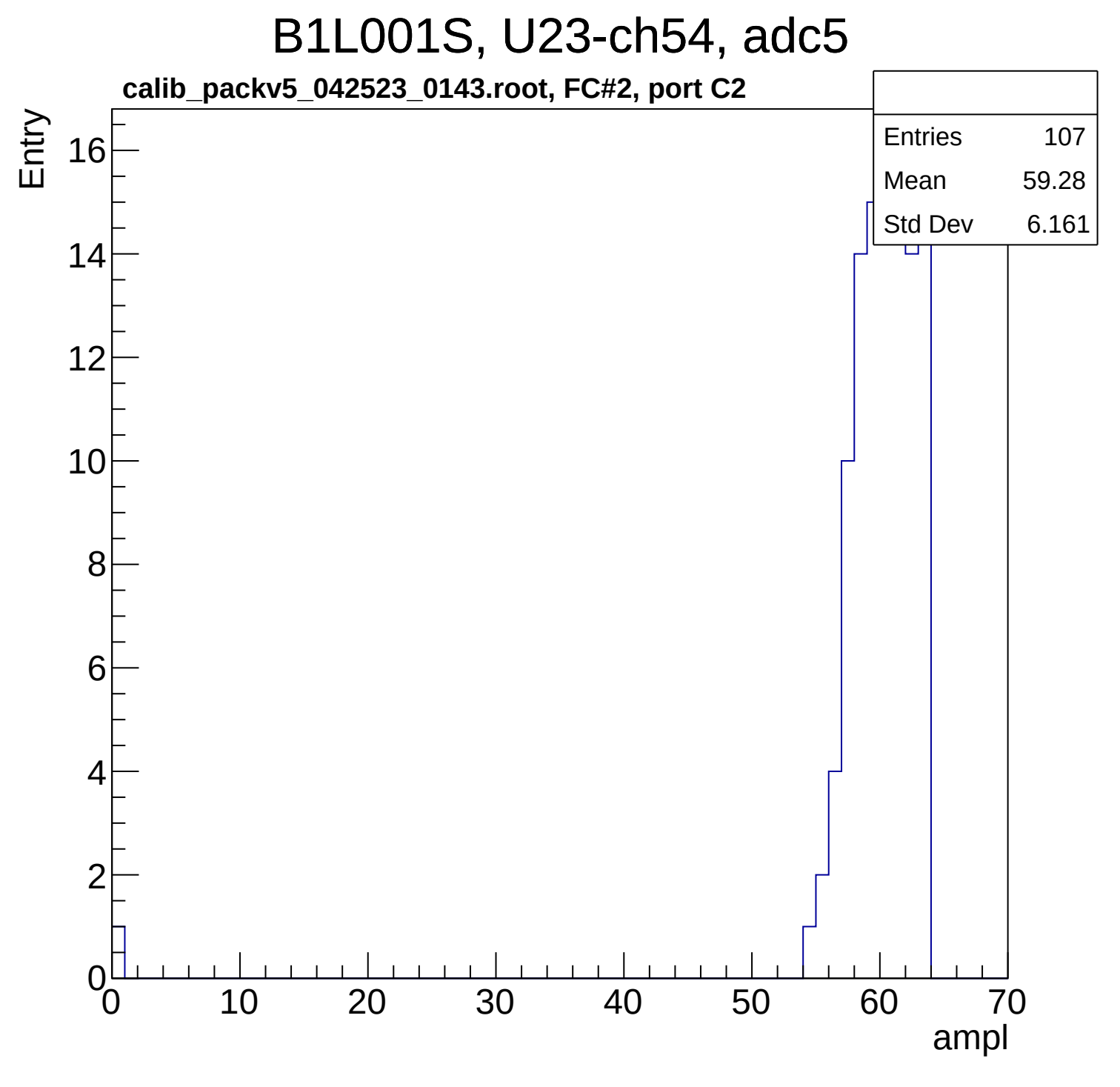
B1L001S, U23-ch54, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 59.28
Std Dev 6.161
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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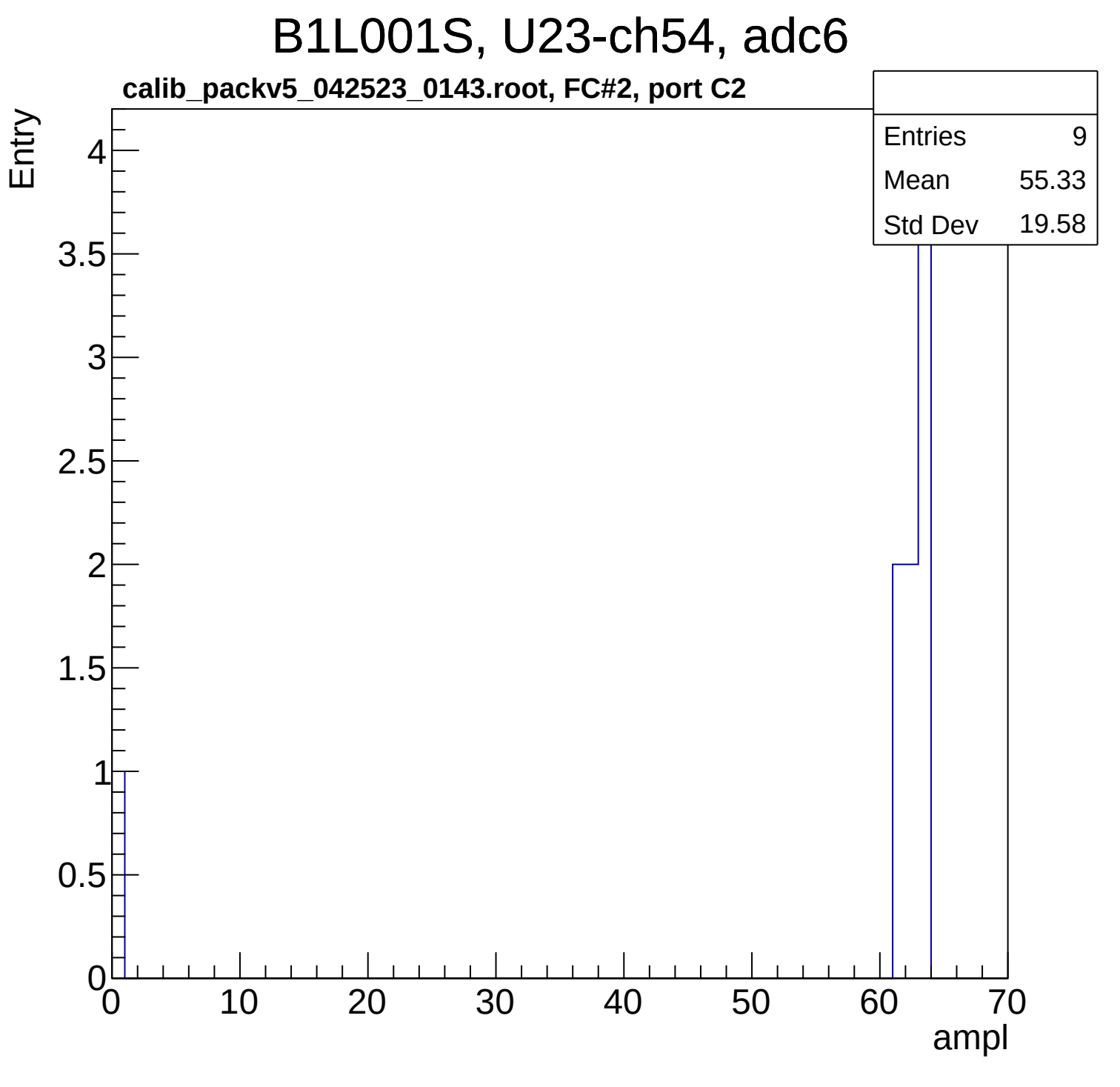
B1L001S, U23-ch54, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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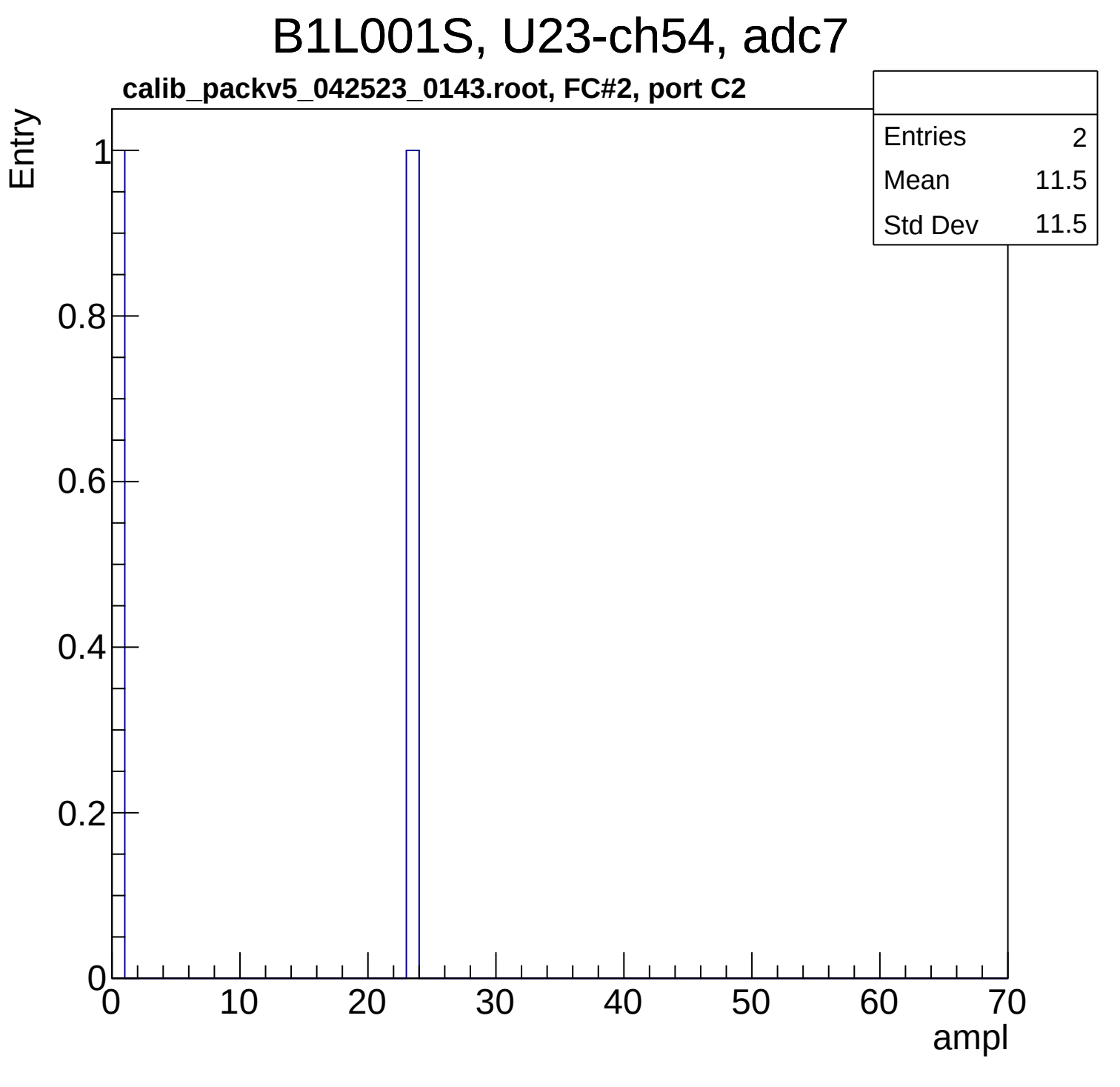
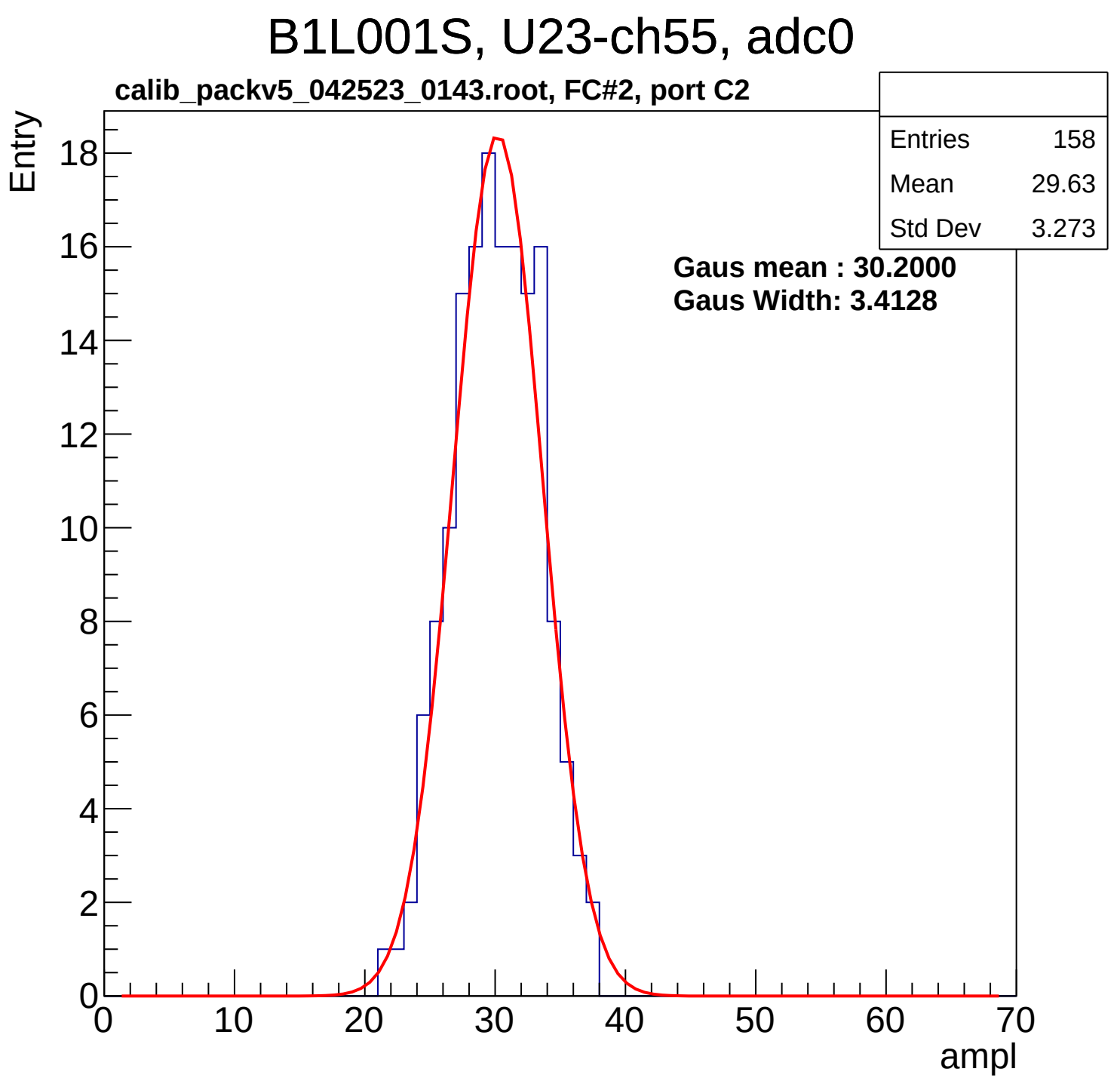
40
50
60
70


B1L001S, U23-ch54, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch55, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 29.63
Std Dev 3.273
Gaus mean : 30.2000
Gaus Width: 3.4128
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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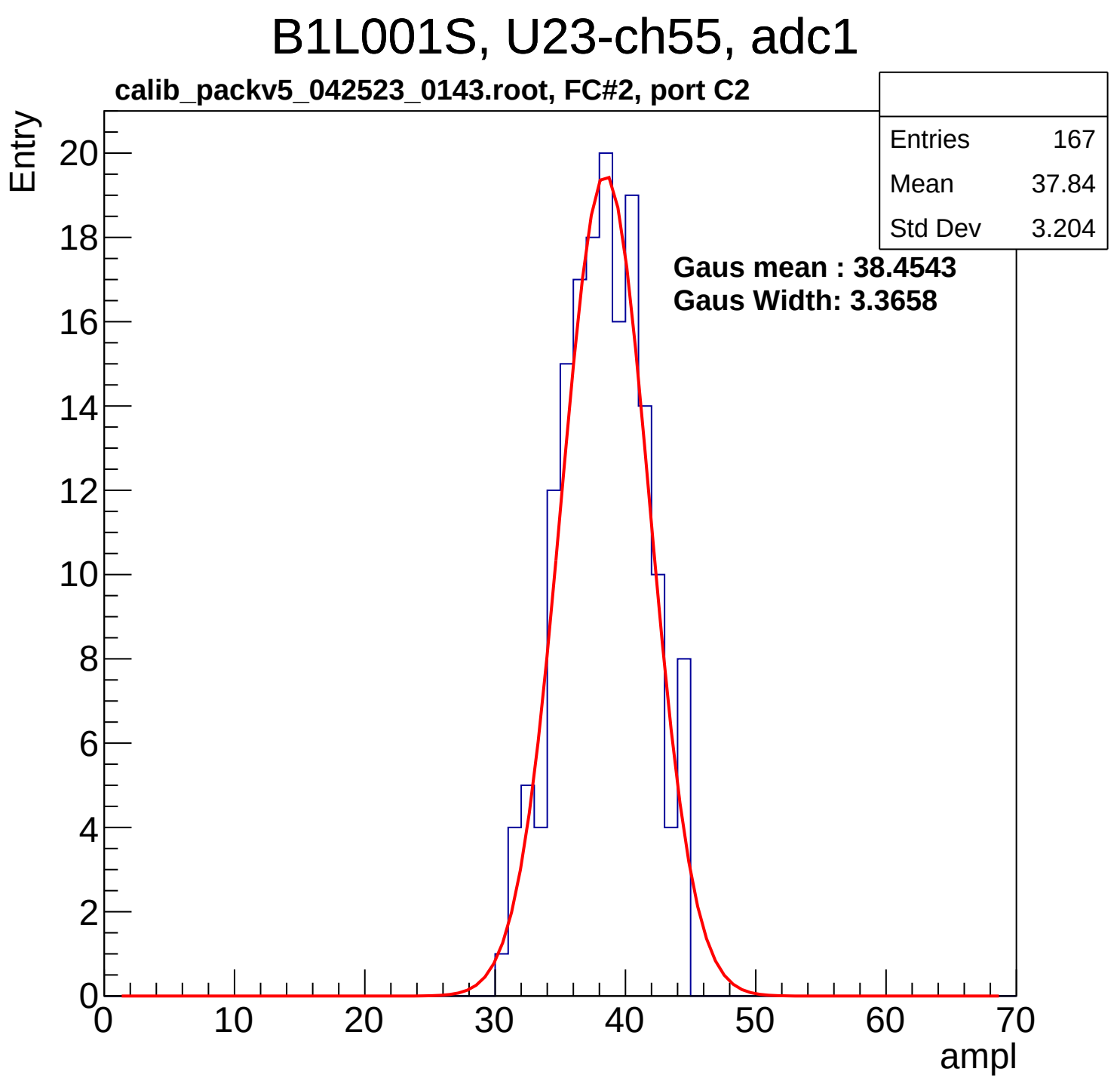
B1L001S, U23-ch55, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 37.84
Std Dev 3.204
Gaus mean : 38.4543
Gaus Width: 3.3658
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
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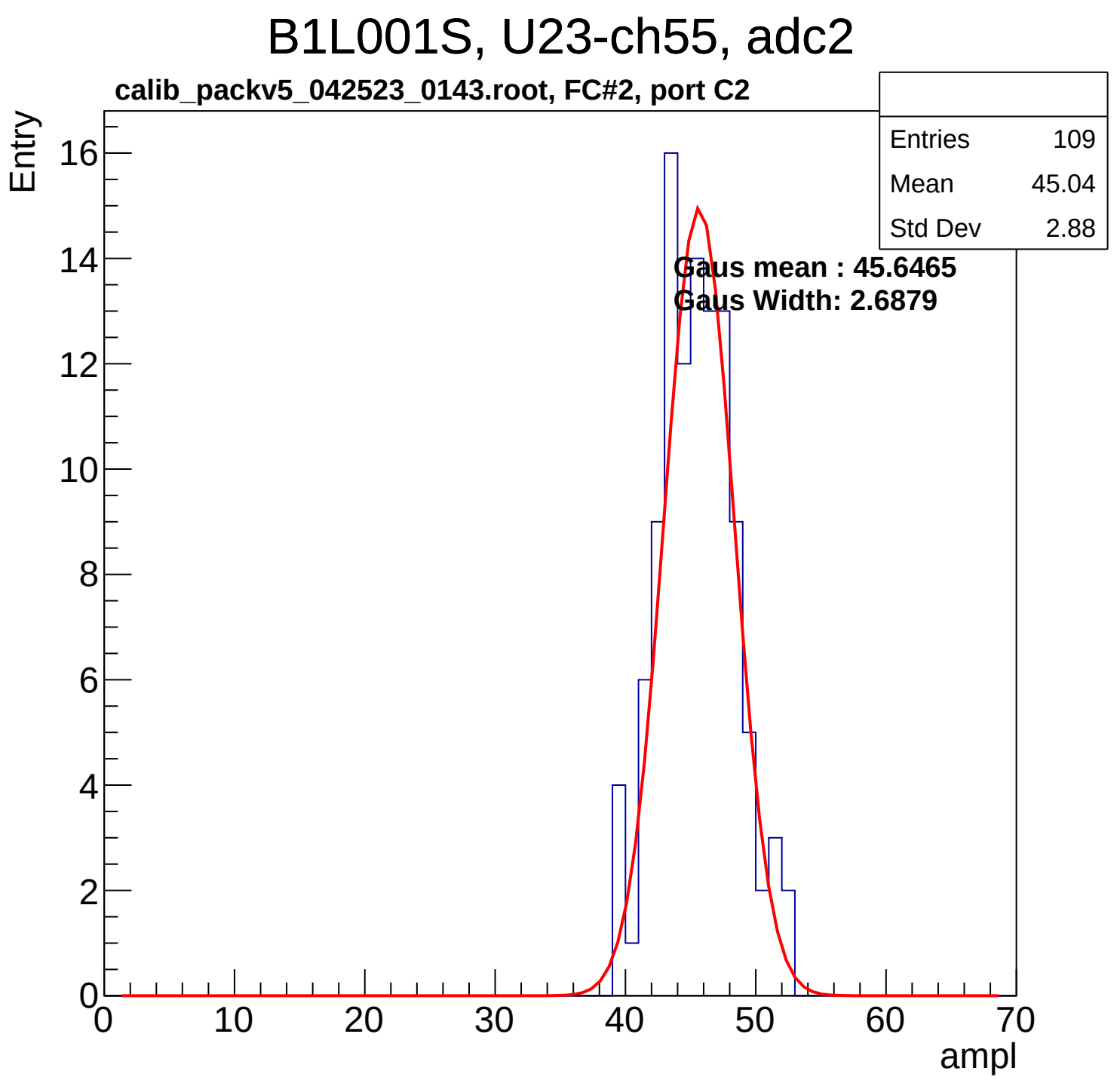
B1L001S, U23-ch55, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 45.04
Std Dev 2.88
Gaus mean : 45.6465
Gaus Width: 2.6879
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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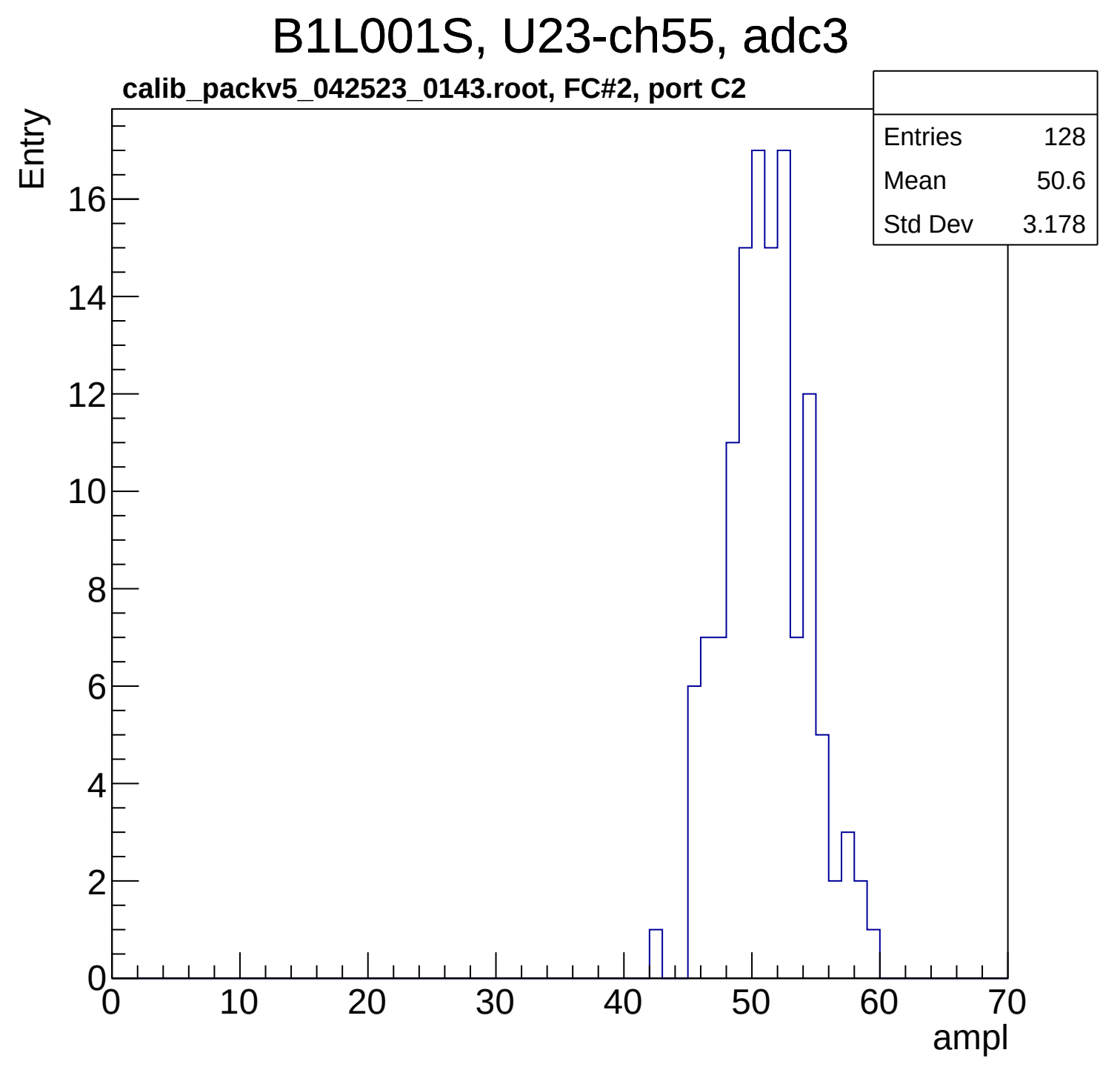
B1L001S, U23-ch55, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 50.6
Std Dev 3.178
Entry
ampl
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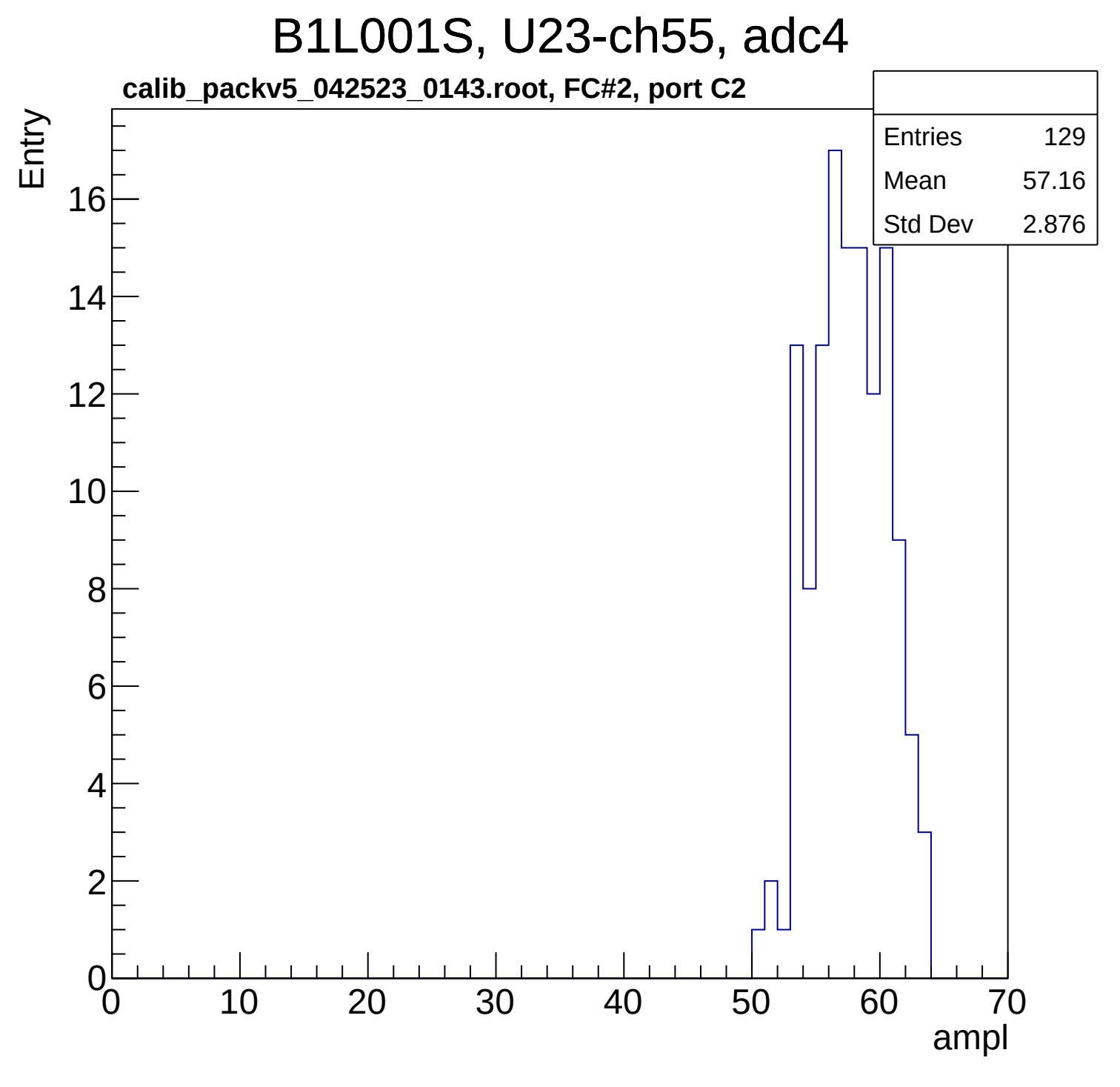
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch55, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 57.16
Std Dev 2.876
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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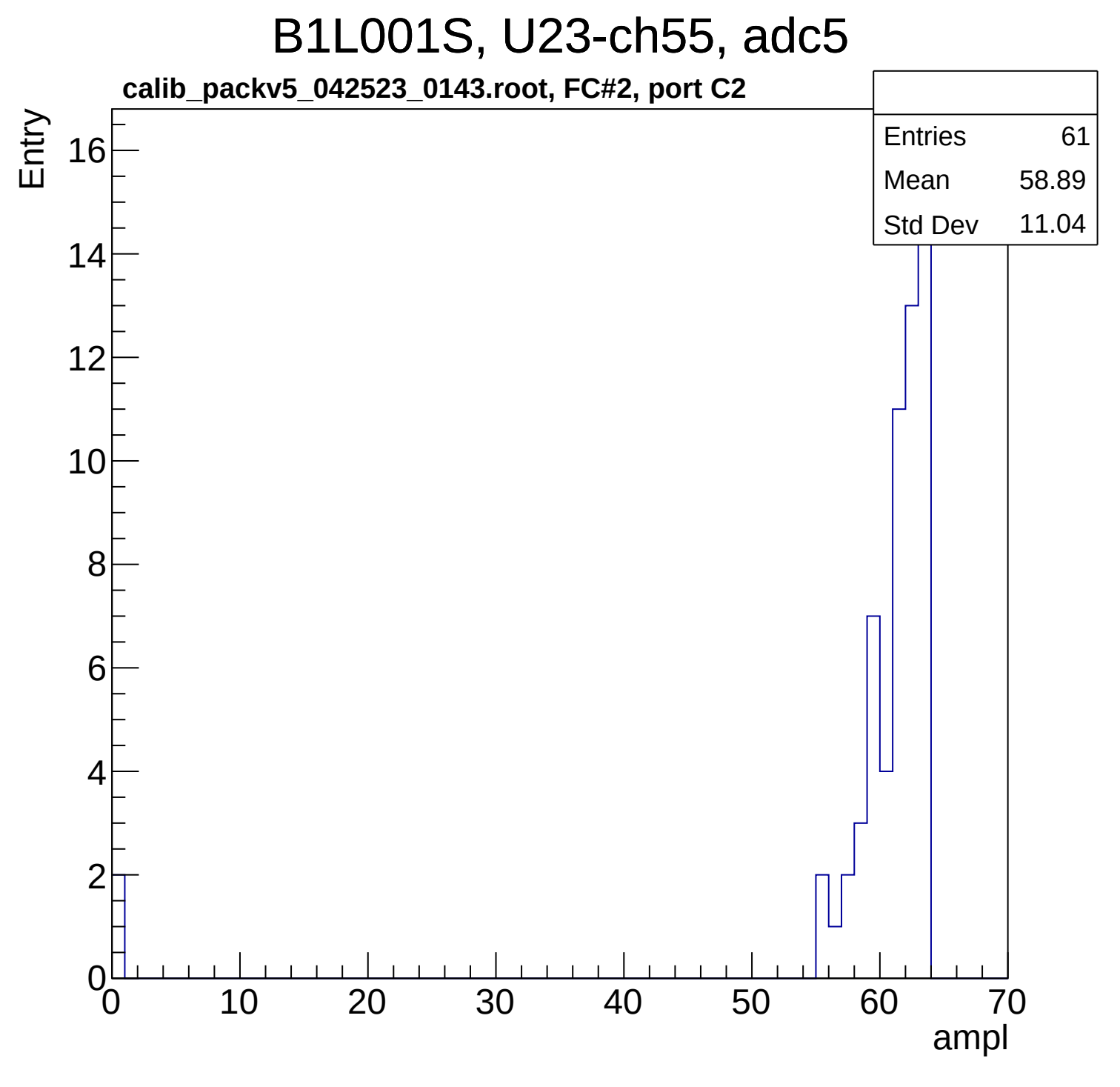
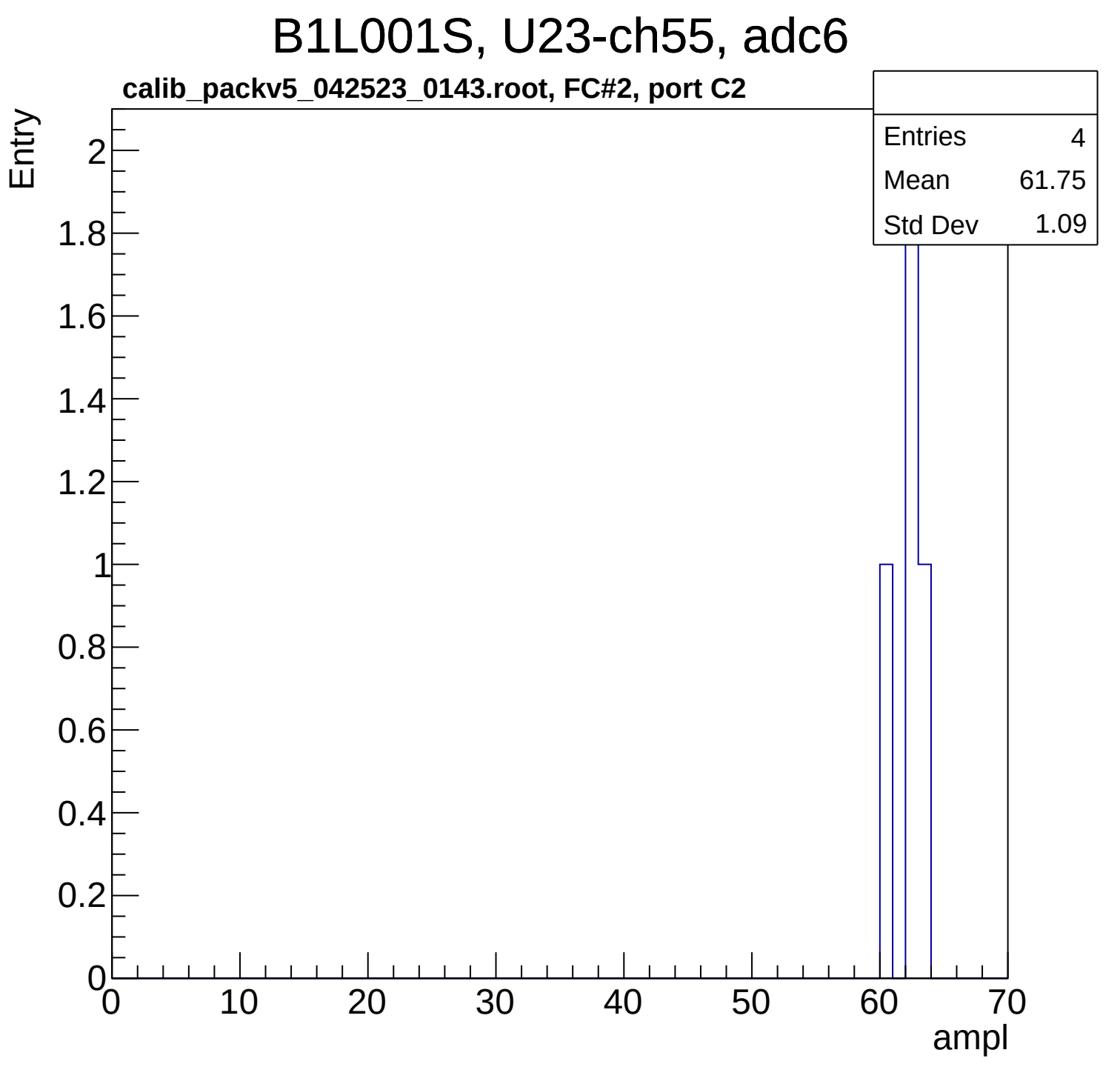
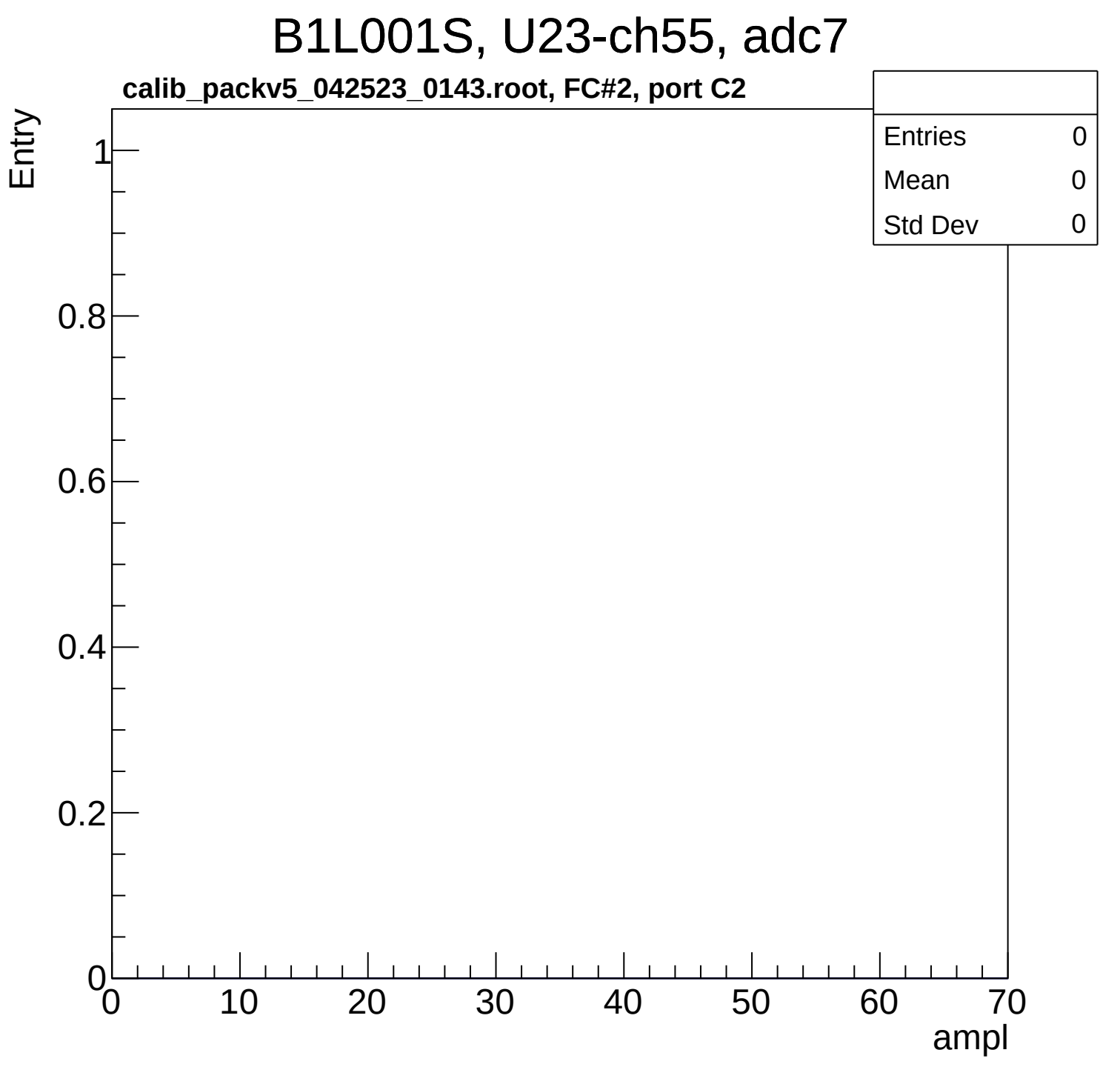
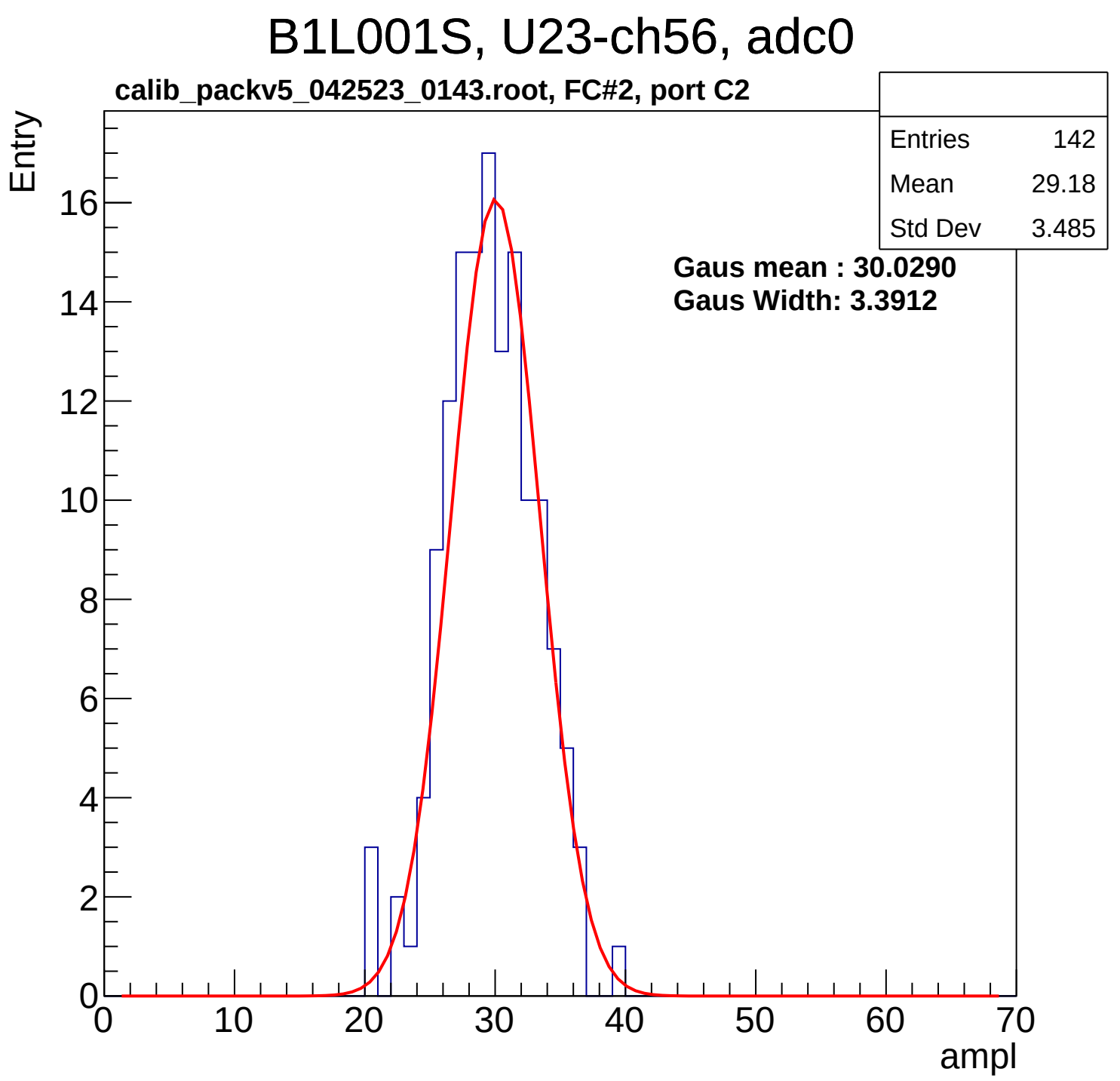

B1L001S, U23-ch55, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 58.89
Std Dev 11.04
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch55, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch55, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch56, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 29.18
Std Dev 3.485
Gaus mean : 30.0290
Gaus Width: 3.3912
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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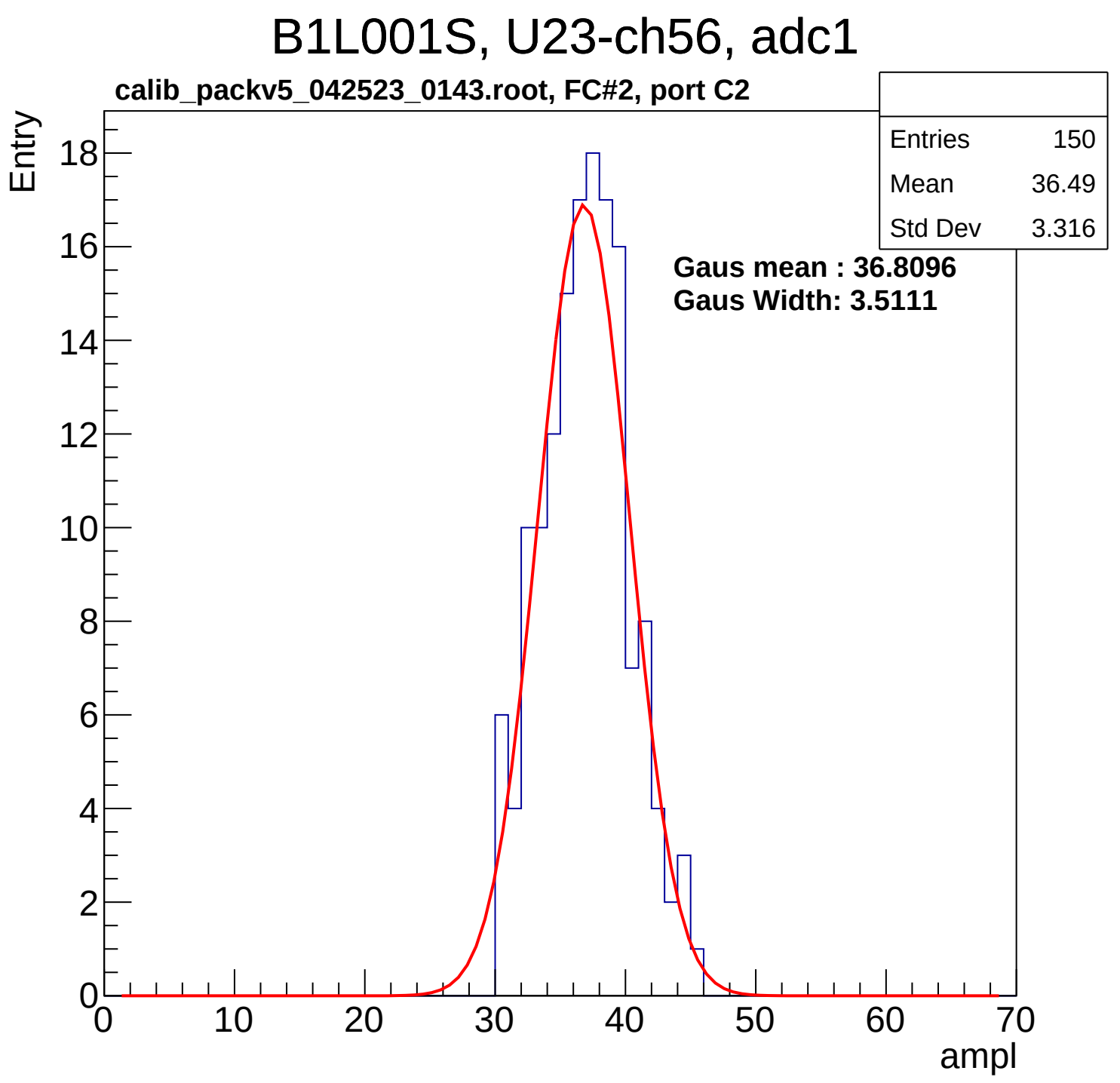
B1L001S, U23-ch56, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 36.49
Std Dev 3.316
Gaus mean : 36.8096
Gaus Width: 3.5111
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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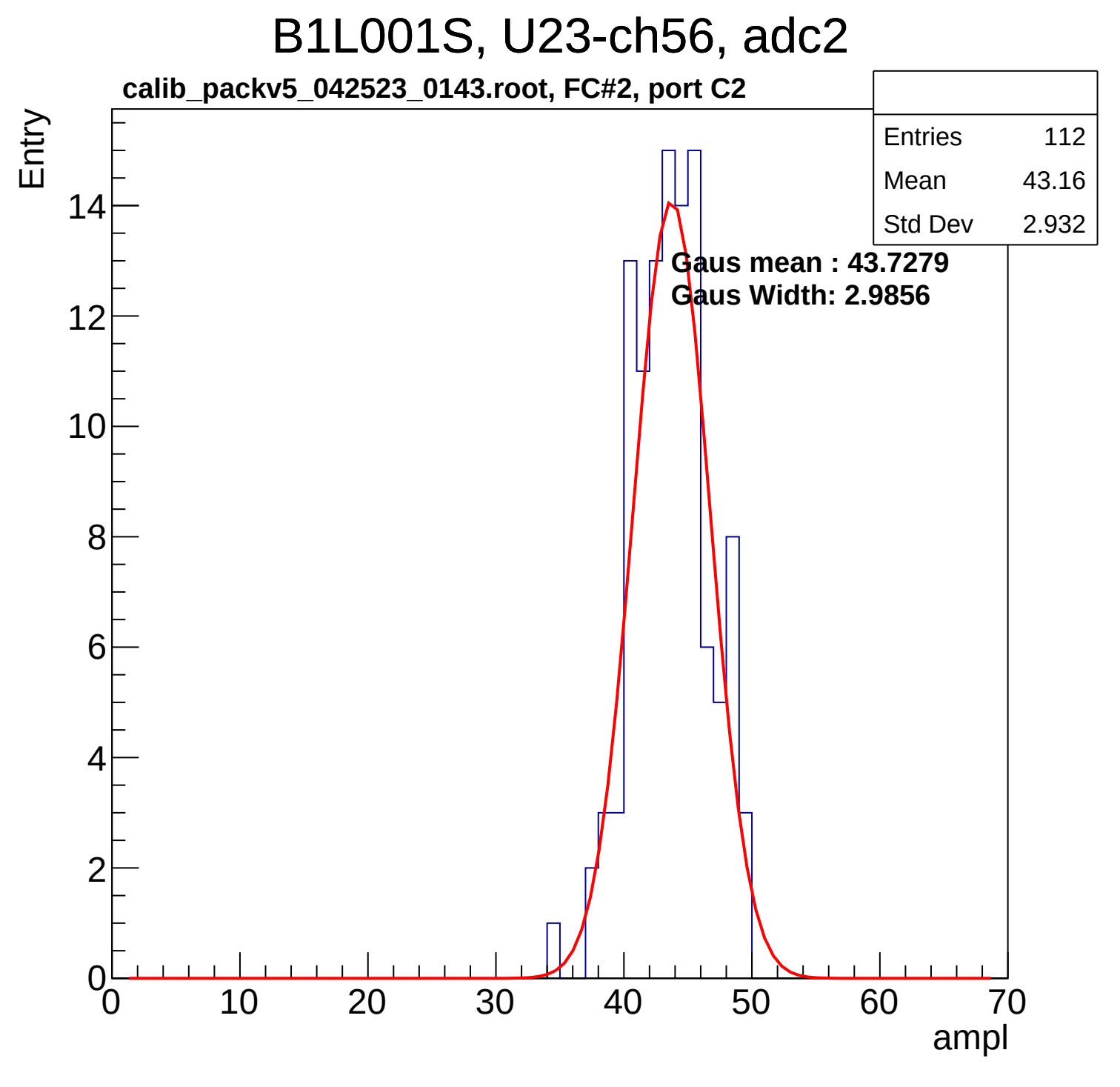
B1L001S, U23-ch56, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43.16
Std Dev 2.932
Gaus mean : 43.7279
Gaus Width: 2.9856
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
30
40
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60
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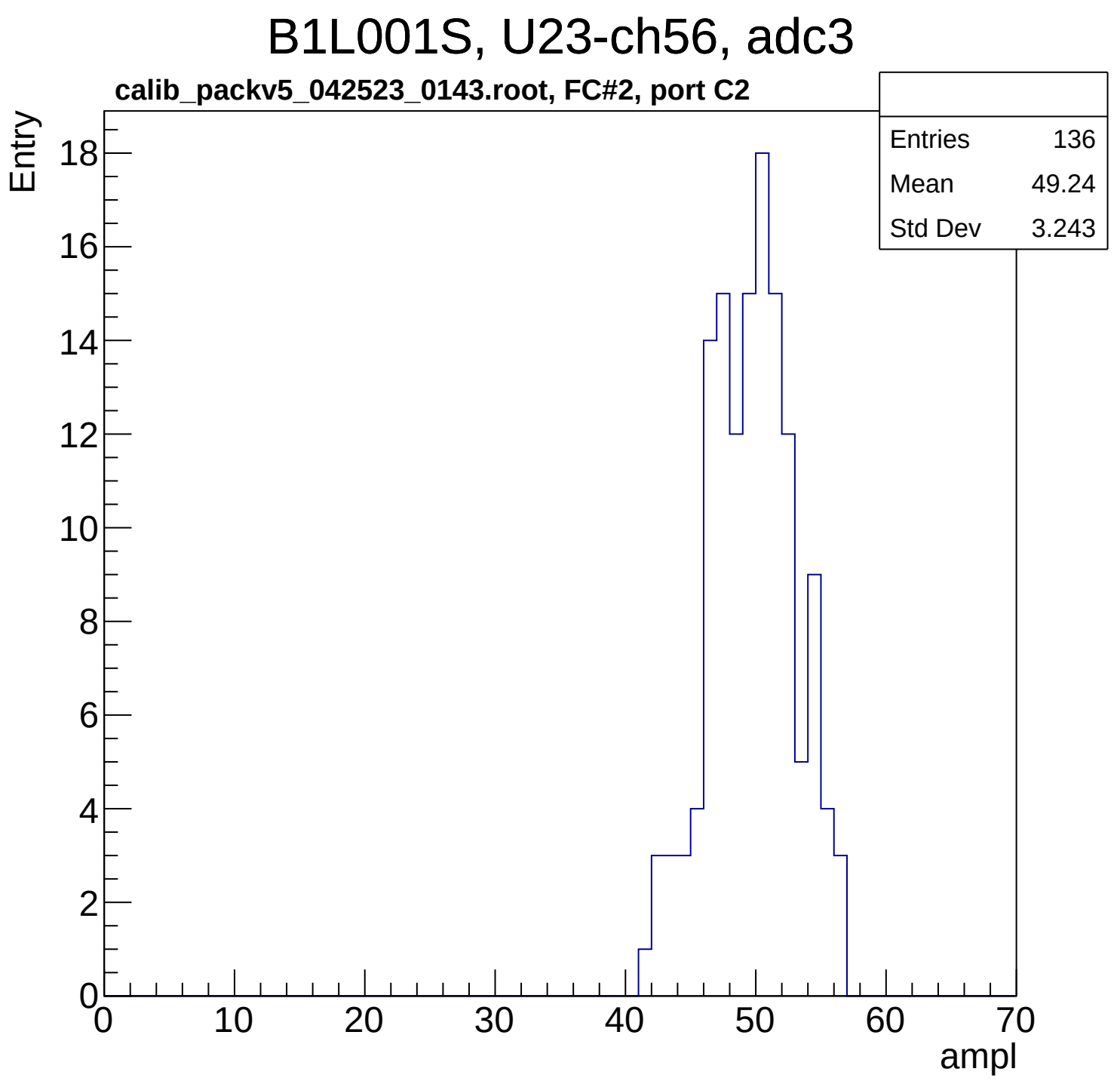
B1L001S, U23-ch56, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 49.24
Std Dev 3.243
Entry
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6
8
10
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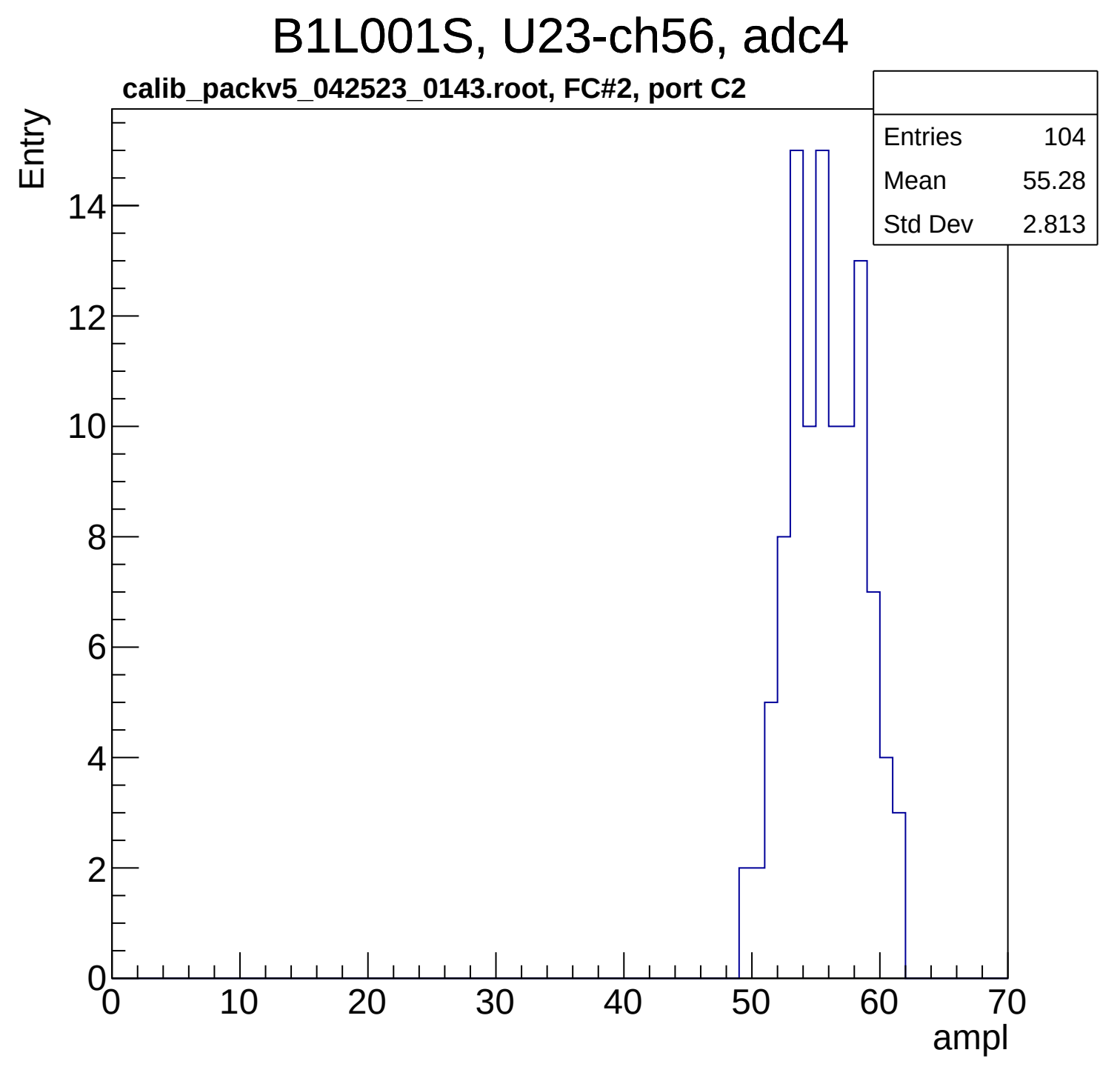
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch56, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 55.28
Std Dev 2.813
Entry
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14
0
10
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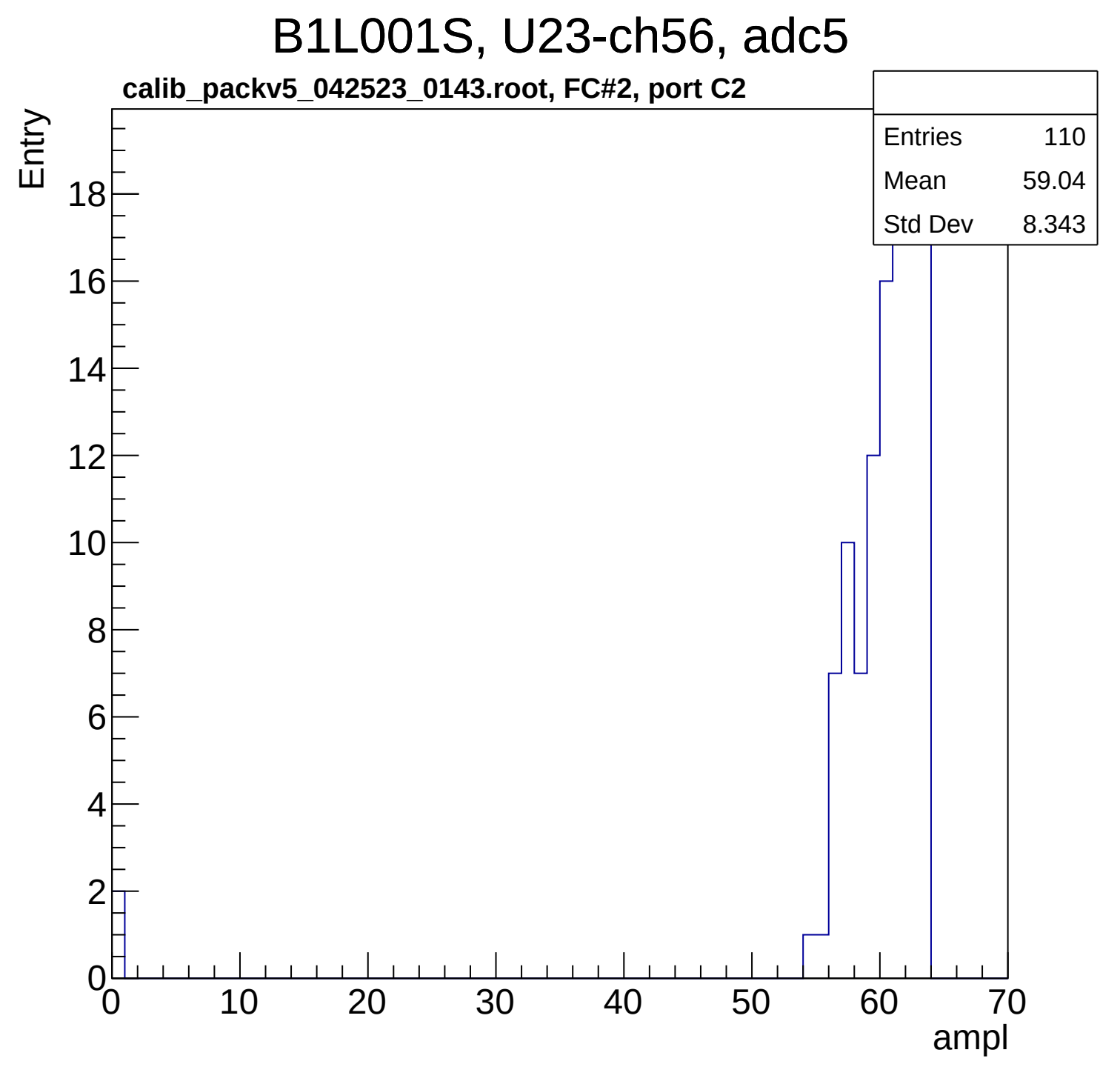
50
60
70
ampl


B1L001S, U23-ch56, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 59.04
Std Dev 8.343
Entry
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6
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10
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14
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18
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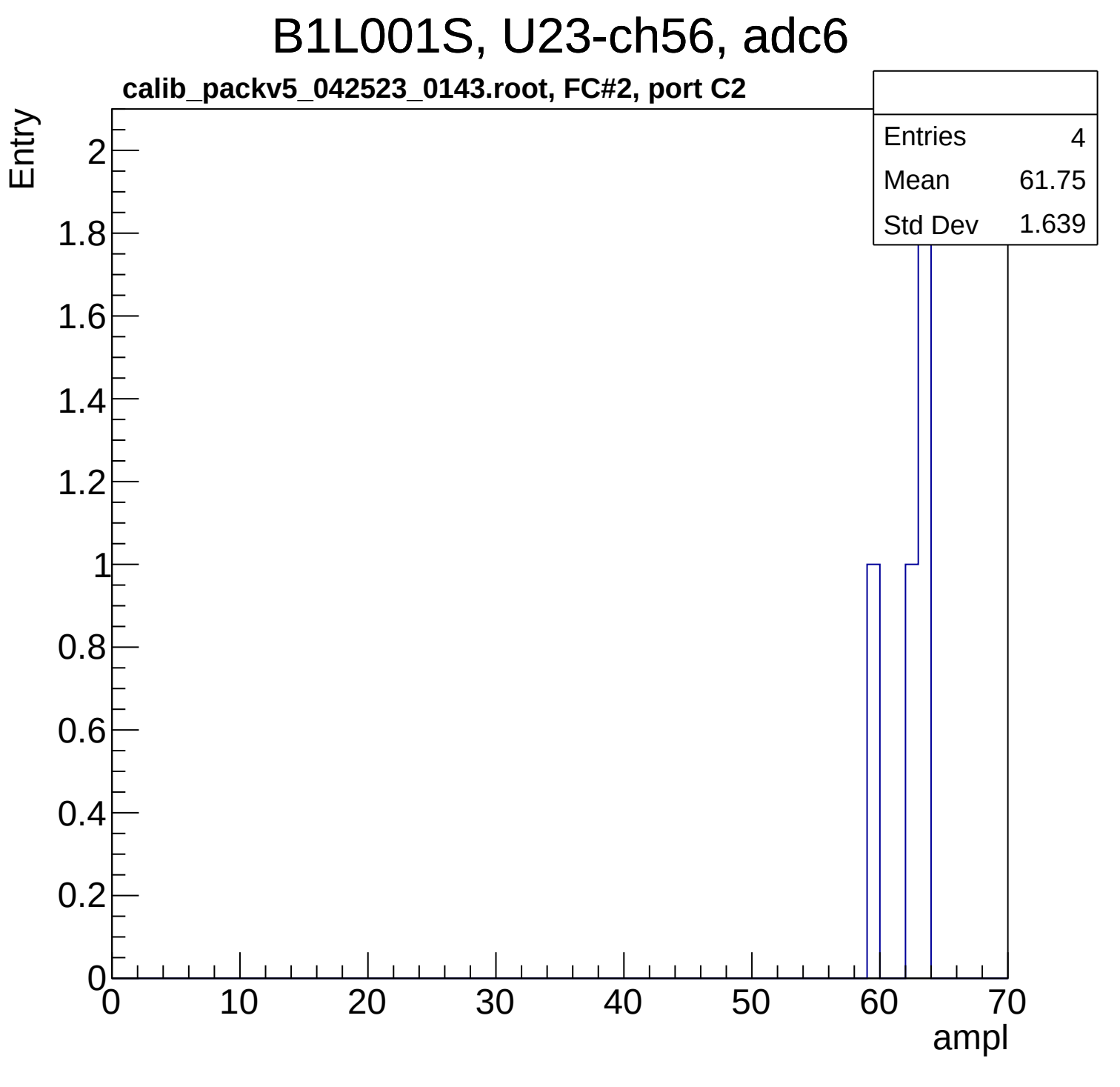
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch56, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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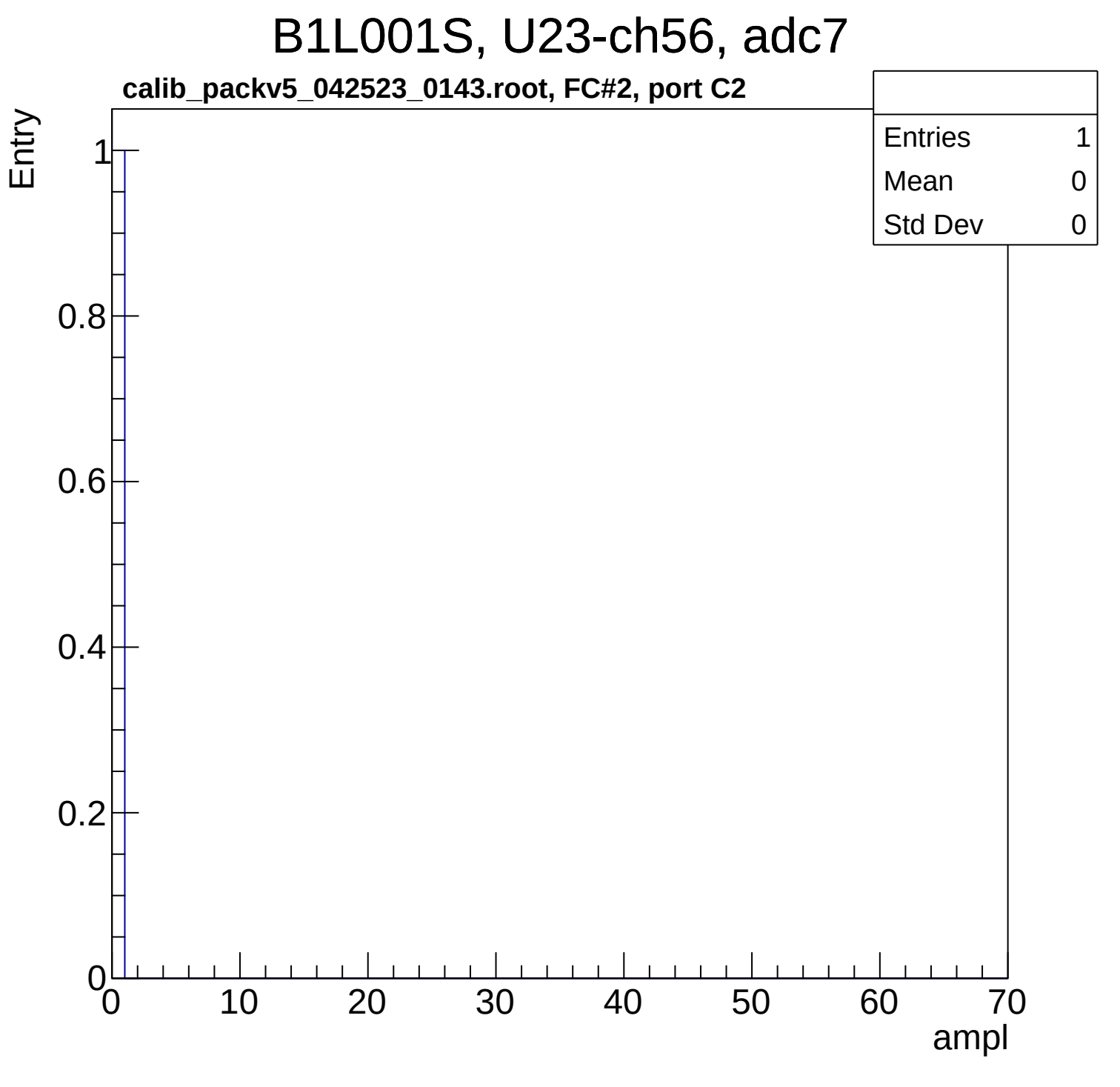
B1L001S, U23-ch56, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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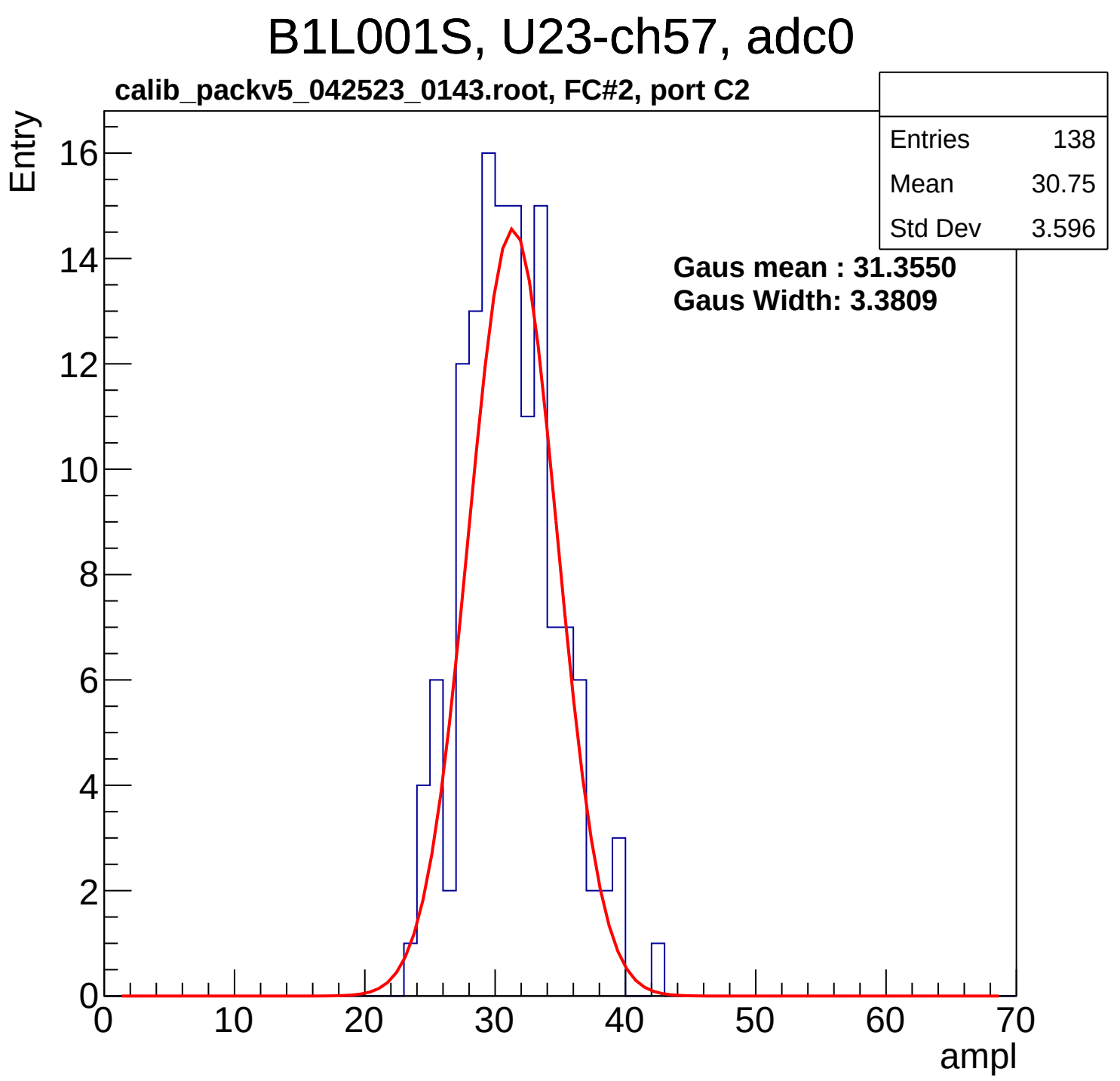
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch57, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 30.75
Std Dev 3.596
Gaus mean : 31.3550
Gaus Width: 3.3809
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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60
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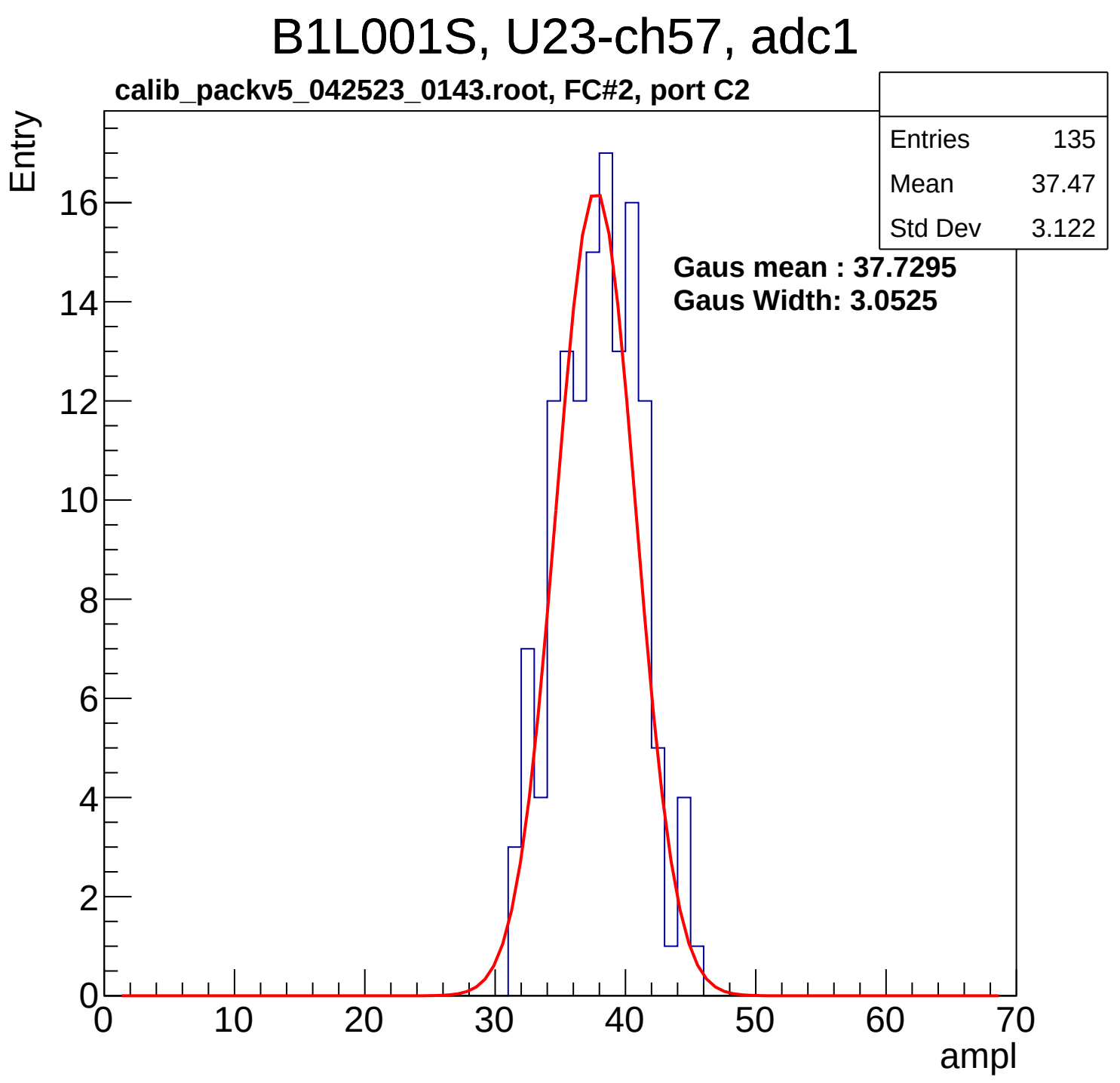
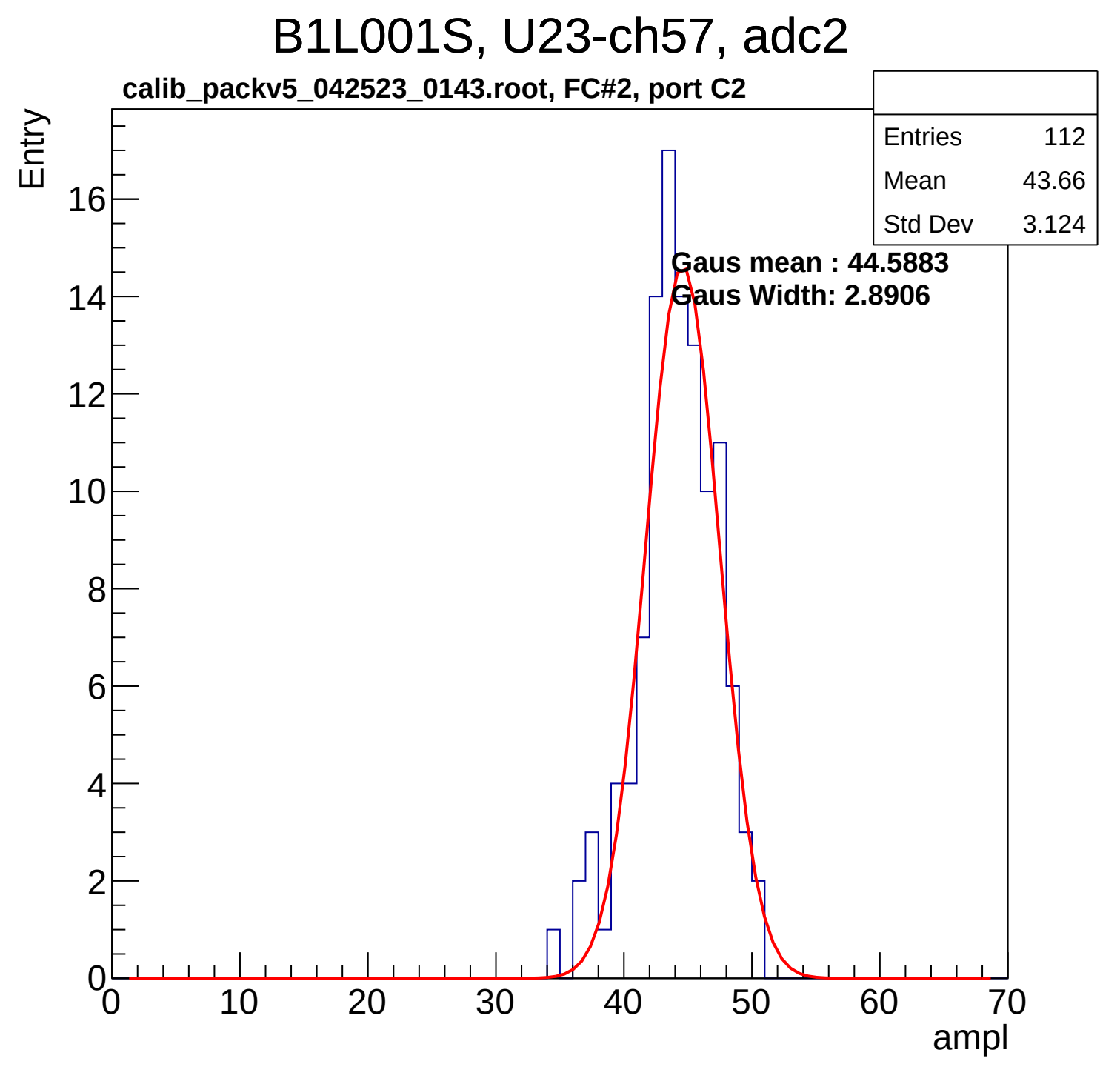

B1L001S, U23-ch57, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 37.47
Std Dev 3.122
Gaus mean : 37.7295
Gaus Width: 3.0525
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch57, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 43.66
Std Dev 3.124
Gaus mean : 44.5883
Gaus Width: 2.8906
Entry
ampl
0
2
4
6
8
10
12
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0
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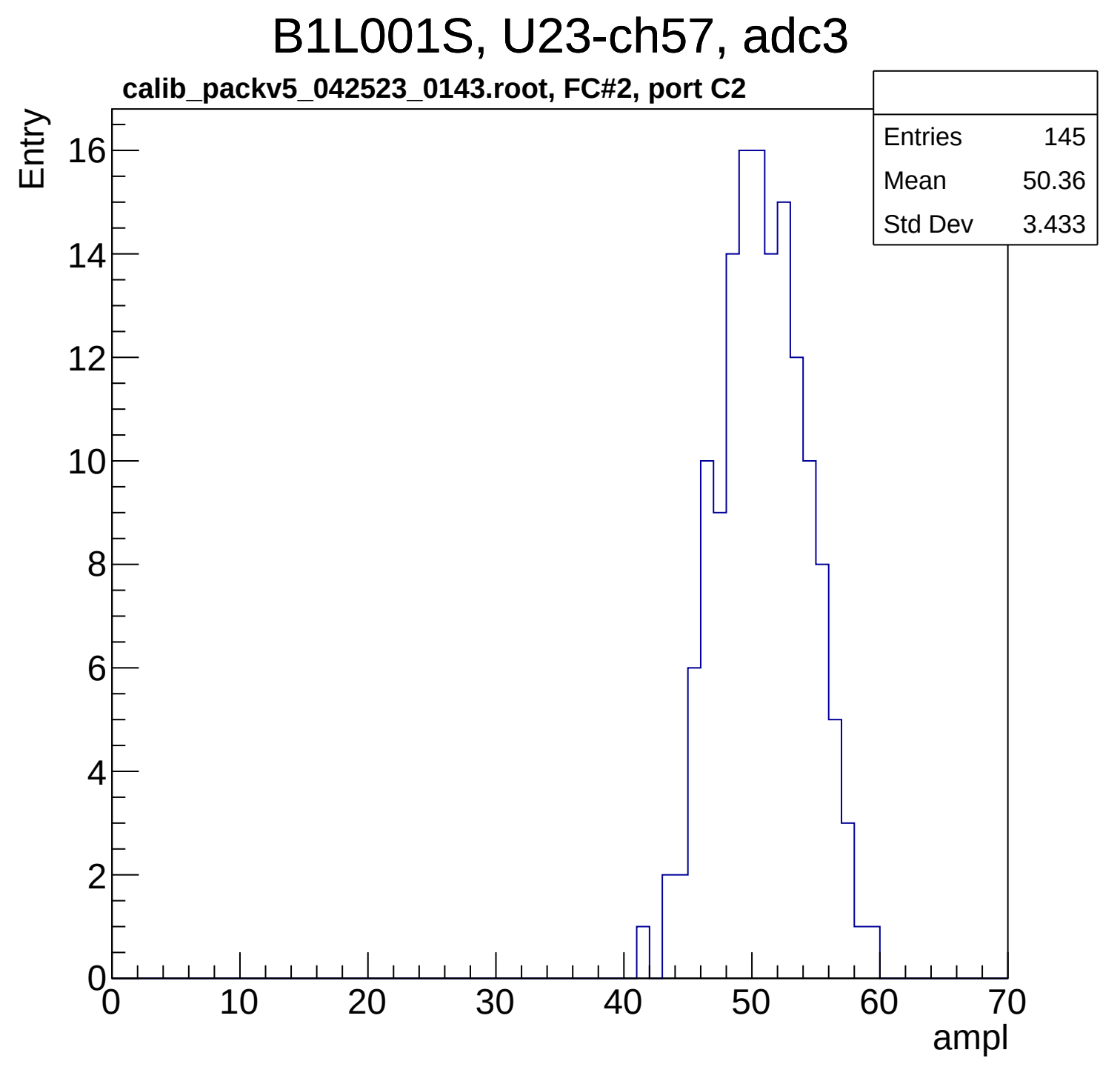
40
50
60
70


B1L001S, U23-ch57, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 50.36
Std Dev 3.433
Entry
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4
6
8
10
12
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16
0
10
20
30
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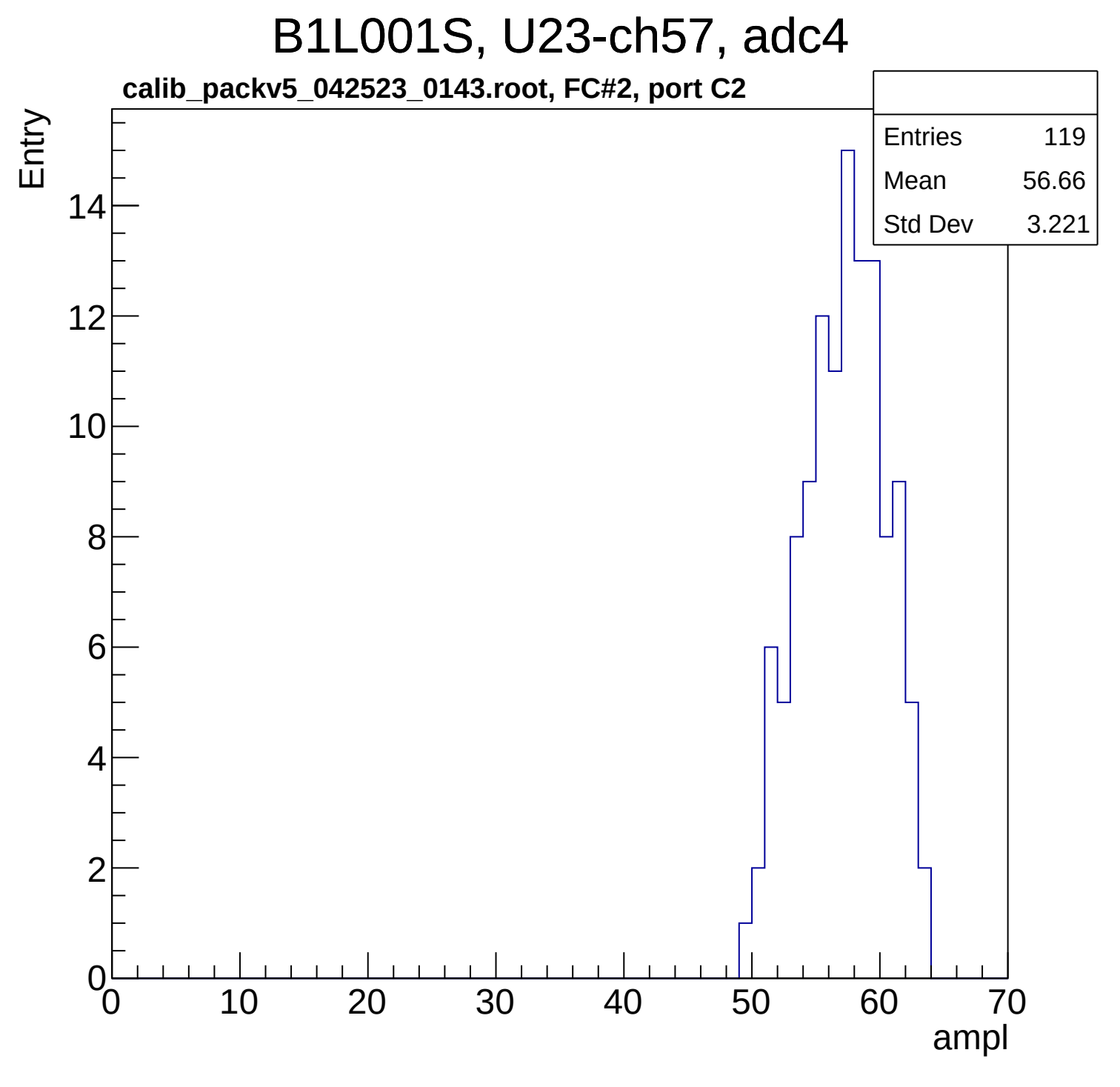
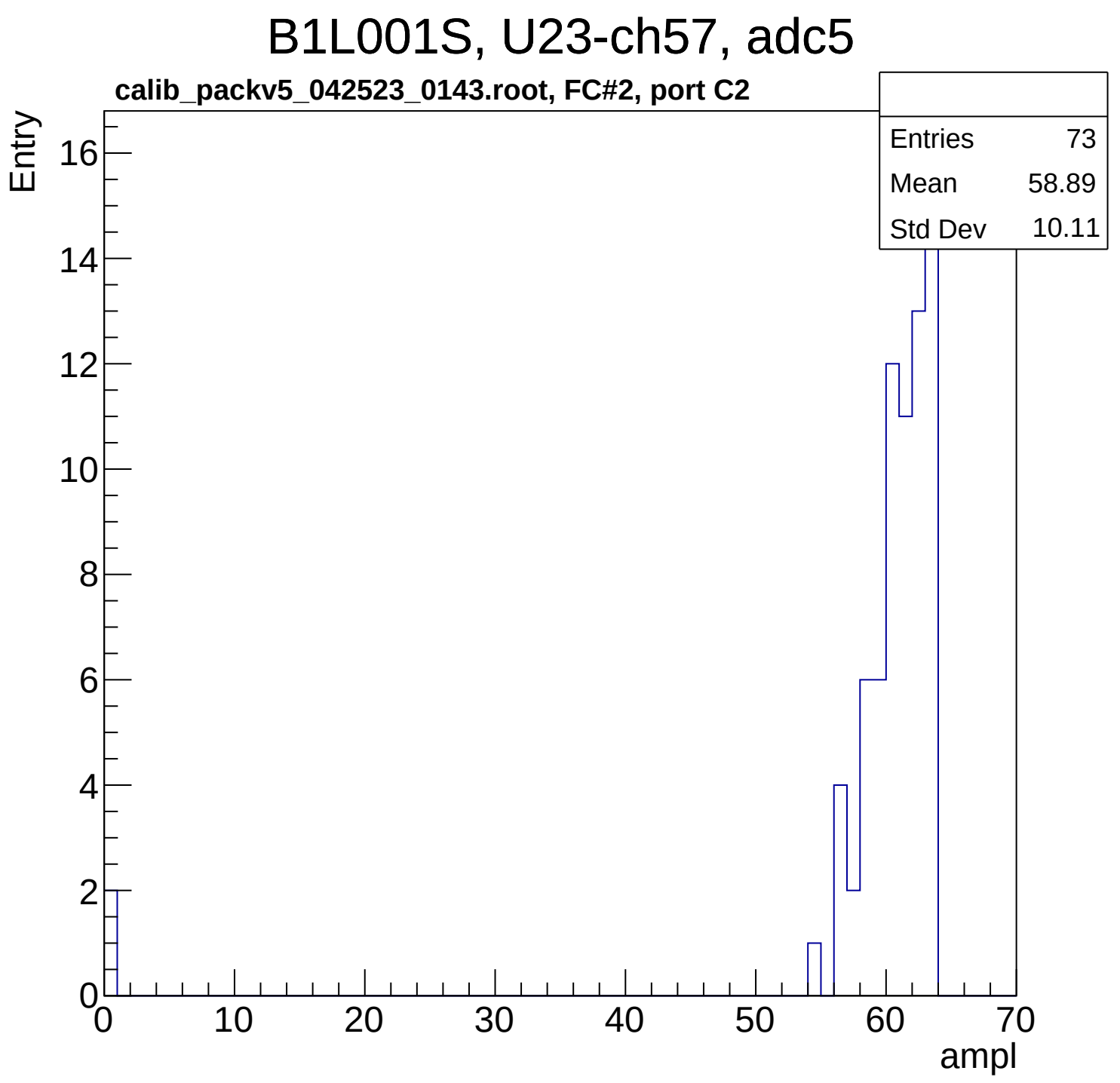
50
60
70
ampl


B1L001S, U23-ch57, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 56.66
Std Dev 3.221
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch57, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 73
Mean 58.89
Std Dev 10.11
Entry
0
2
4
6
8
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12
14
16
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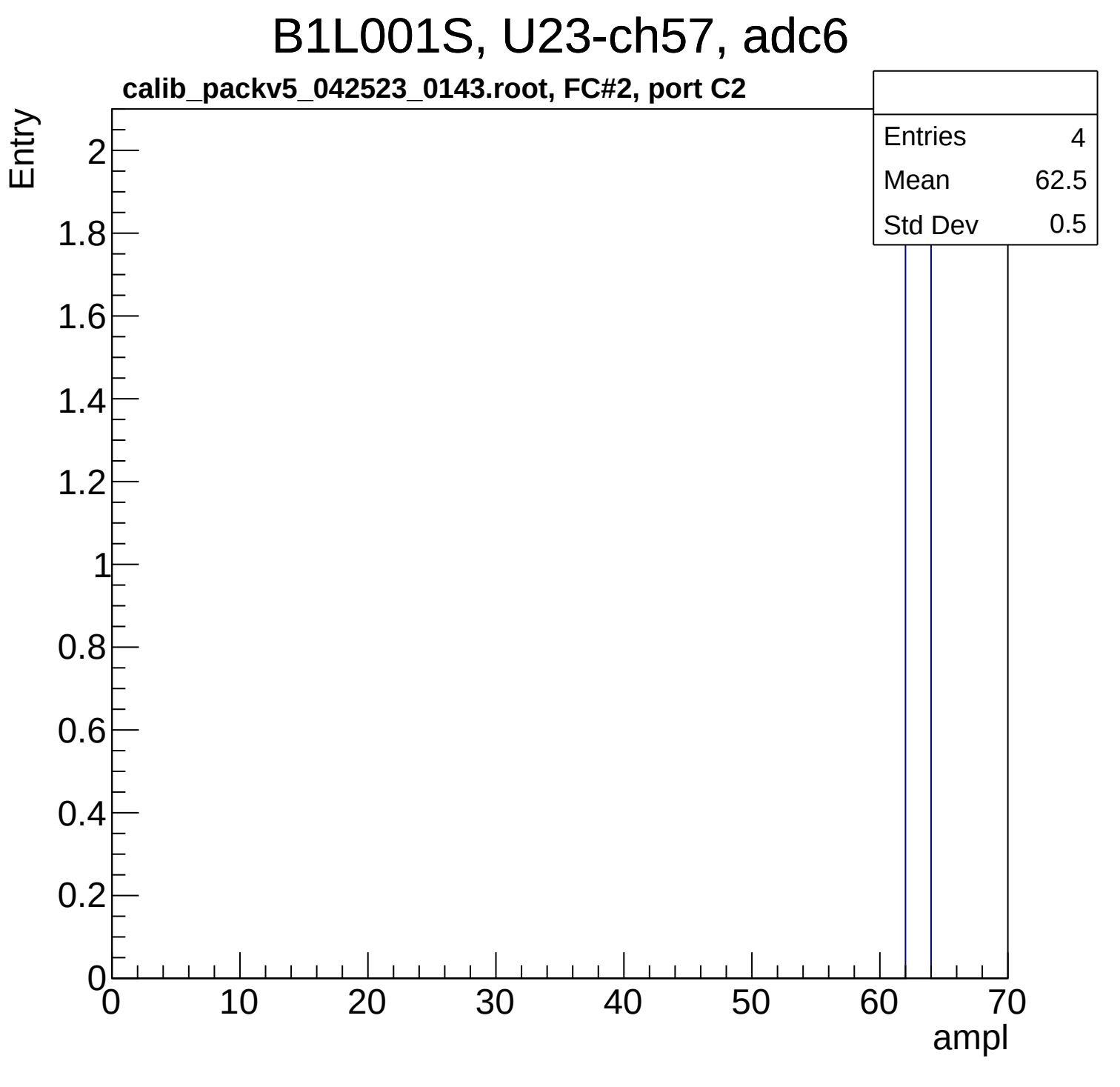
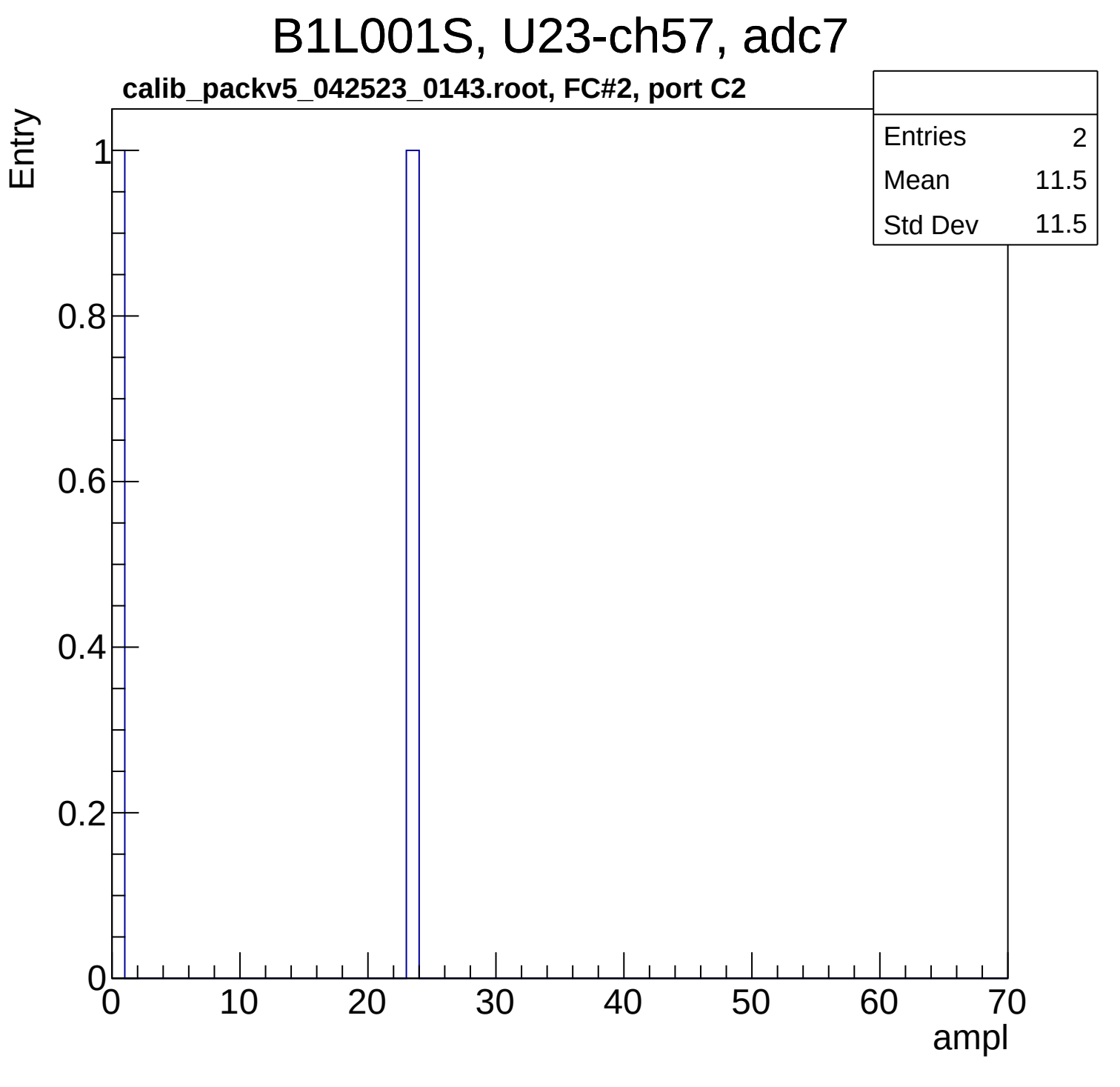
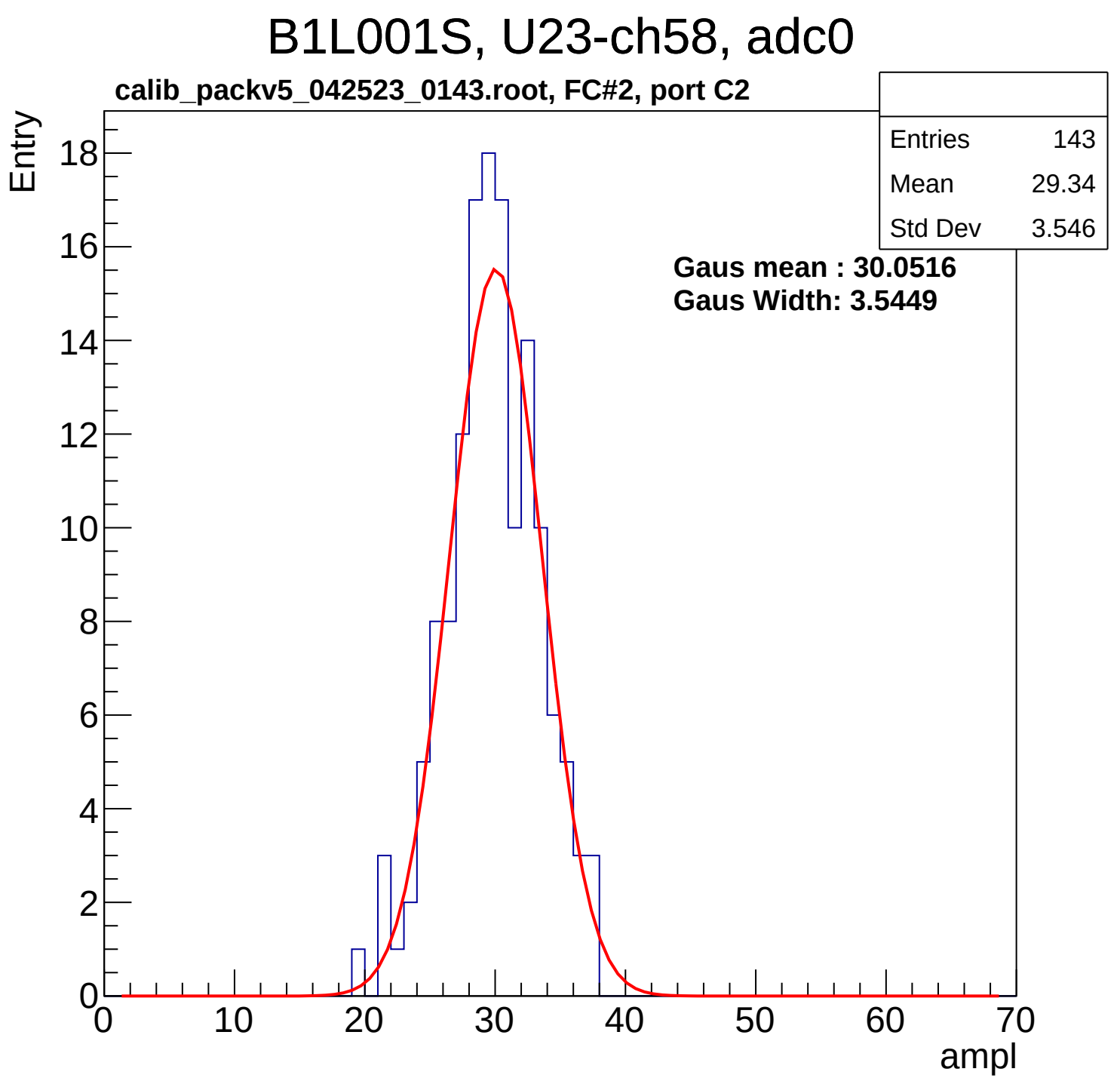
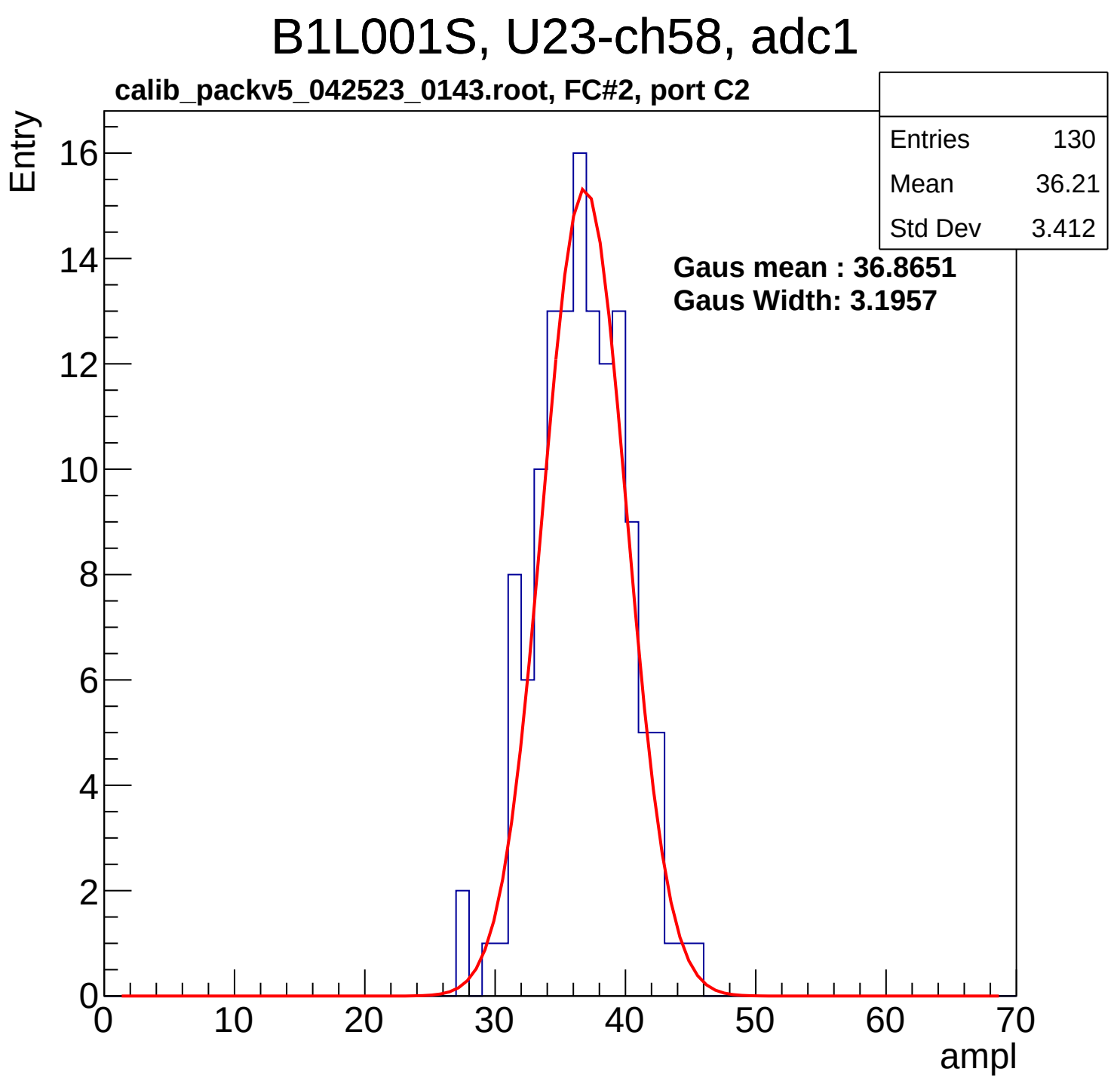
60
70
ampl


B1L001S, U23-ch57, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch57, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch58, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 29.34
Std Dev 3.546
Gaus mean : 30.0516
Gaus Width: 3.5449
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch58, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 36.21
Std Dev 3.412
Gaus mean : 36.8651
Gaus Width: 3.1957
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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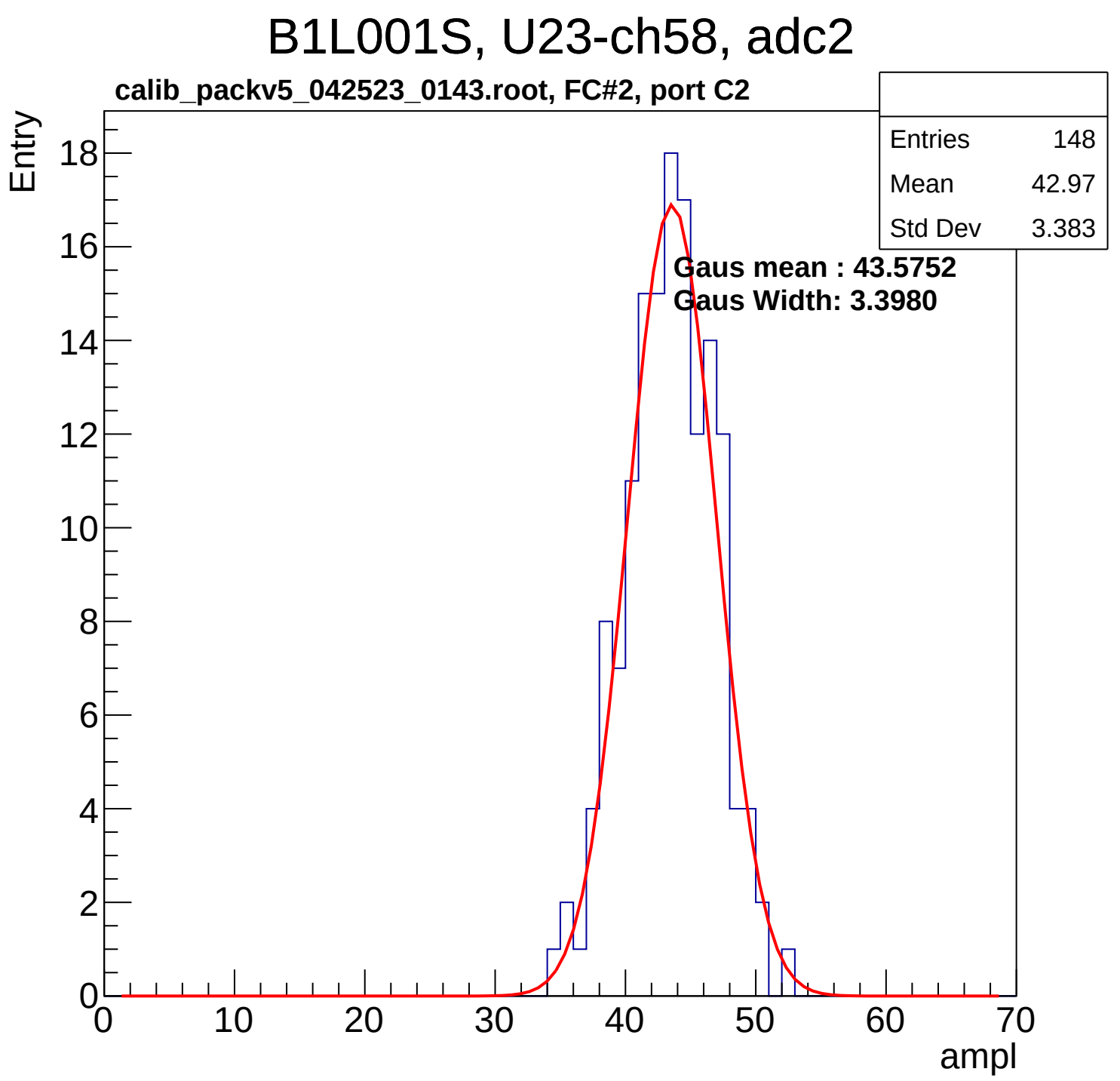
B1L001S, U23-ch58, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 42.97
Std Dev 3.383
Gaus mean : 43.5752
Gaus Width: 3.3980
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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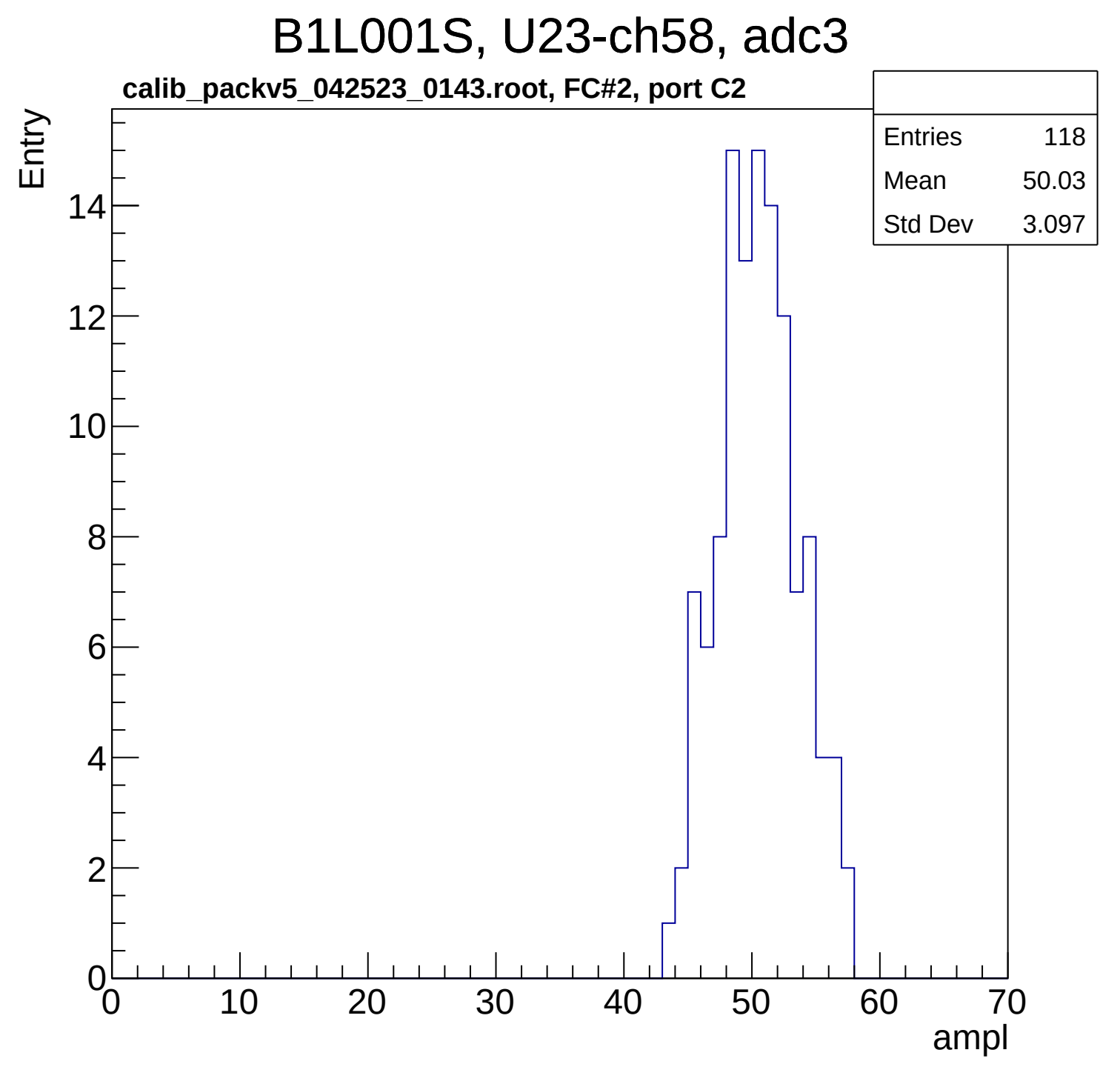
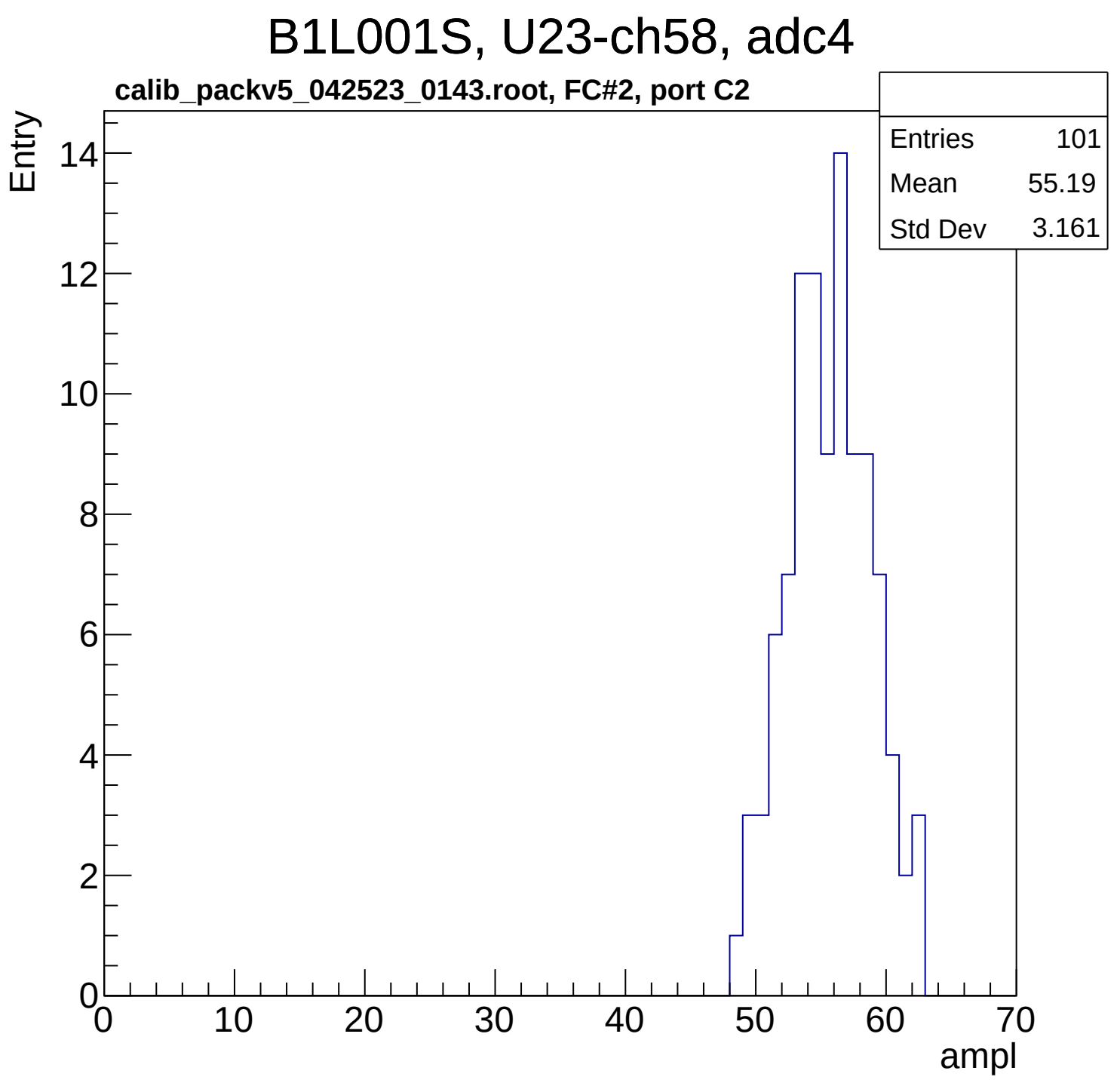
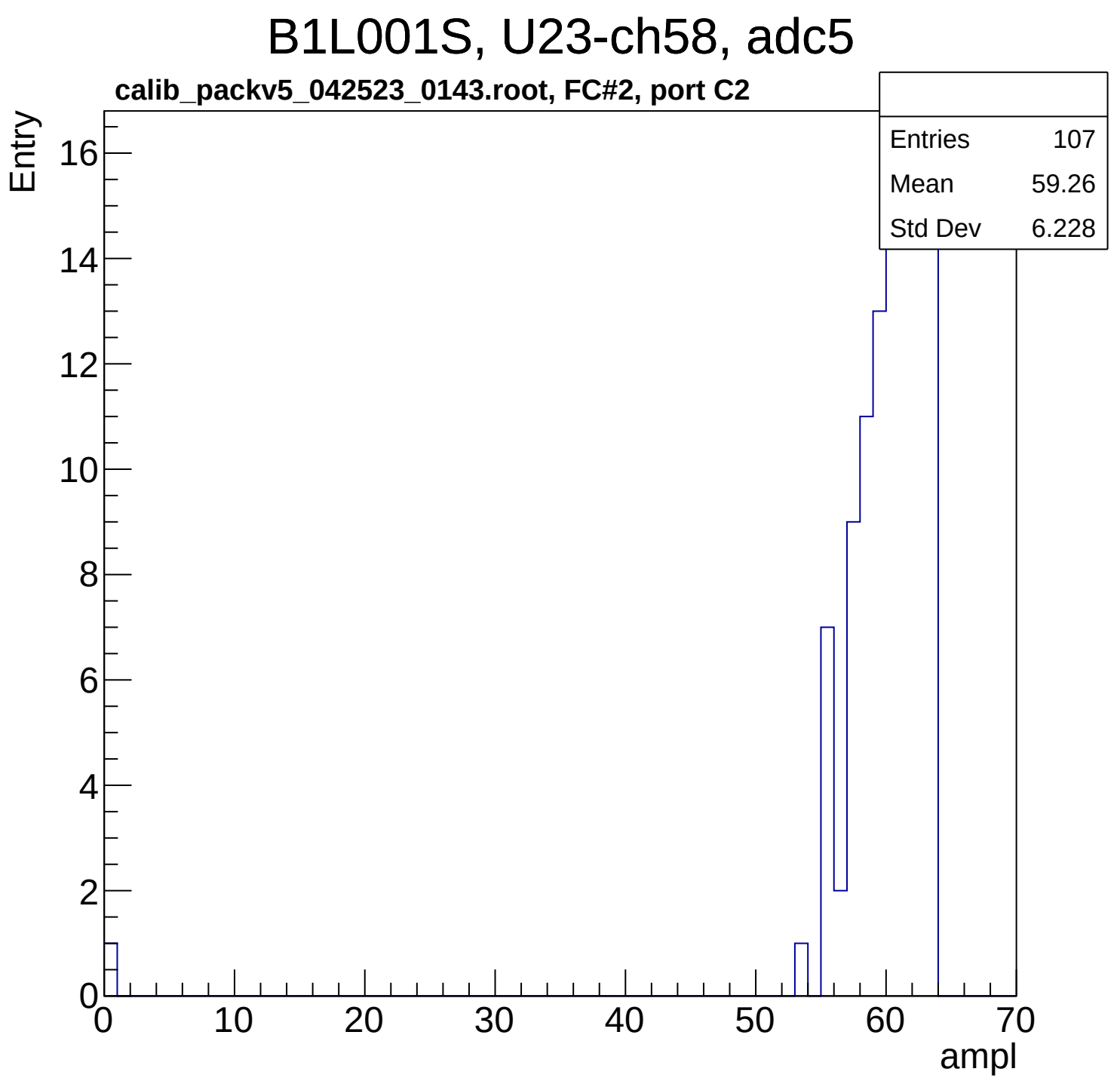
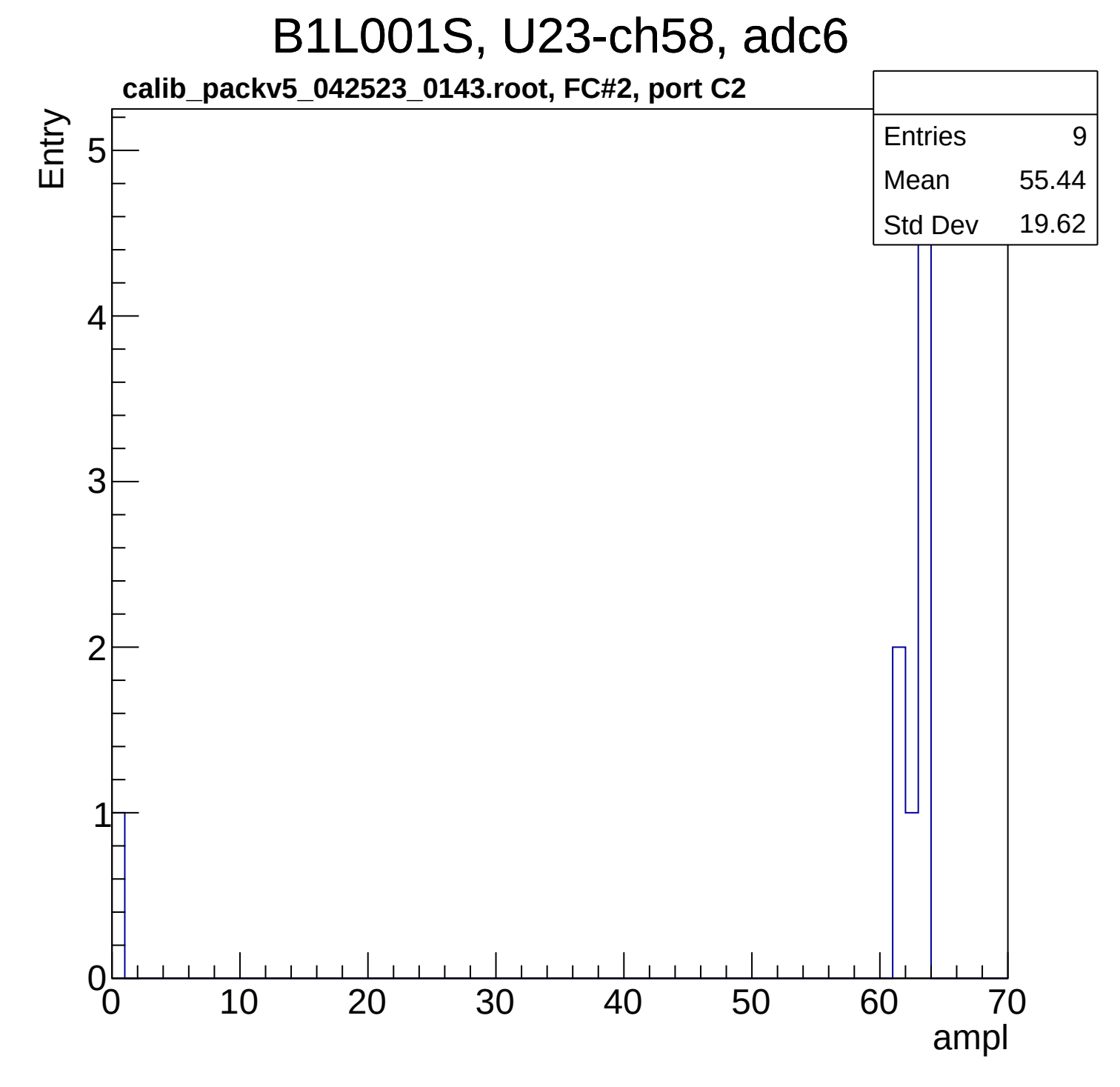
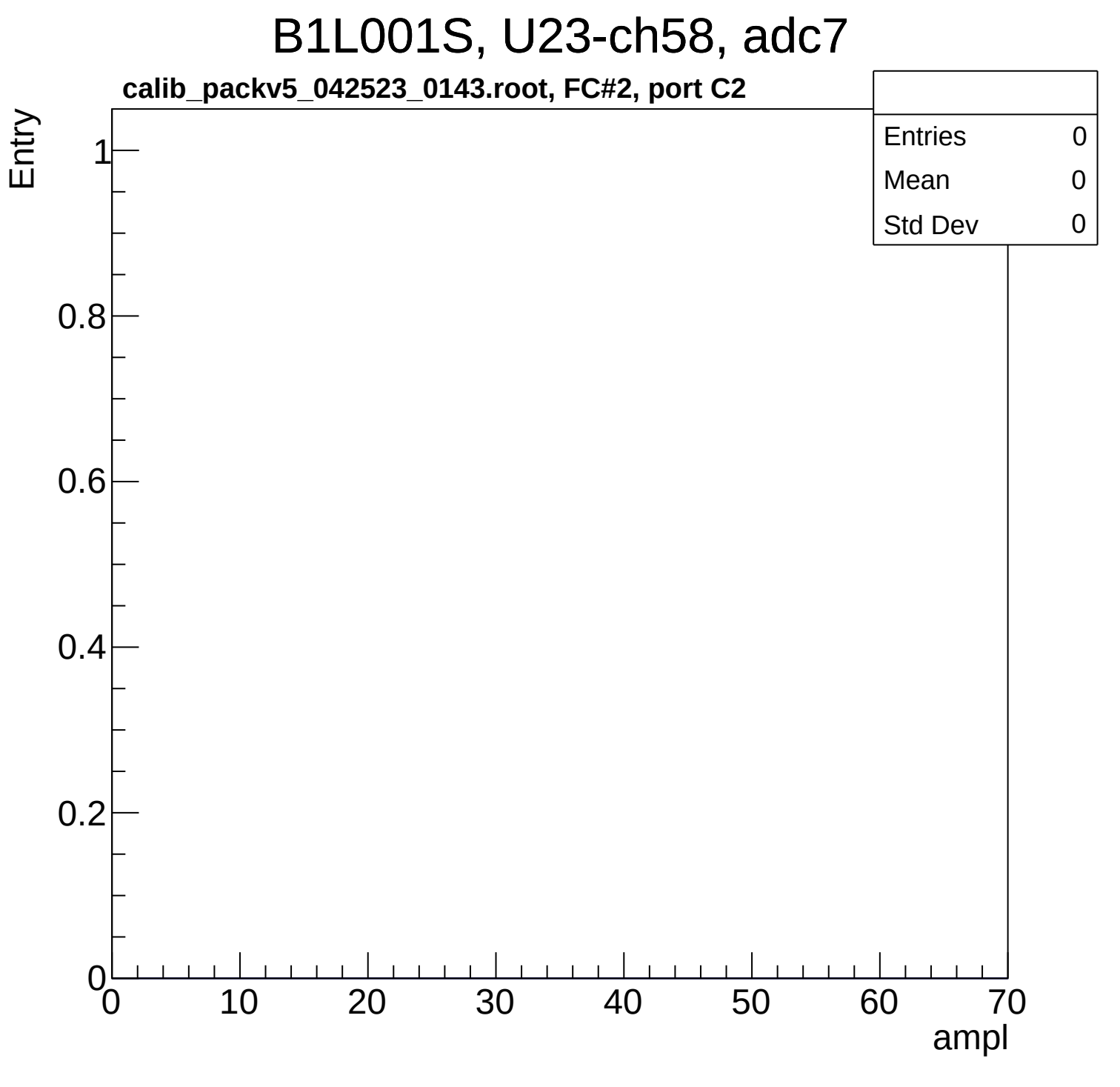

B1L001S, U23-ch58, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 50.03
Std Dev 3.097
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch58, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 55.19
Std Dev 3.161
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch58, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 59.26
Std Dev 6.228
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch58, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 55.44
Std Dev 19.62
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch58, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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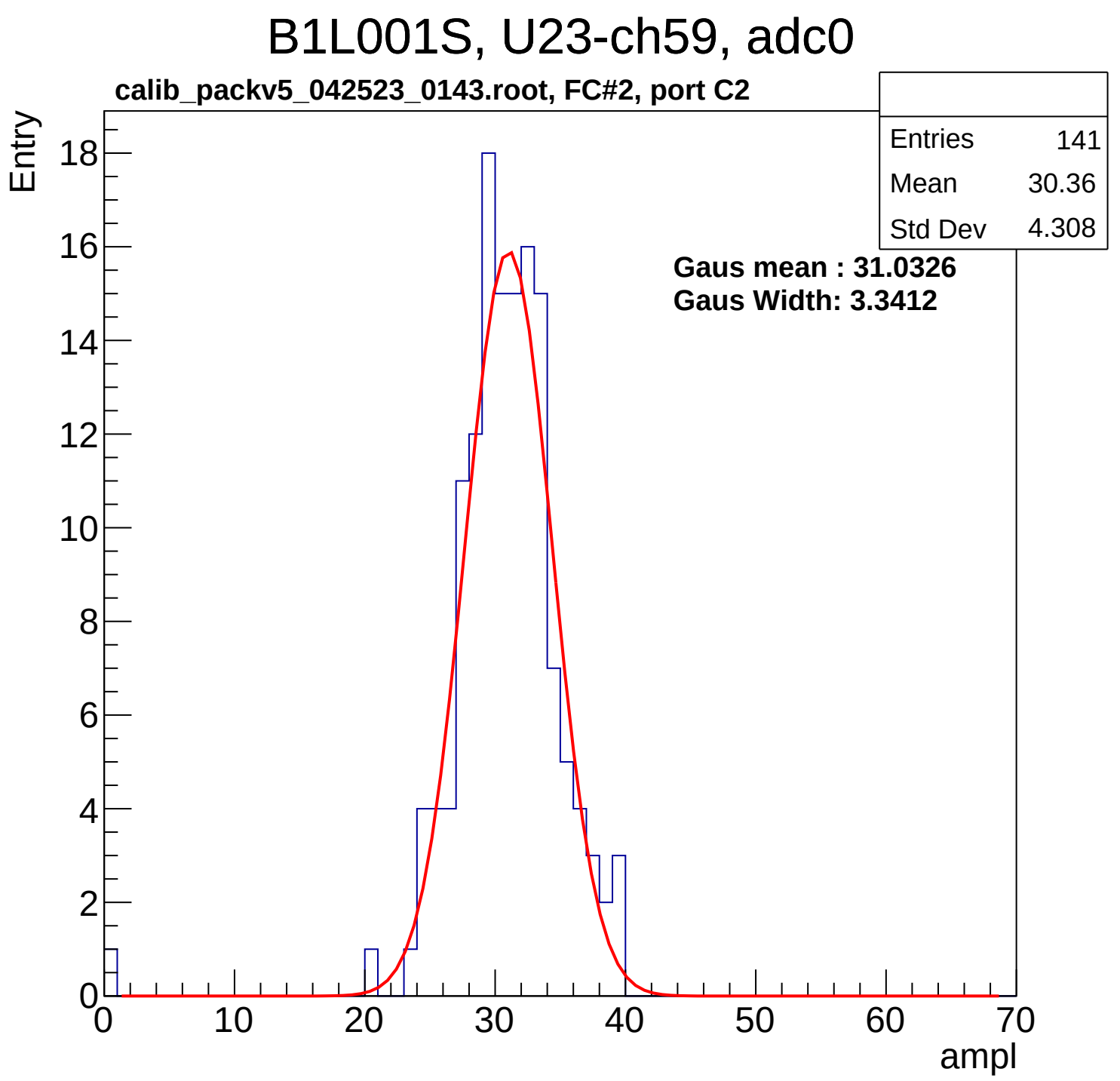
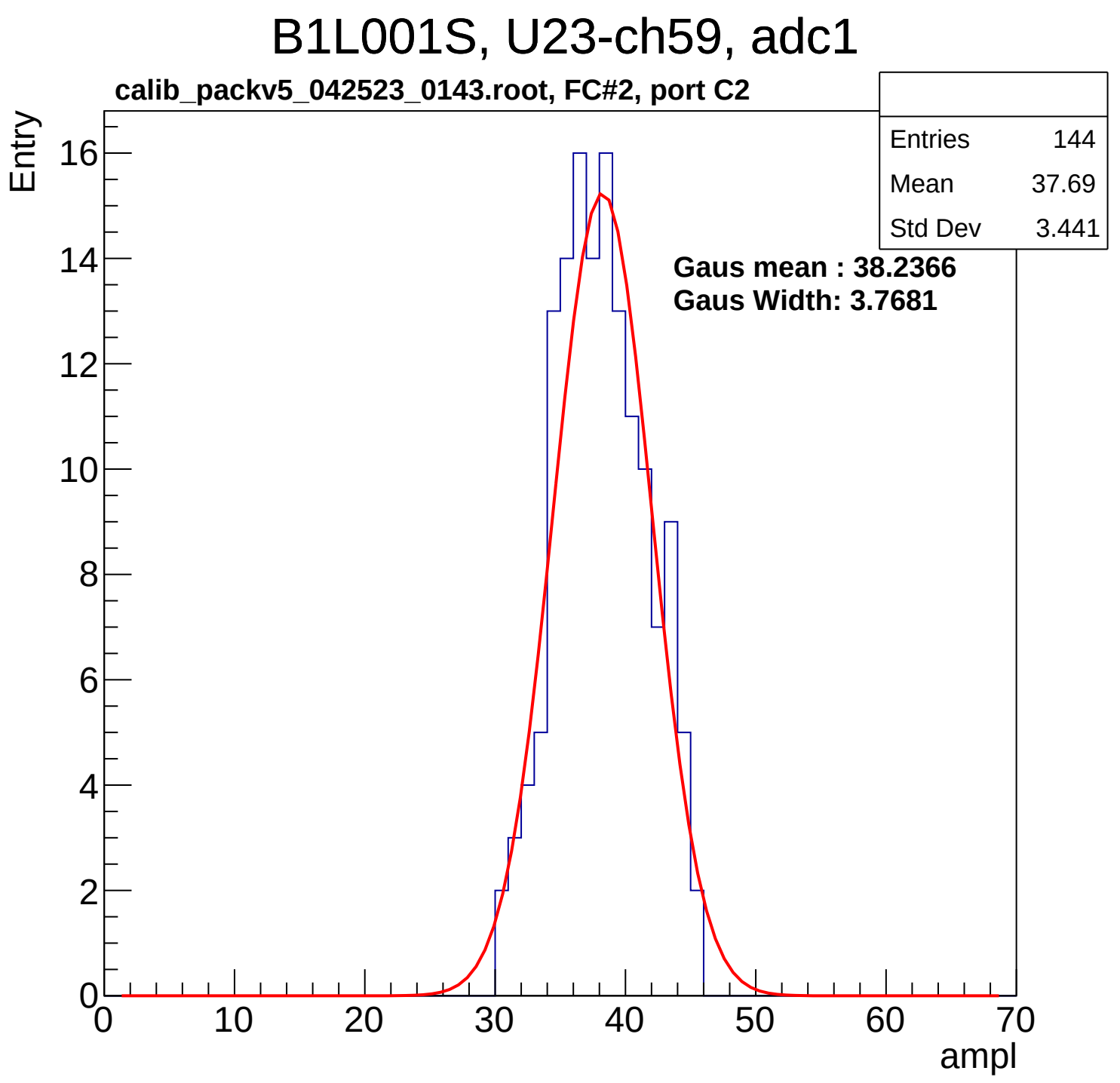
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch59, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 30.36
Std Dev 4.308
Gaus mean : 31.0326
Gaus Width: 3.3412
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch59, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 37.69
Std Dev 3.441
Gaus mean : 38.2366
Gaus Width: 3.7681
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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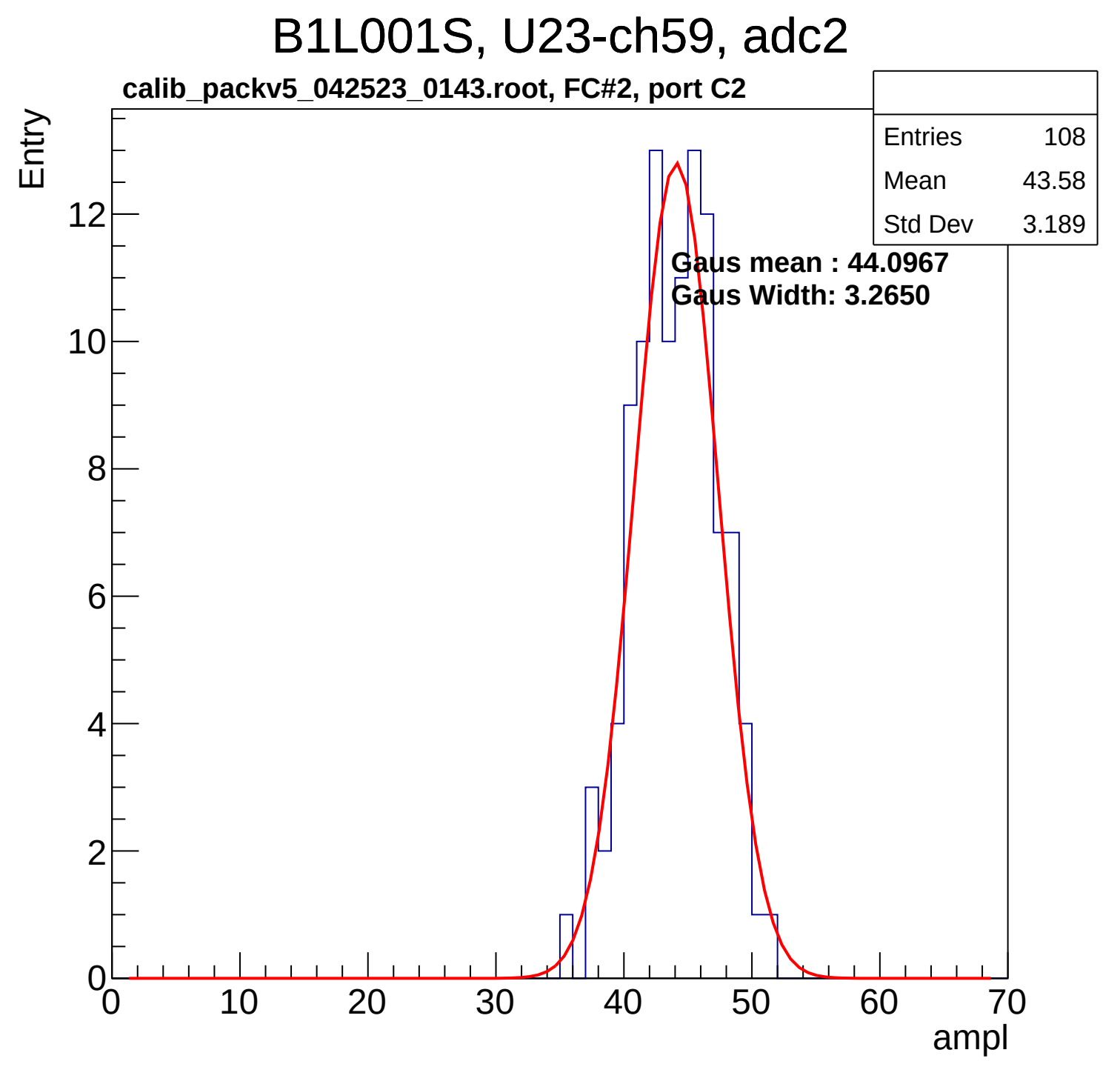
B1L001S, U23-ch59, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 43.58
Std Dev 3.189
Gaus mean : 44.0967
Gaus Width: 3.2650
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
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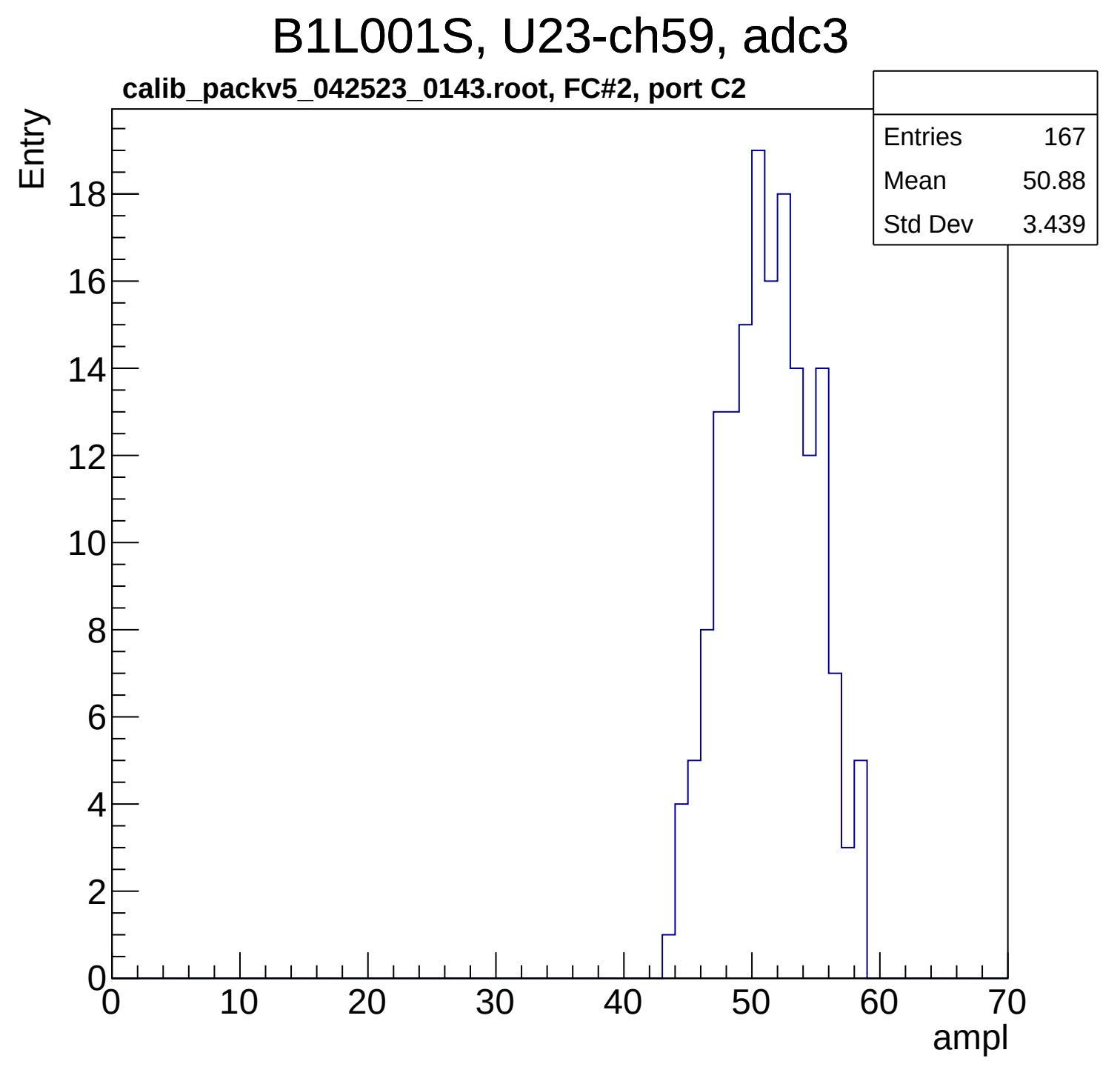
B1L001S, U23-ch59, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 50.88
Std Dev 3.439
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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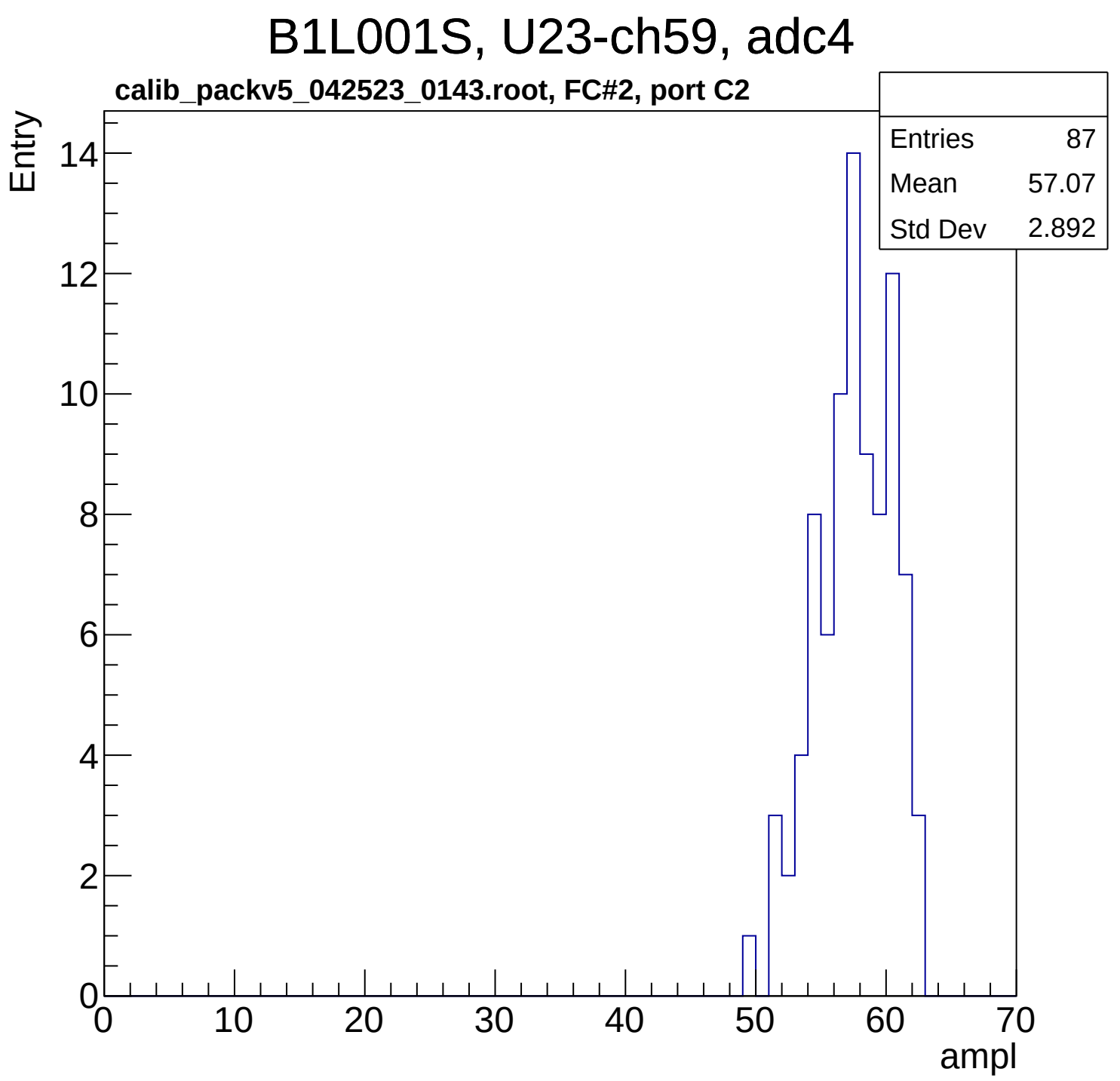
B1L001S, U23-ch59, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 57.07
Std Dev 2.892
Entry
ampl
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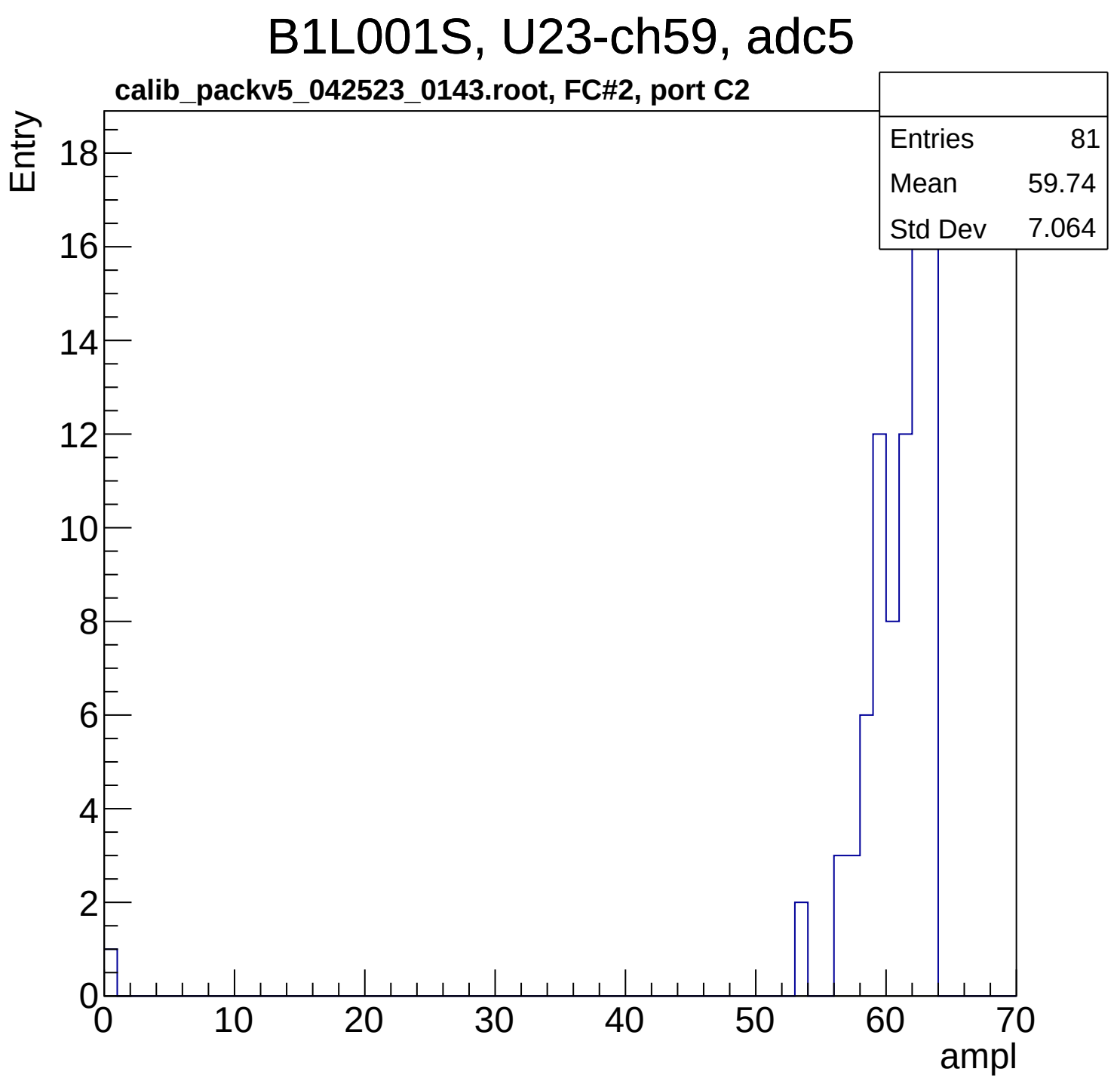
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch59, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 59.74
Std Dev 7.064
Entry
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4
6
8
10
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16
18
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10
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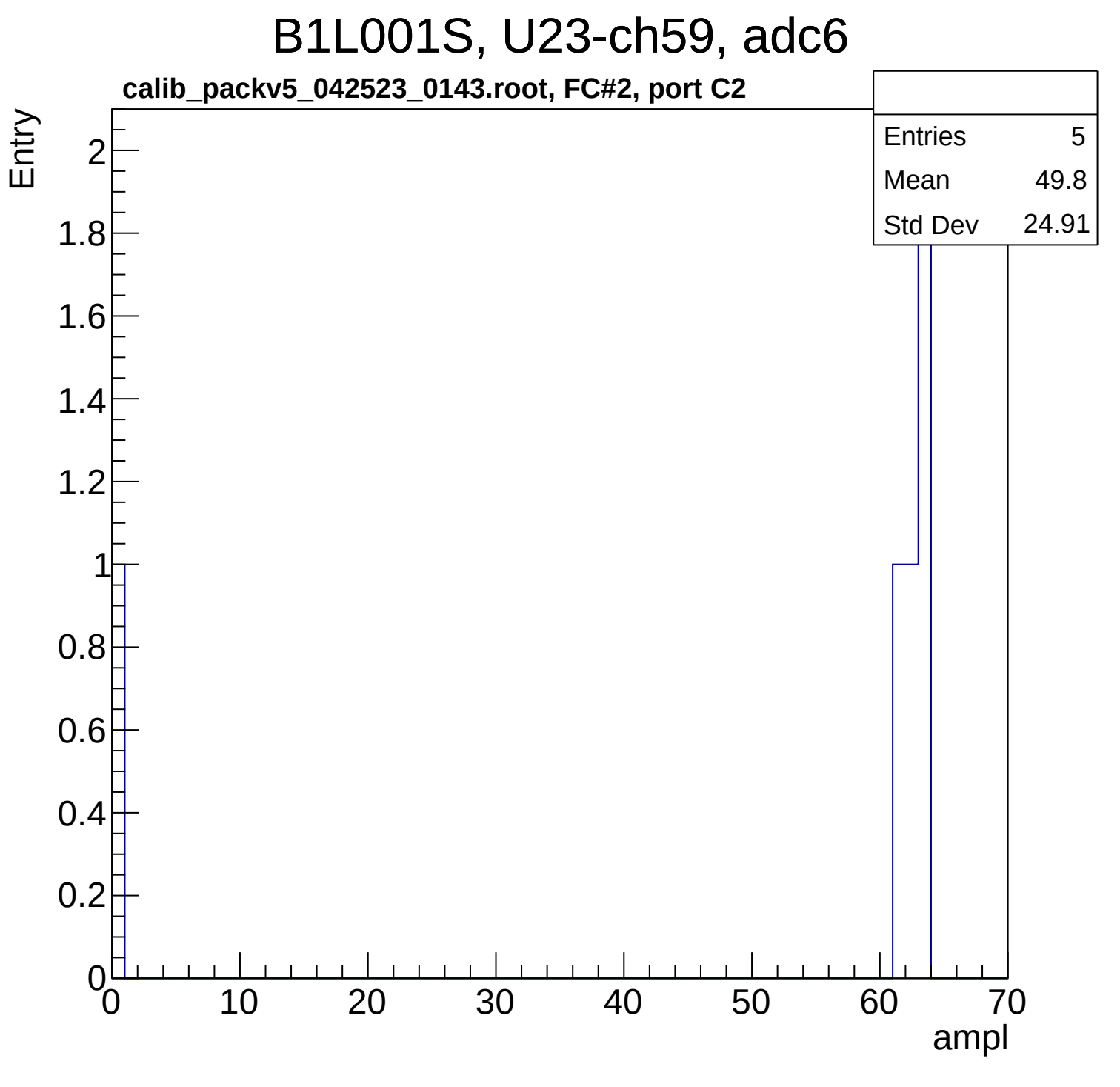
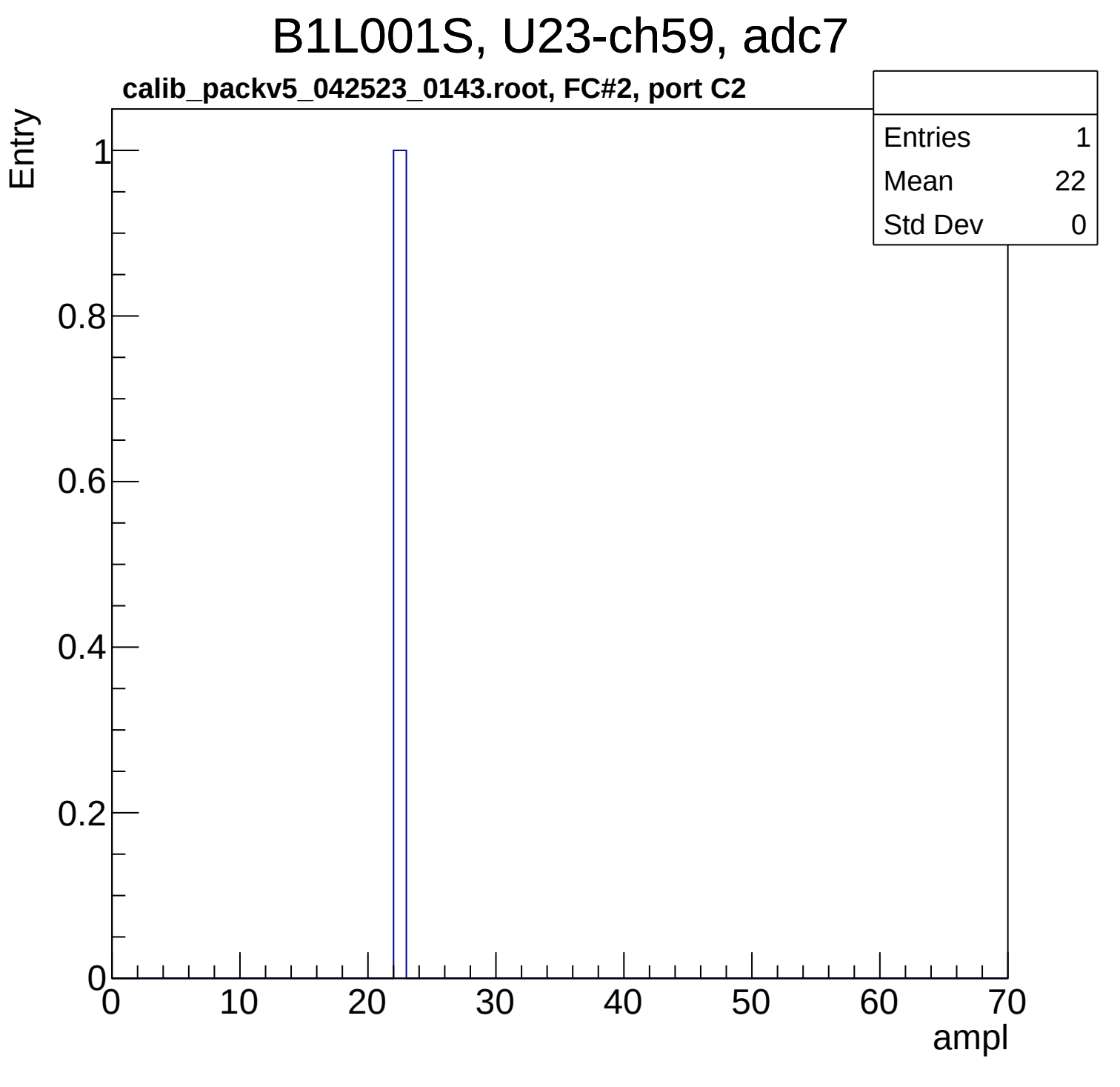
50
60
70
ampl


B1L001S, U23-ch59, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch59, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
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0
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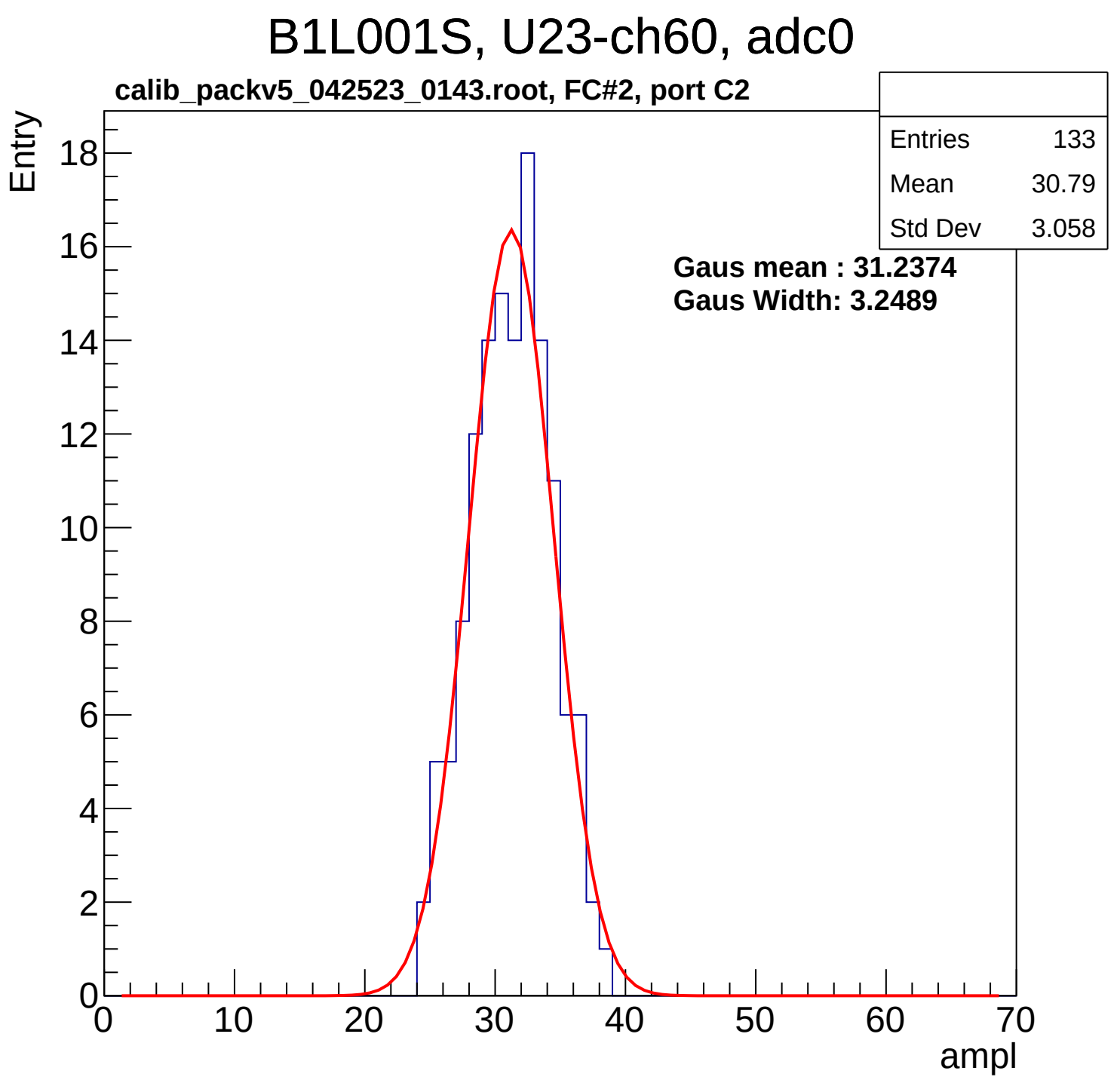
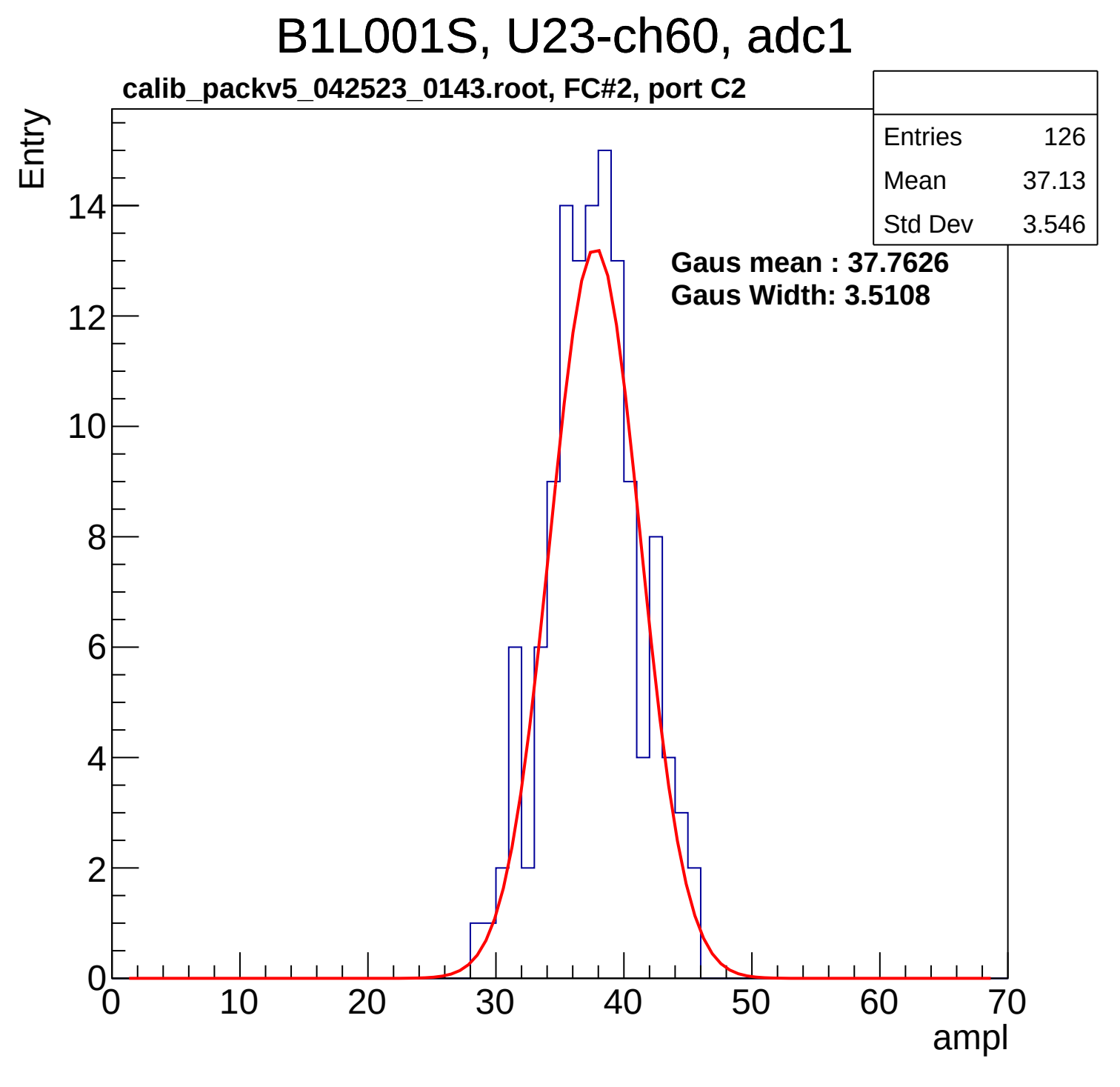
50
60
70
ampl


B1L001S, U23-ch60, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 30.79
Std Dev 3.058
Gaus mean : 31.2374
Gaus Width: 3.2489
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch60, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 37.13
Std Dev 3.546
Gaus mean : 37.7626
Gaus Width: 3.5108
Entry
ampl
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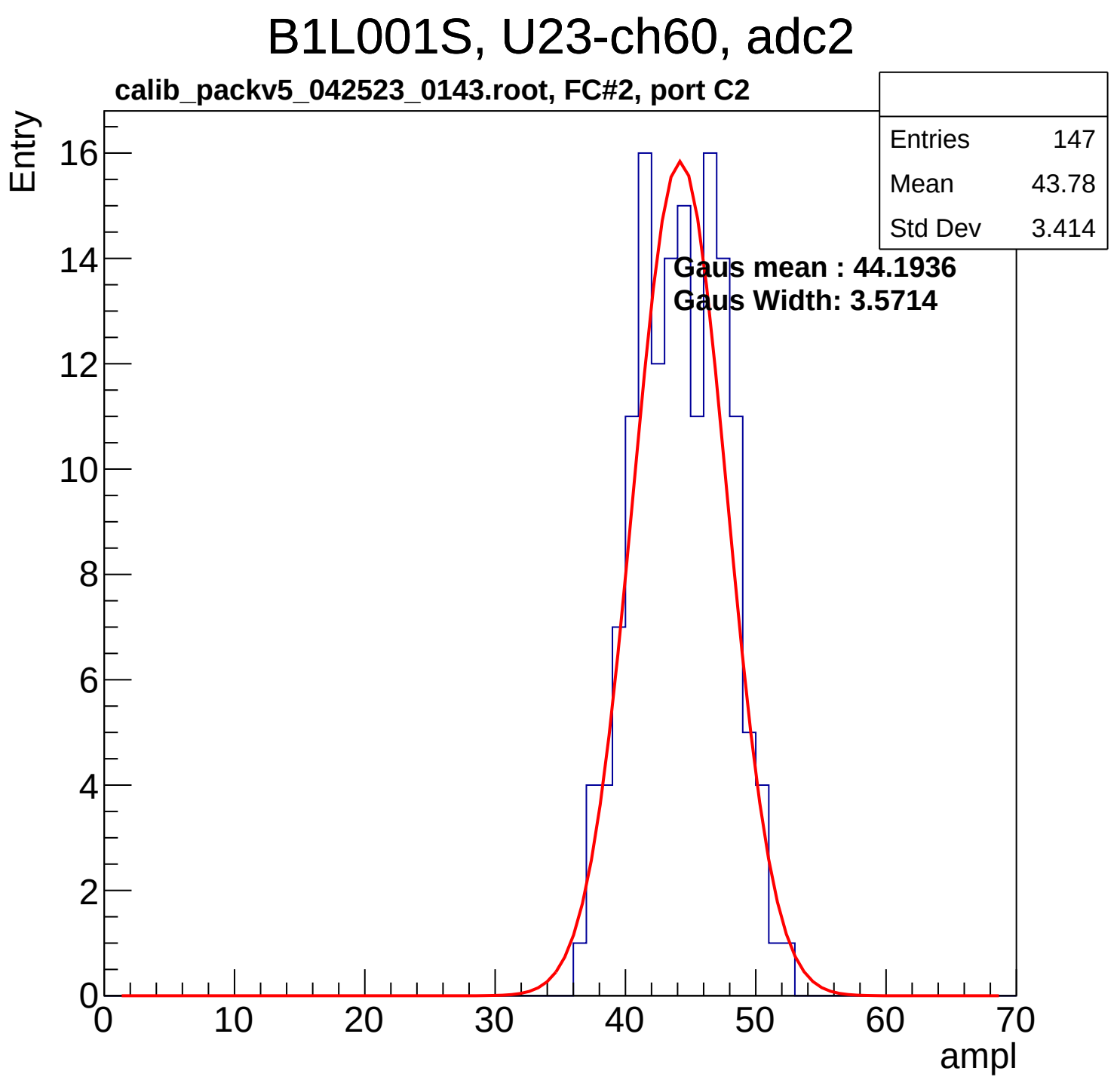
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch60, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 43.78
Std Dev 3.414
Gaus mean : 44.1936
Gaus Width: 3.5714
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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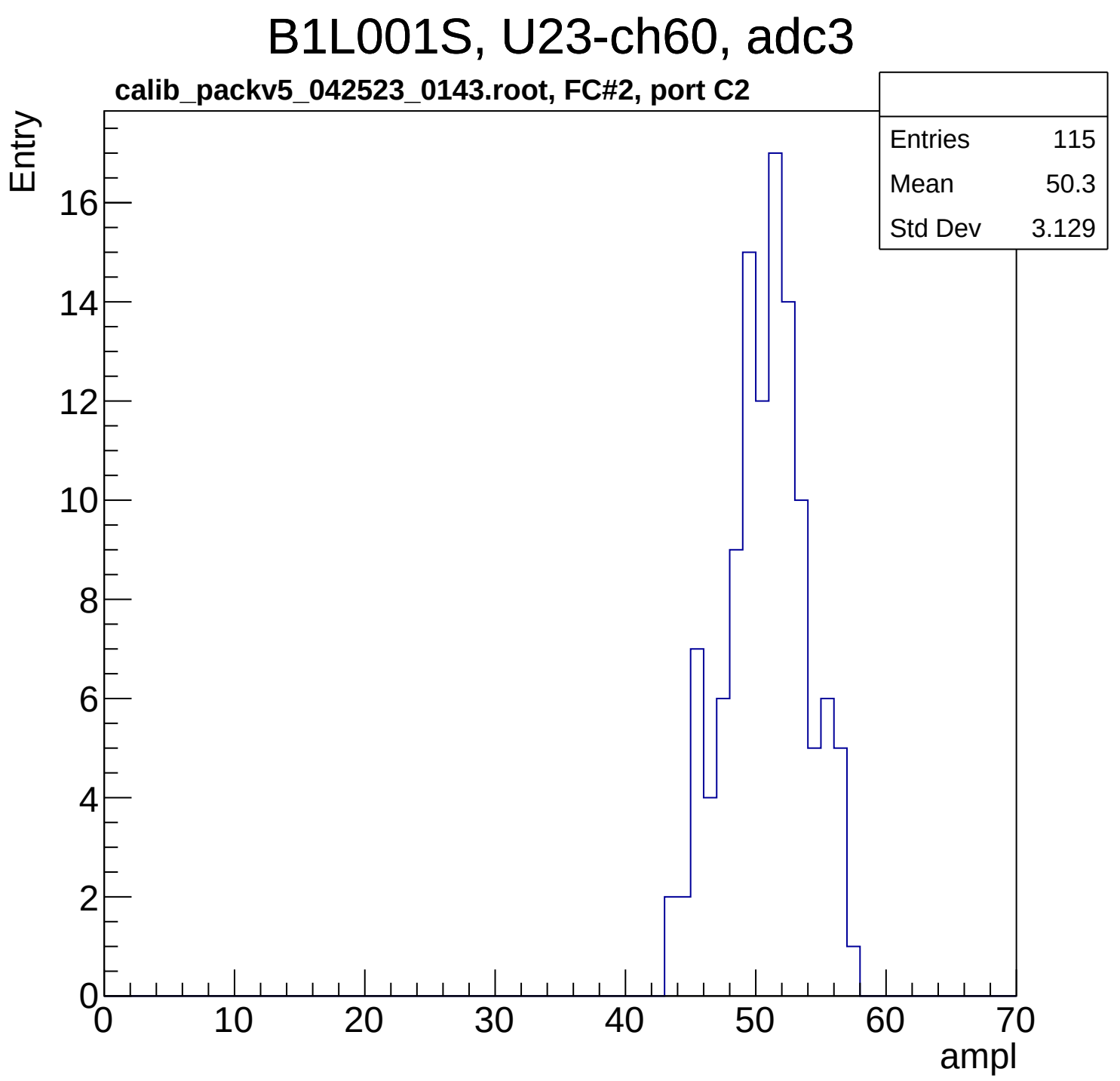
B1L001S, U23-ch60, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 50.3
Std Dev 3.129
Entry
ampl
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4
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10
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0
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60
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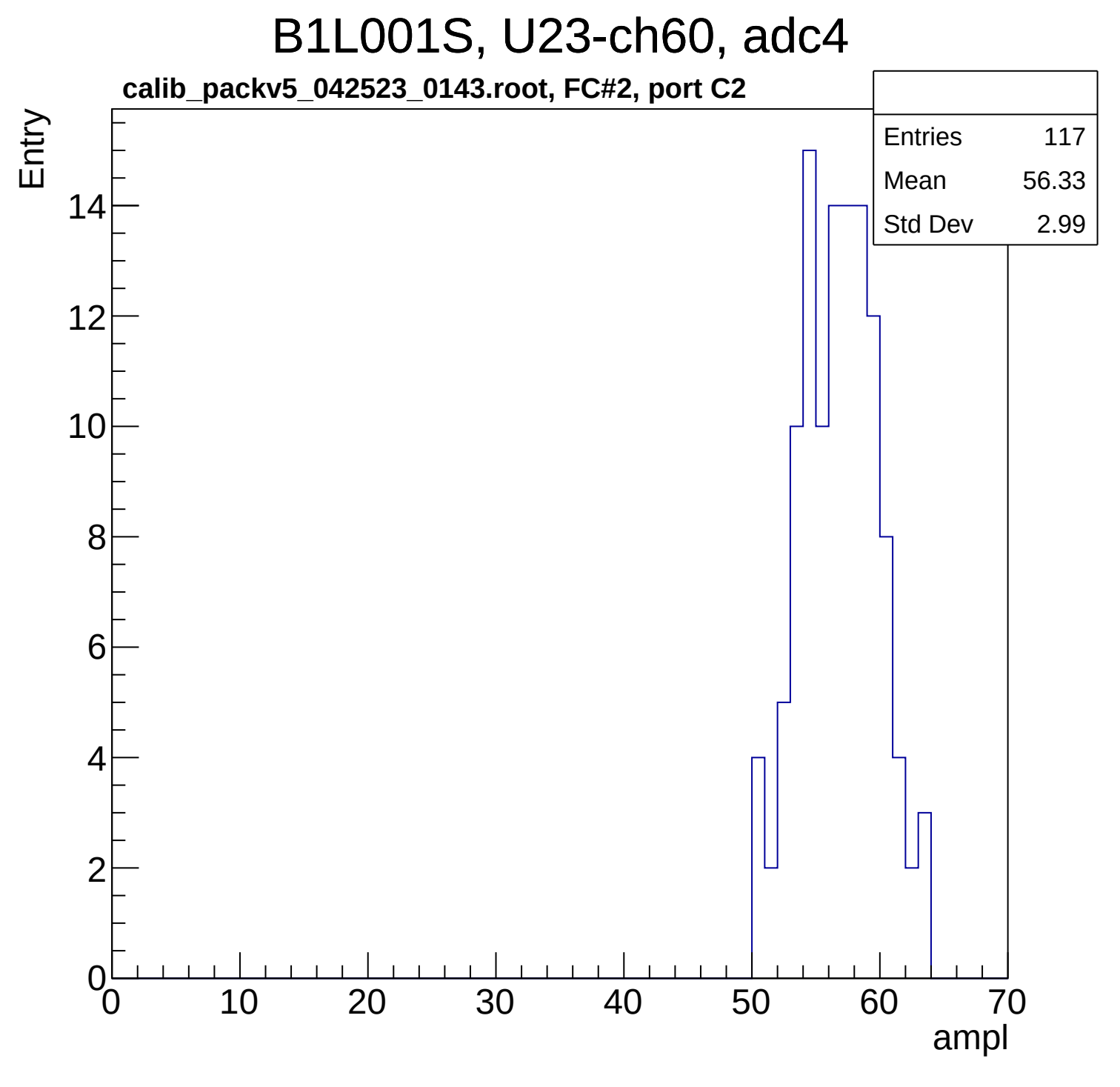
B1L001S, U23-ch60, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 56.33
Std Dev 2.99
Entry
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2
4
6
8
10
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14
0
10
20
30
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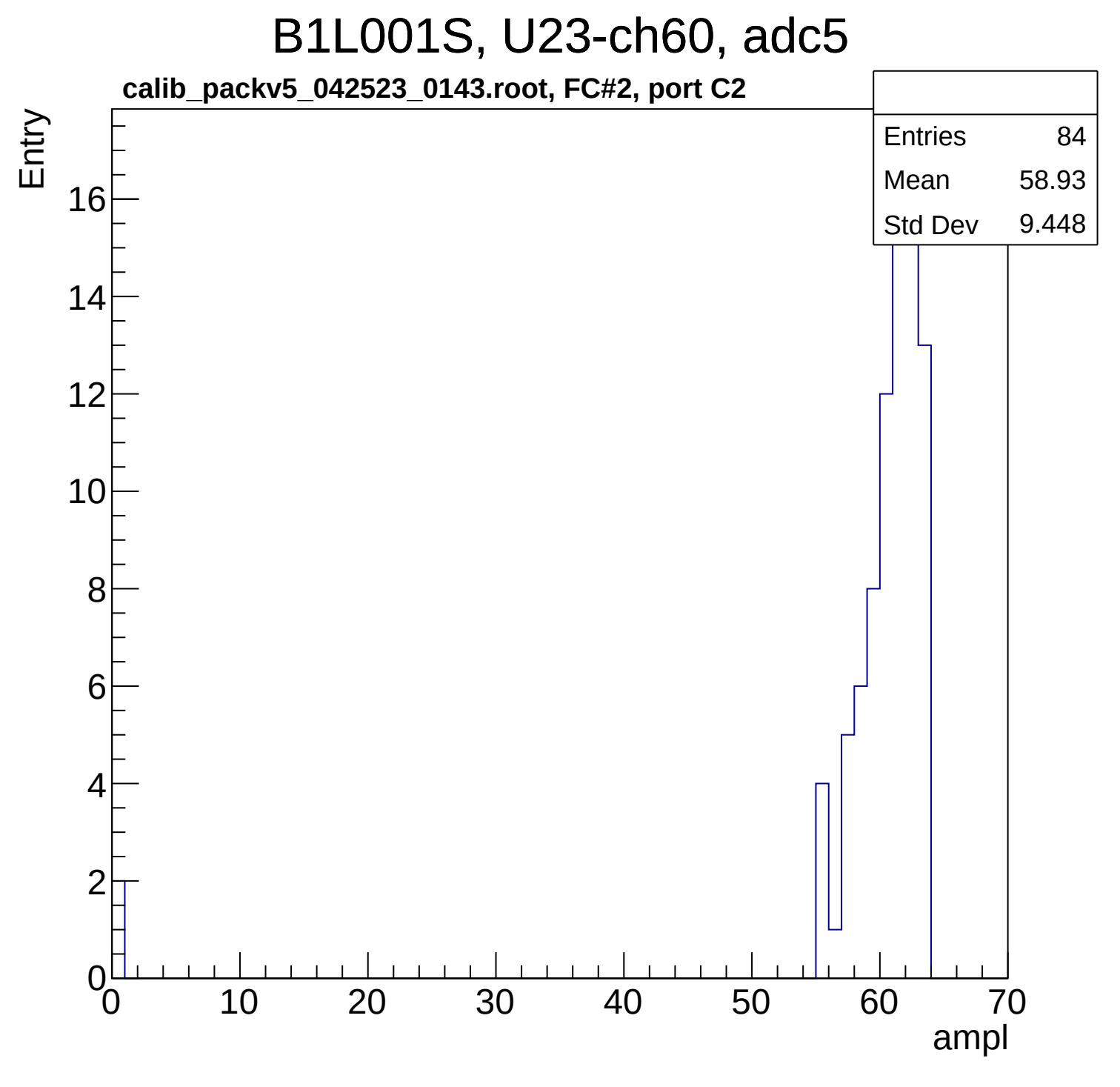
50
60
70
ampl


B1L001S, U23-ch60, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 58.93
Std Dev 9.448
Entry
ampl
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2
4
6
8
10
12
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0
10
20
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40
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60
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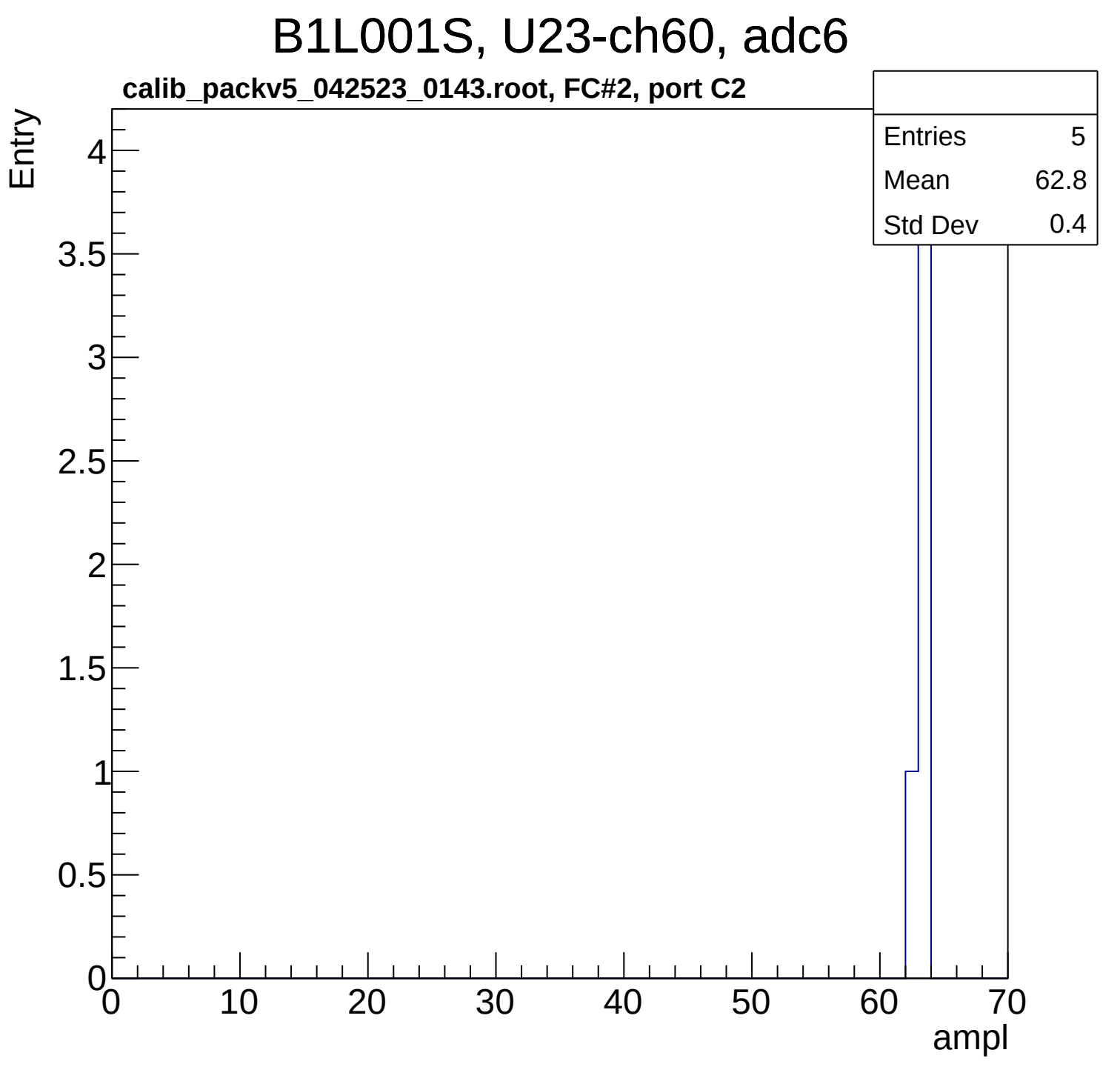
B1L001S, U23-ch60, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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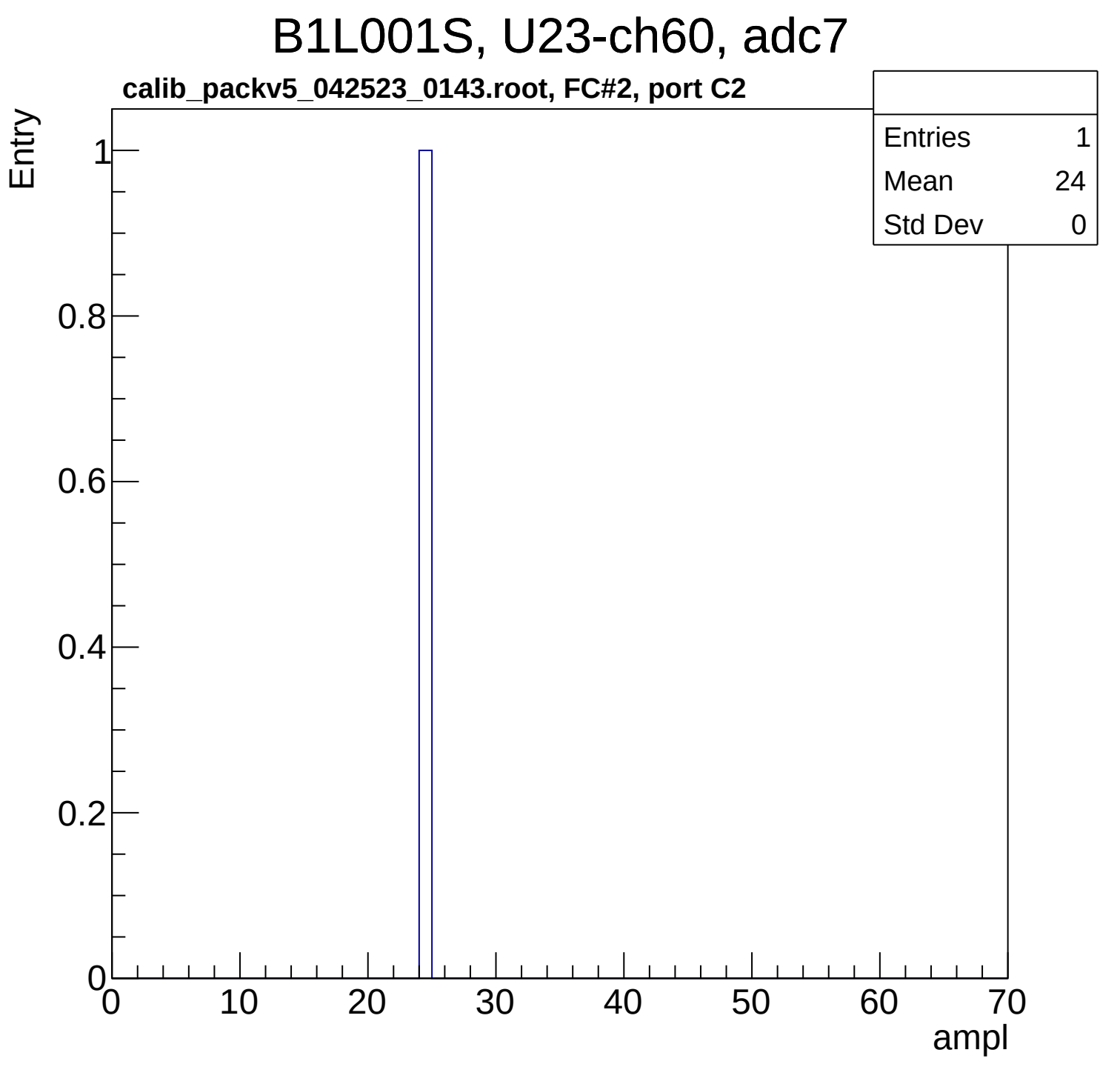
40
50
60
70


B1L001S, U23-ch60, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 24
Std Dev 0
Entry
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20
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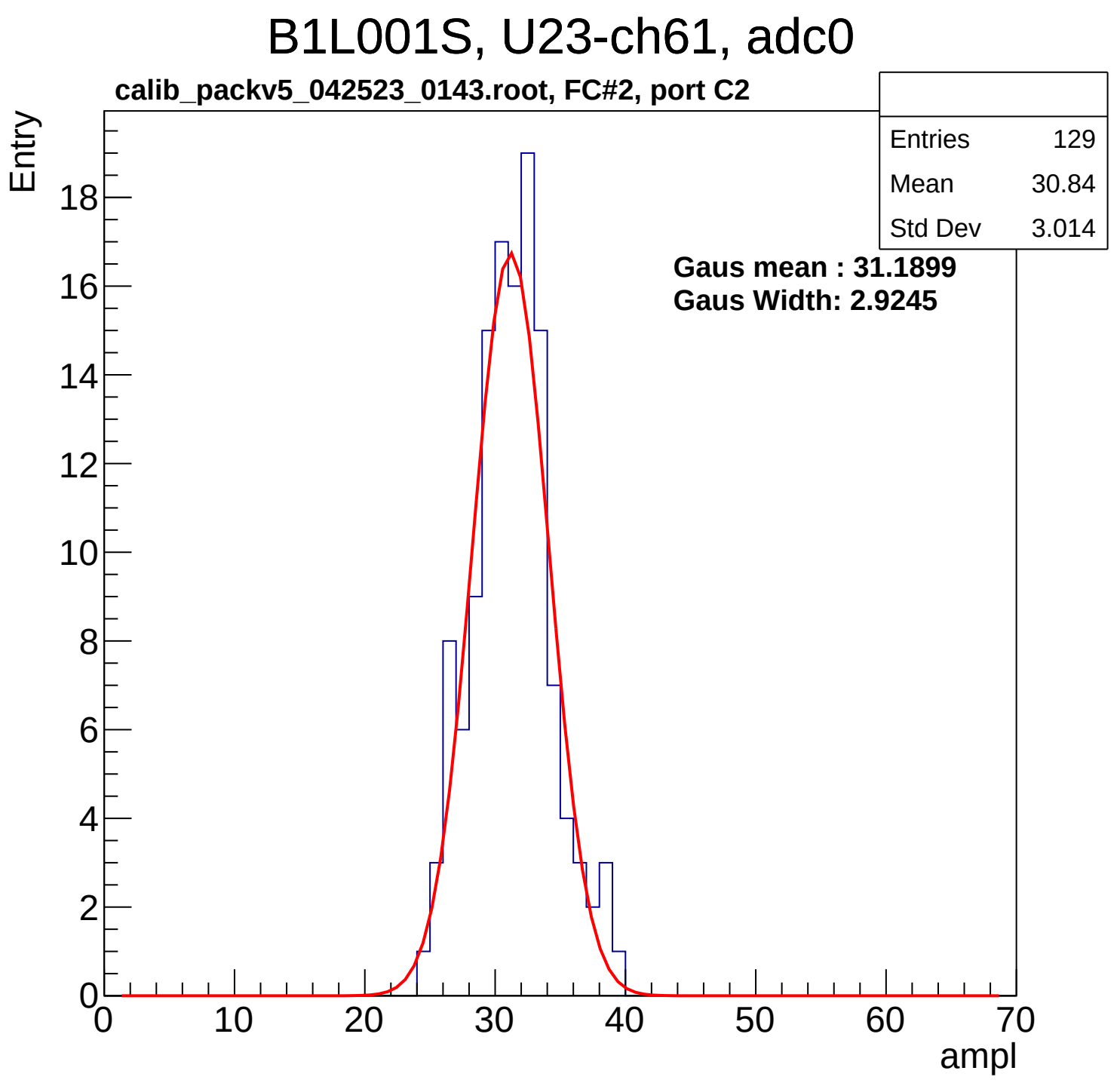
50
60
70
ampl


B1L001S, U23-ch61, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 30.84
Std Dev 3.014
Gaus mean : 31.1899
Gaus Width: 2.9245
Entry
ampl
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4
6
8
10
12
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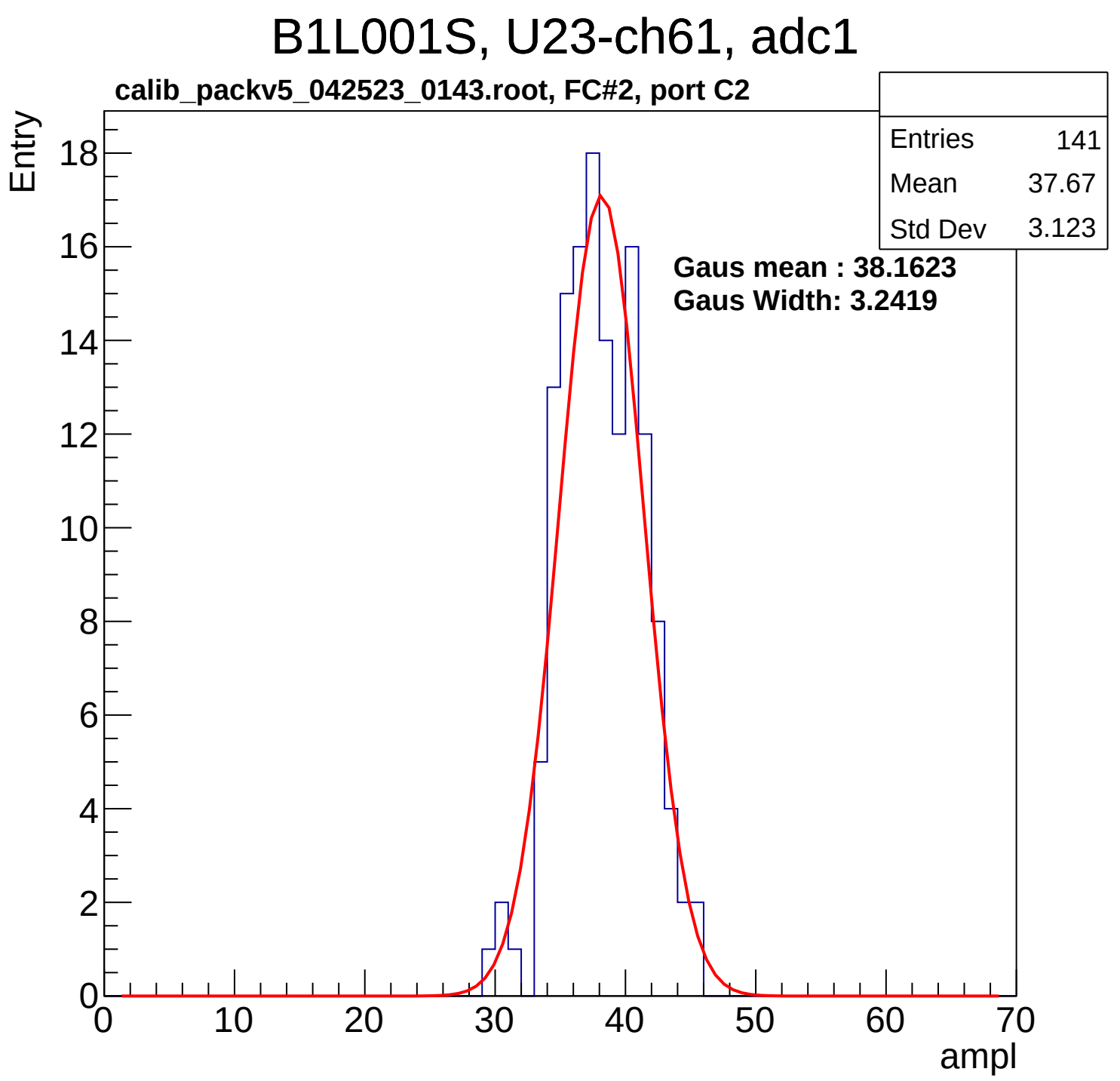
B1L001S, U23-ch61, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 37.67
Std Dev 3.123
Gaus mean : 38.1623
Gaus Width: 3.2419
Entry
ampl
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4
6
8
10
12
14
16
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0
10
20
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40
50
60
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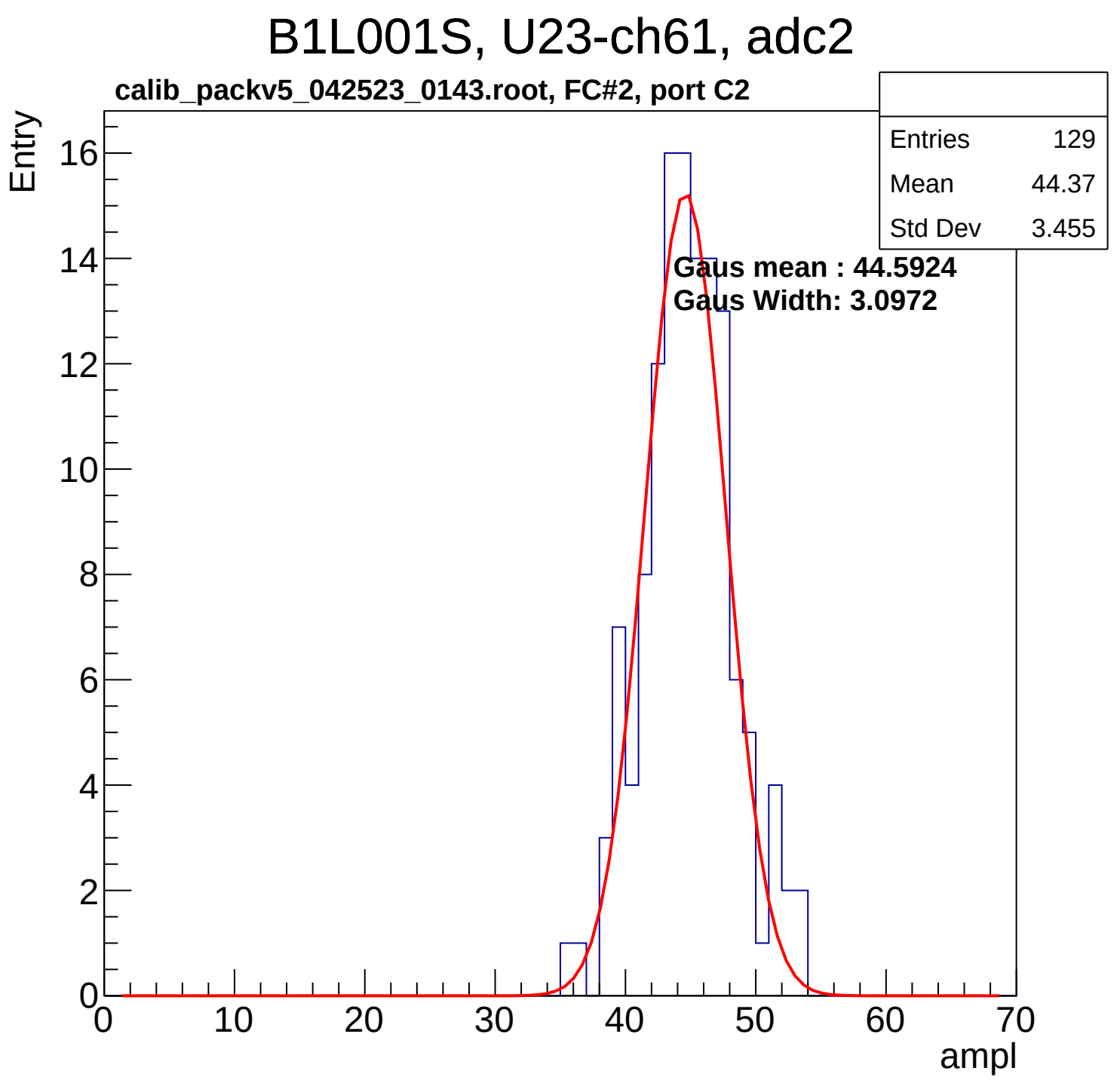
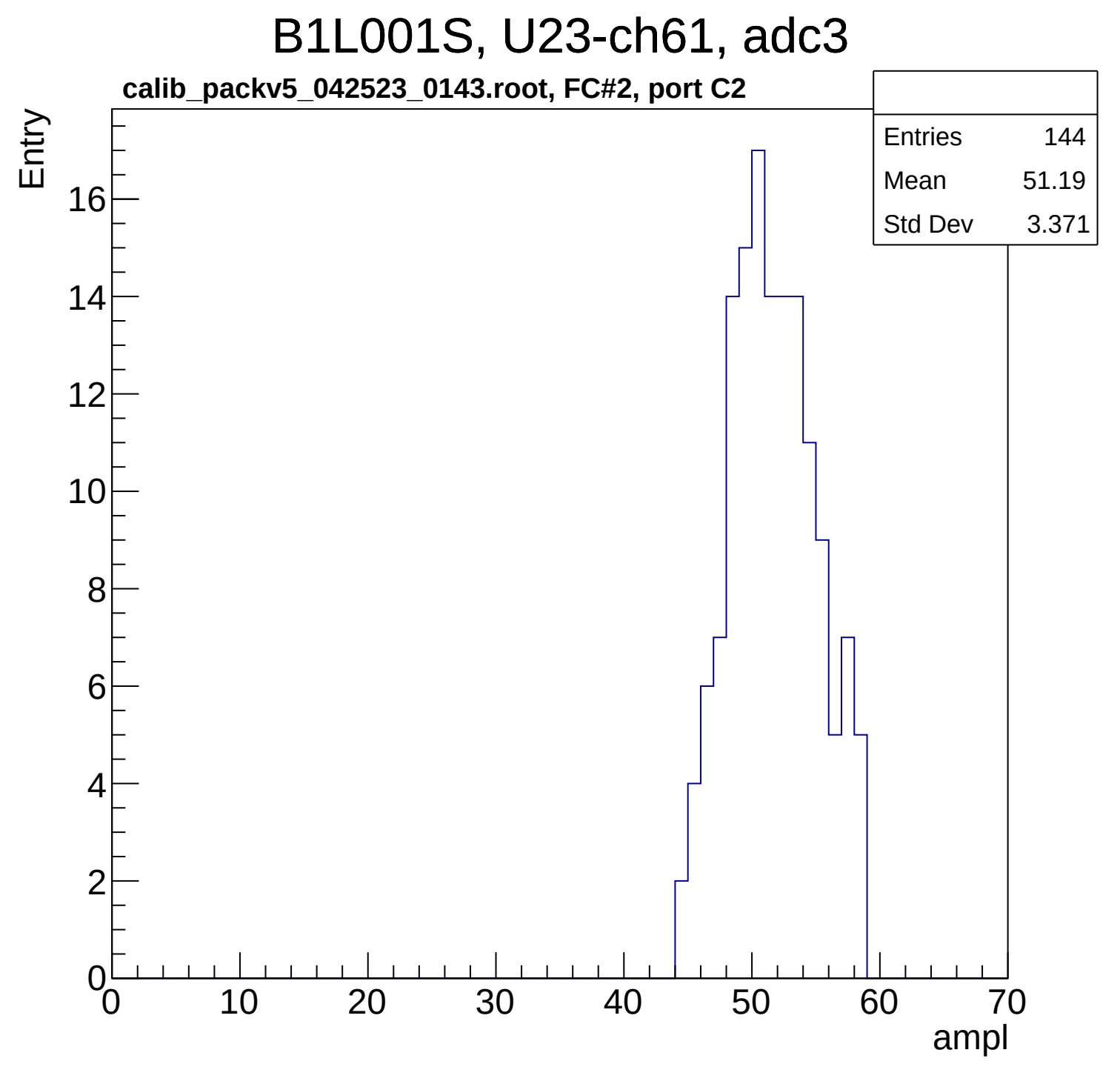

B1L001S, U23-ch61, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 44.37
Std Dev 3.455
Gaus mean : 44.5924
Gaus Width: 3.0972
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch61, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 51.19
Std Dev 3.371
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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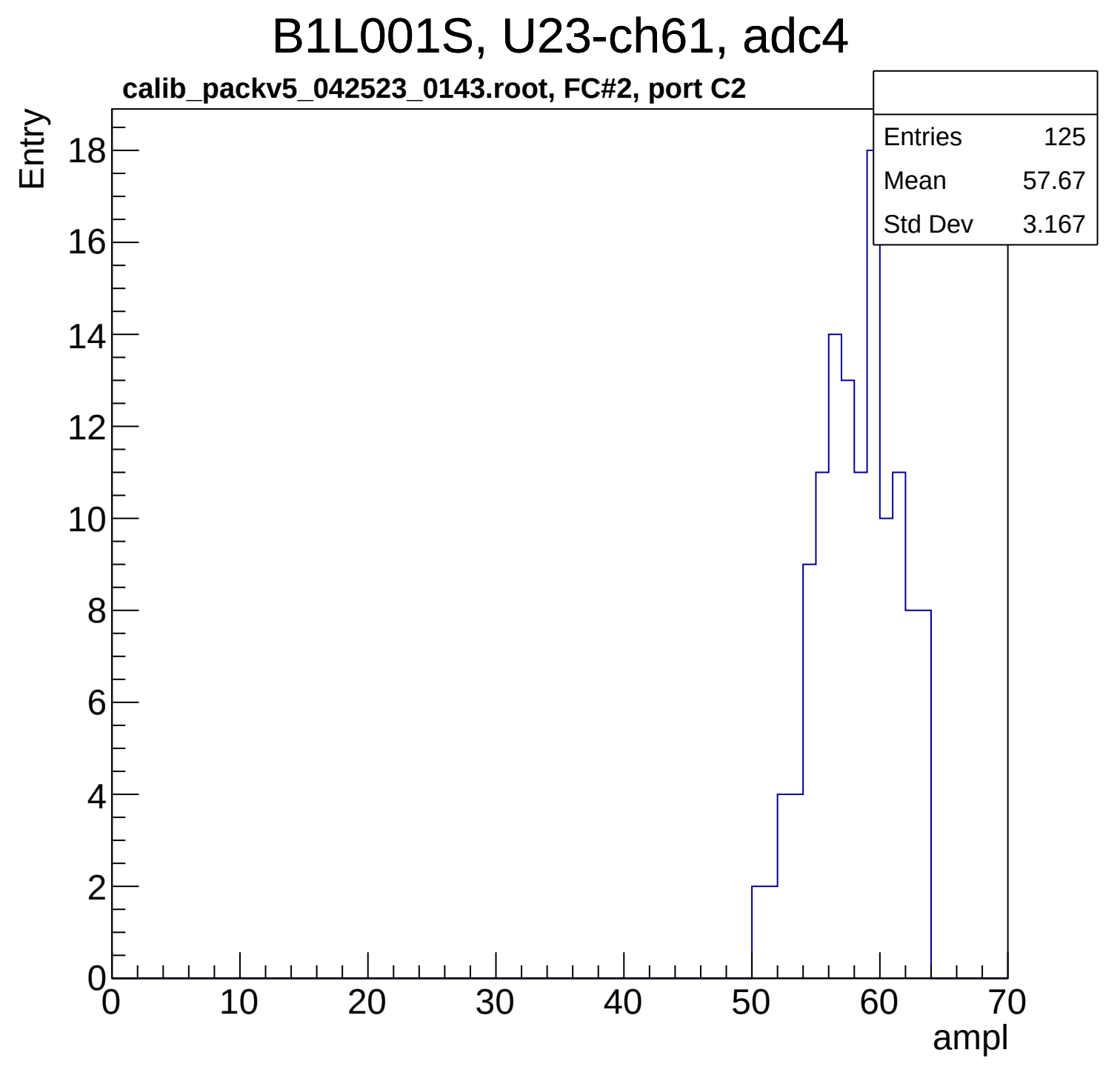
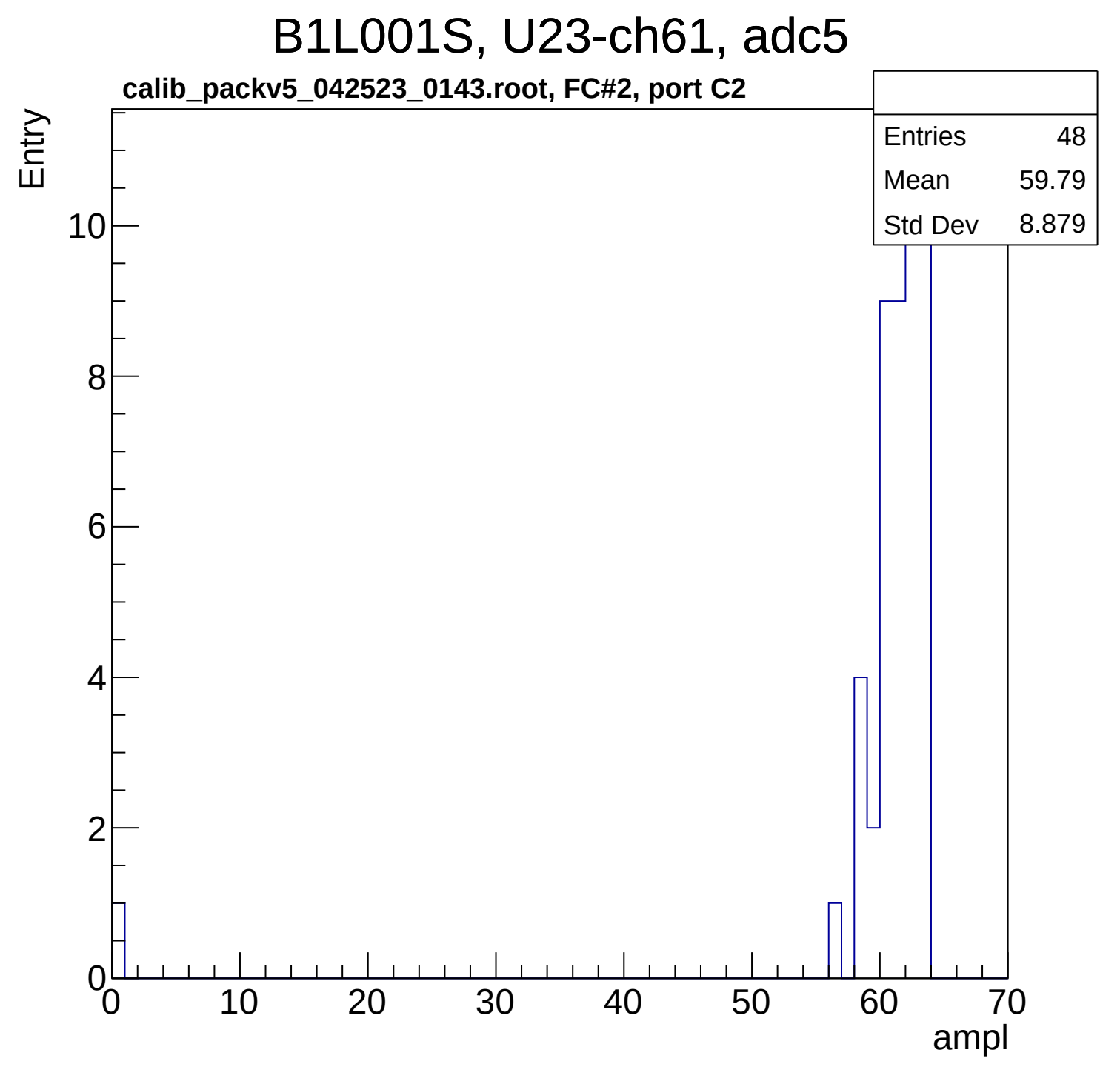
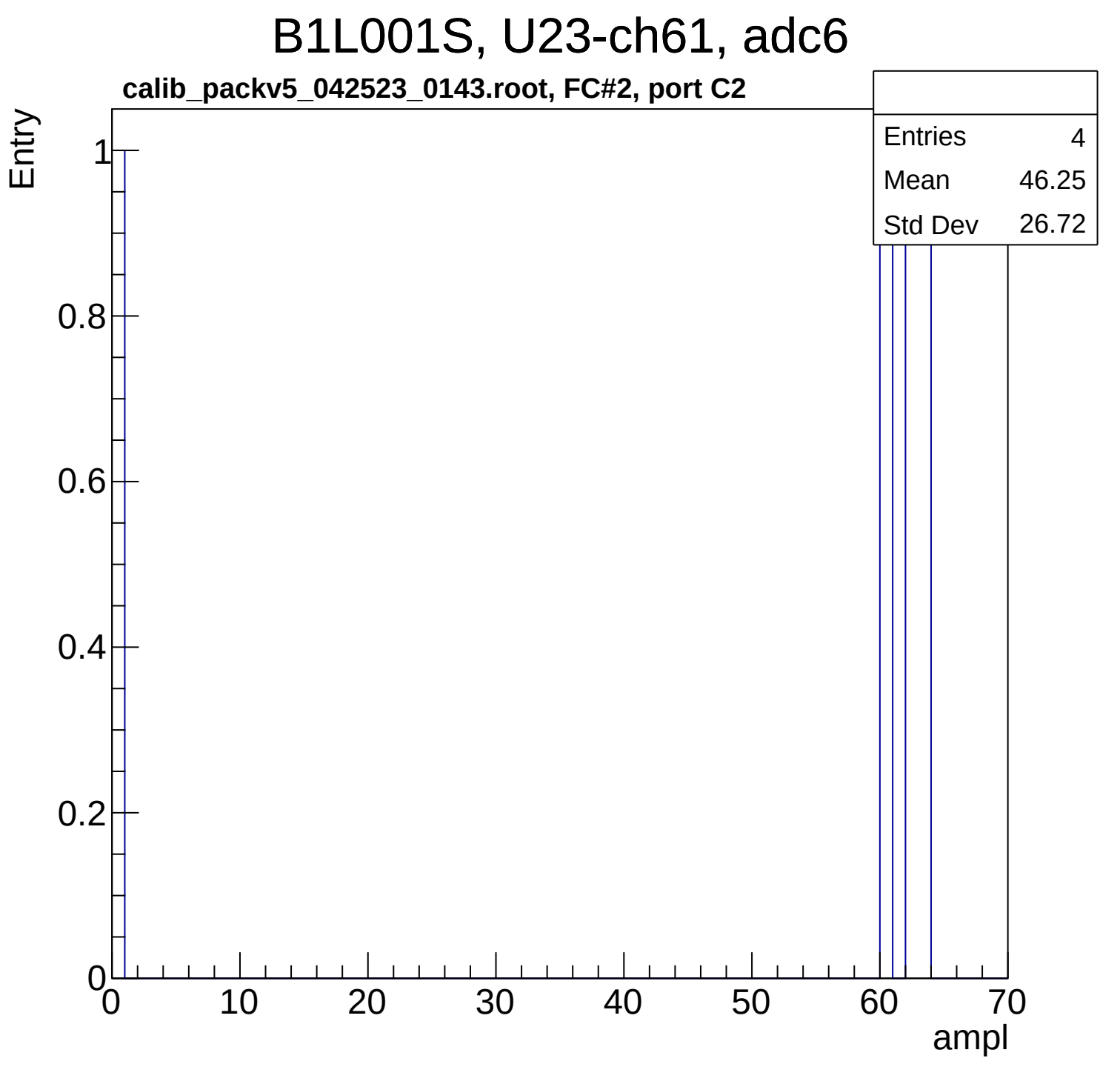
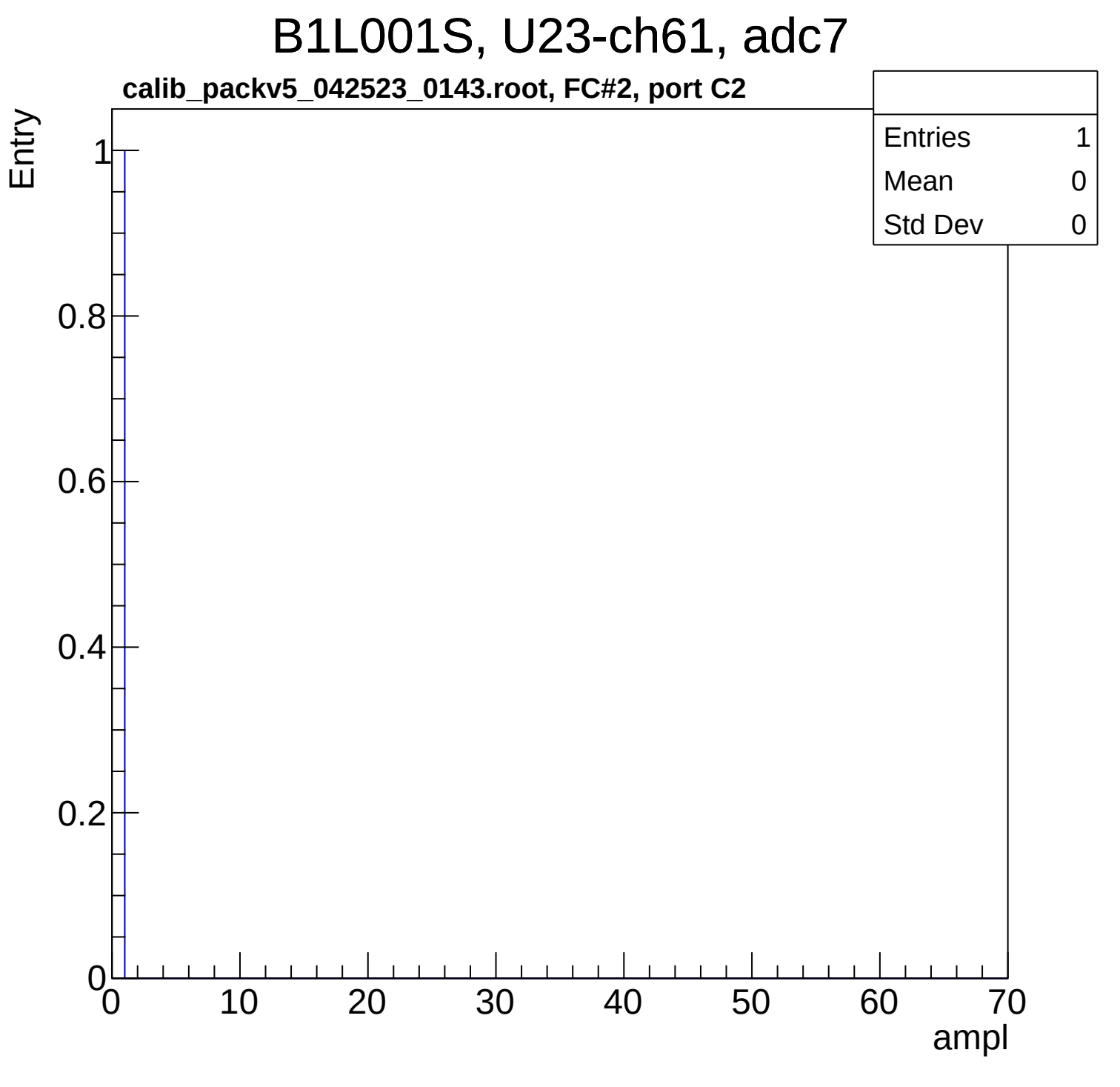
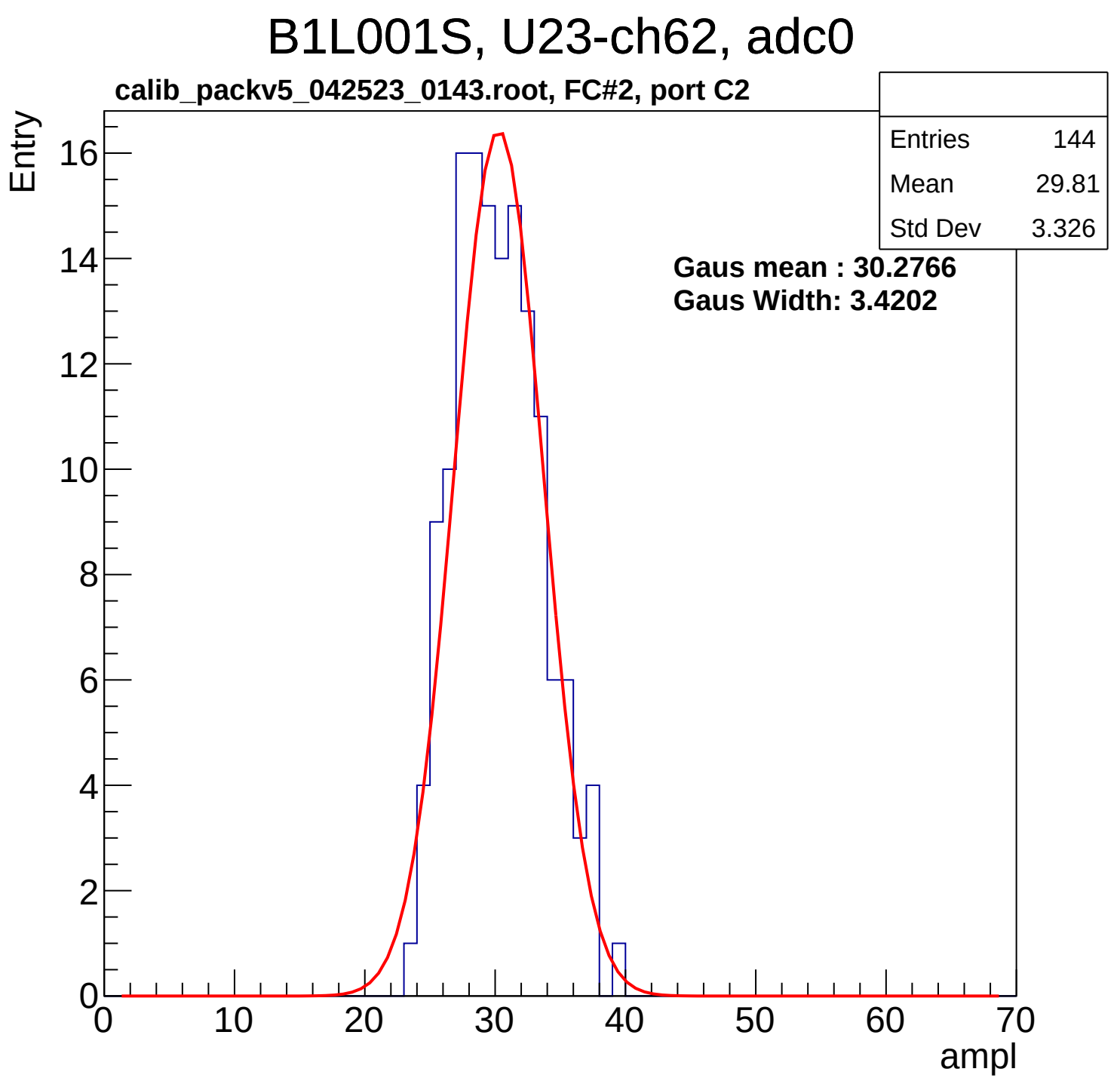
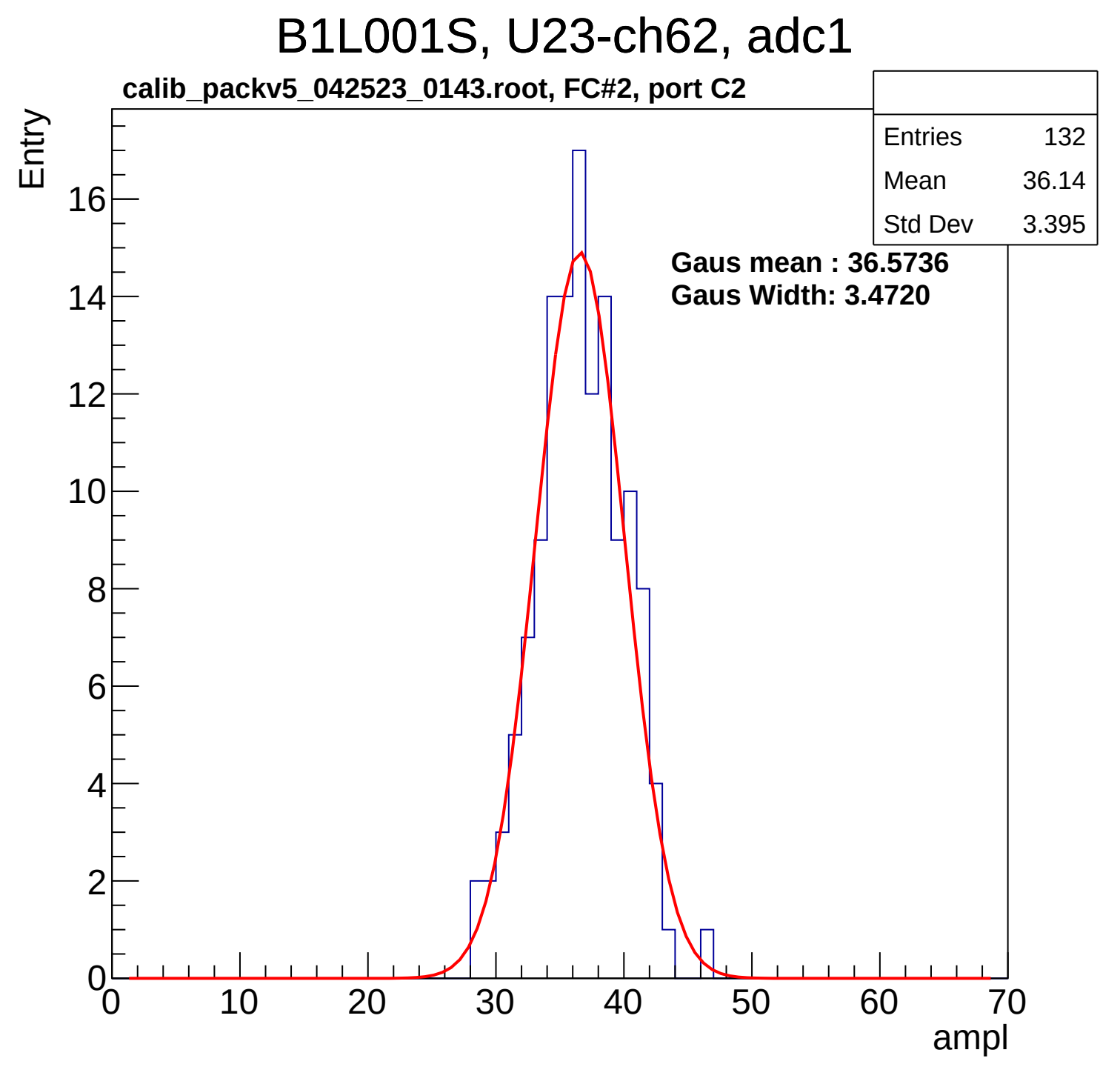

B1L001S, U23-ch61, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 57.67
Std Dev 3.167
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch61, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 48
Mean 59.79
Std Dev 8.879
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch61, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 46.25
Std Dev 26.72
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch61, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch62, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 29.81
Std Dev 3.326
Gaus mean : 30.2766
Gaus Width: 3.4202
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch62, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 36.14
Std Dev 3.395
Gaus mean : 36.5736
Gaus Width: 3.4720
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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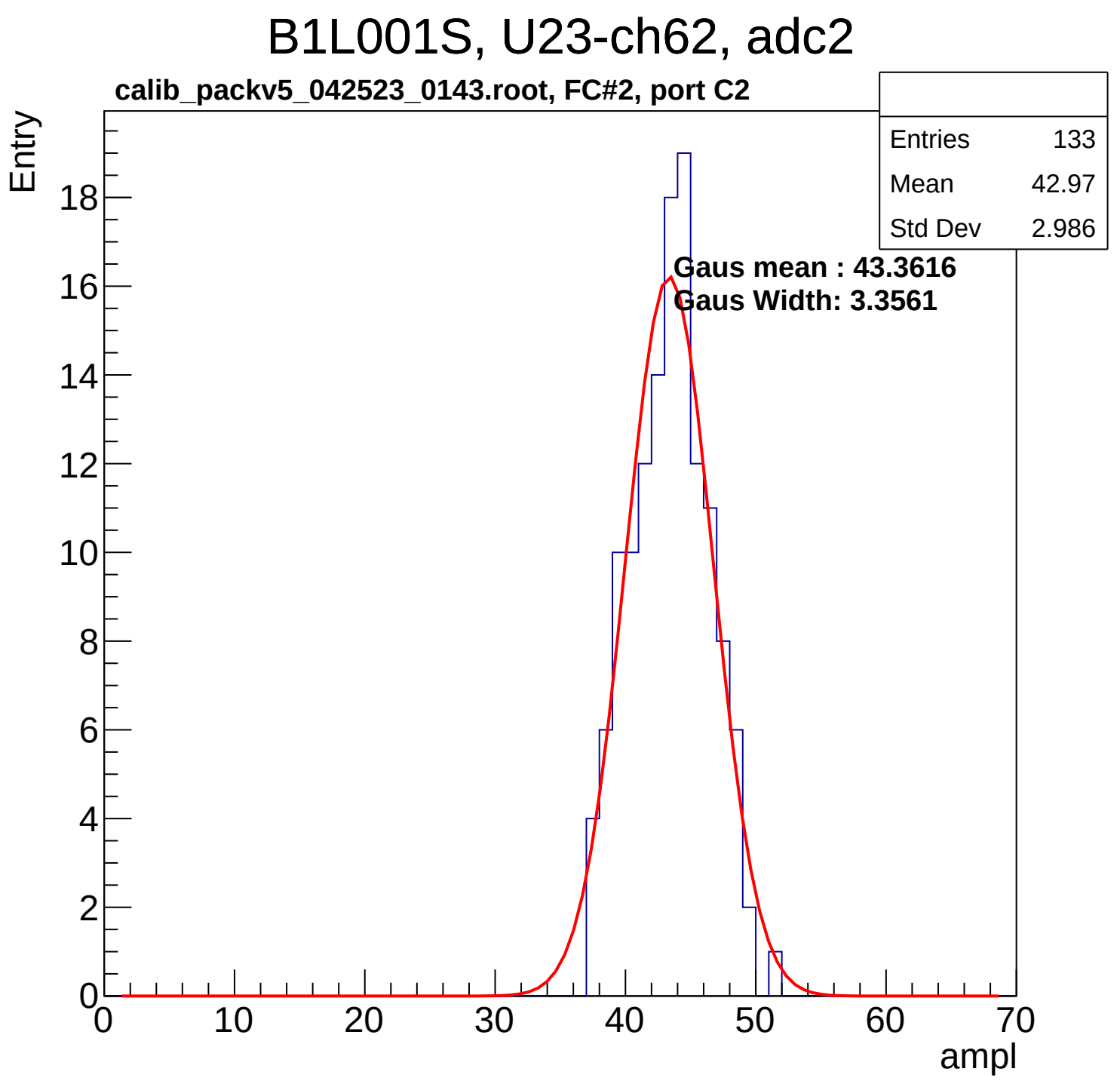
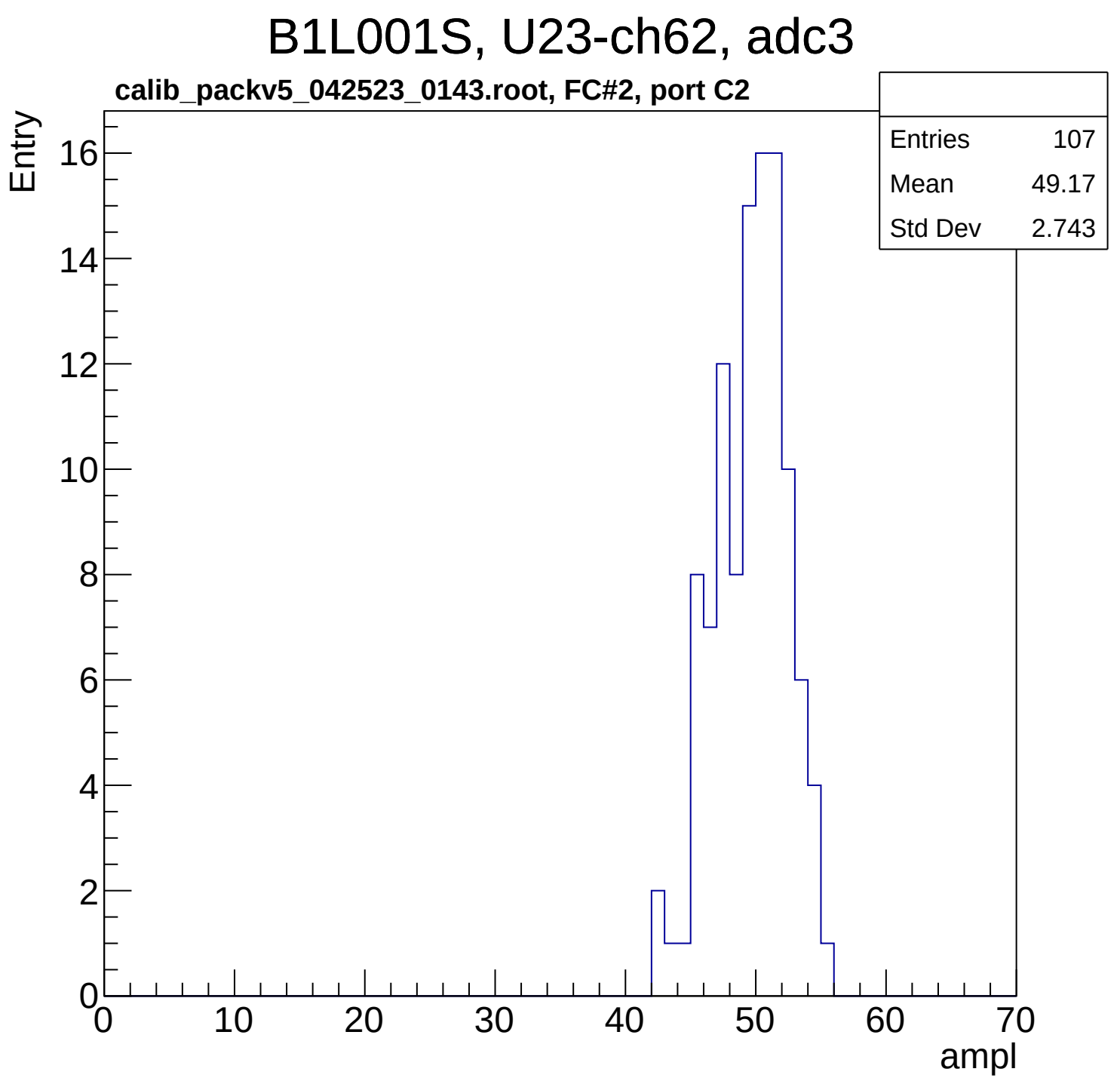
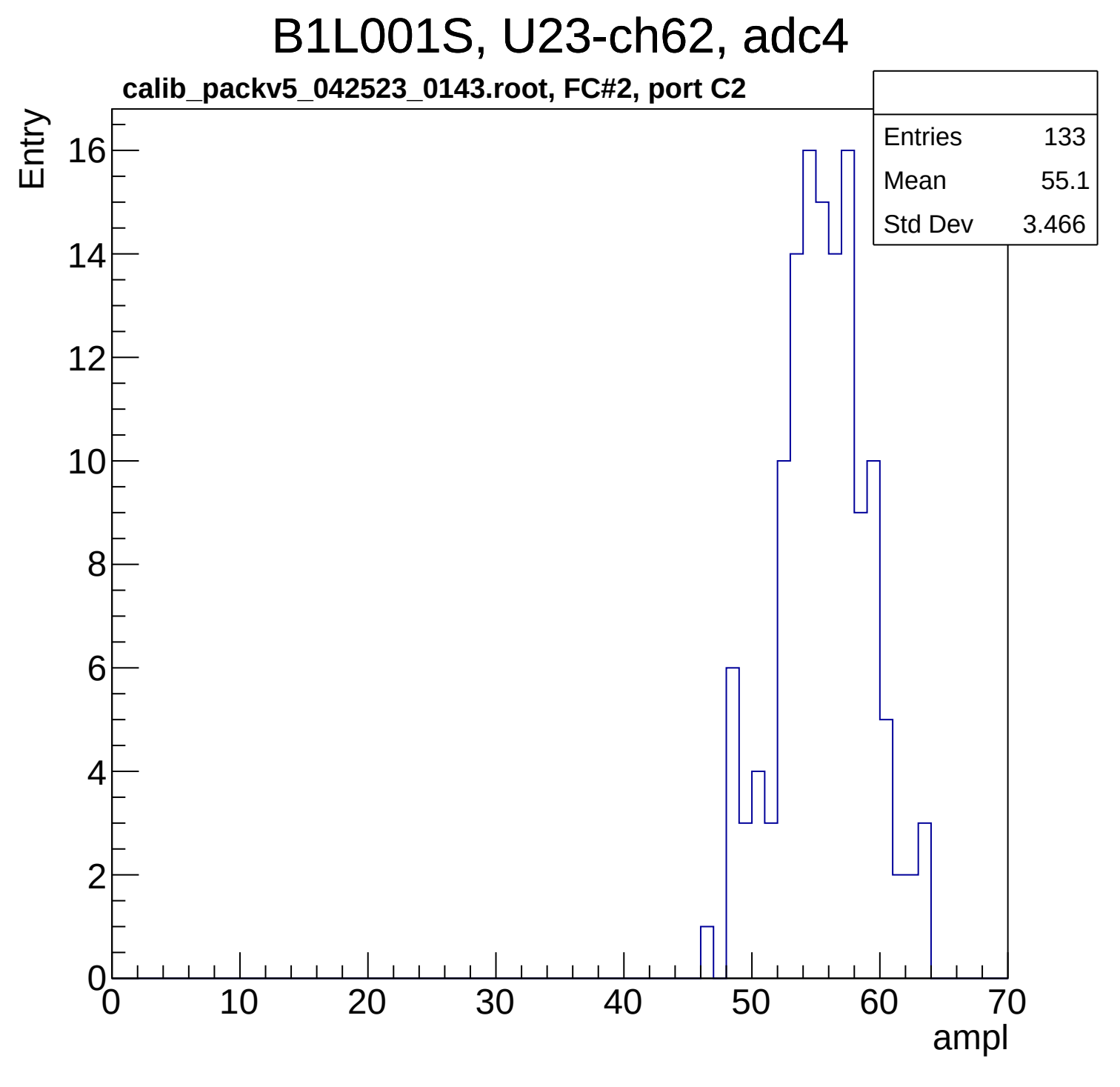

B1L001S, U23-ch62, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 42.97
Std Dev 2.986
Gaus mean : 43.3616
Gaus Width: 3.3561
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch62, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 49.17
Std Dev 2.743
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch62, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 55.1
Std Dev 3.466
Entry
ampl
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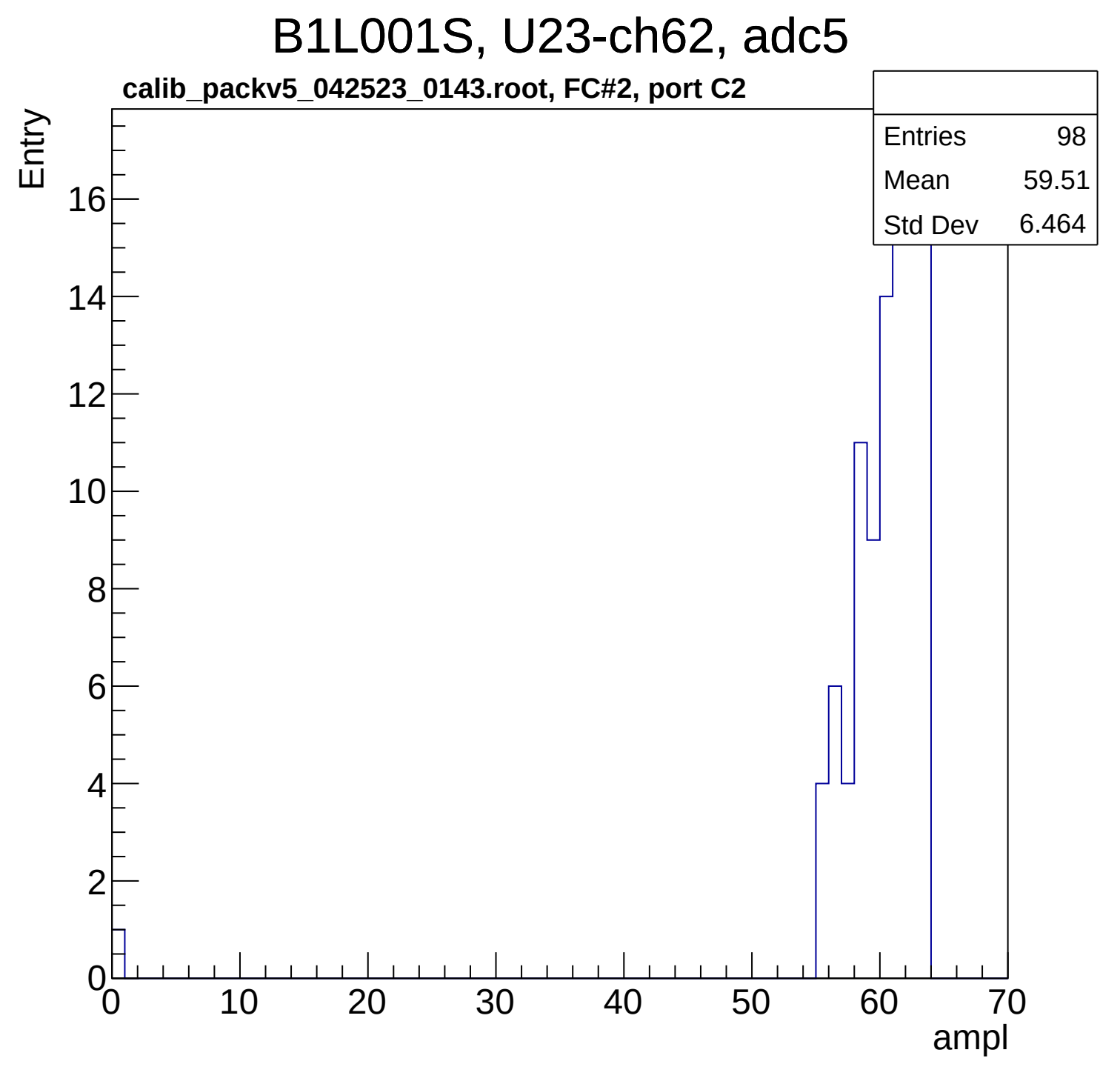
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch62, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 59.51
Std Dev 6.464
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
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60
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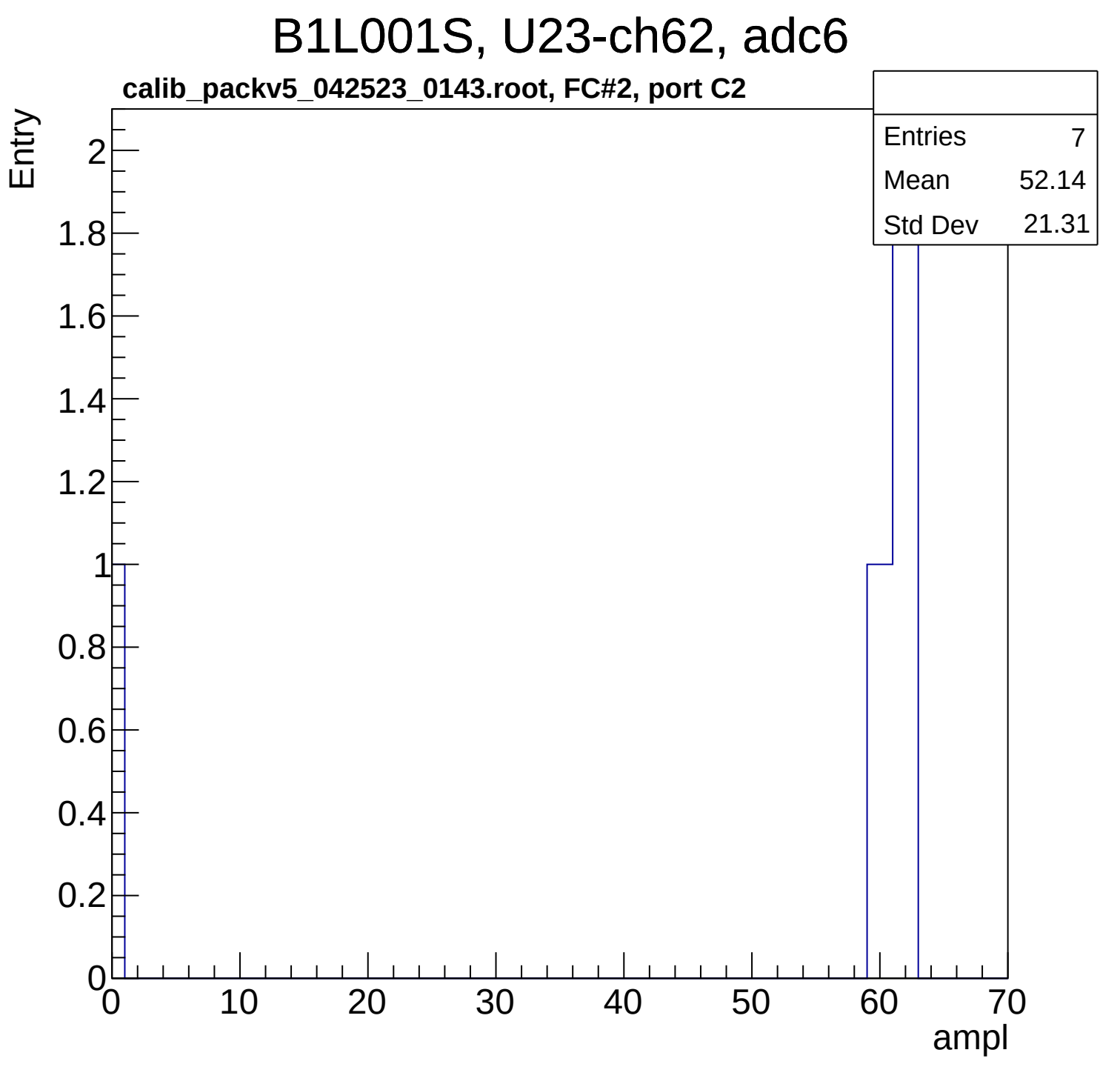
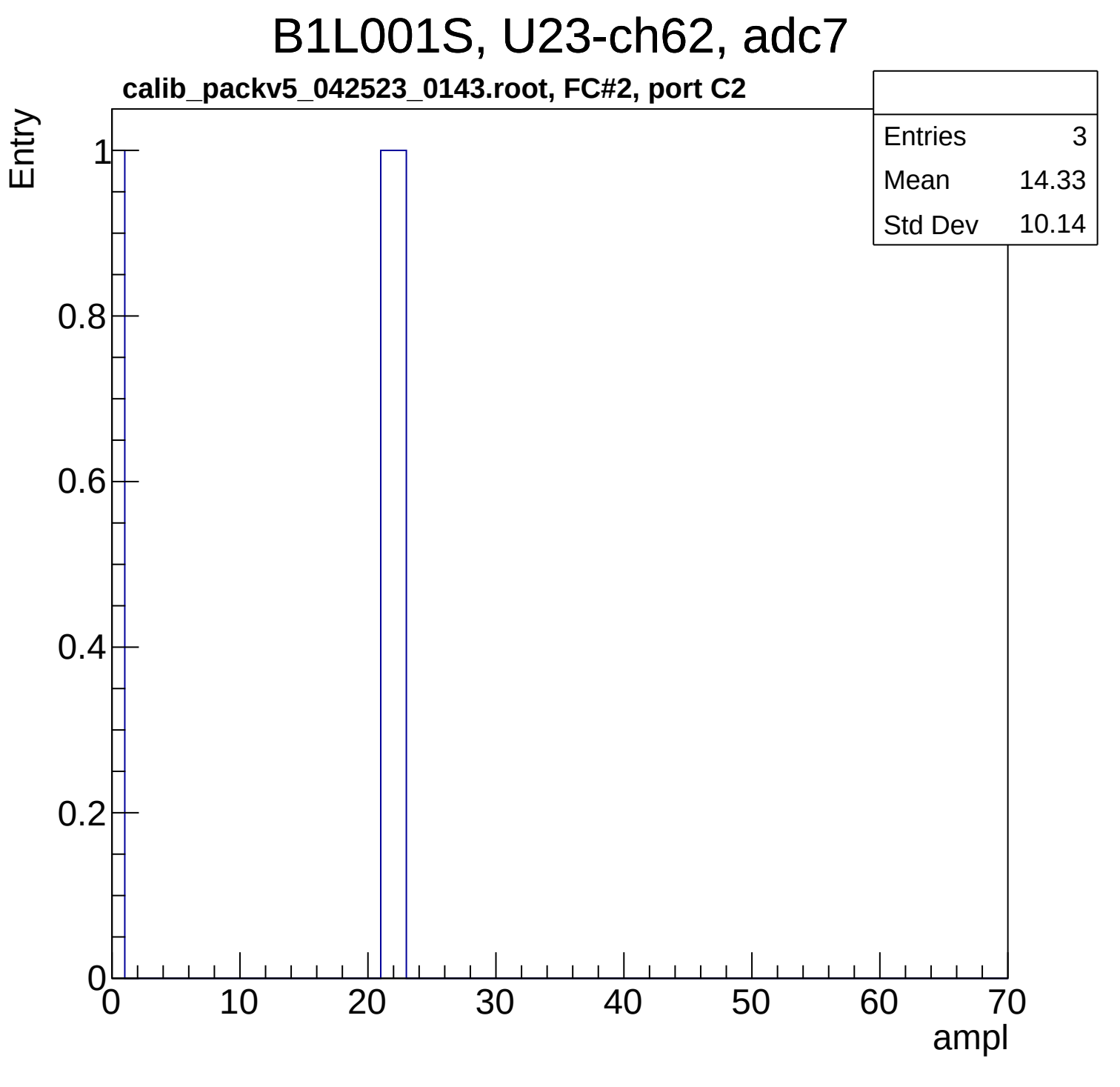
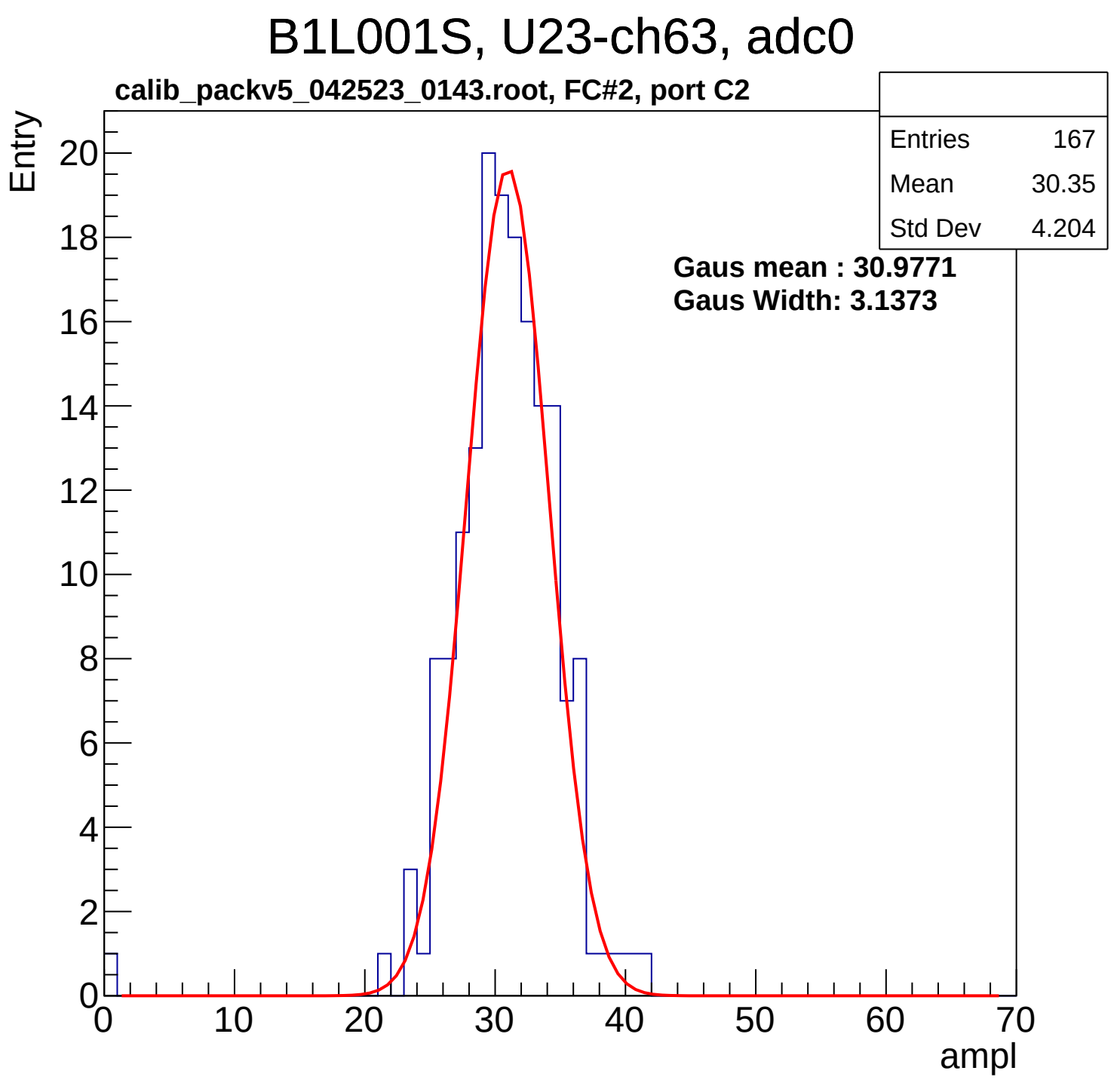
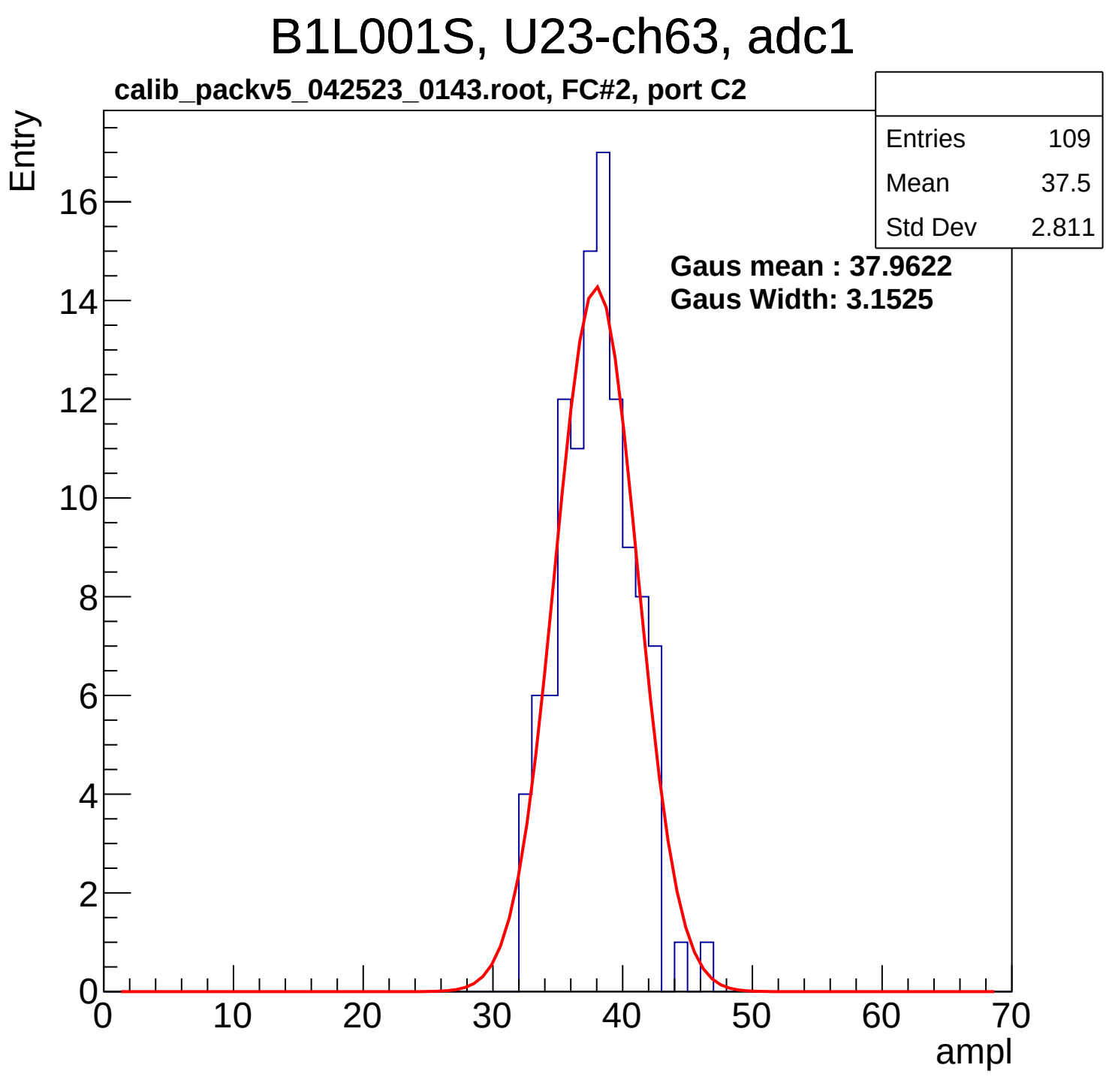
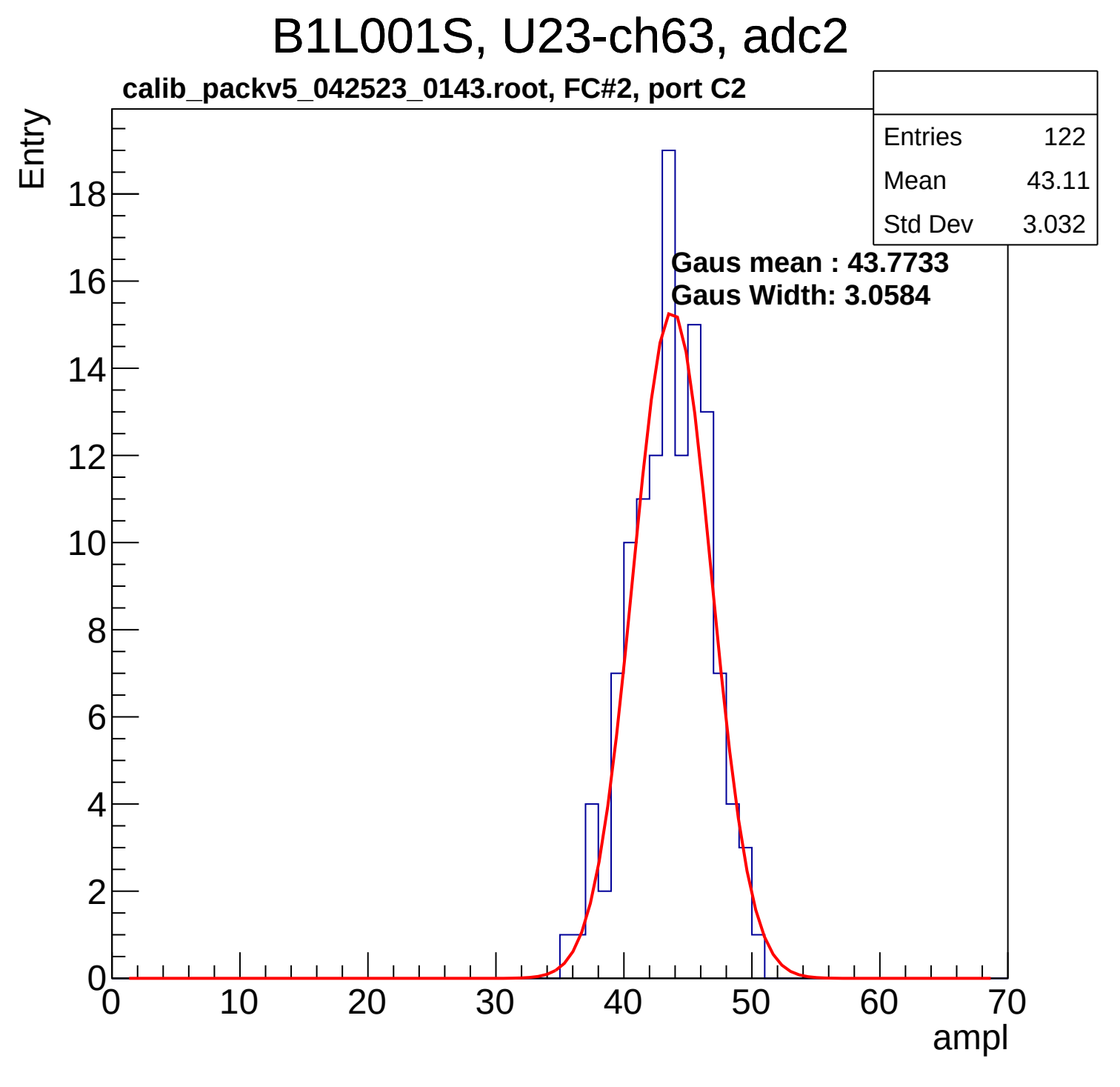

B1L001S, U23-ch62, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 52.14
Std Dev 21.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch62, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 14.33
Std Dev 10.14
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch63, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 30.35
Std Dev 4.204
Gaus mean : 30.9771
Gaus Width: 3.1373
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch63, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 37.5
Std Dev 2.811
Gaus mean : 37.9622
Gaus Width: 3.1525
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch63, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 43.11
Std Dev 3.032
Gaus mean : 43.7733
Gaus Width: 3.0584
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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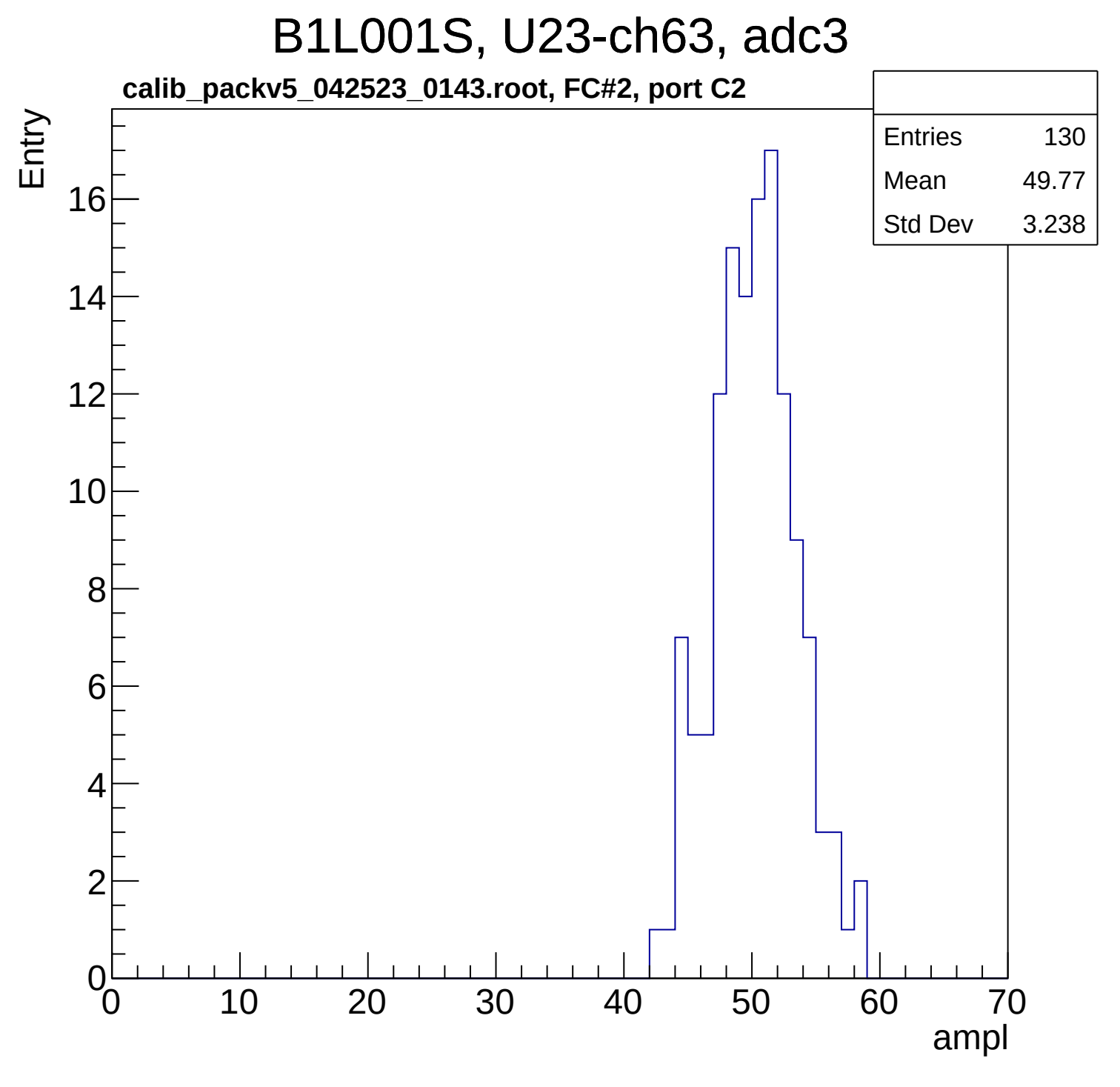
B1L001S, U23-ch63, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 49.77
Std Dev 3.238
Entry
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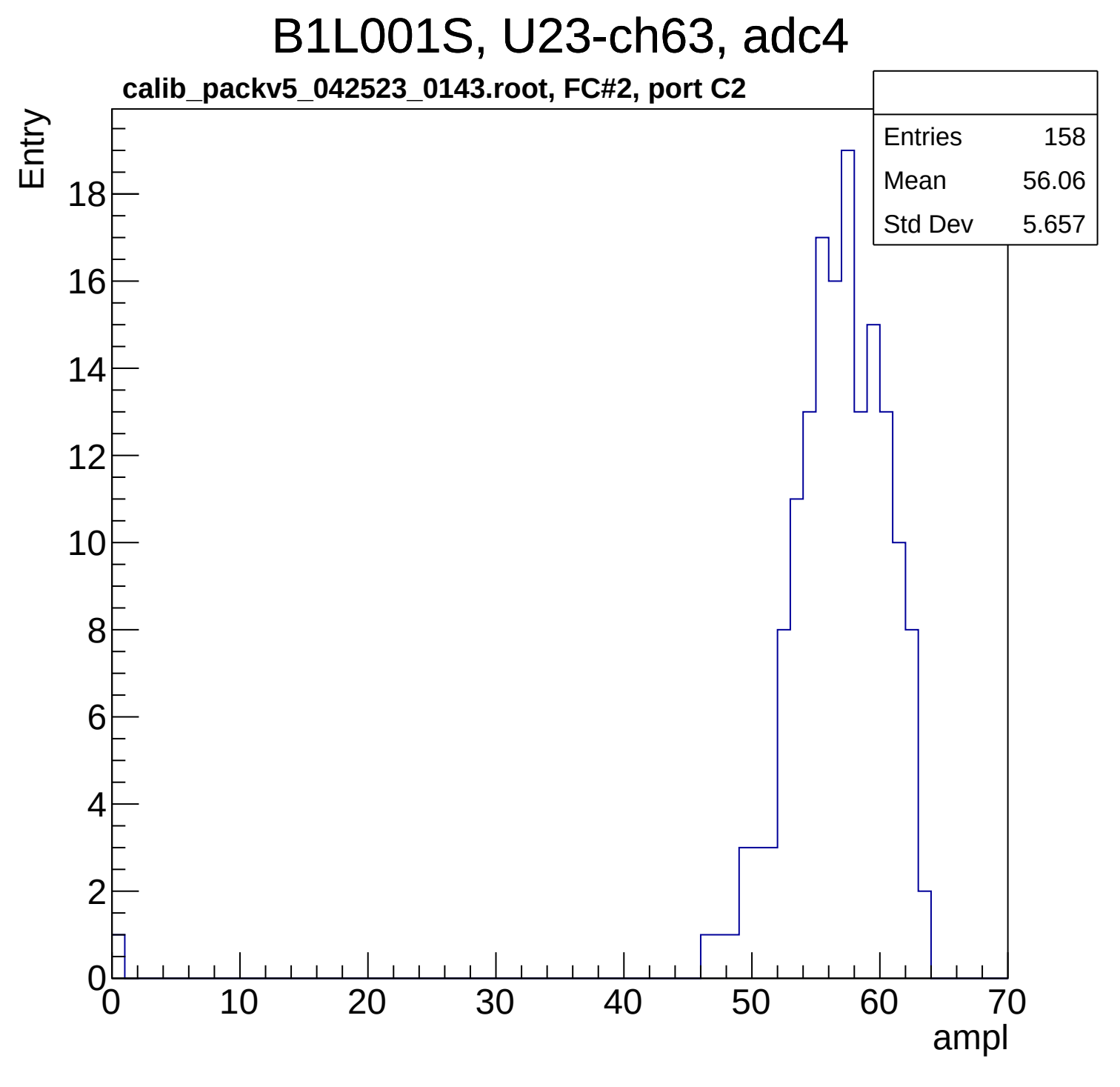
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch63, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 56.06
Std Dev 5.657
Entry
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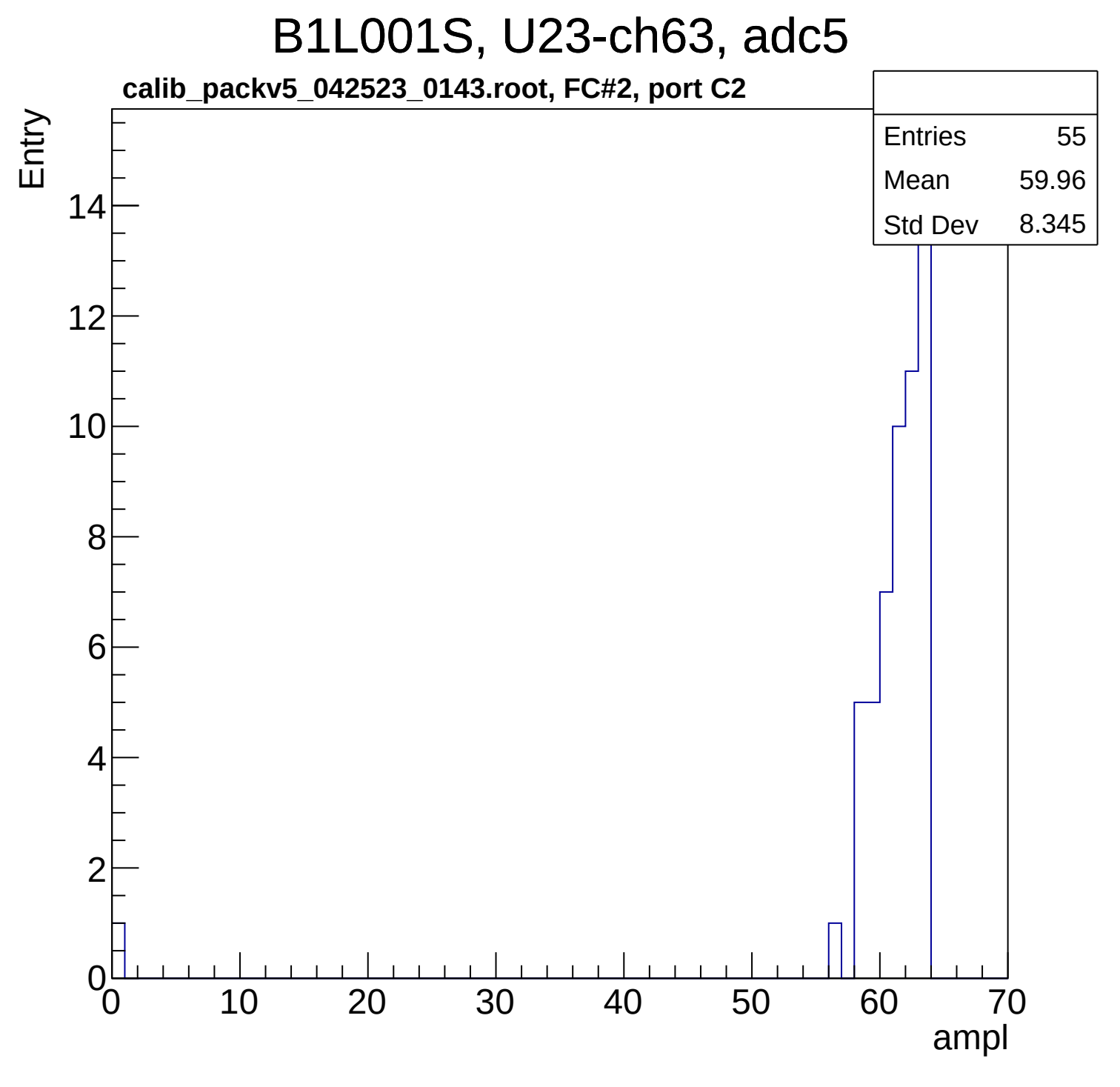
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch63, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 55
Mean 59.96
Std Dev 8.345
Entry
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4
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8
10
12
14
0
10
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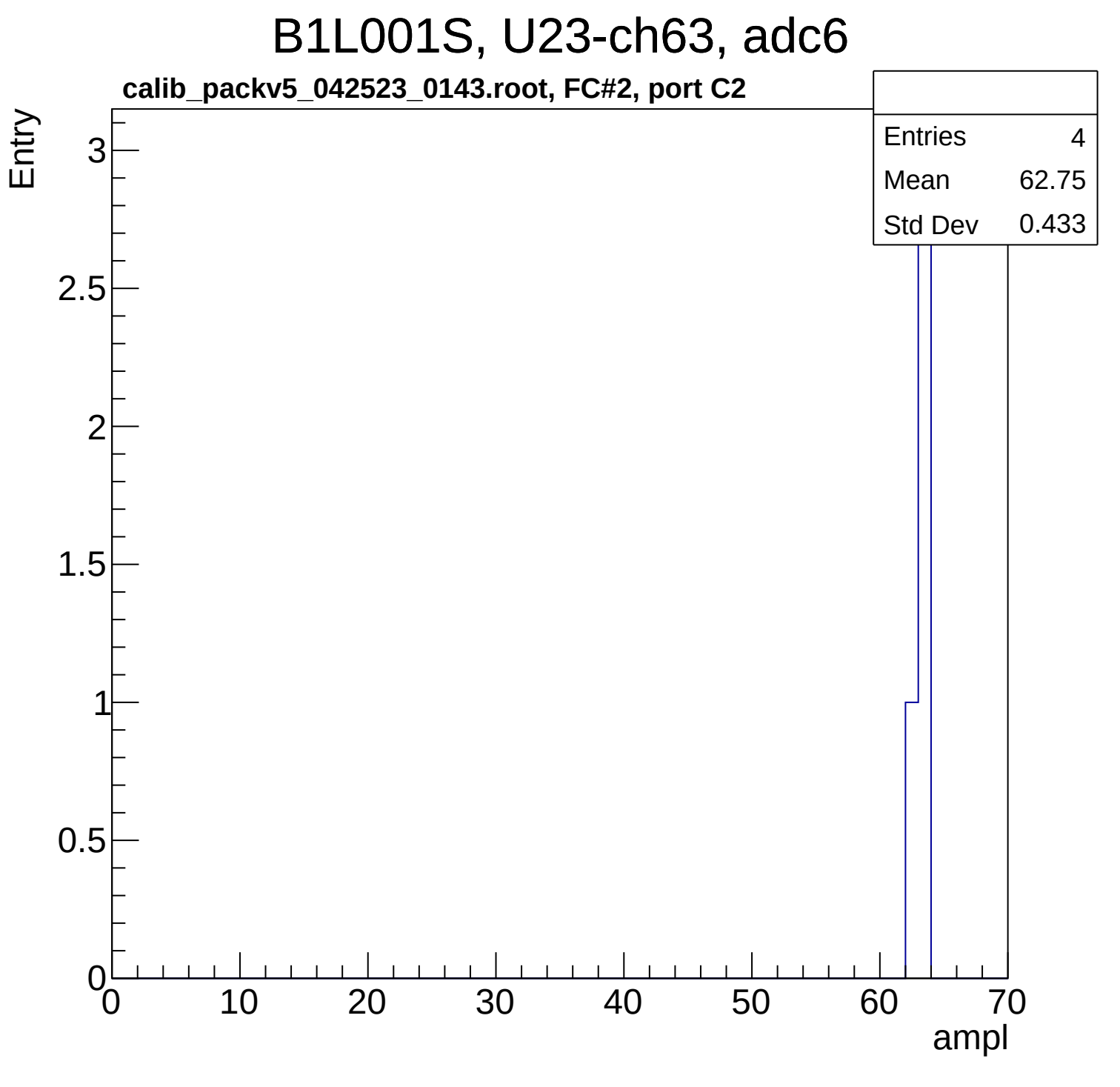
30
40
50
60
70
ampl


B1L001S, U23-ch63, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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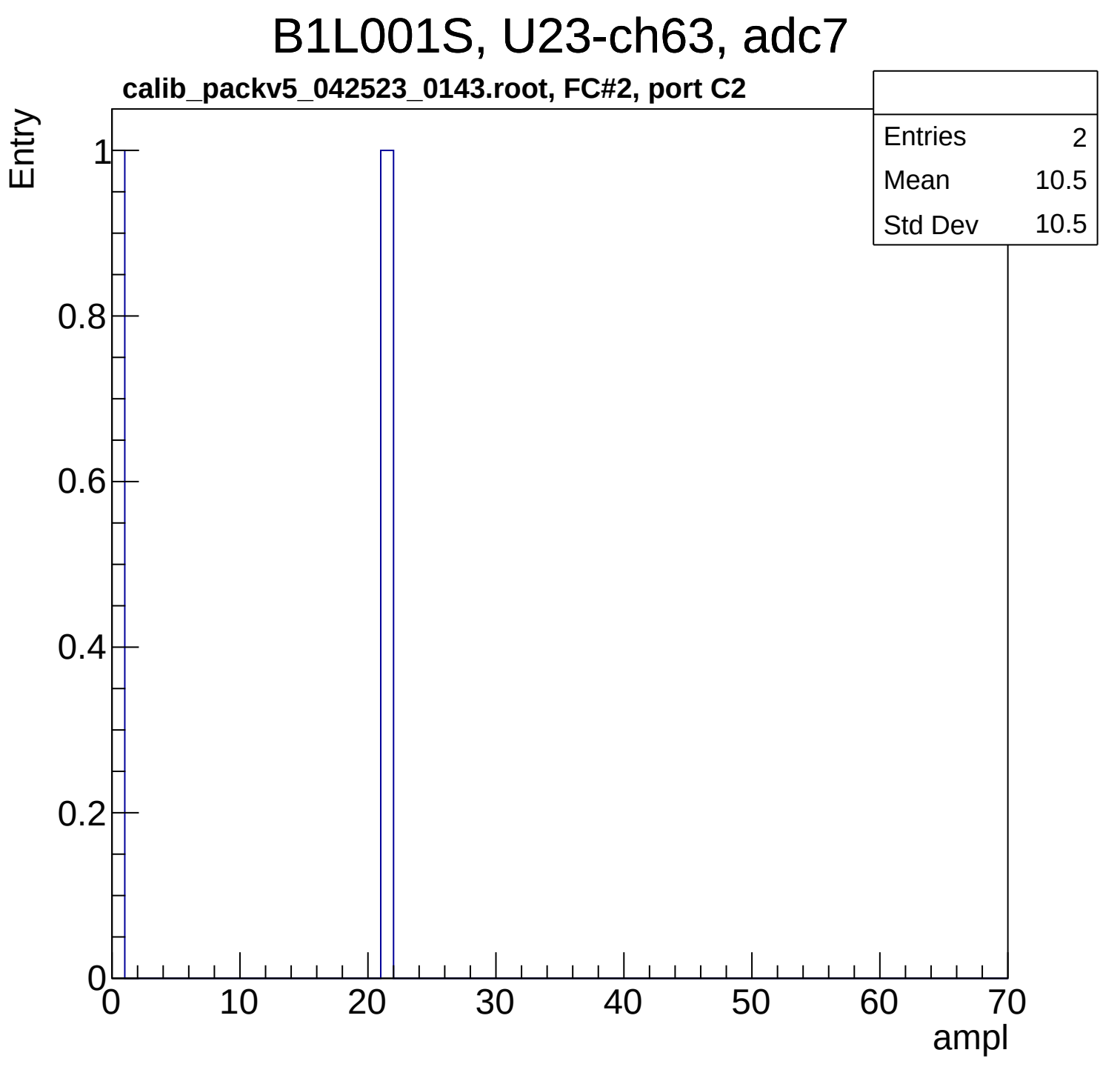
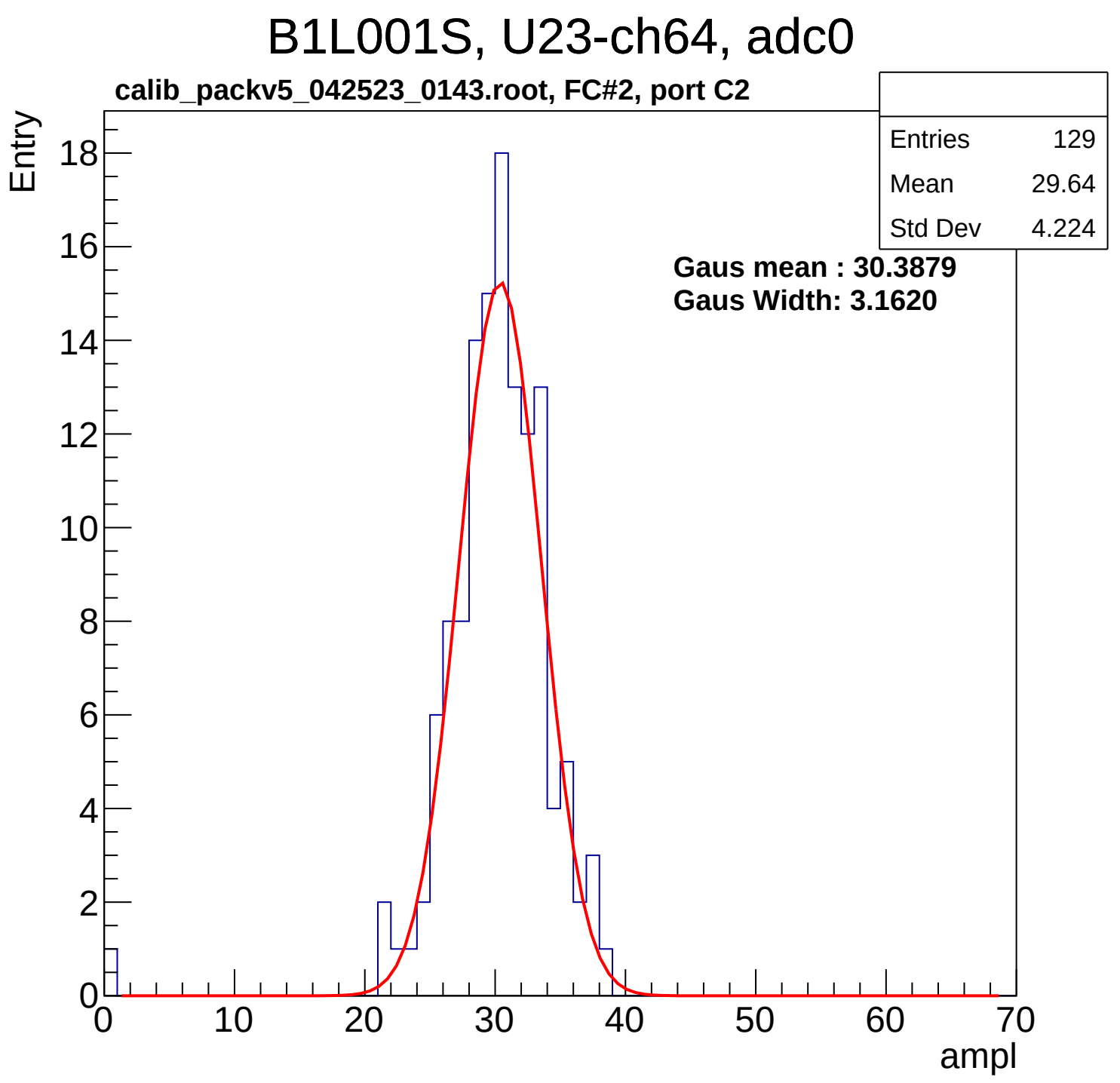
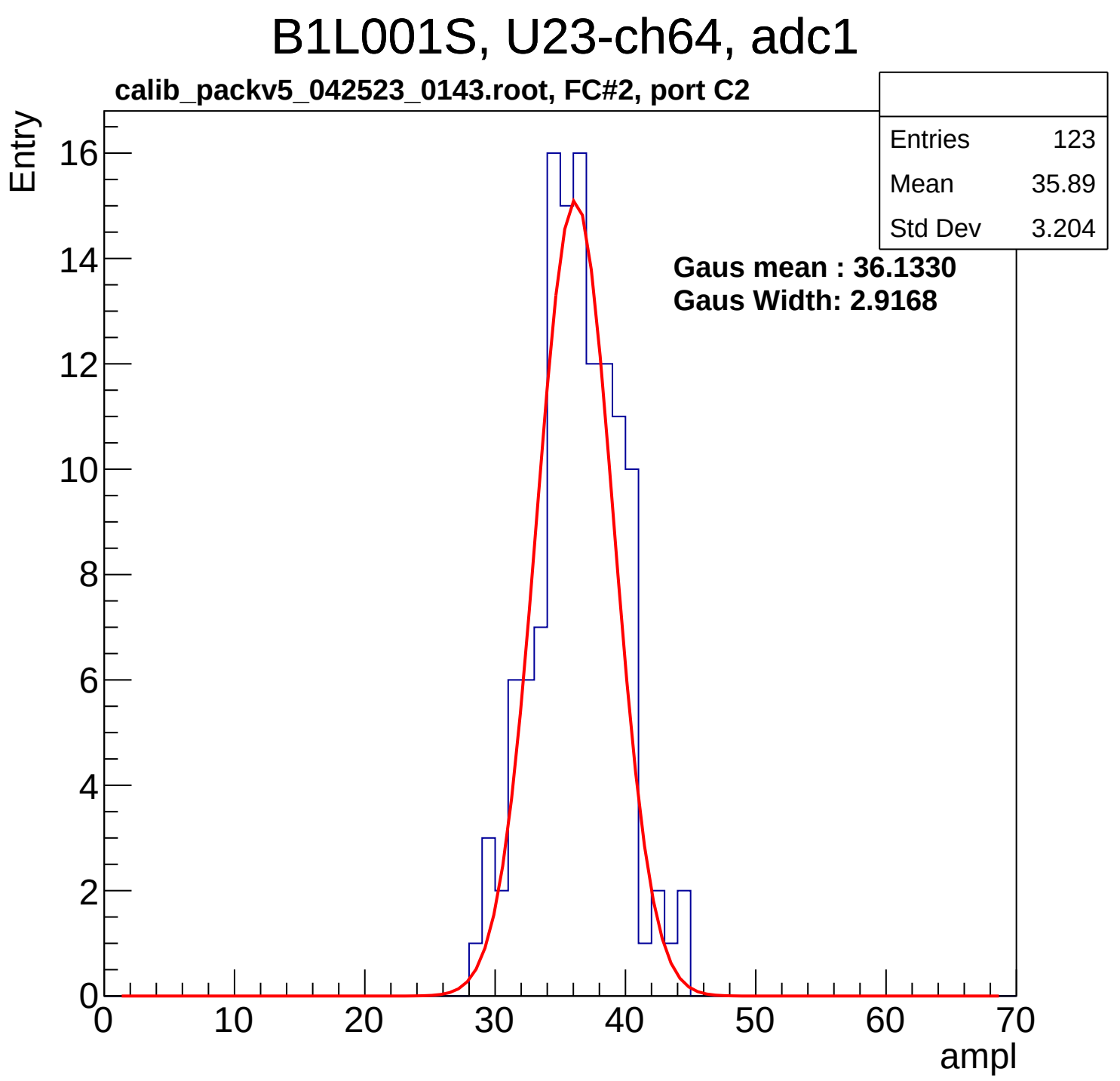
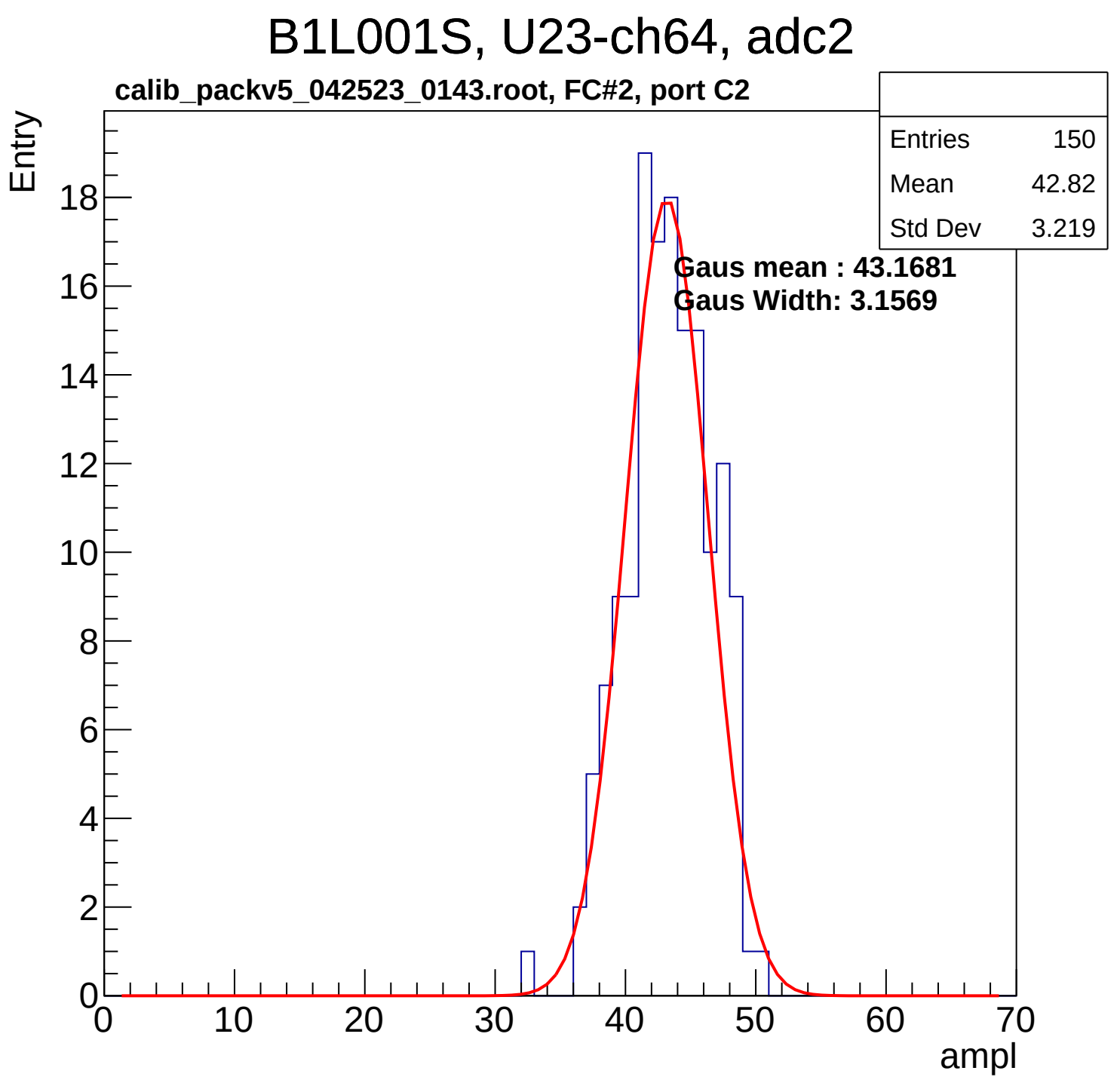
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch63, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch64, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 29.64
Std Dev 4.224
Gaus mean : 30.3879
Gaus Width: 3.1620
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch64, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 35.89
Std Dev 3.204
Gaus mean : 36.1330
Gaus Width: 2.9168
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch64, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 42.82
Std Dev 3.219
Gaus mean : 43.1681
Gaus Width: 3.1569
Entry
ampl
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4
6
8
10
12
14
16
18
0
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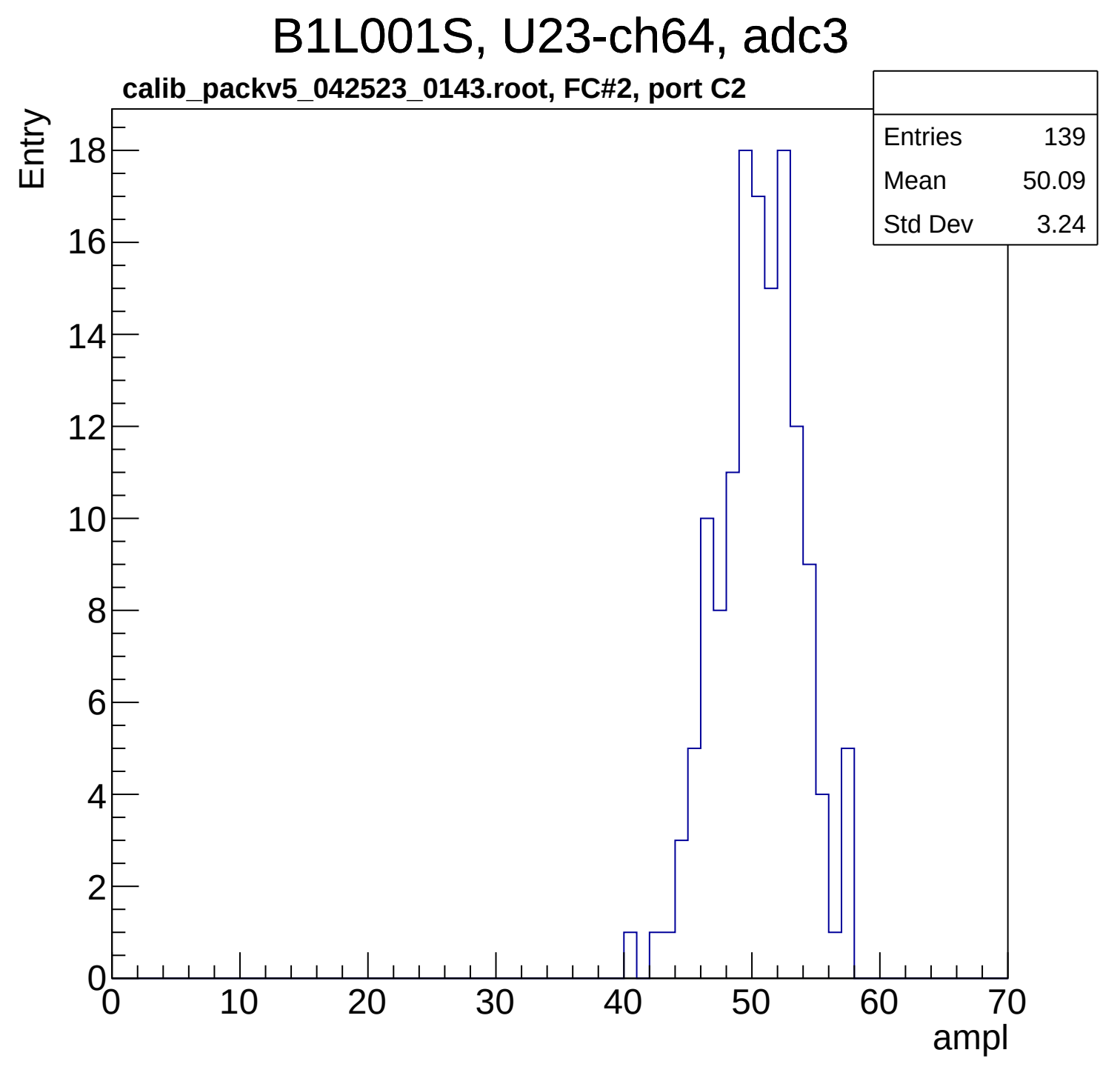
B1L001S, U23-ch64, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 50.09
Std Dev 3.24
Entry
ampl
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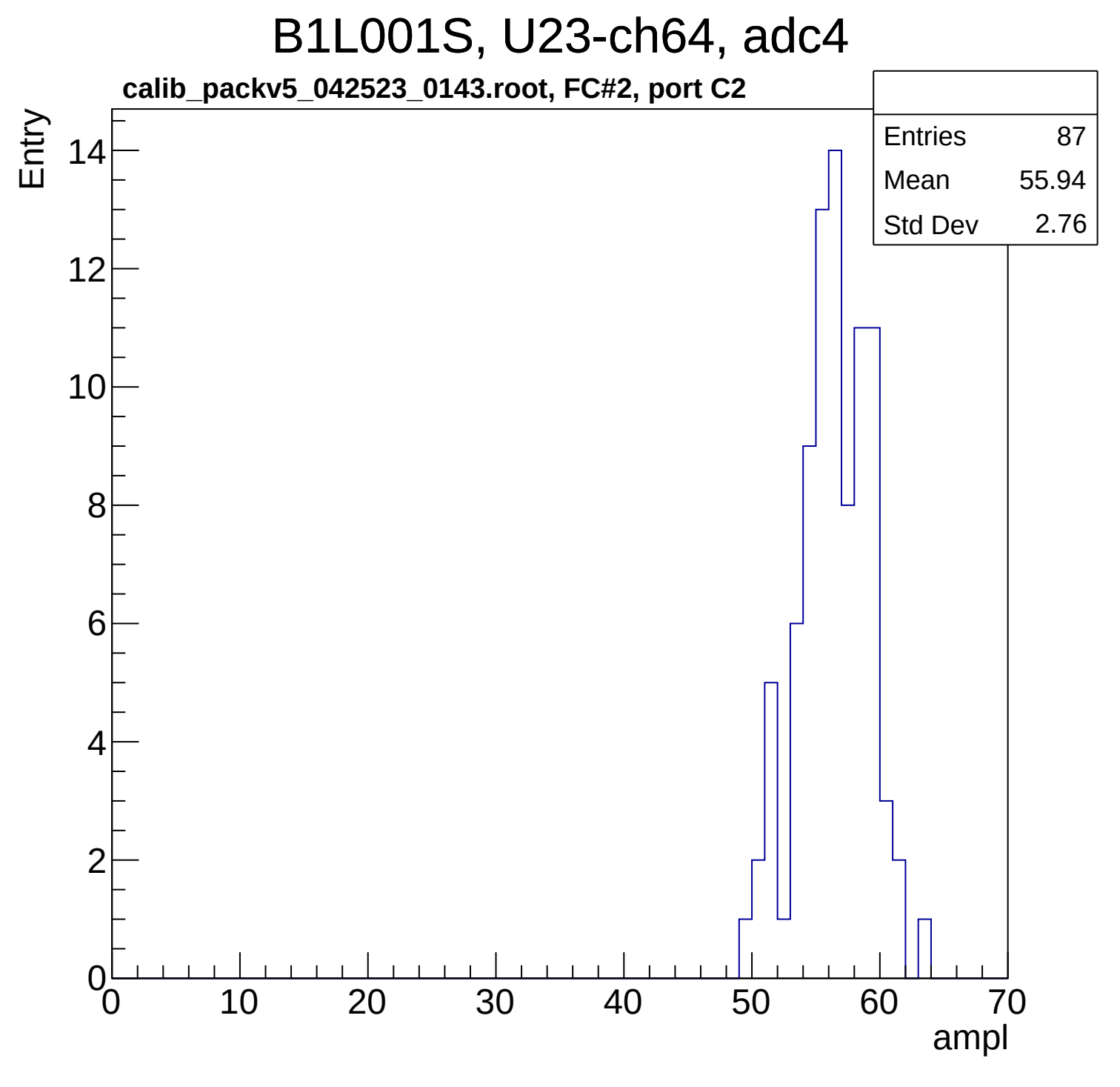
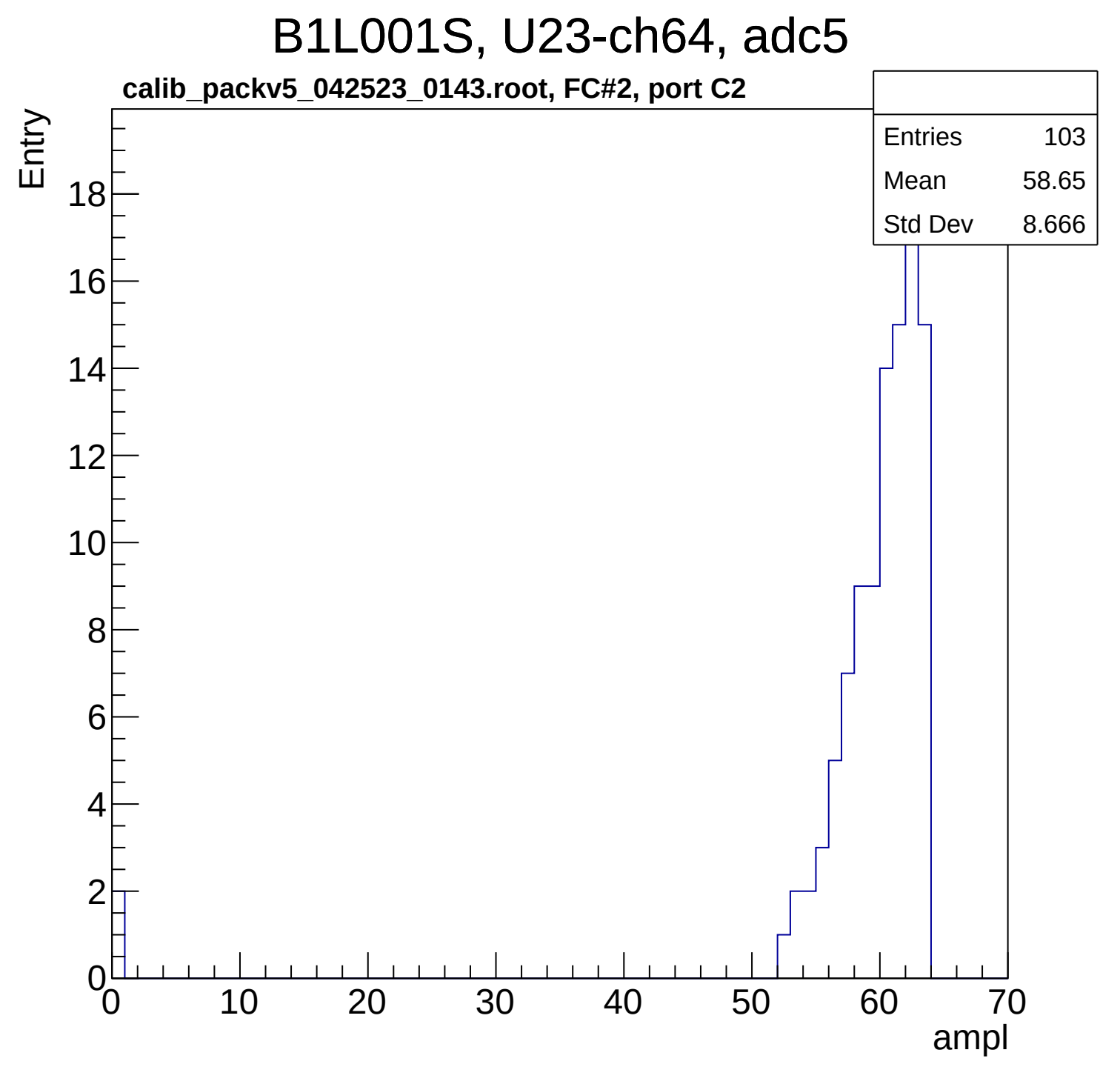
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch64, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 55.94
Std Dev 2.76
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch64, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.65
Std Dev 8.666
Entry
ampl
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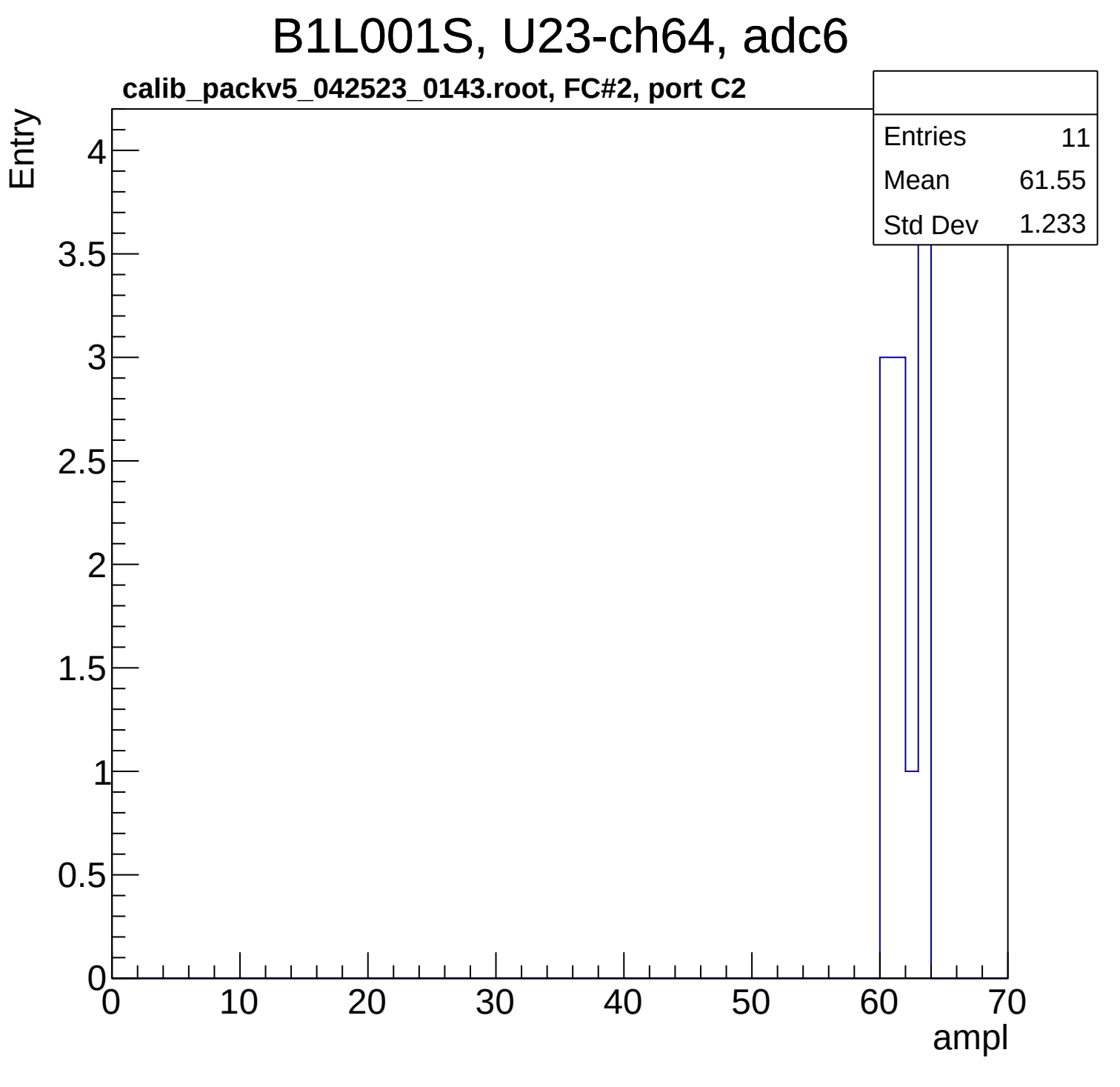
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch64, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 61.55
Std Dev 1.233
Entry
ampl
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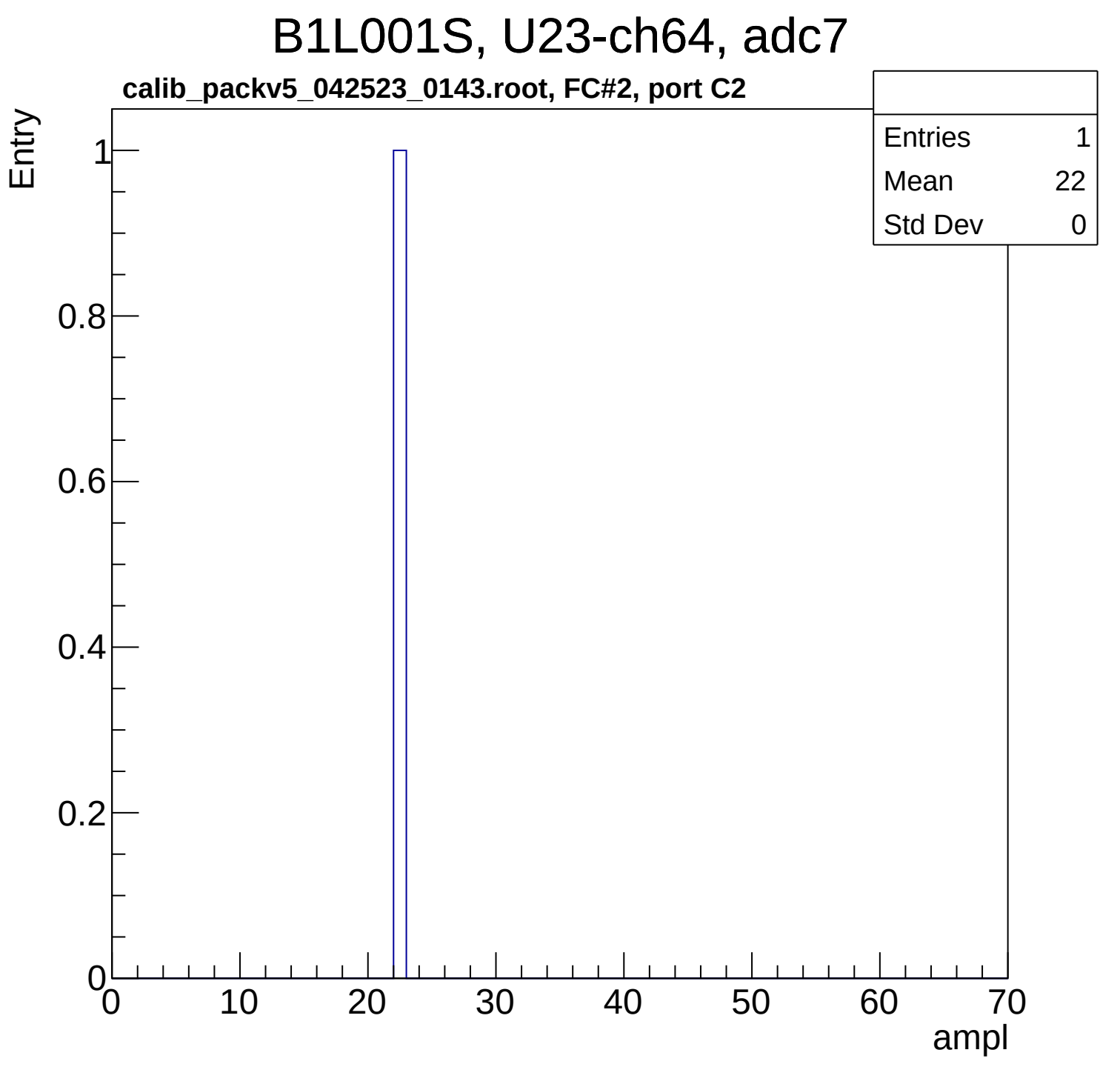
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch64, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
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10
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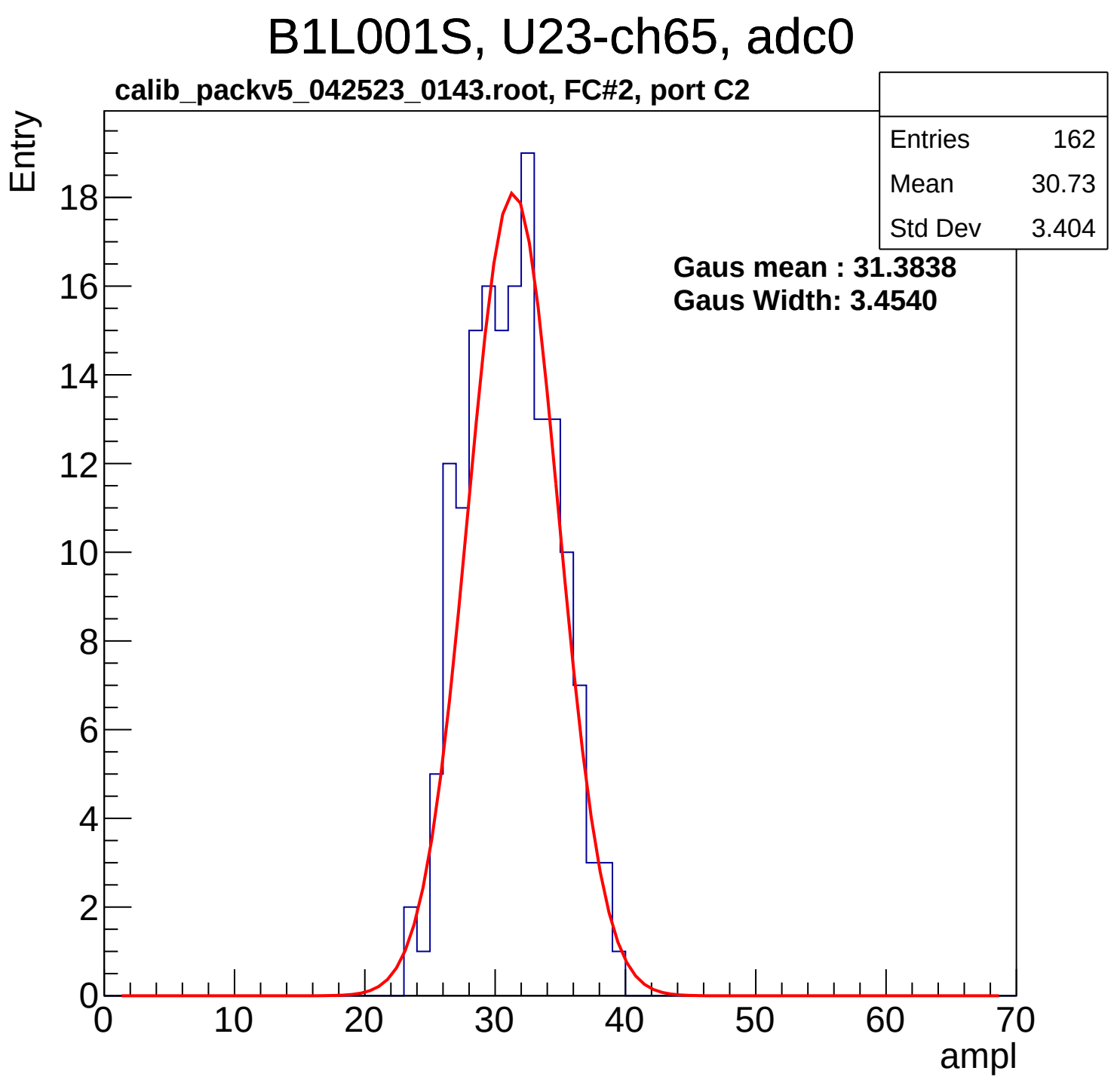
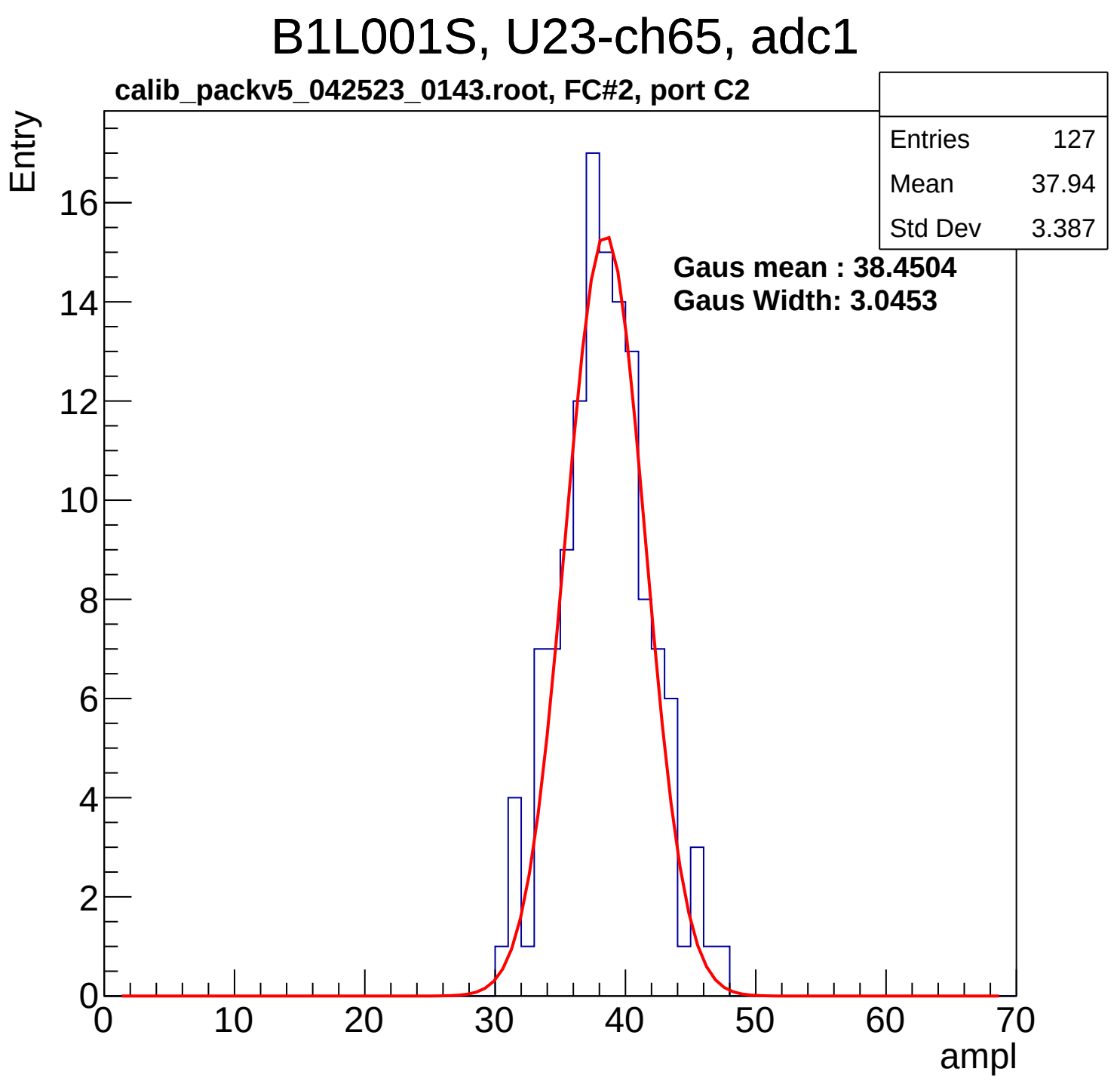
50
60
70
ampl


B1L001S, U23-ch65, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 162
Mean 30.73
Std Dev 3.404
Gaus mean : 31.3838
Gaus Width: 3.4540
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch65, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 37.94
Std Dev 3.387
Gaus mean : 38.4504
Gaus Width: 3.0453
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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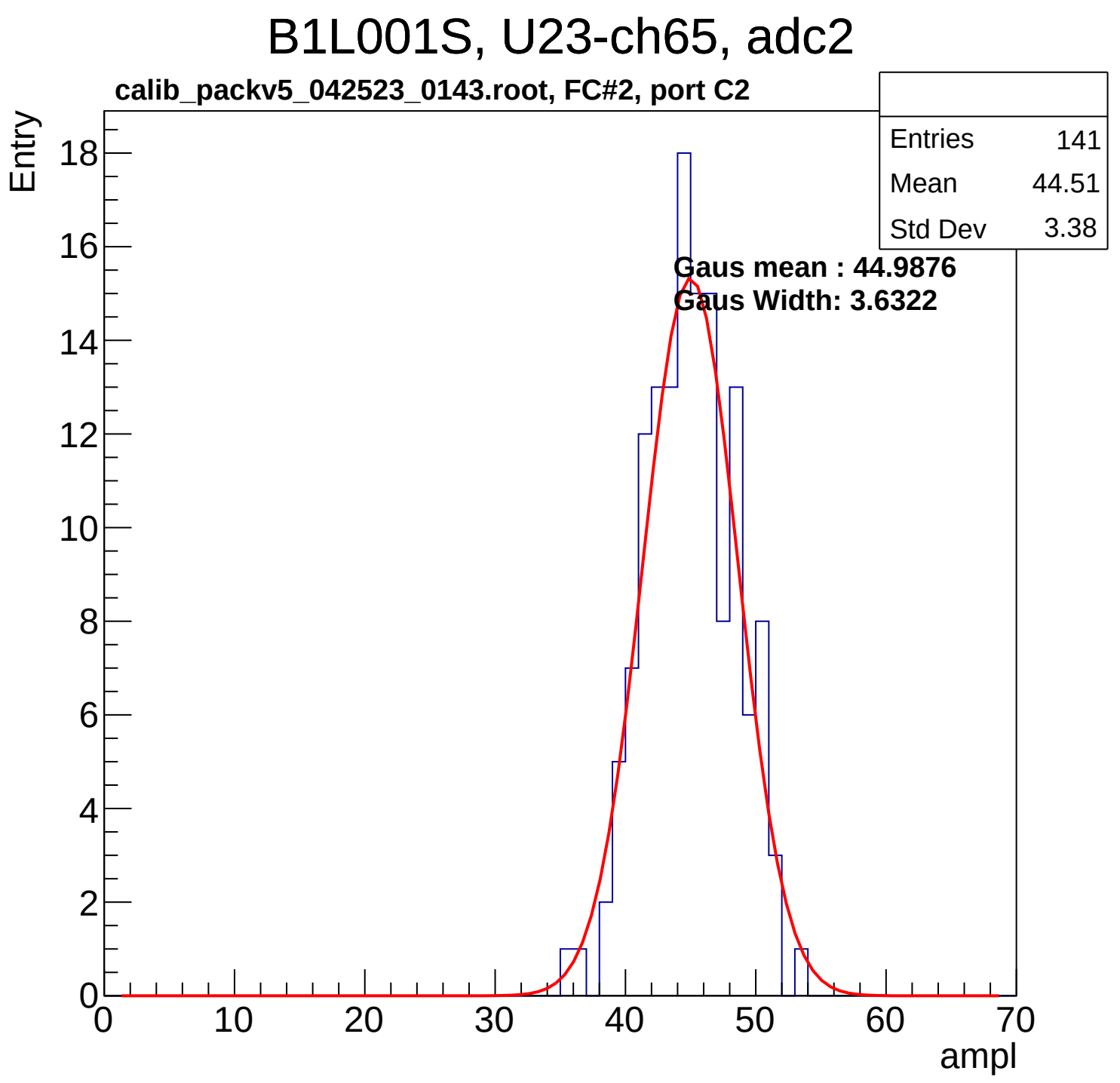
40
50
60
70


B1L001S, U23-ch65, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 44.51
Std Dev 3.38
Gaus mean : 44.9876
Gaus Width: 3.6322
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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60
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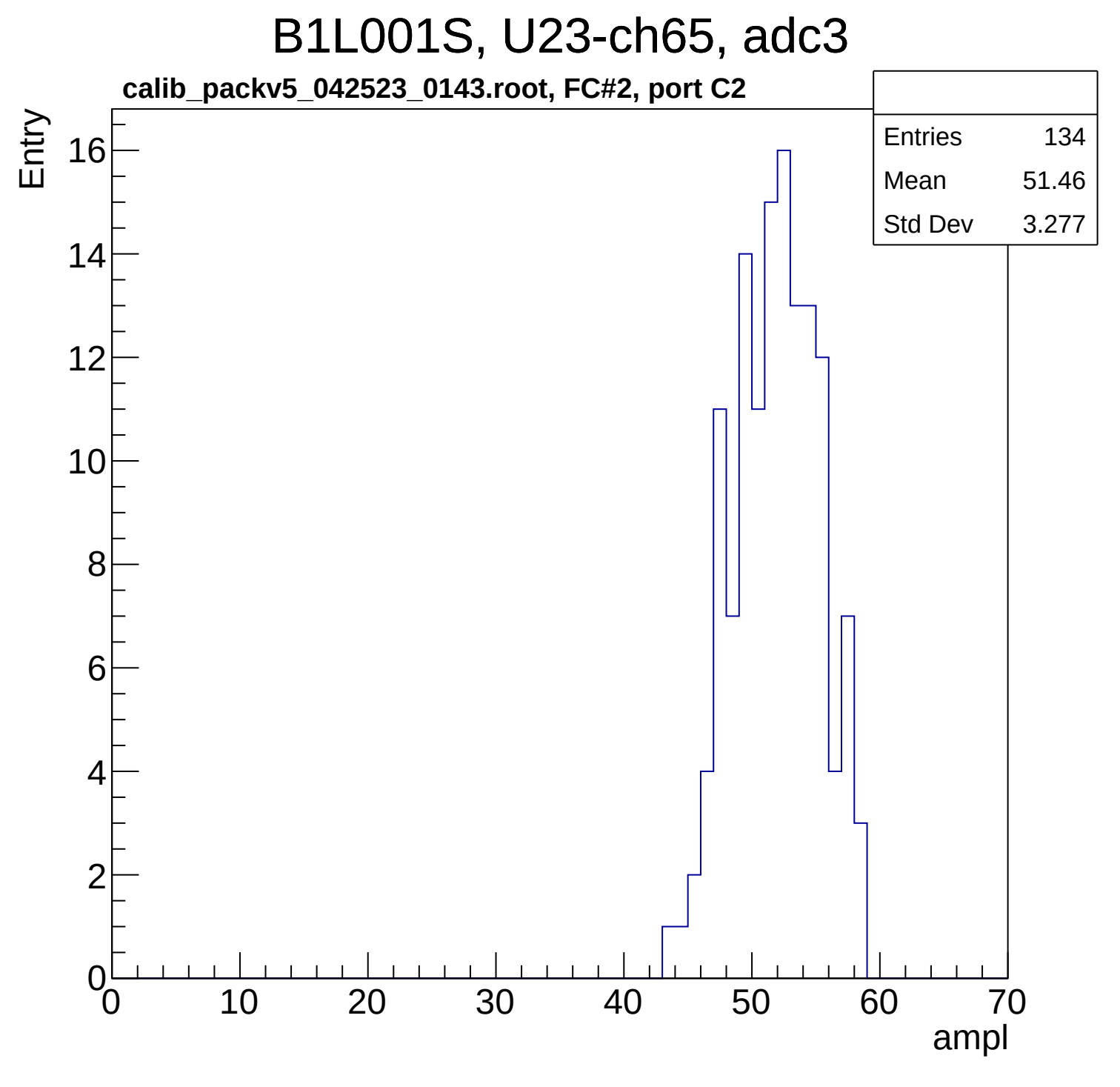
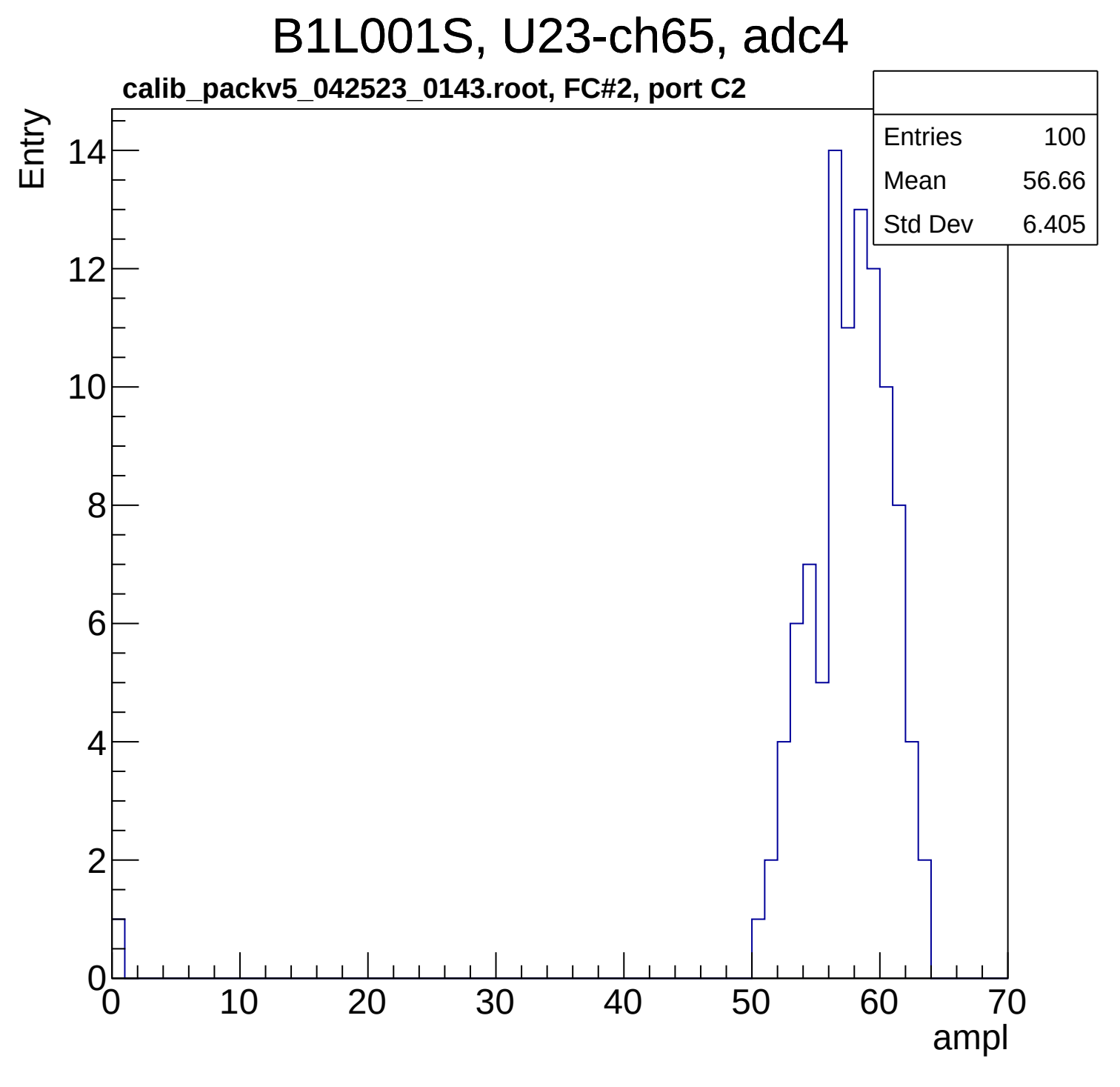
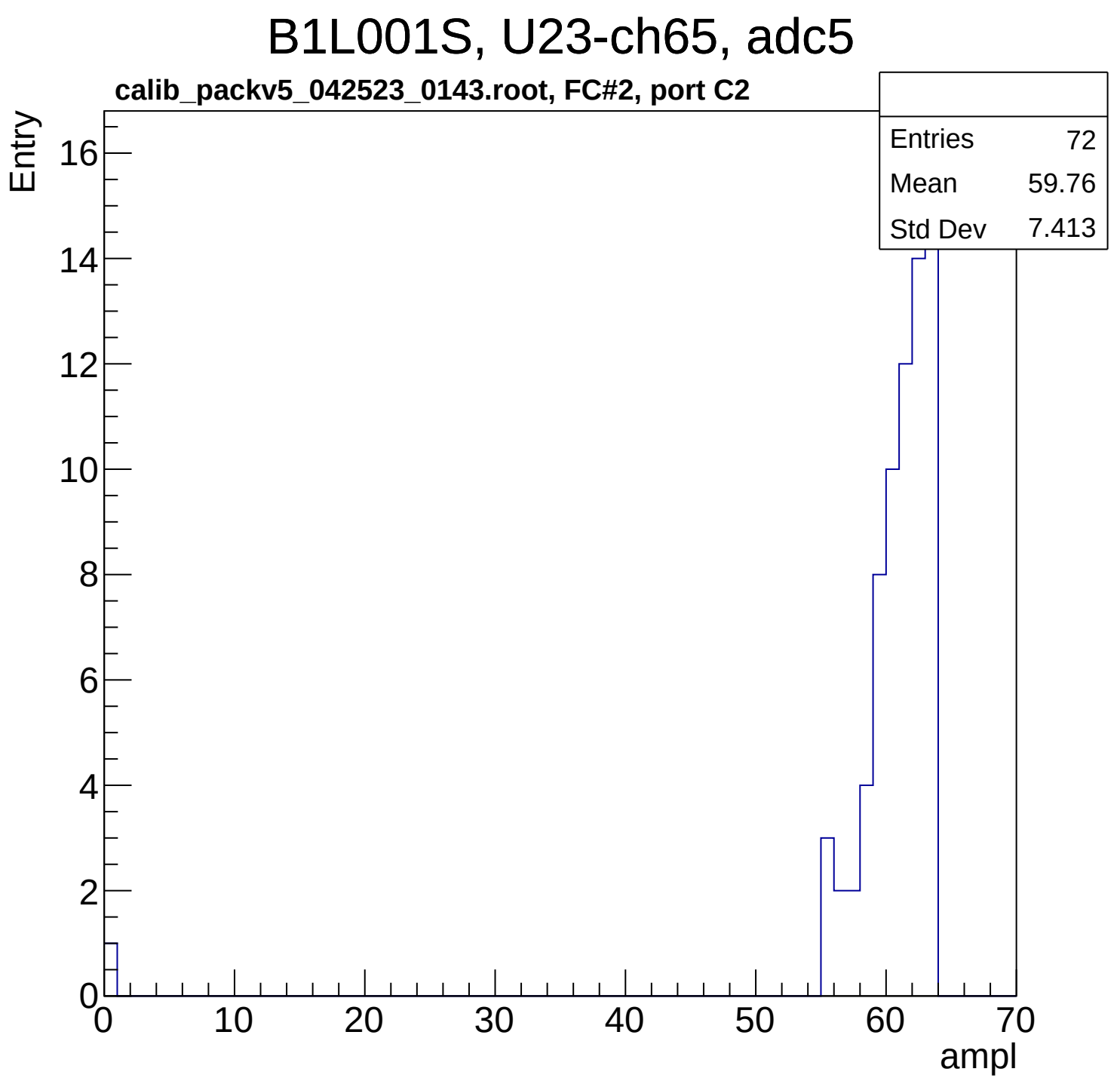

B1L001S, U23-ch65, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 51.46
Std Dev 3.277
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch65, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 56.66
Std Dev 6.405
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch65, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 59.76
Std Dev 7.413
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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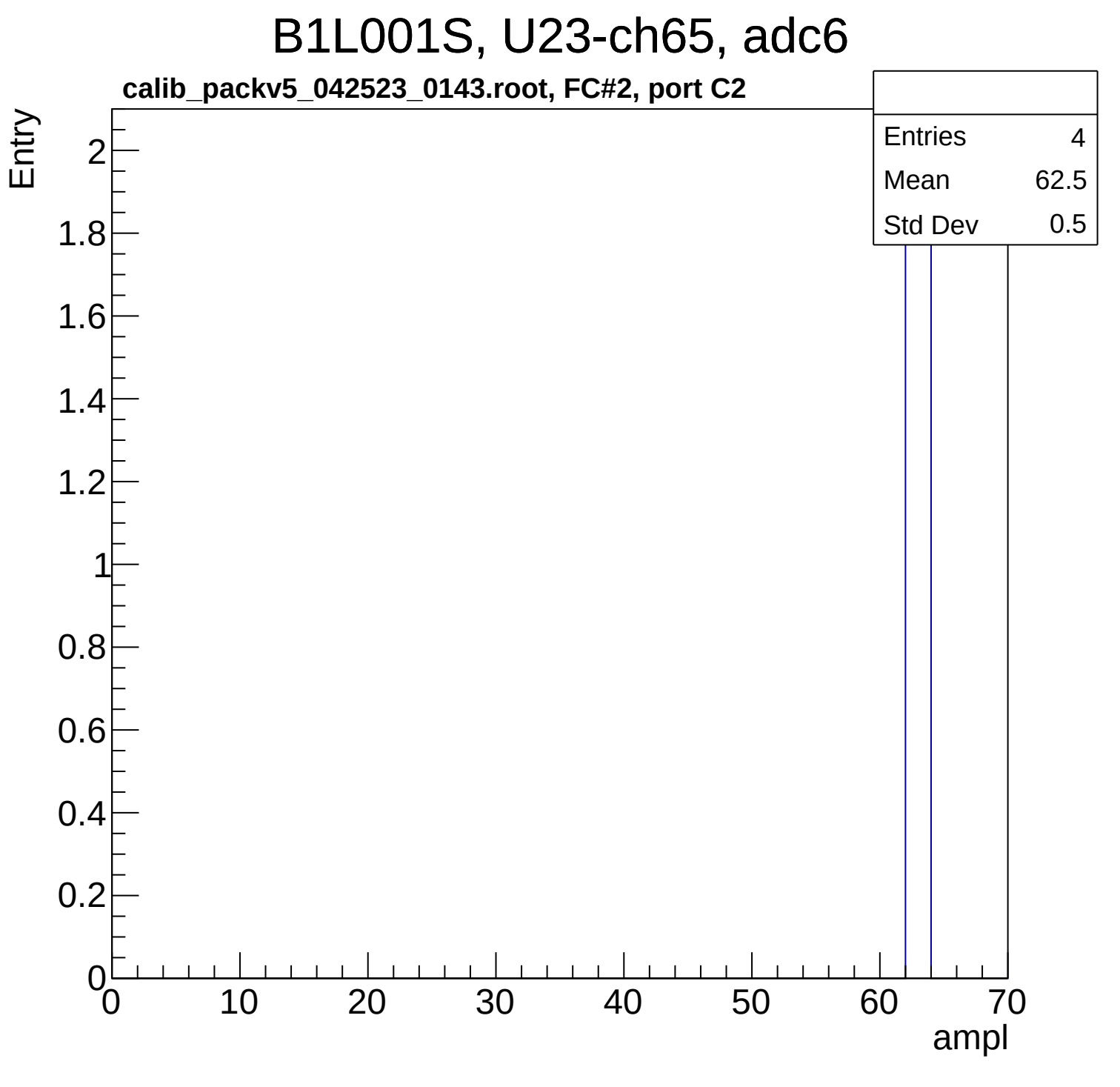
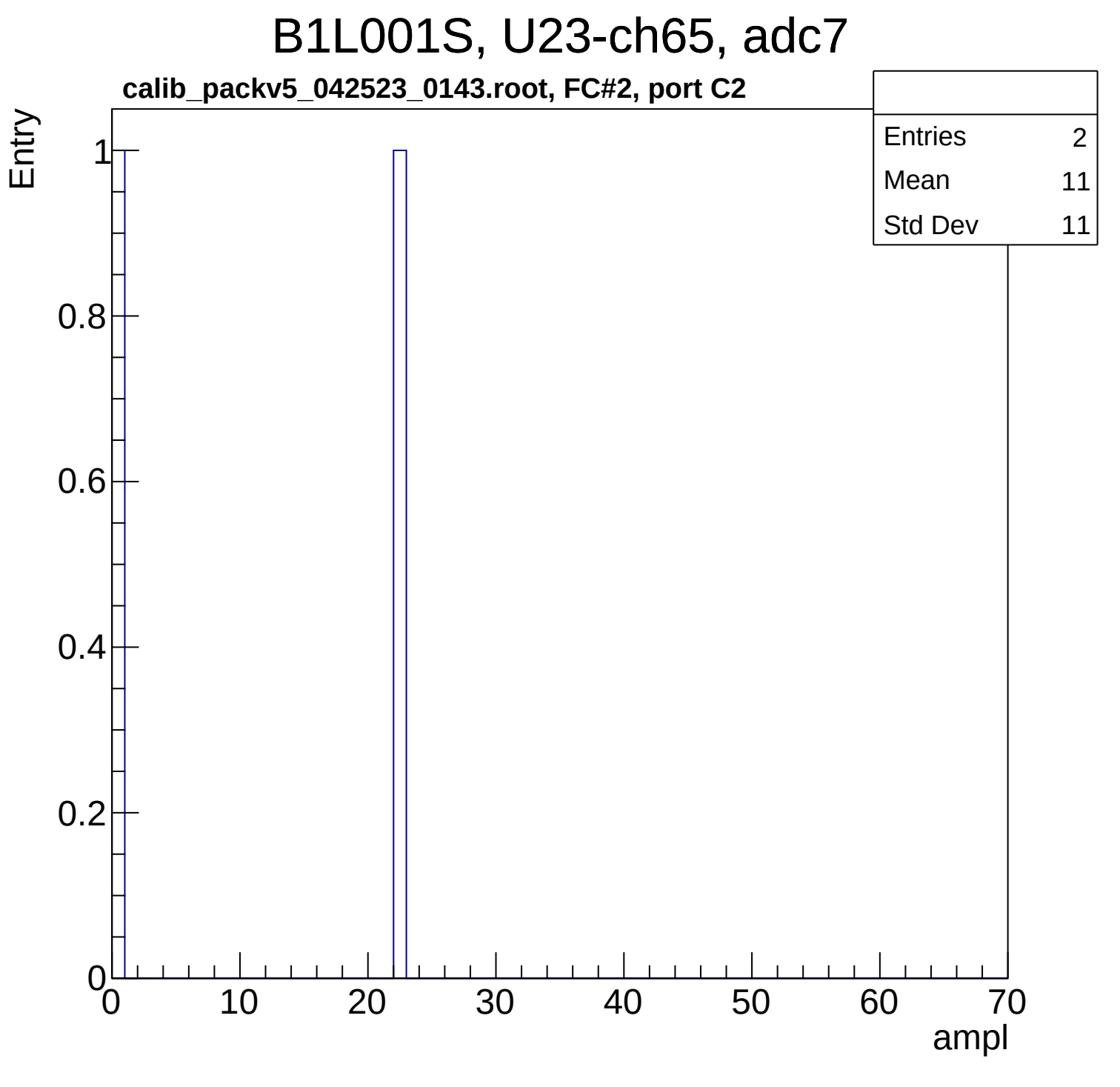
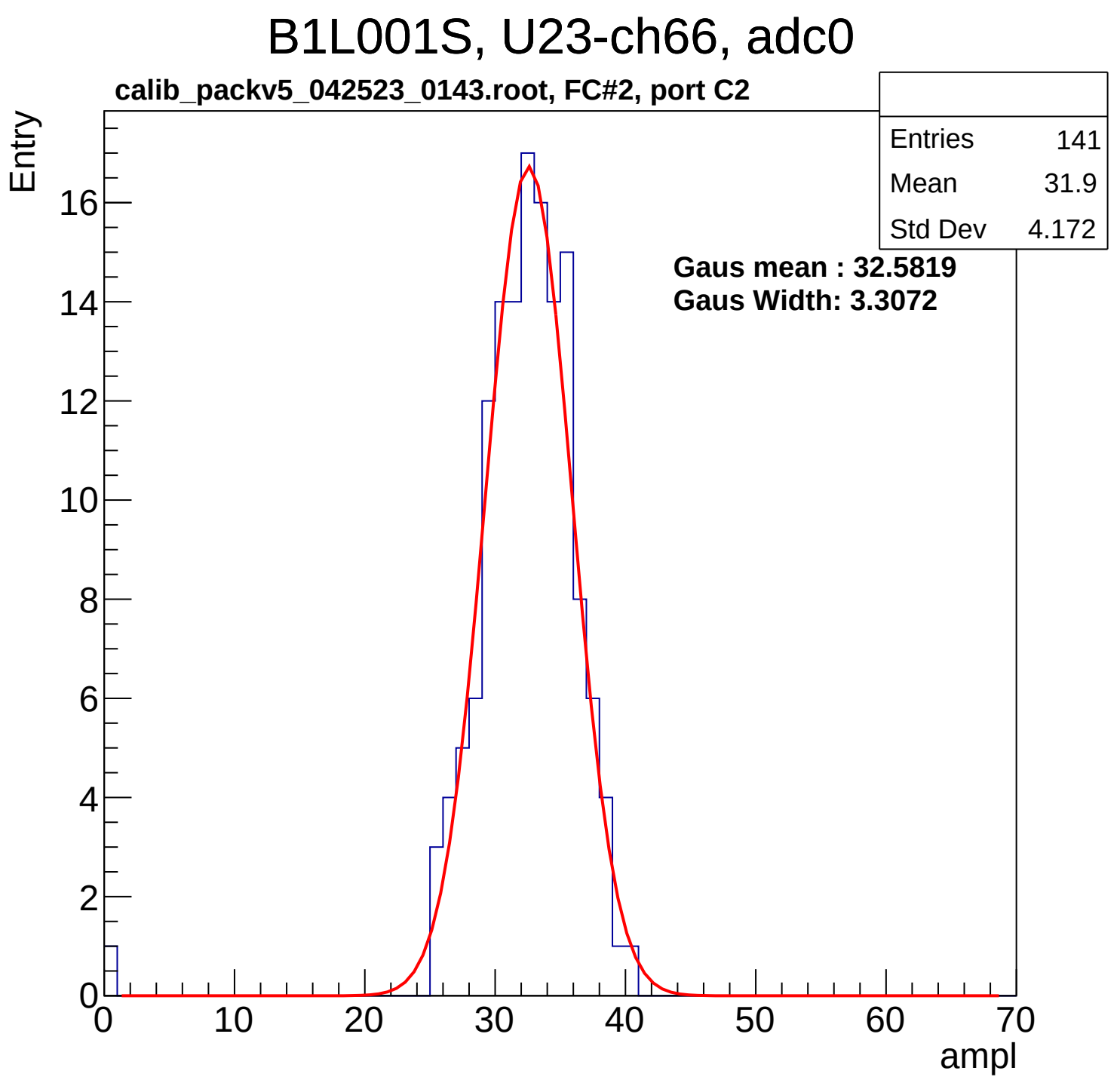
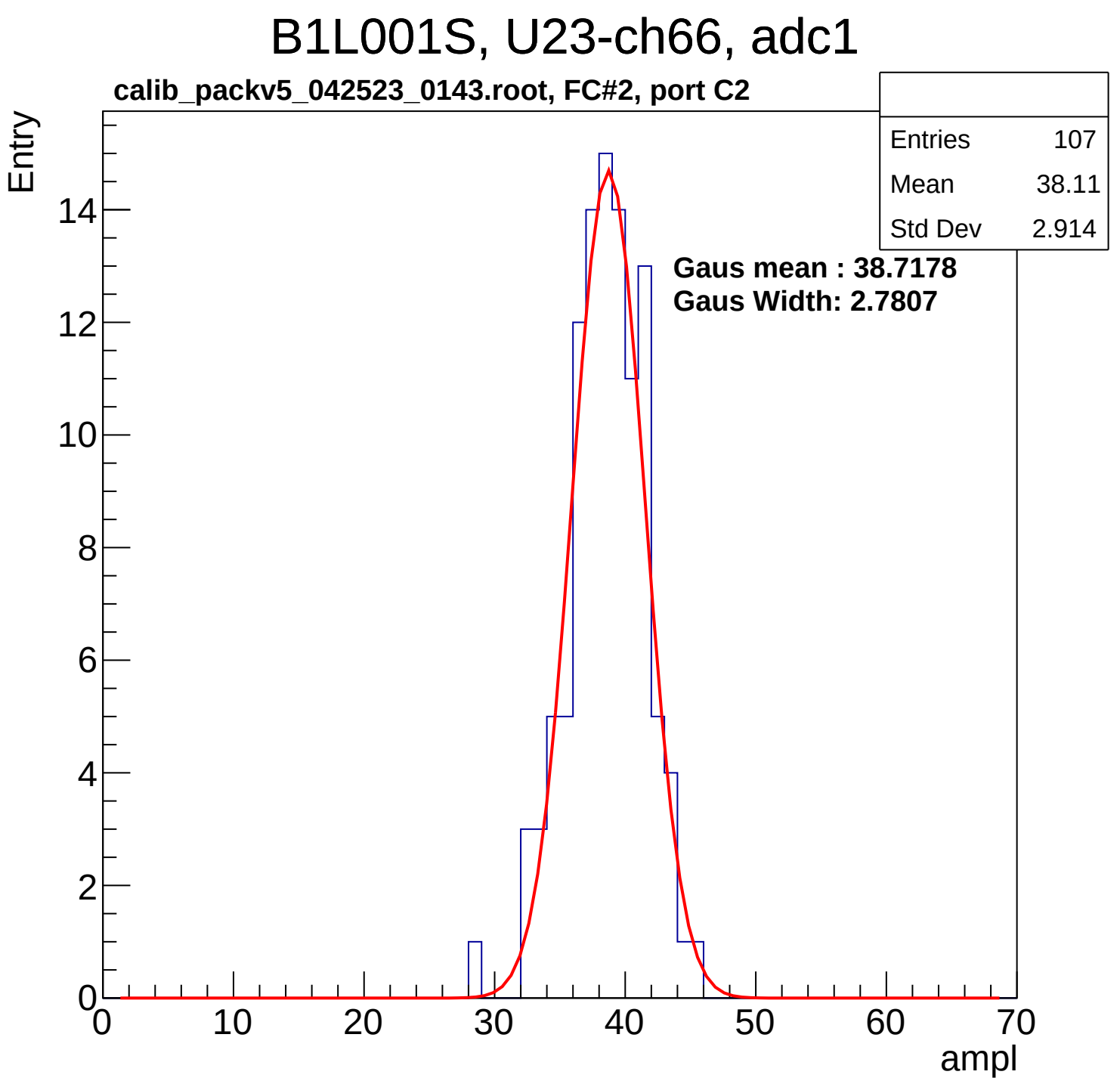
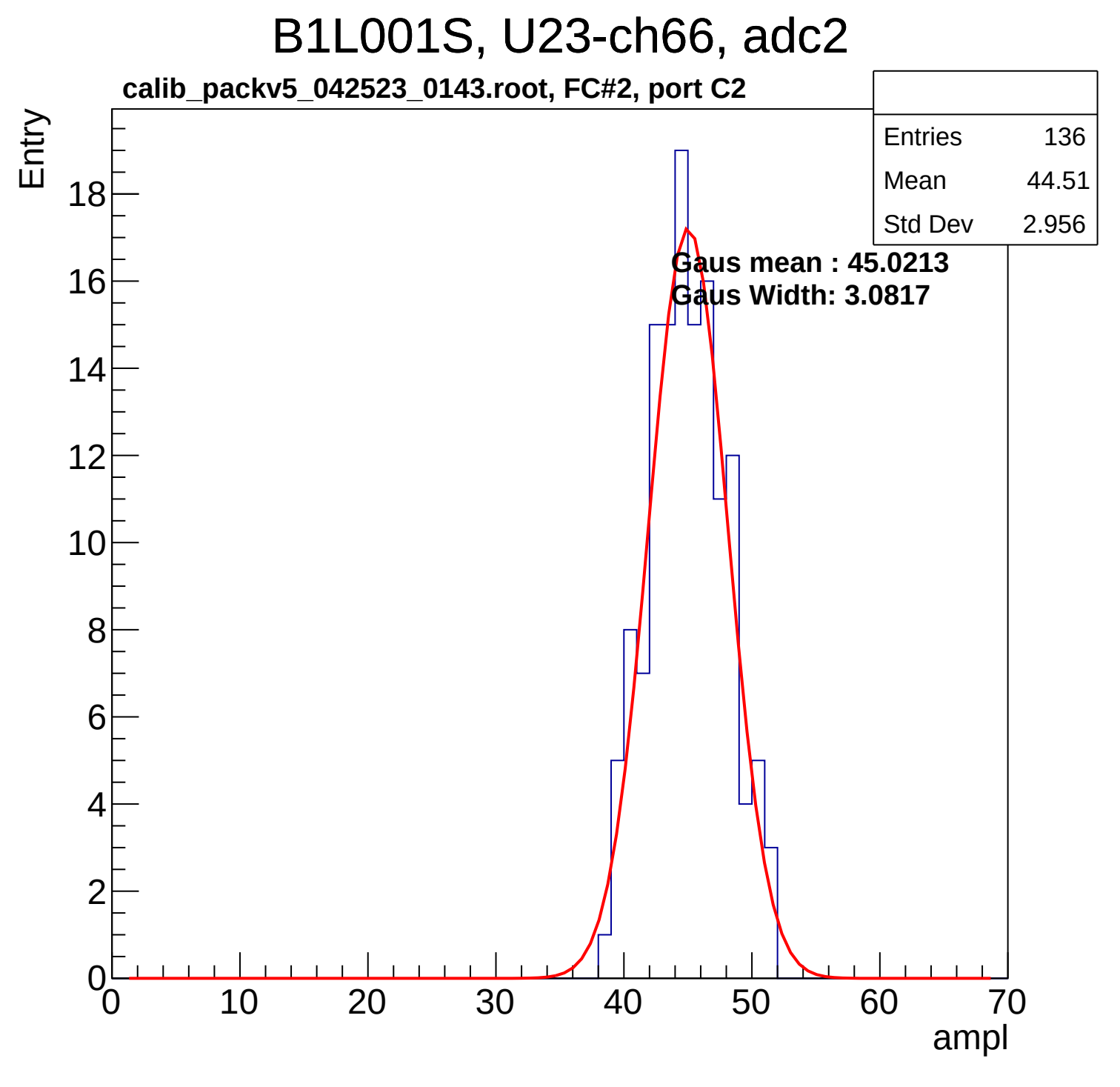

B1L001S, U23-ch65, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch65, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch66, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 31.9
Std Dev 4.172
Gaus mean : 32.5819
Gaus Width: 3.3072
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch66, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 38.11
Std Dev 2.914
Gaus mean : 38.7178
Gaus Width: 2.7807
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch66, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 44.51
Std Dev 2.956
Gaus mean : 45.0213
Gaus Width: 3.0817
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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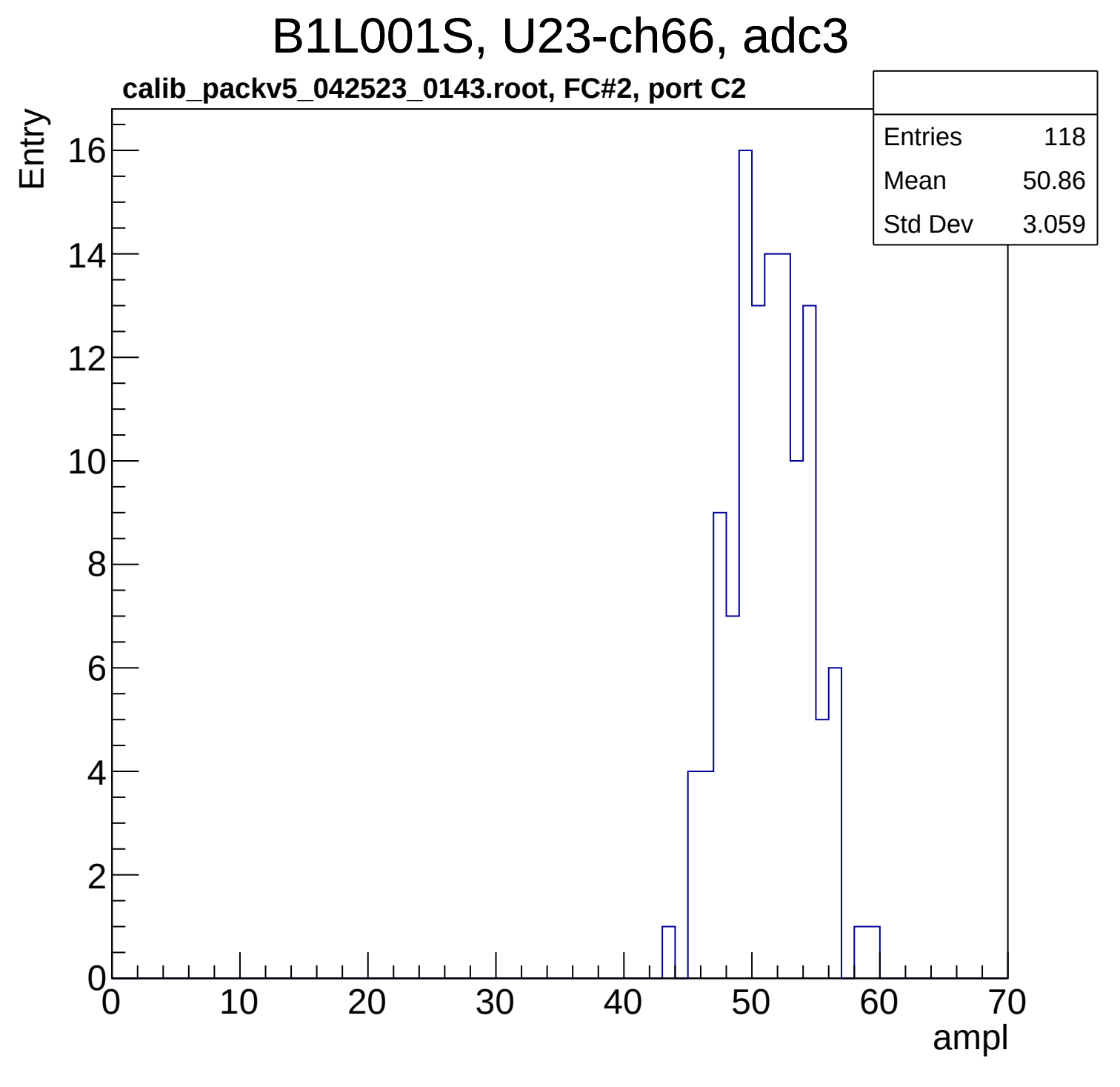
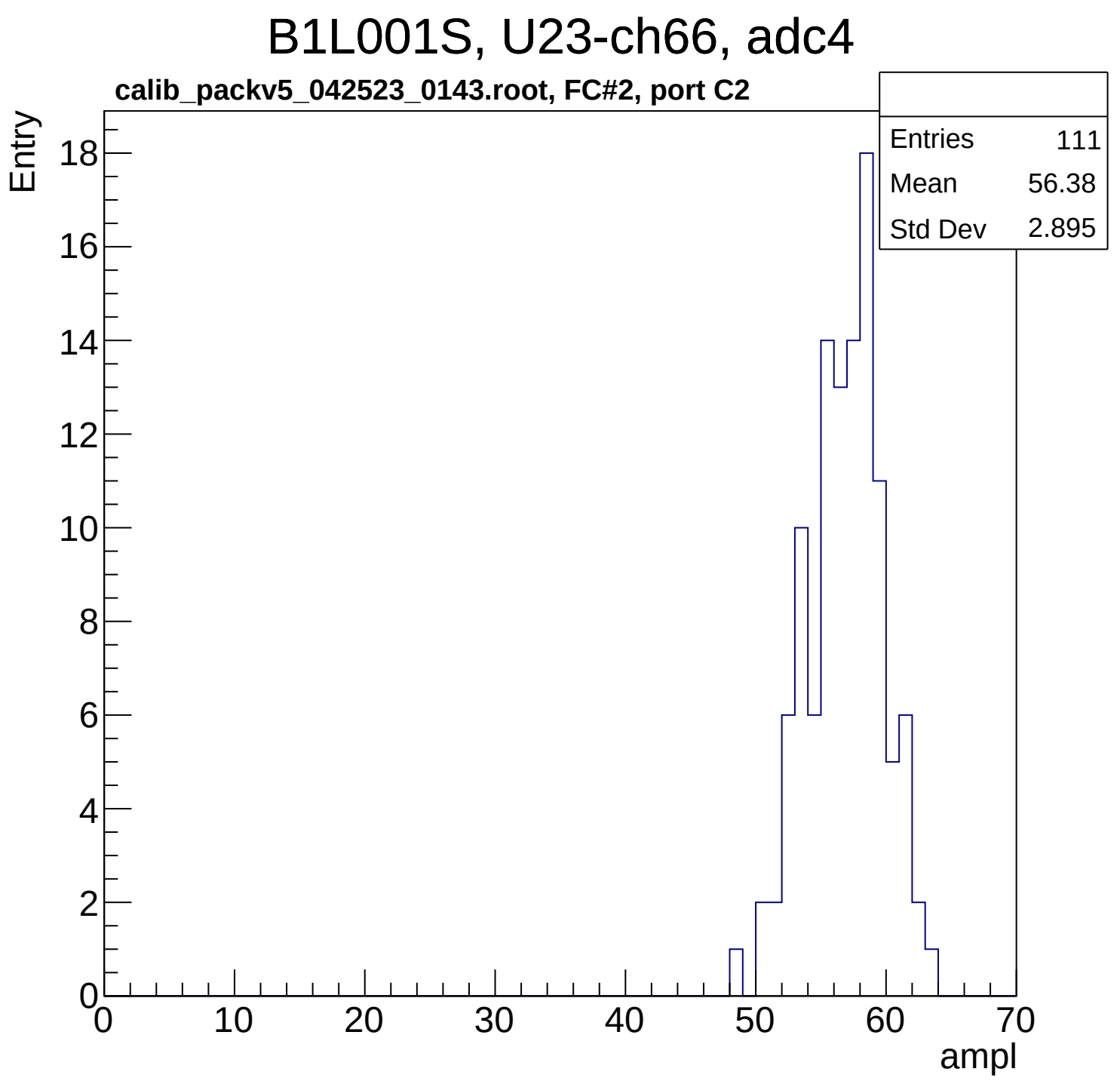

B1L001S, U23-ch66, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 50.86
Std Dev 3.059
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch66, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 56.38
Std Dev 2.895
Entry
ampl
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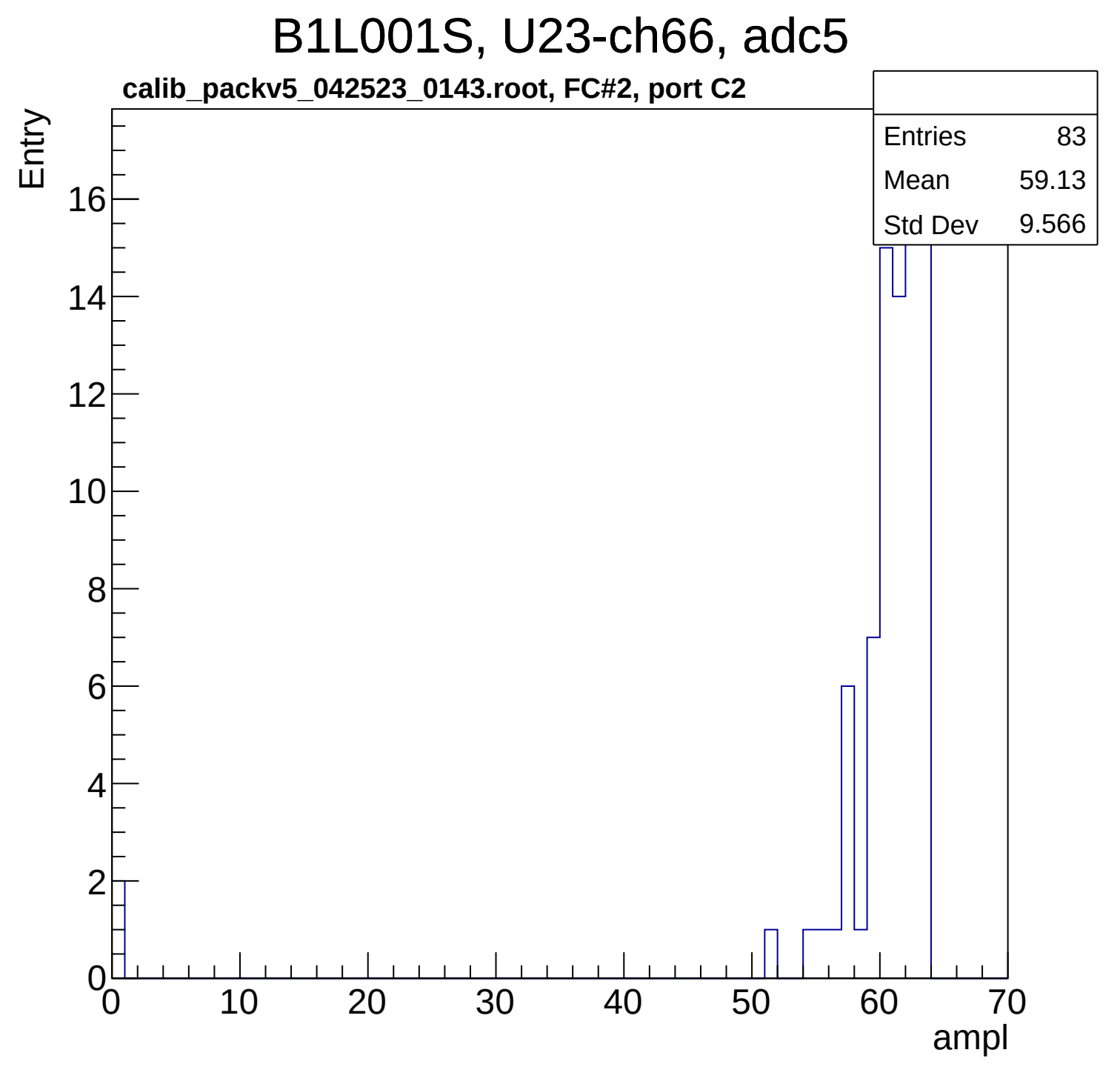
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch66, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 59.13
Std Dev 9.566
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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60
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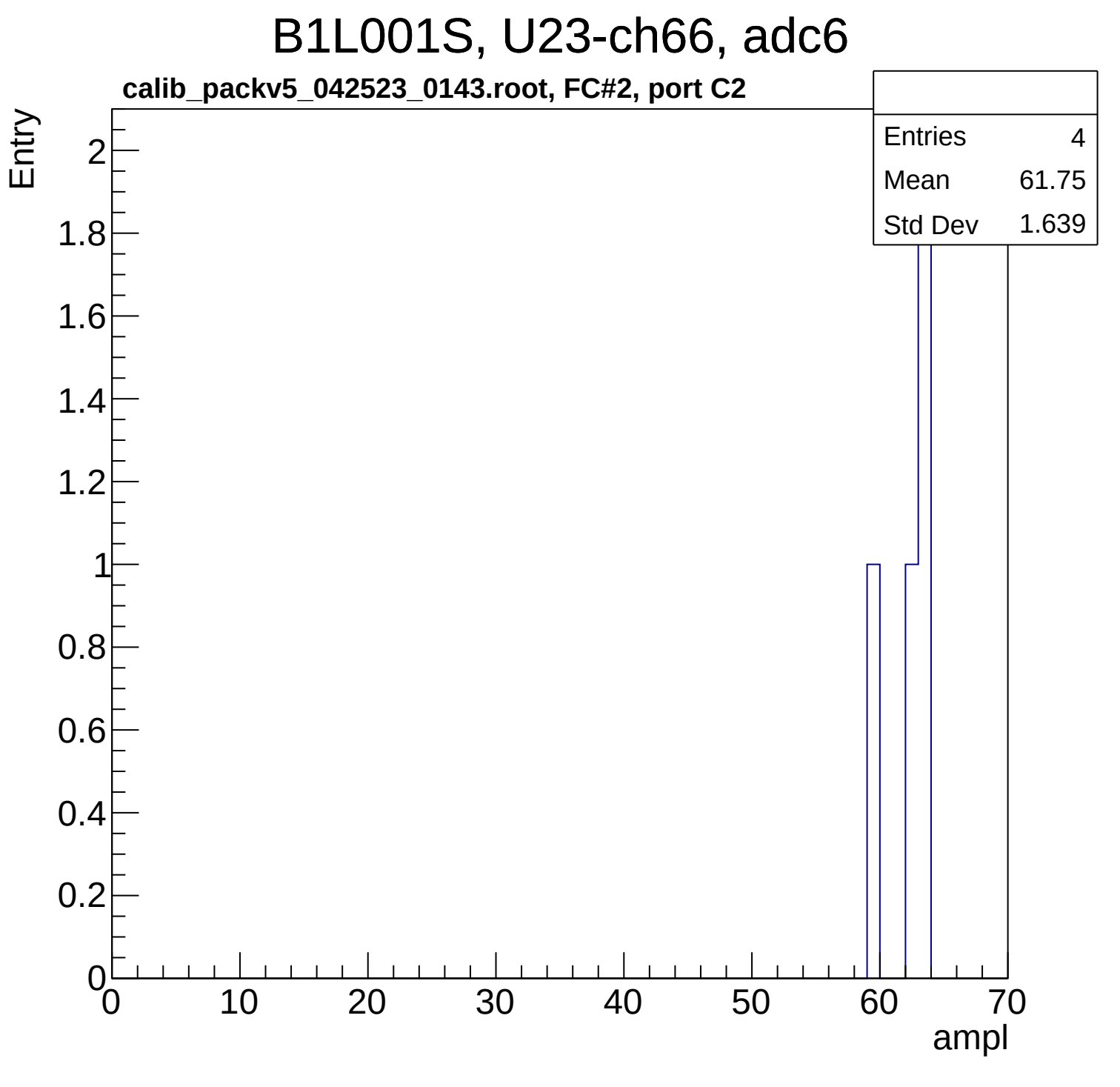
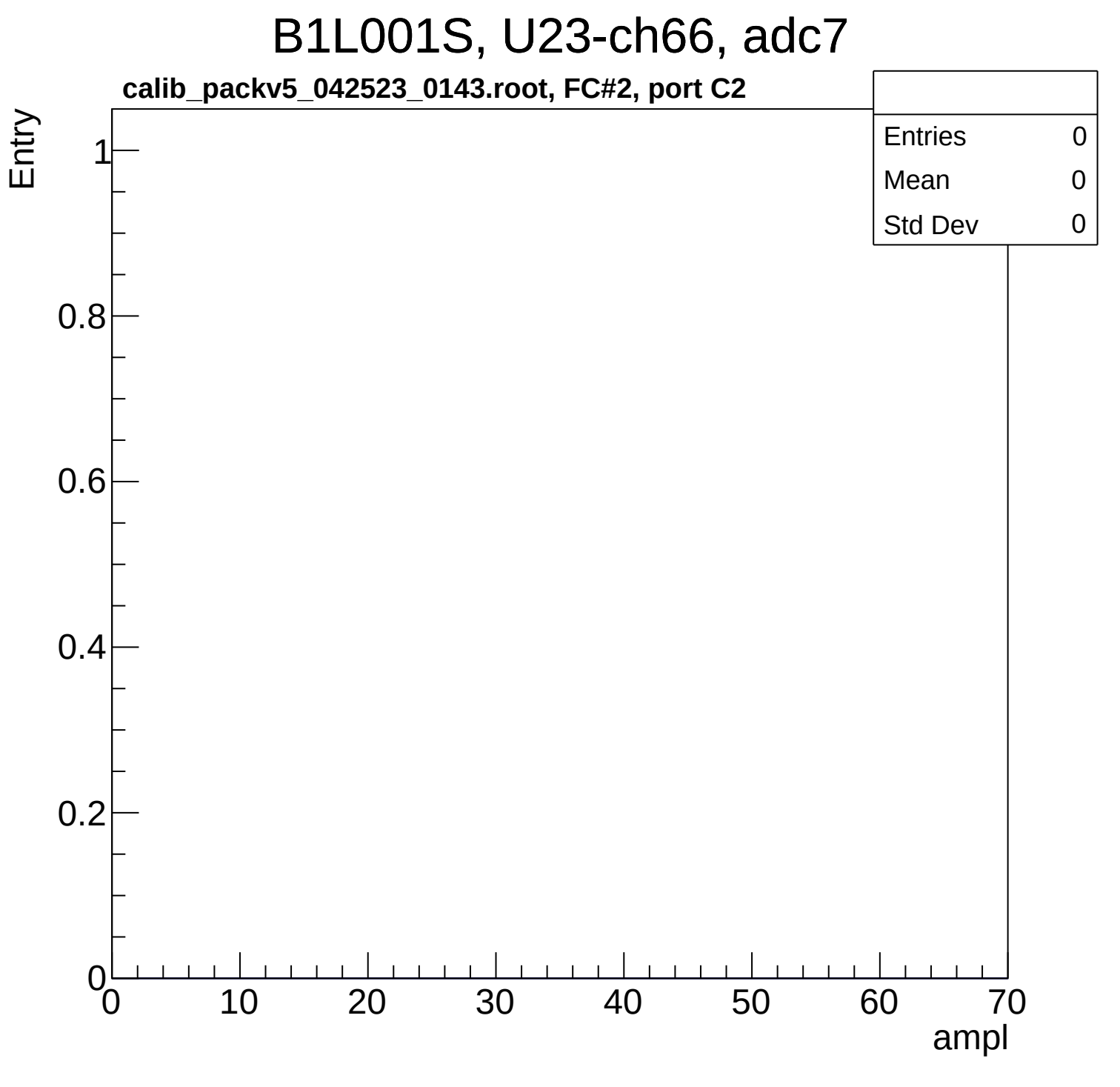
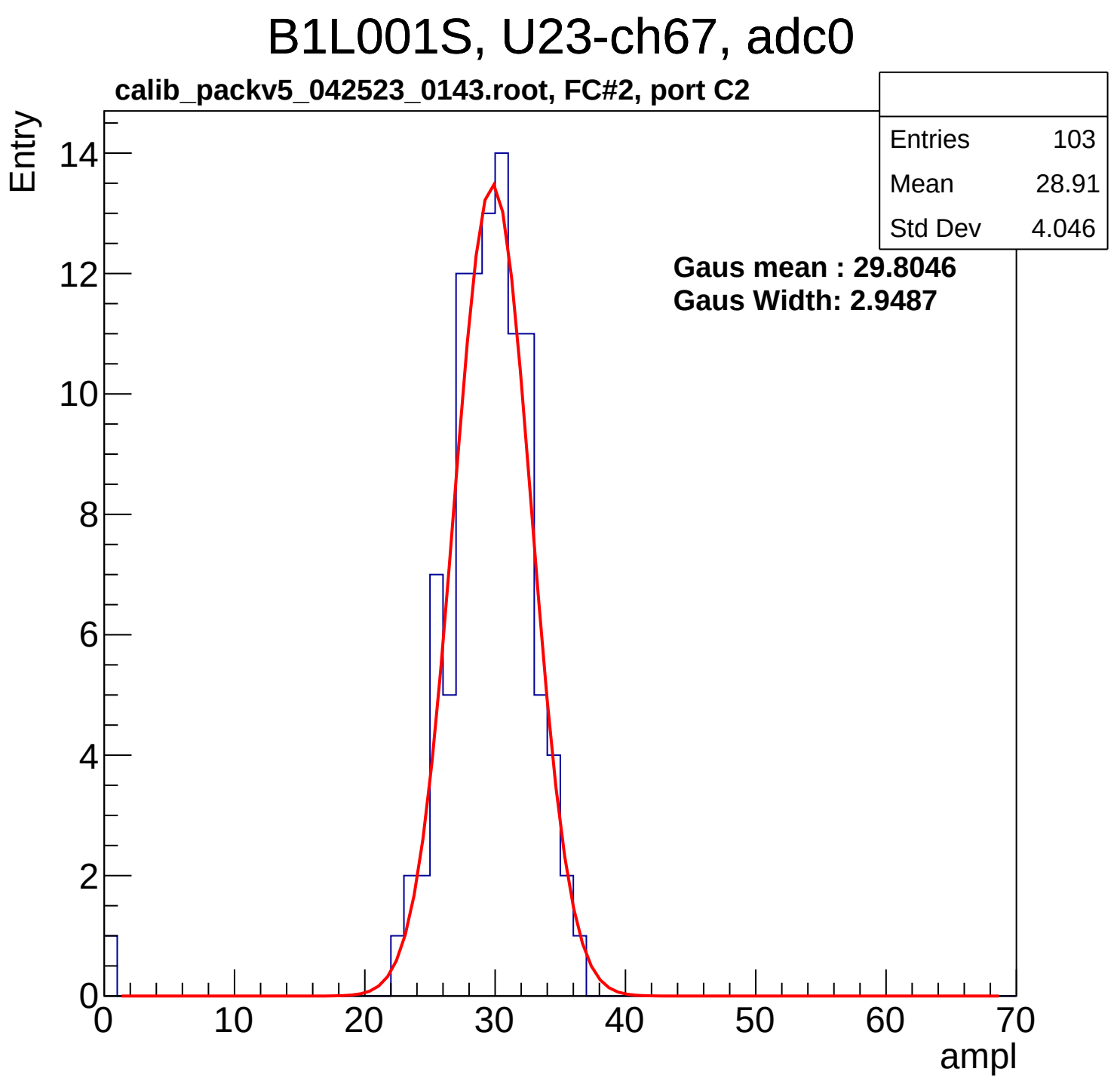
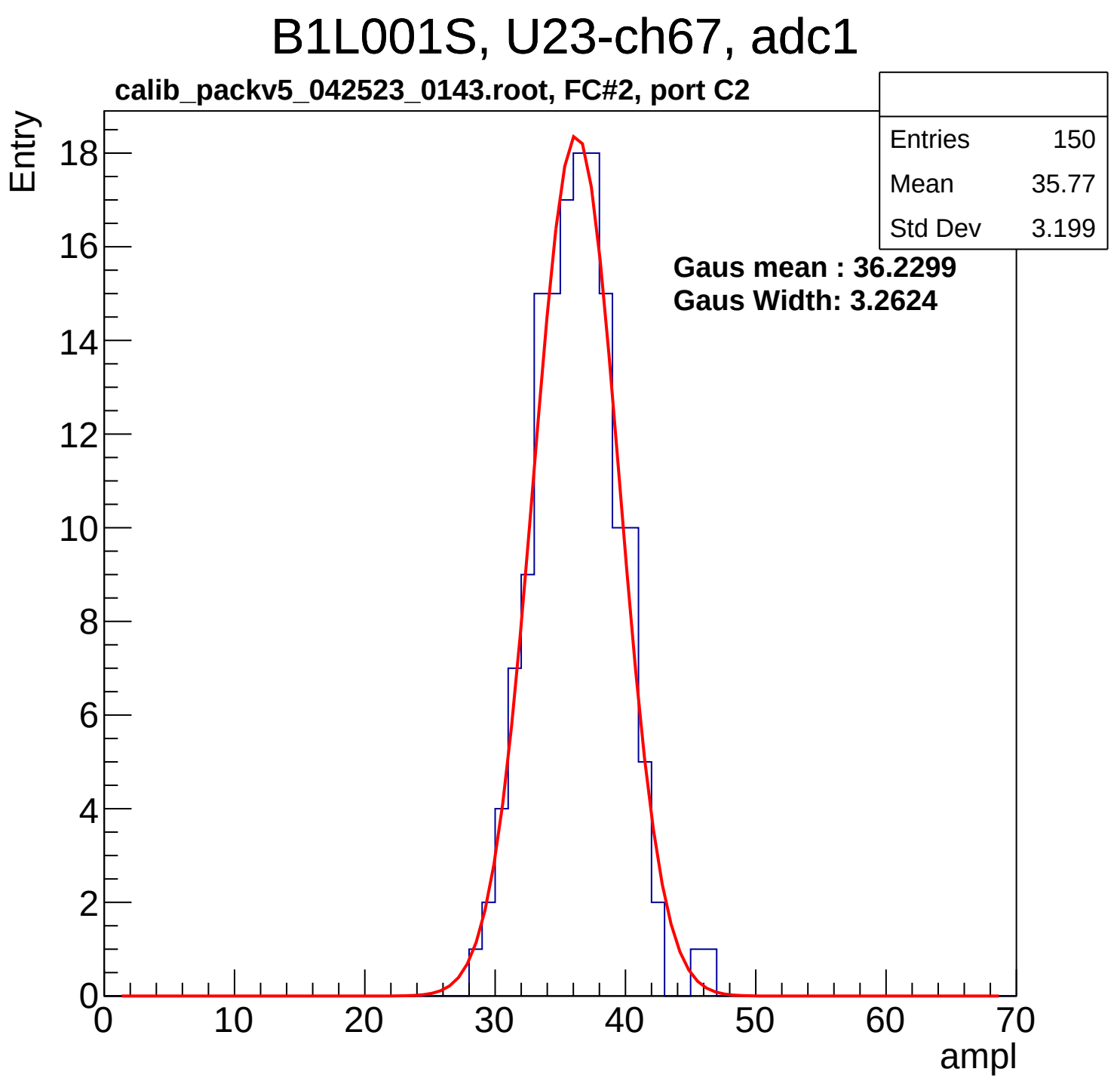
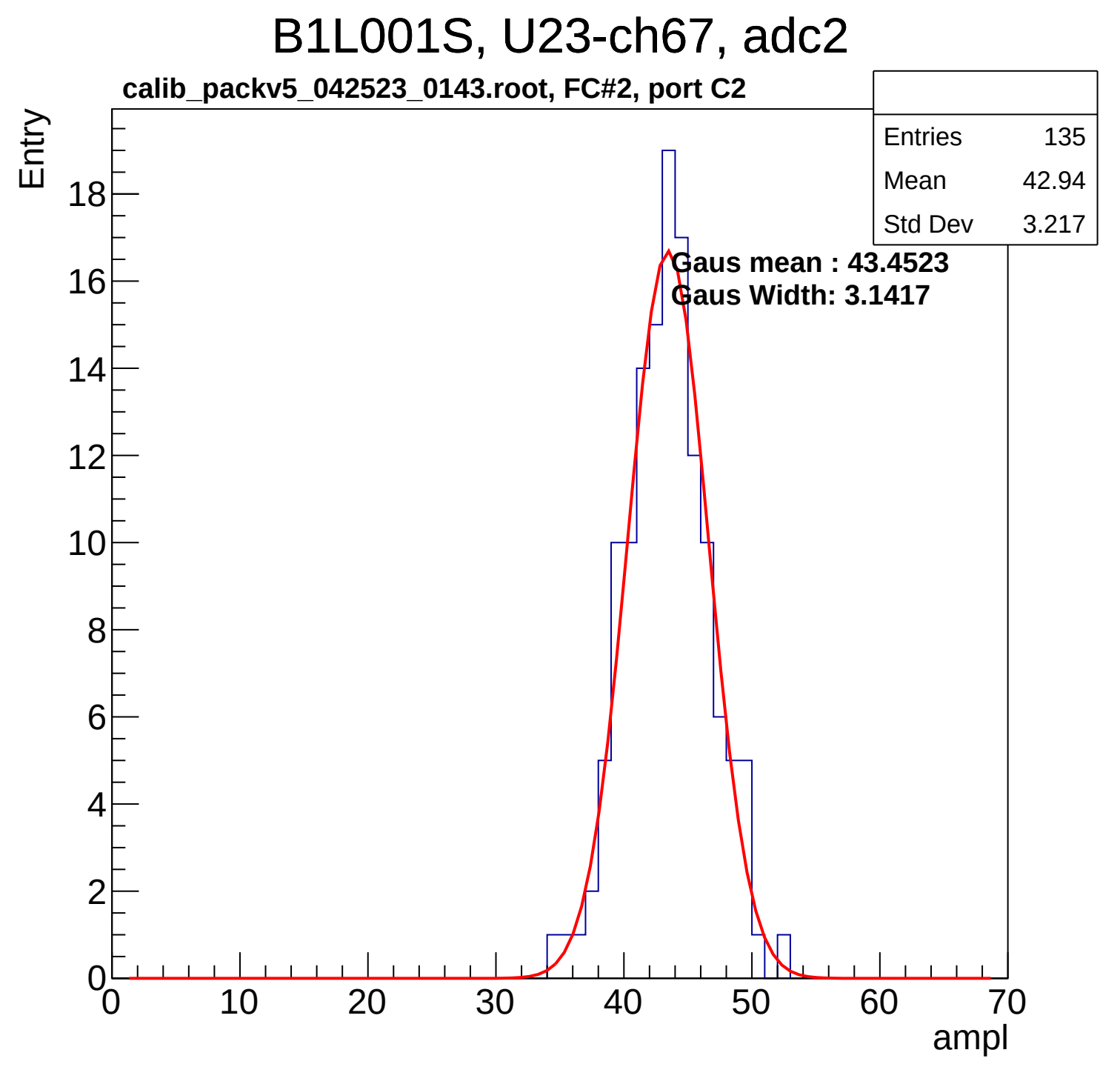

B1L001S, U23-ch66, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch66, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch67, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 28.91
Std Dev 4.046
Gaus mean : 29.8046
Gaus Width: 2.9487
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch67, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 35.77
Std Dev 3.199
Gaus mean : 36.2299
Gaus Width: 3.2624
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch67, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 42.94
Std Dev 3.217
Gaus mean : 43.4523
Gaus Width: 3.1417
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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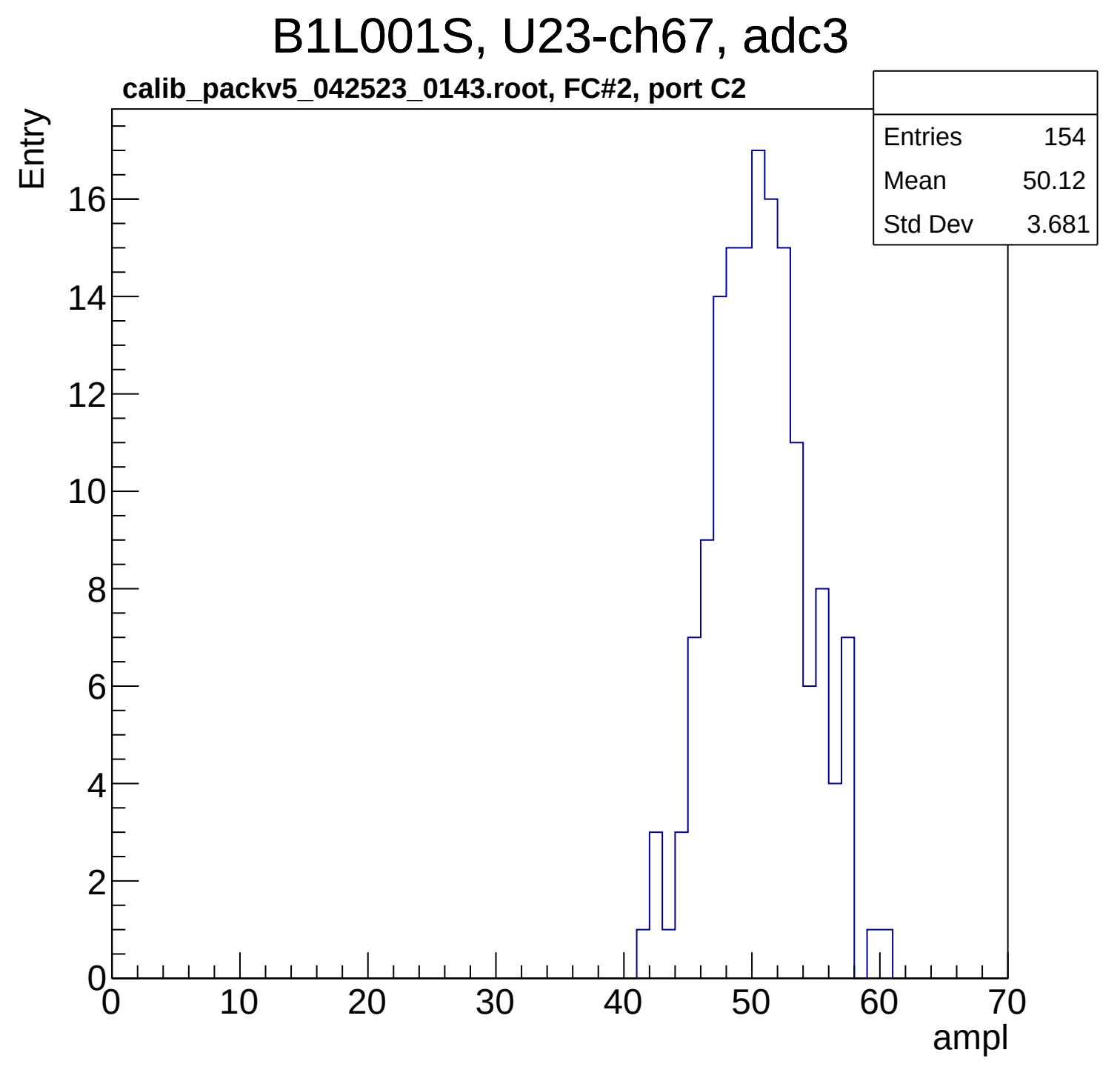
B1L001S, U23-ch67, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 50.12
Std Dev 3.681
Entry
ampl
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2
4
6
8
10
12
14
16
0
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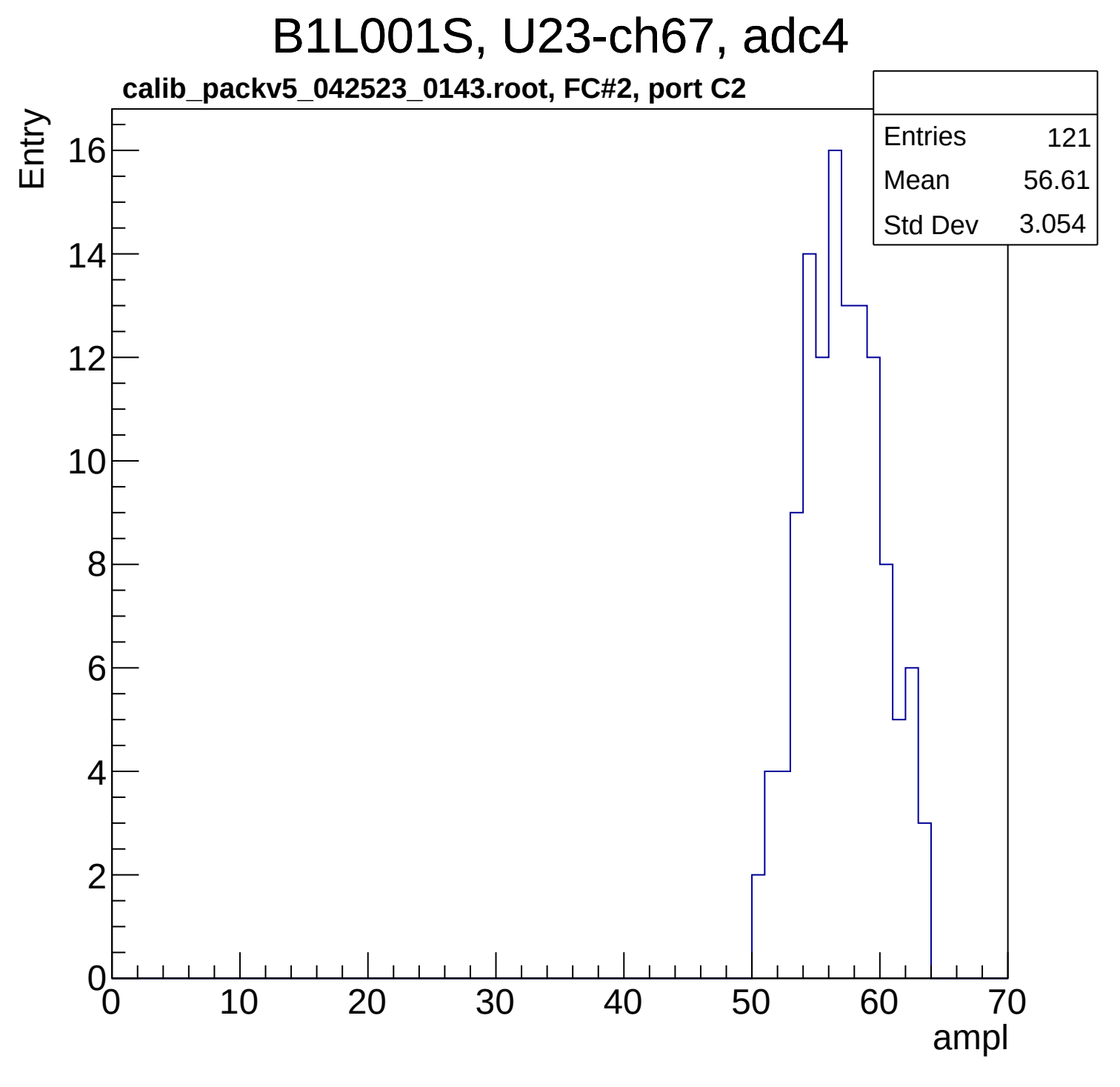
20
30
40
50
60
70


B1L001S, U23-ch67, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 56.61
Std Dev 3.054
Entry
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6
8
10
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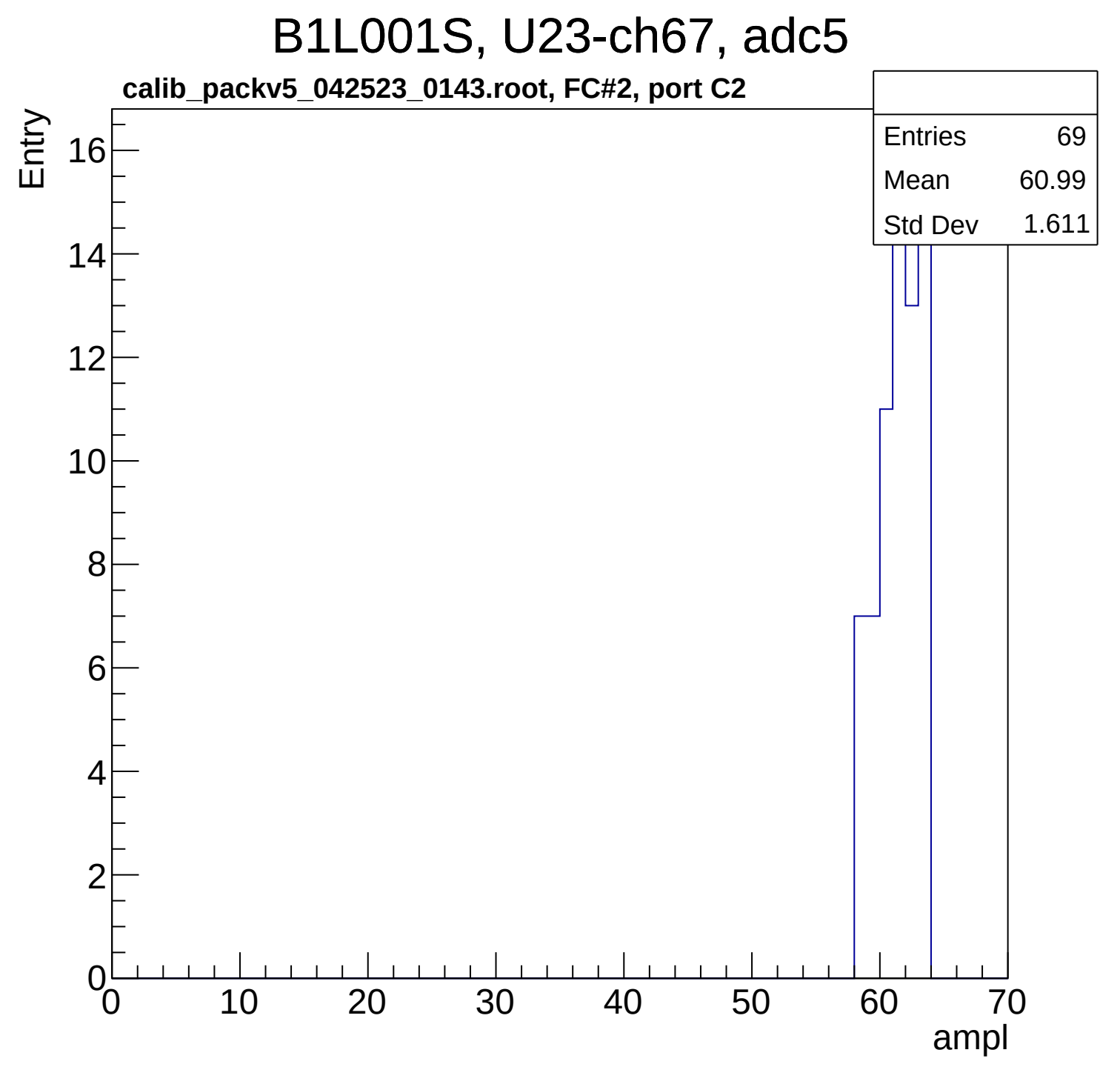
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch67, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 69
Mean 60.99
Std Dev 1.611
Entry
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6
8
10
12
14
16
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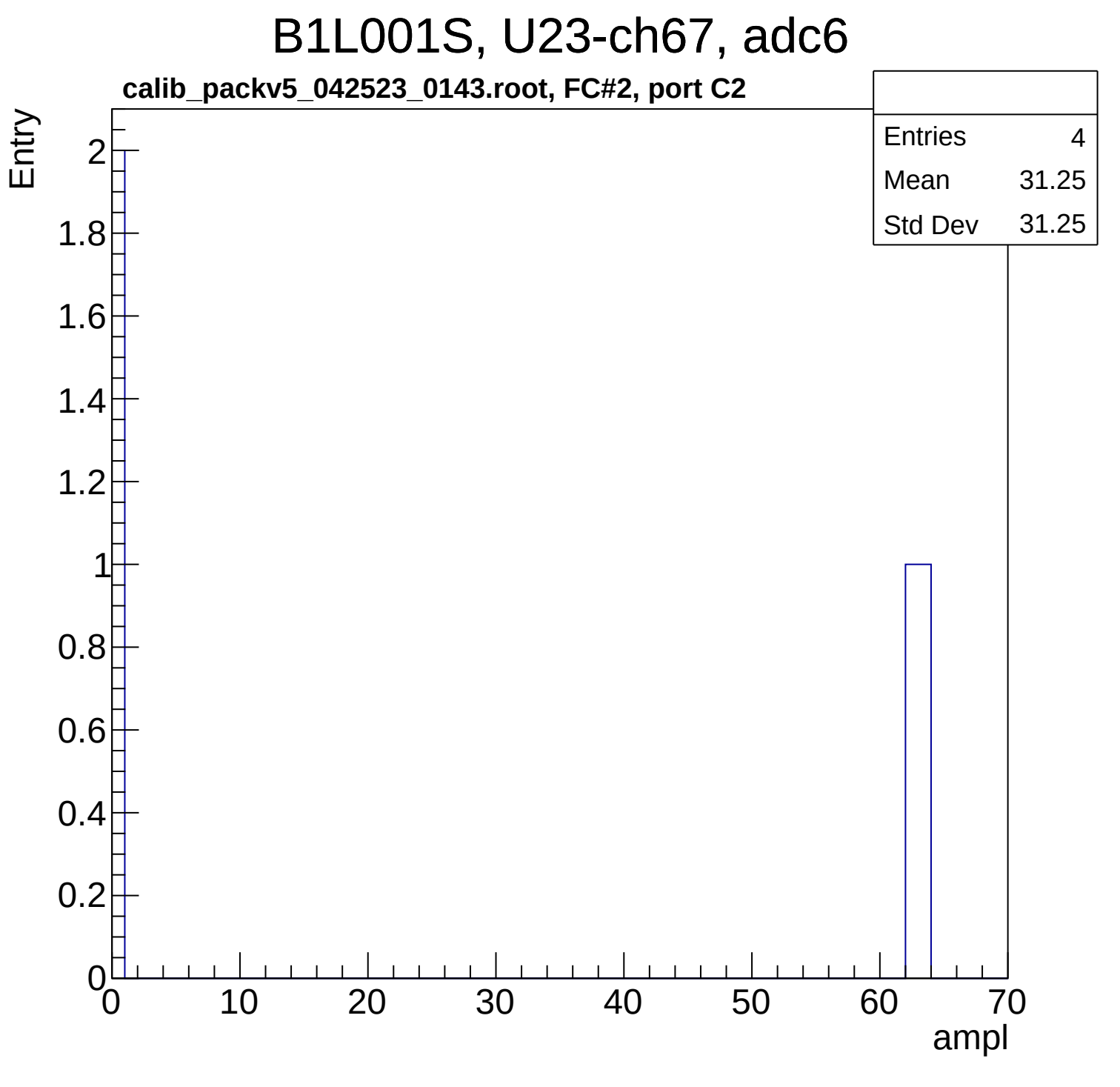
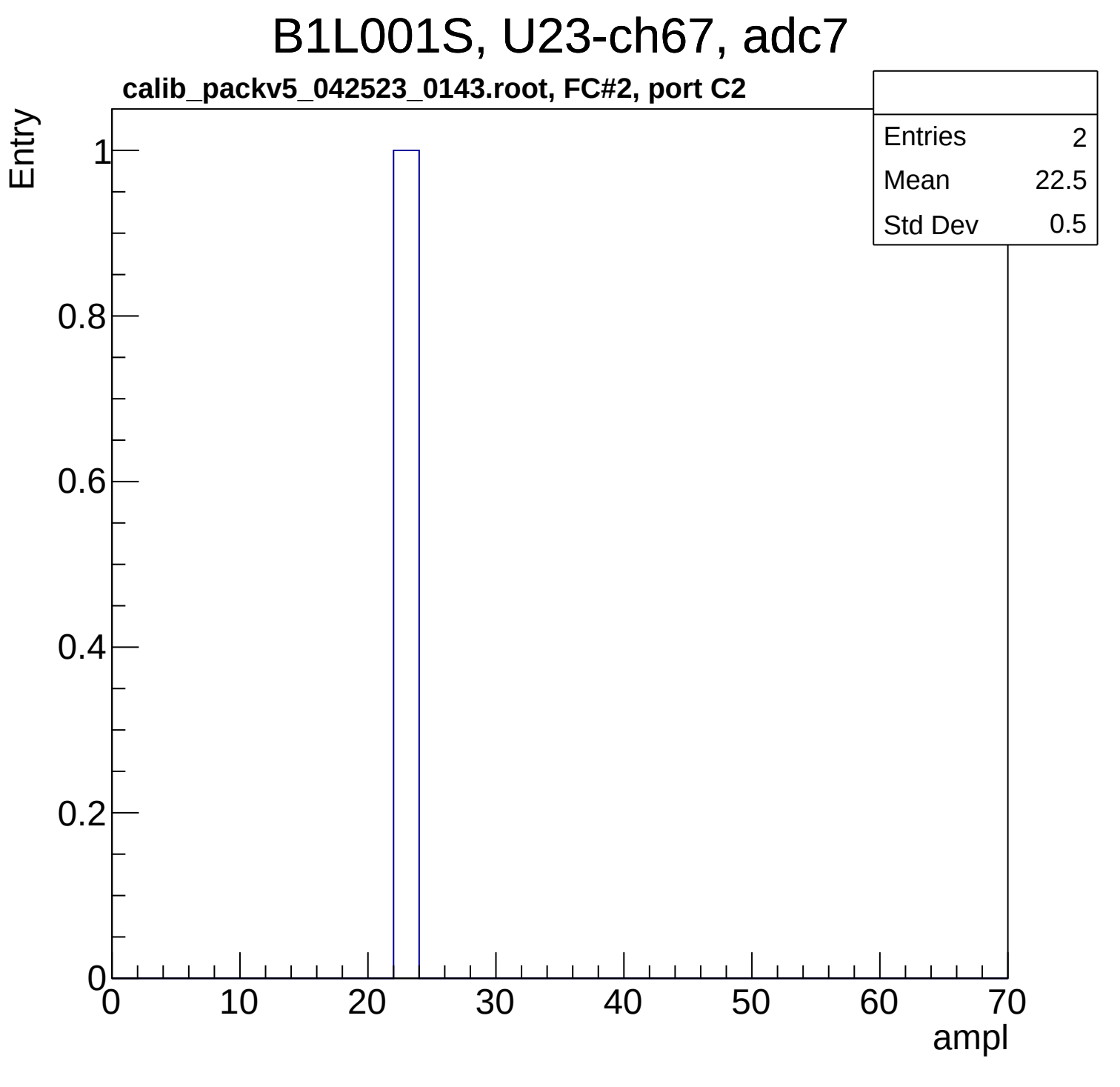
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch67, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 31.25
Std Dev 31.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch67, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 22.5
Std Dev 0.5
Entry
ampl
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0.2
0.4
0.6
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1
0
10
20
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40
50
60
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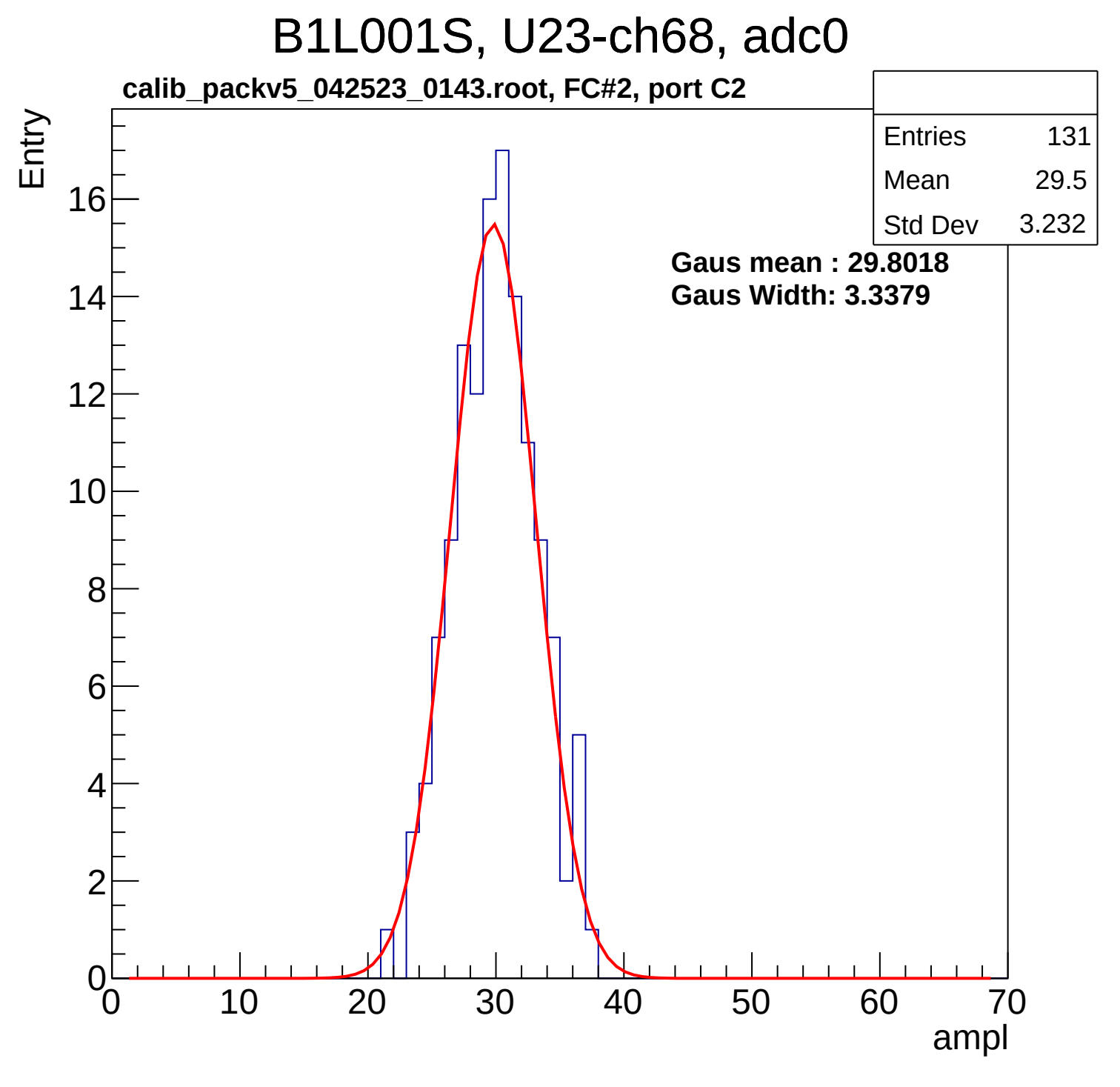
B1L001S, U23-ch68, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 29.5
Std Dev 3.232
Gaus mean : 29.8018
Gaus Width: 3.3379
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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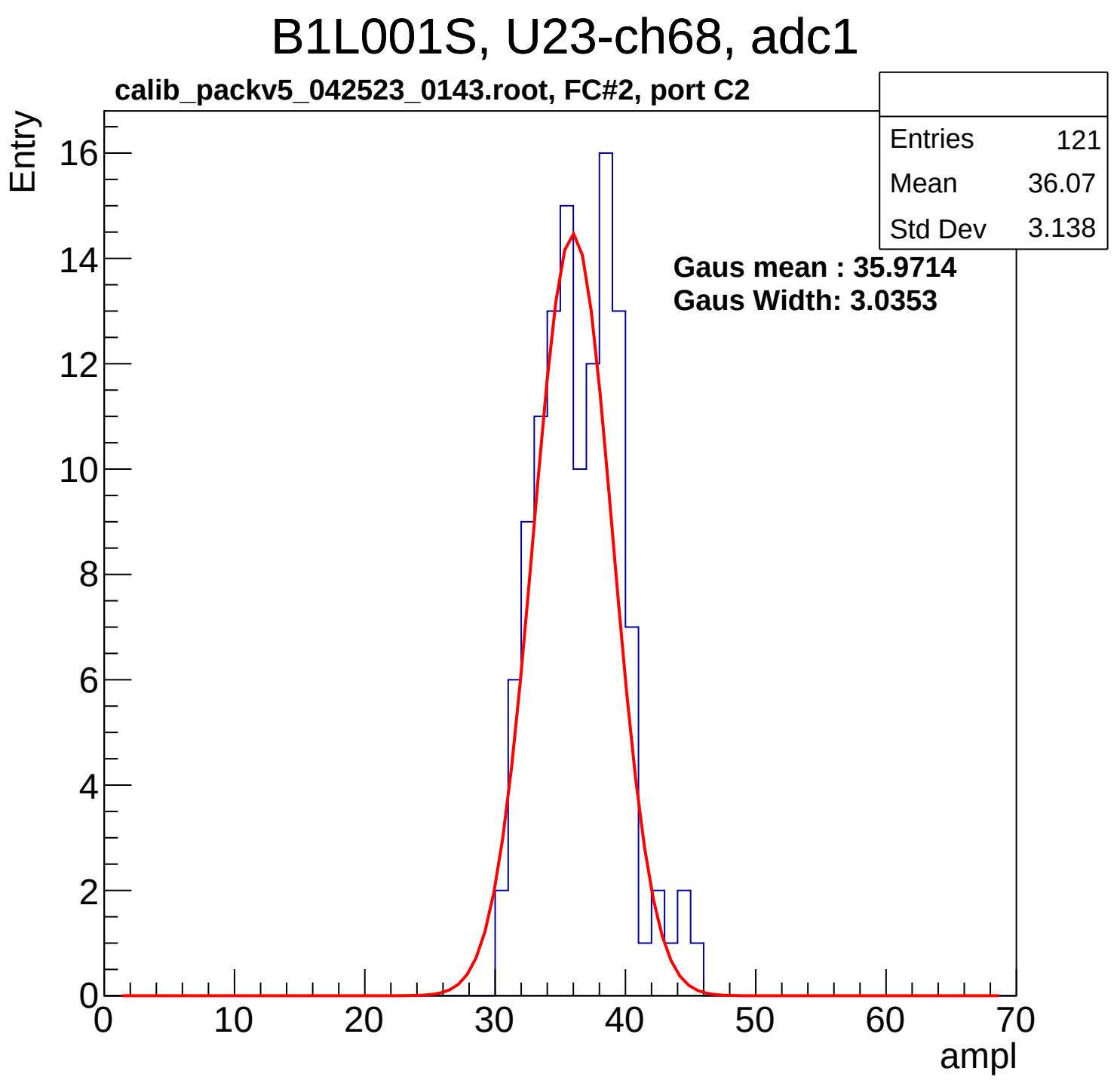
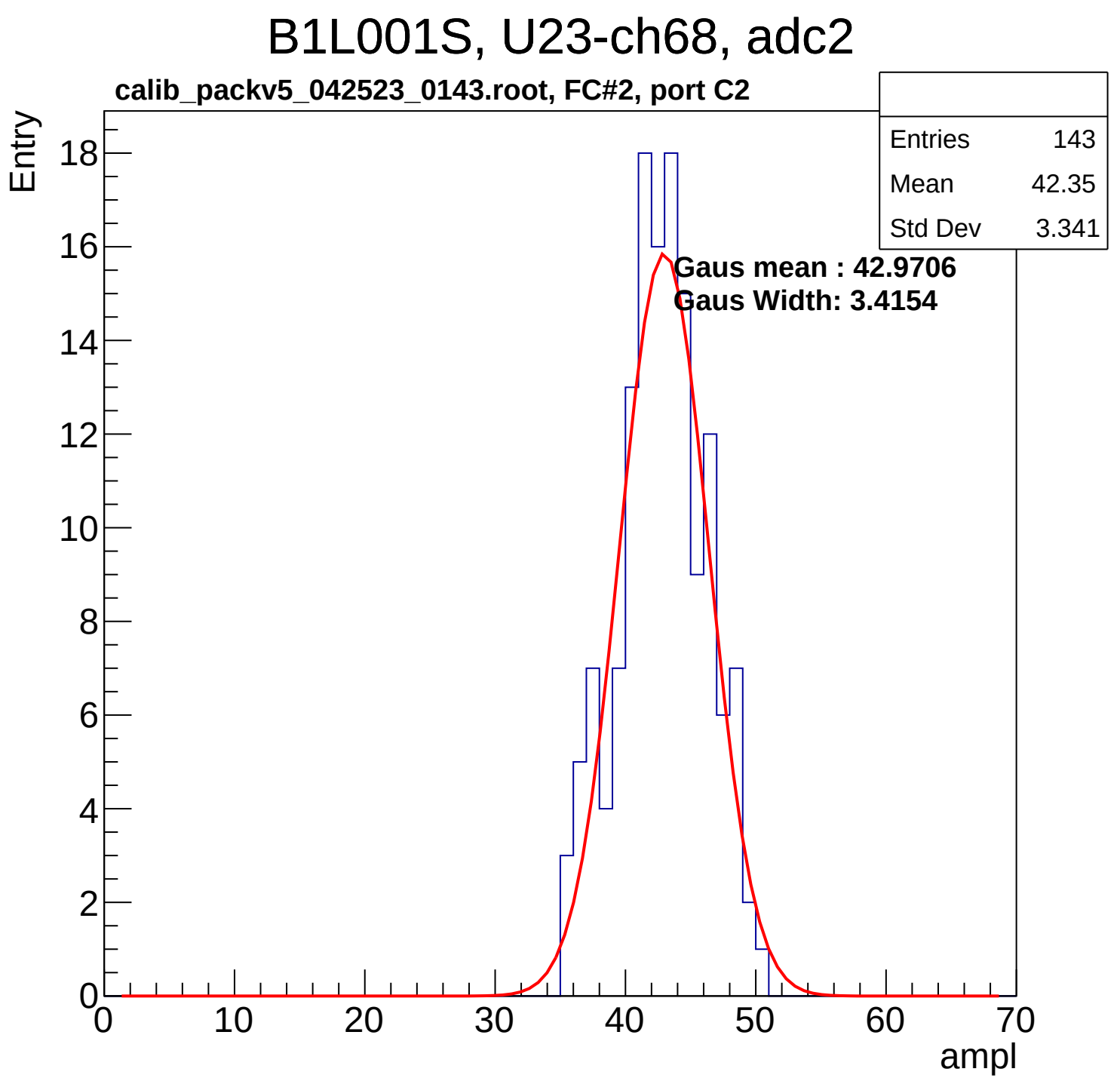
50
60
70


B1L001S, U23-ch68, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 36.07
Std Dev 3.138
Gaus mean : 35.9714
Gaus Width: 3.0353
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch68, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 42.35
Std Dev 3.341
Gaus mean : 42.9706
Gaus Width: 3.4154
Entry
ampl
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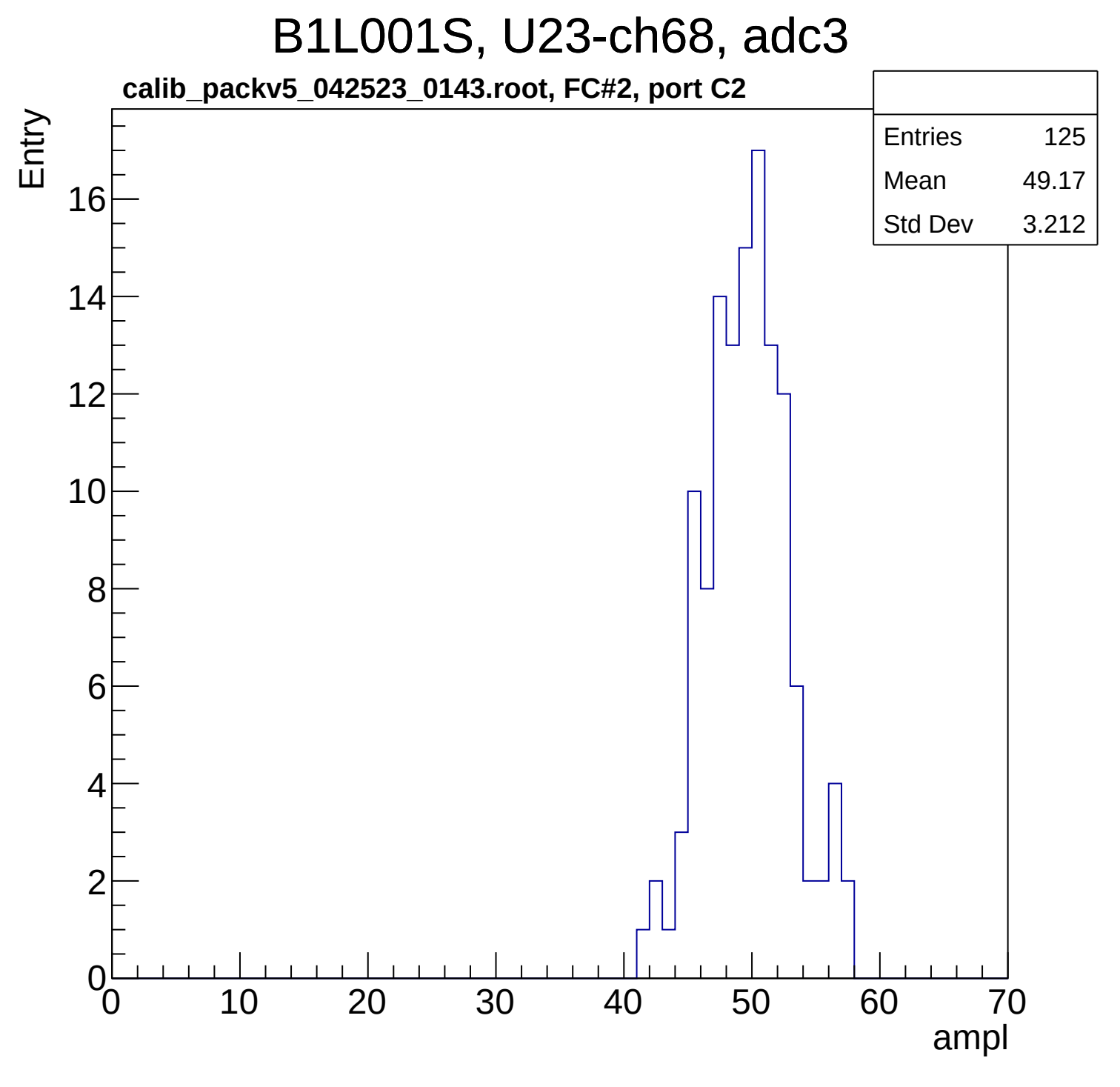
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch68, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 49.17
Std Dev 3.212
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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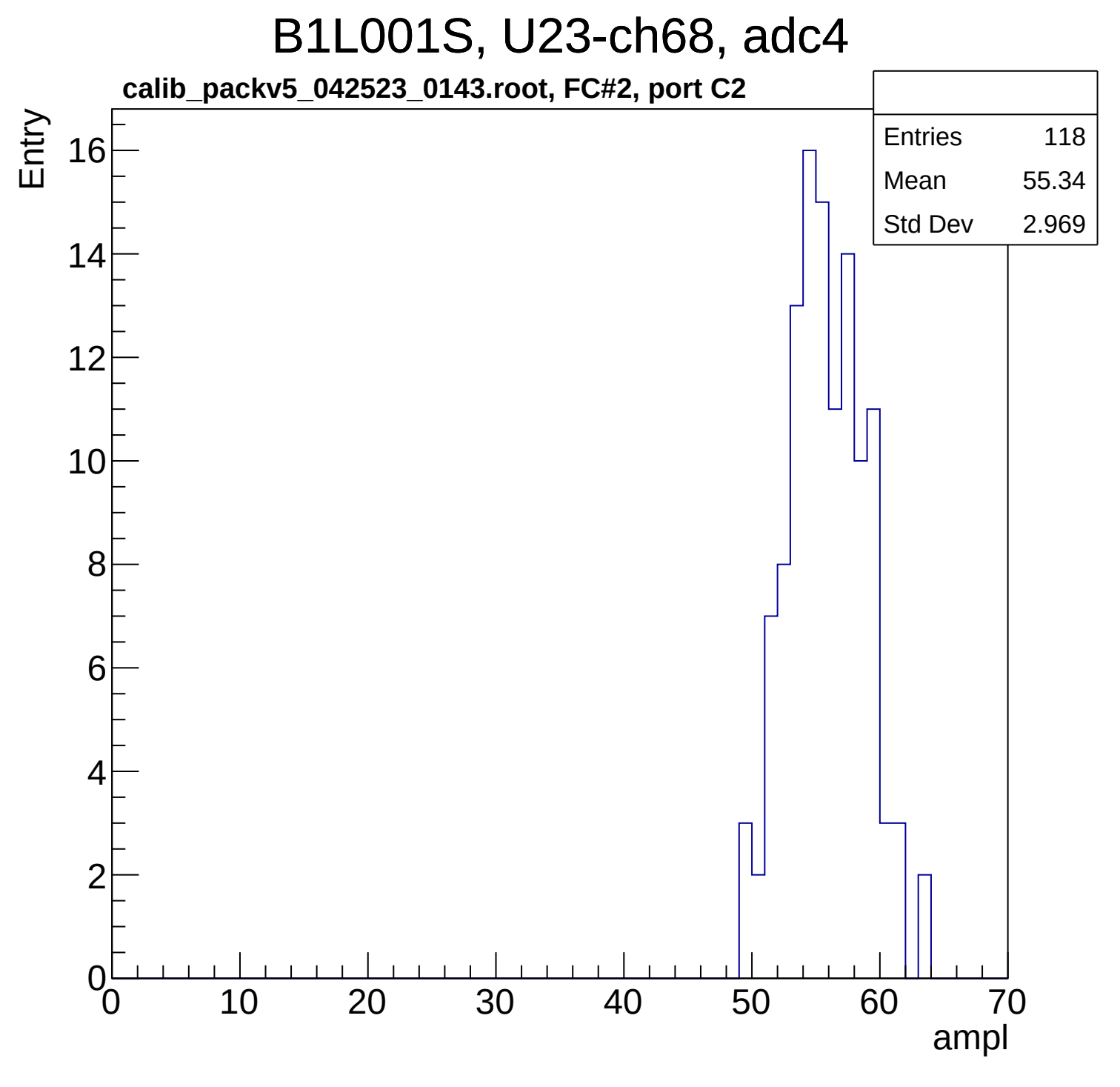
40
50
60
70


B1L001S, U23-ch68, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 55.34
Std Dev 2.969
Entry
ampl
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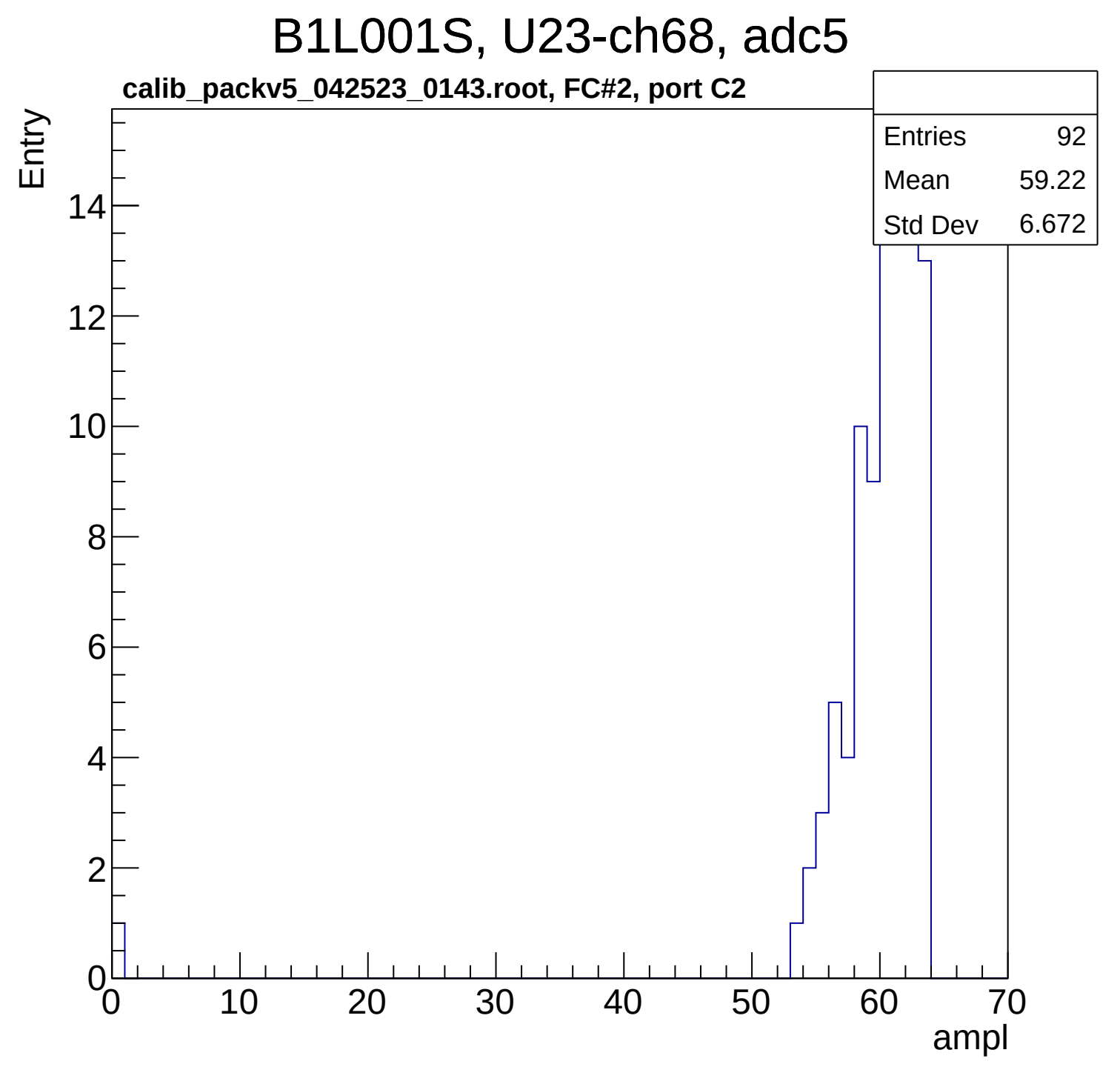
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch68, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 92
Mean 59.22
Std Dev 6.672
Entry
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2
4
6
8
10
12
14
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30
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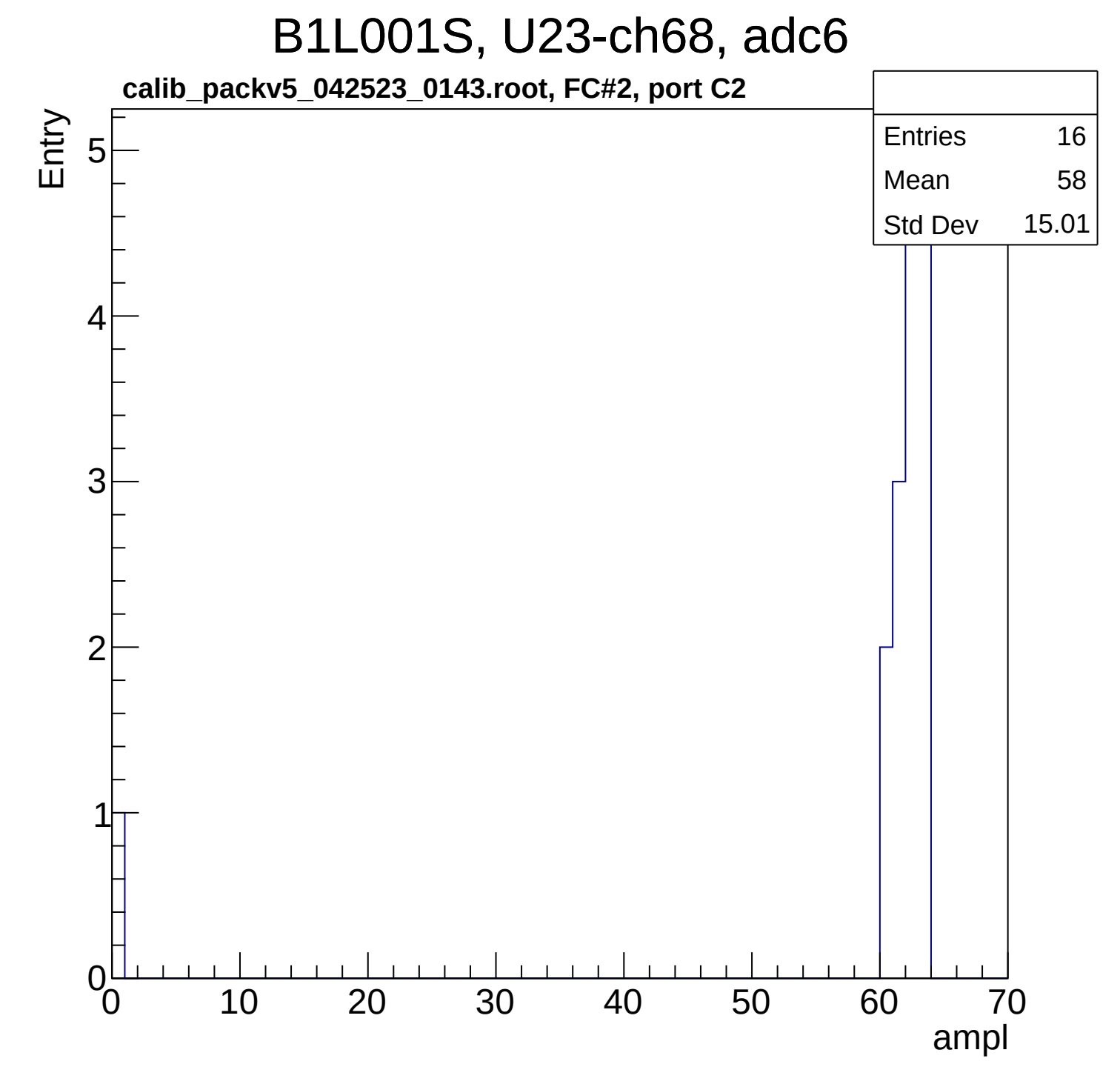
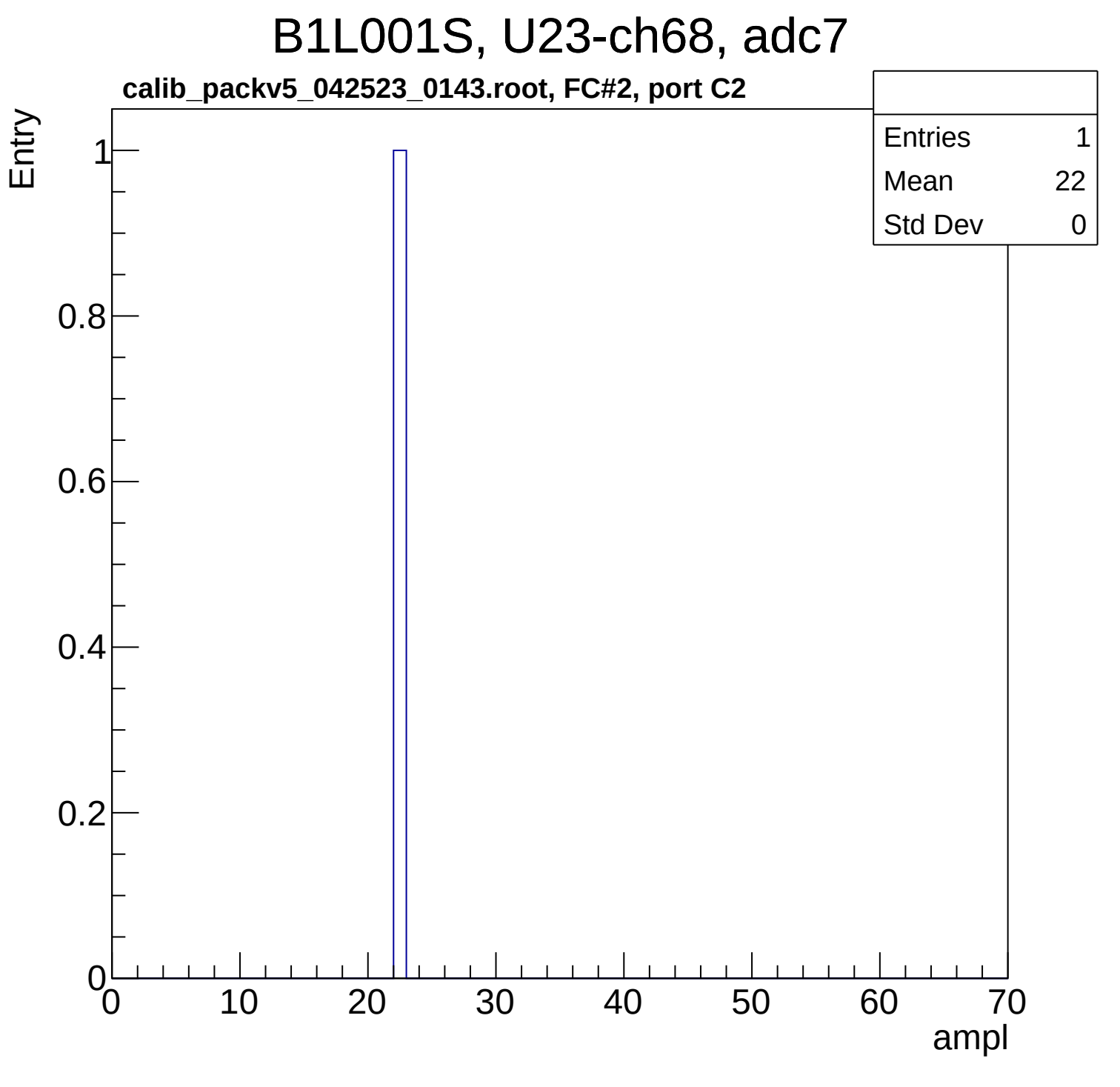
50
60
70
ampl


B1L001S, U23-ch68, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 58
Std Dev 15.01
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch68, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
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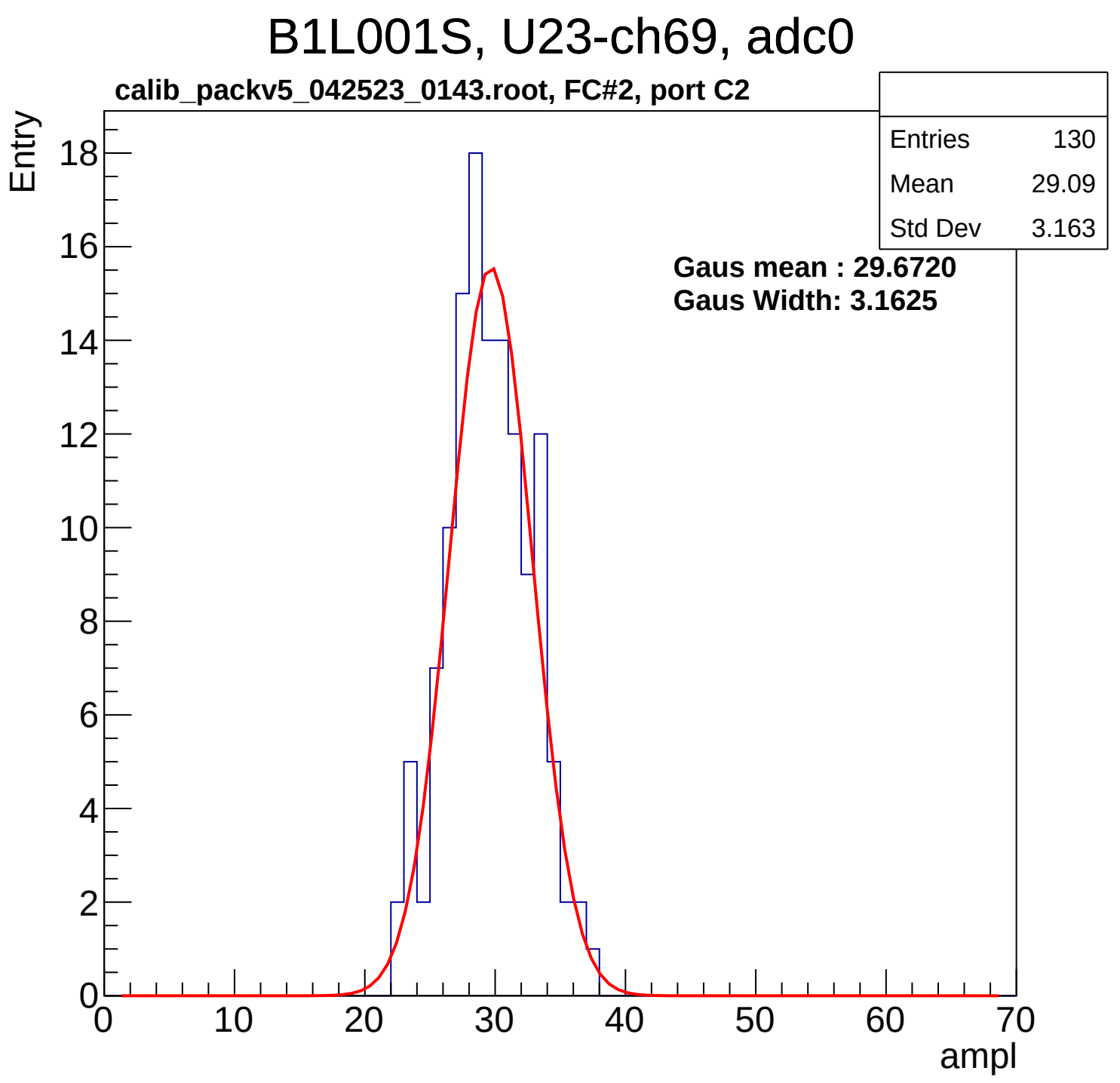
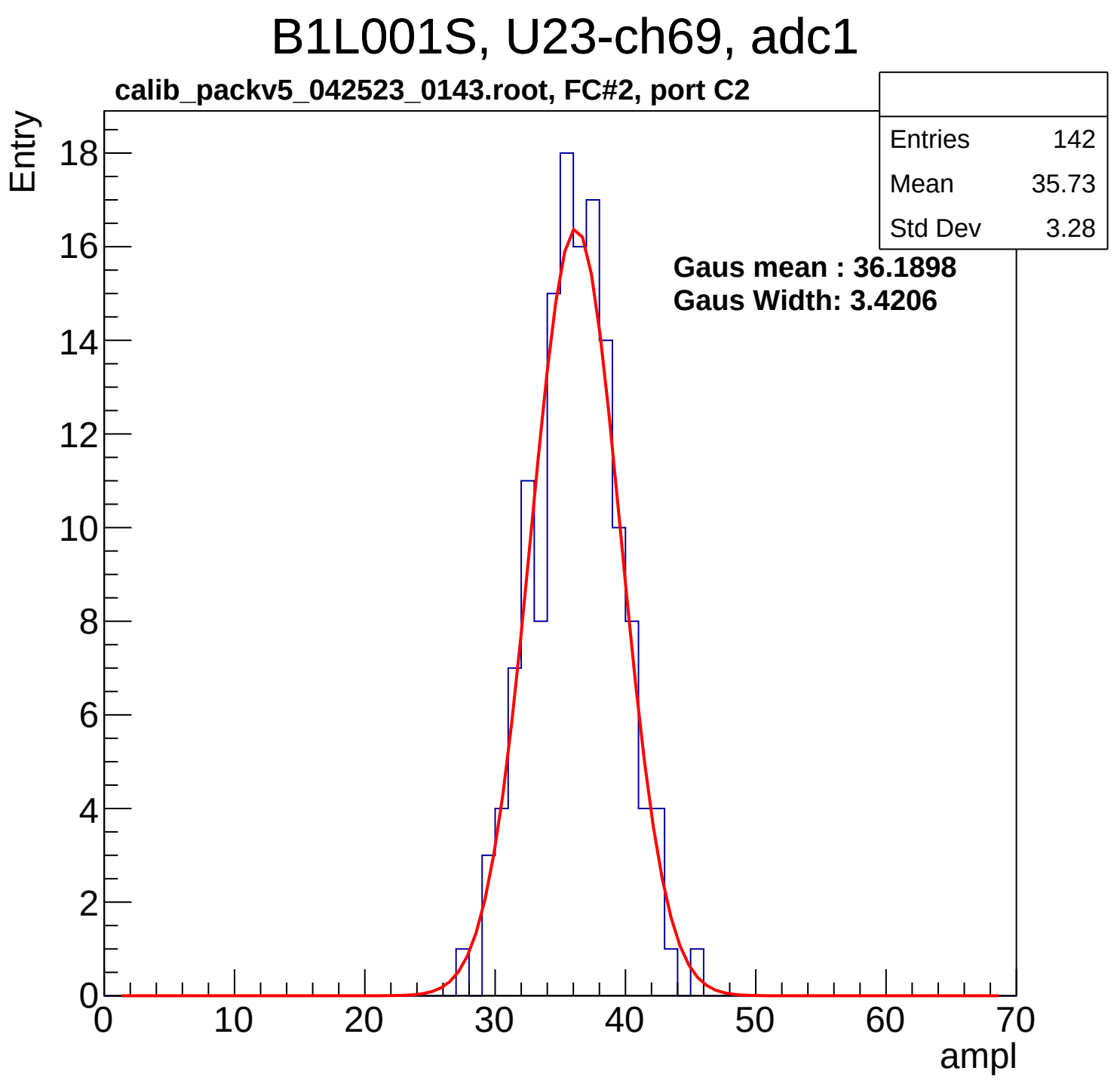
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch69, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 29.09
Std Dev 3.163
Gaus mean : 29.6720
Gaus Width: 3.1625
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch69, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 35.73
Std Dev 3.28
Gaus mean : 36.1898
Gaus Width: 3.4206
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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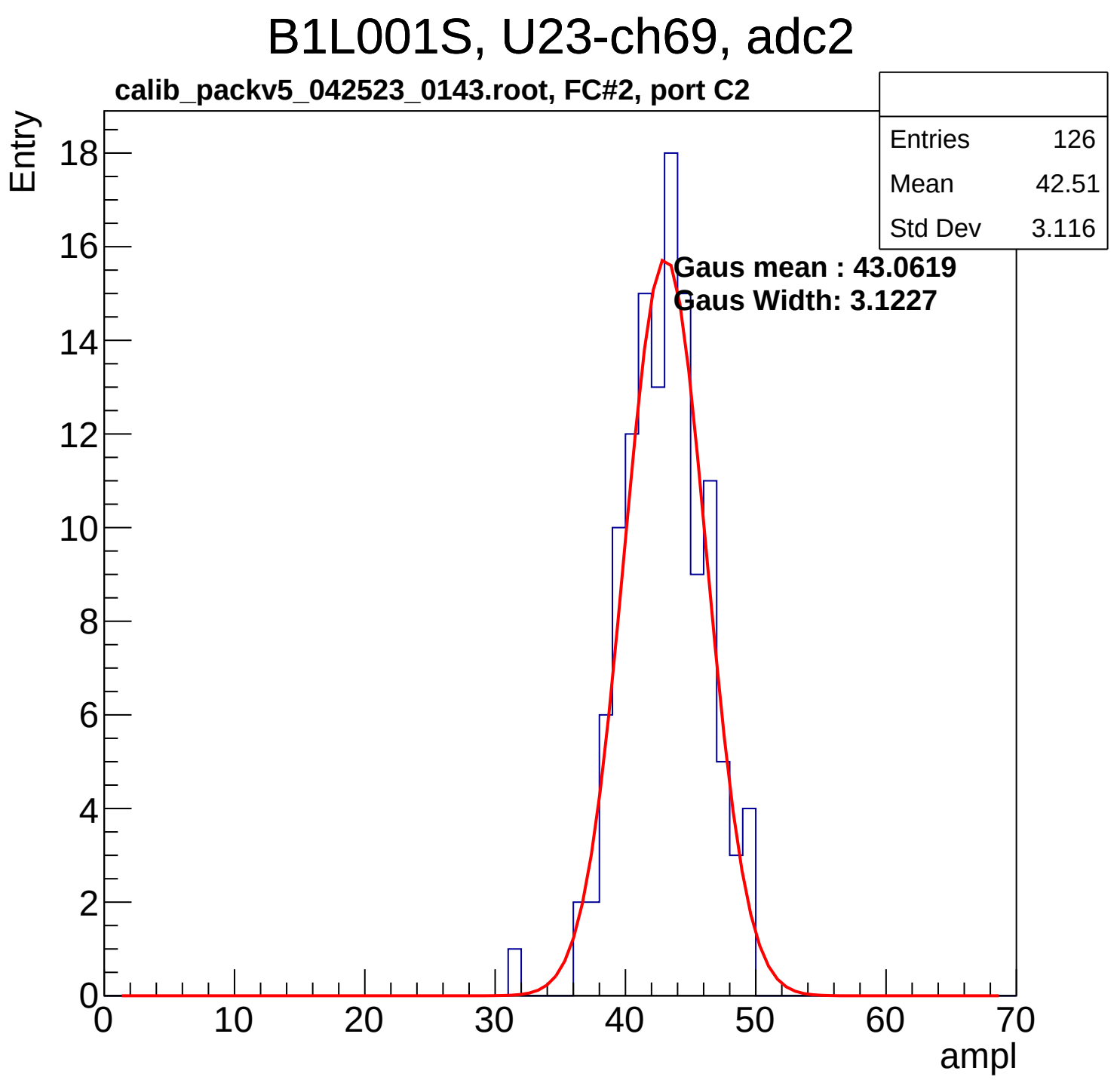
B1L001S, U23-ch69, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 42.51
Std Dev 3.116
Gaus mean : 43.0619
Gaus Width: 3.1227
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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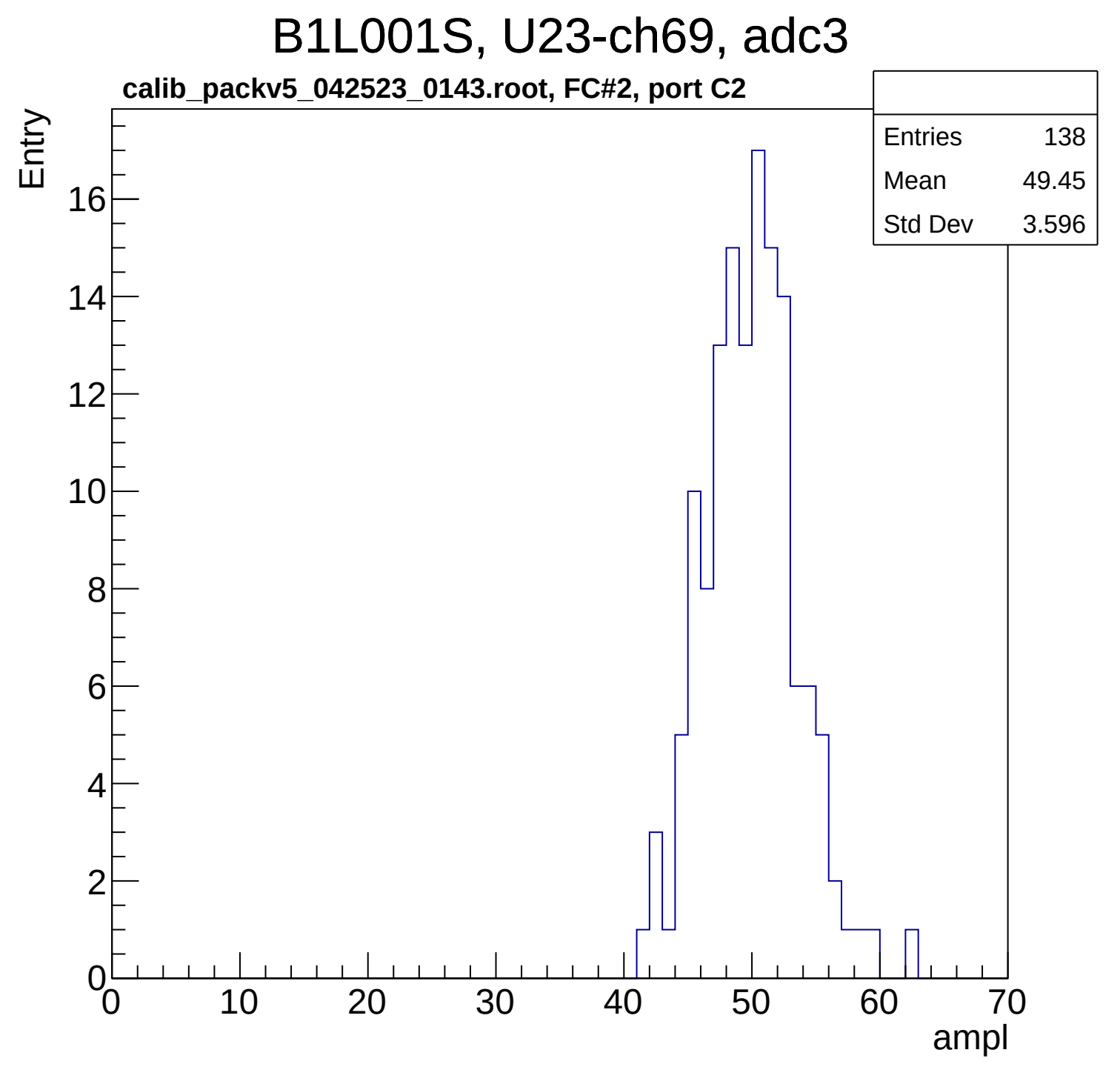
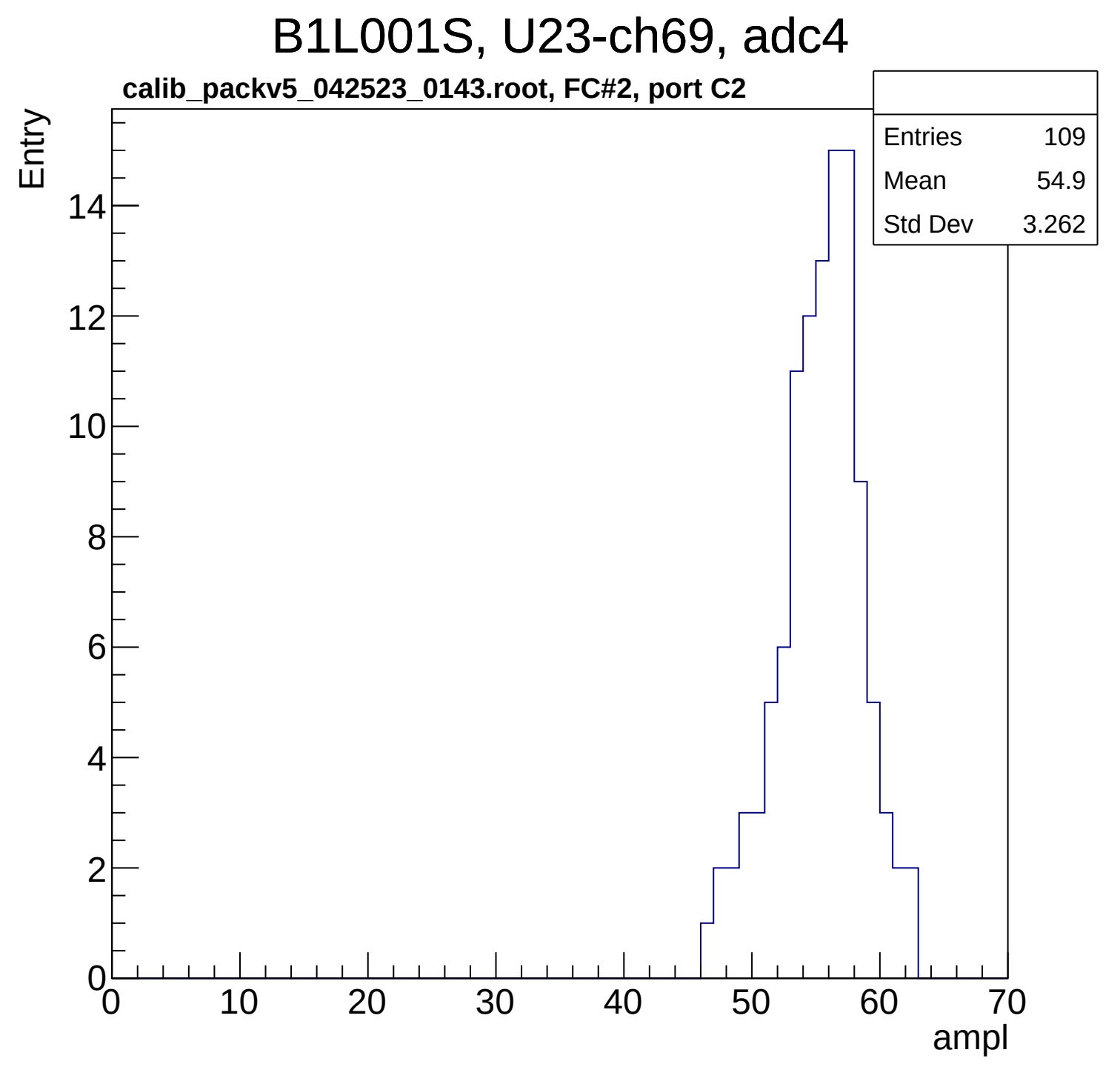
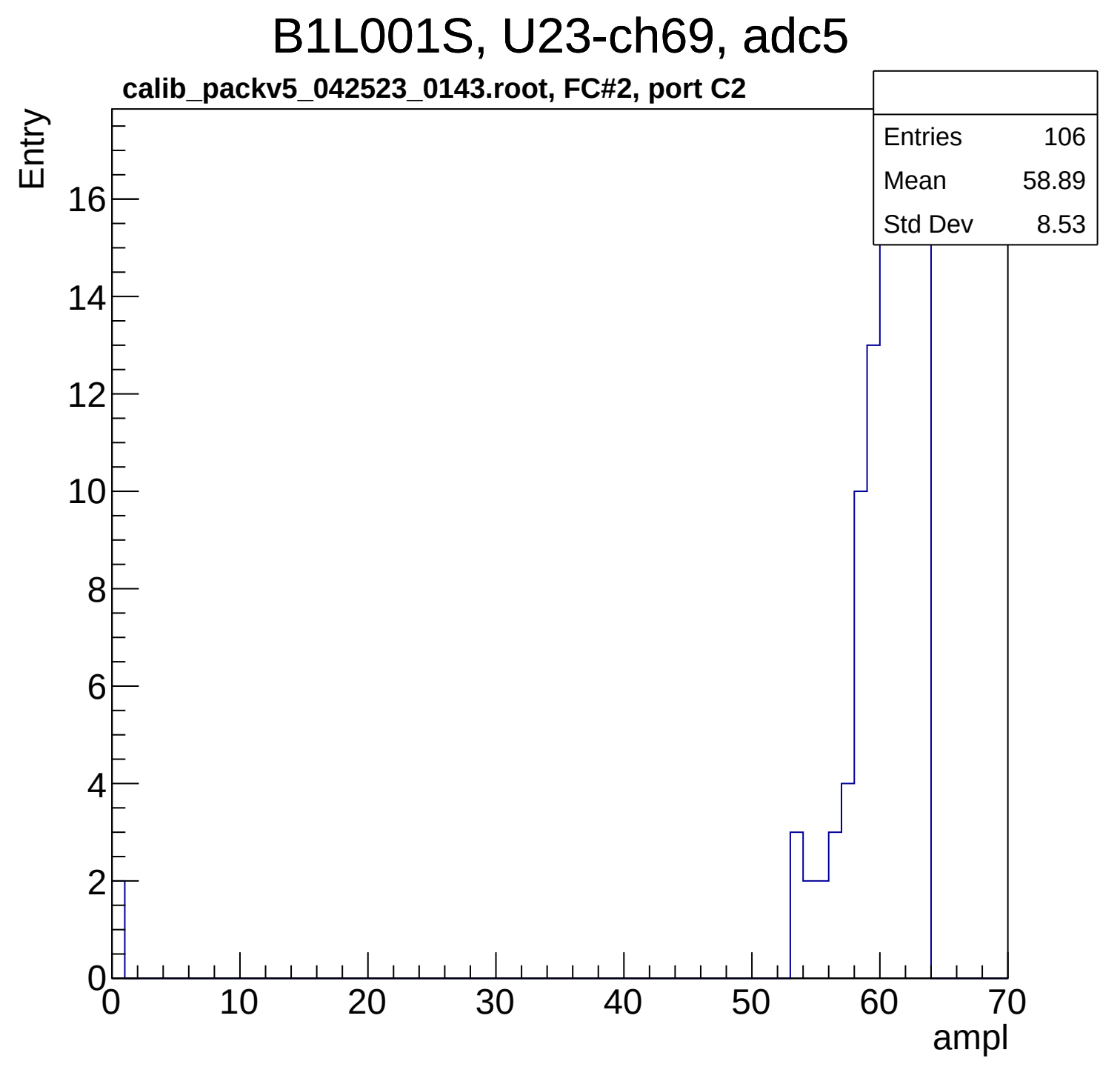
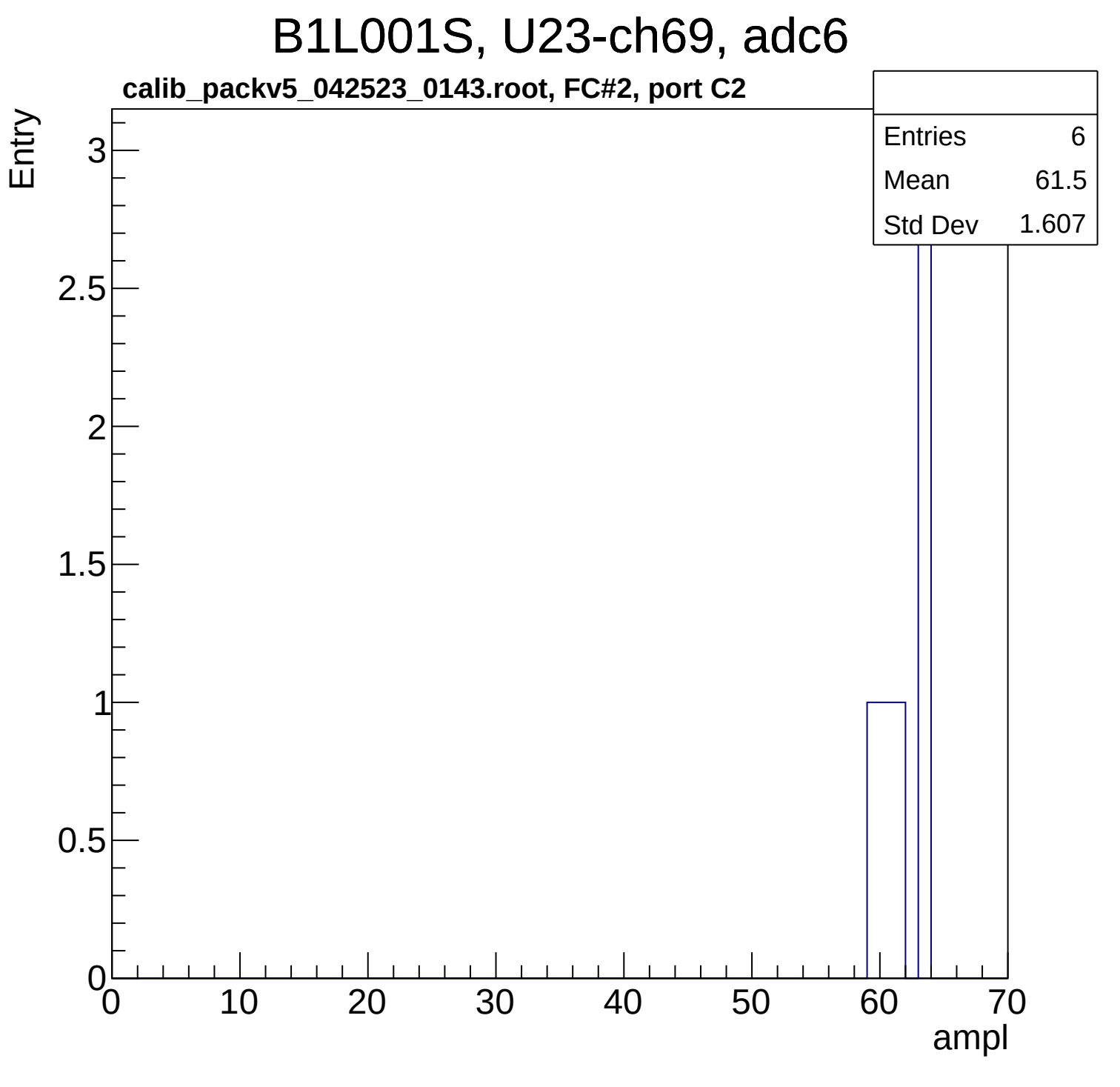
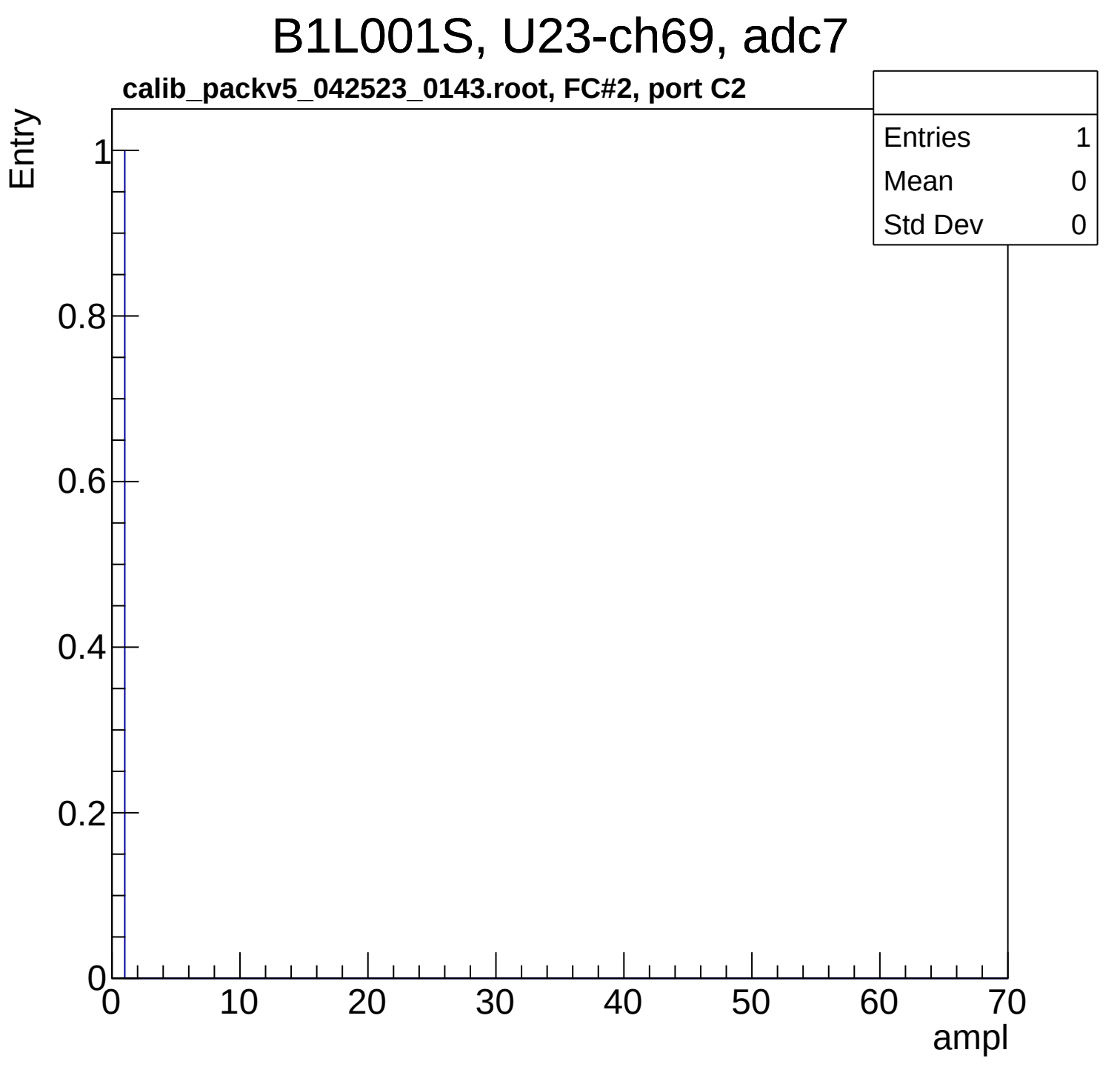
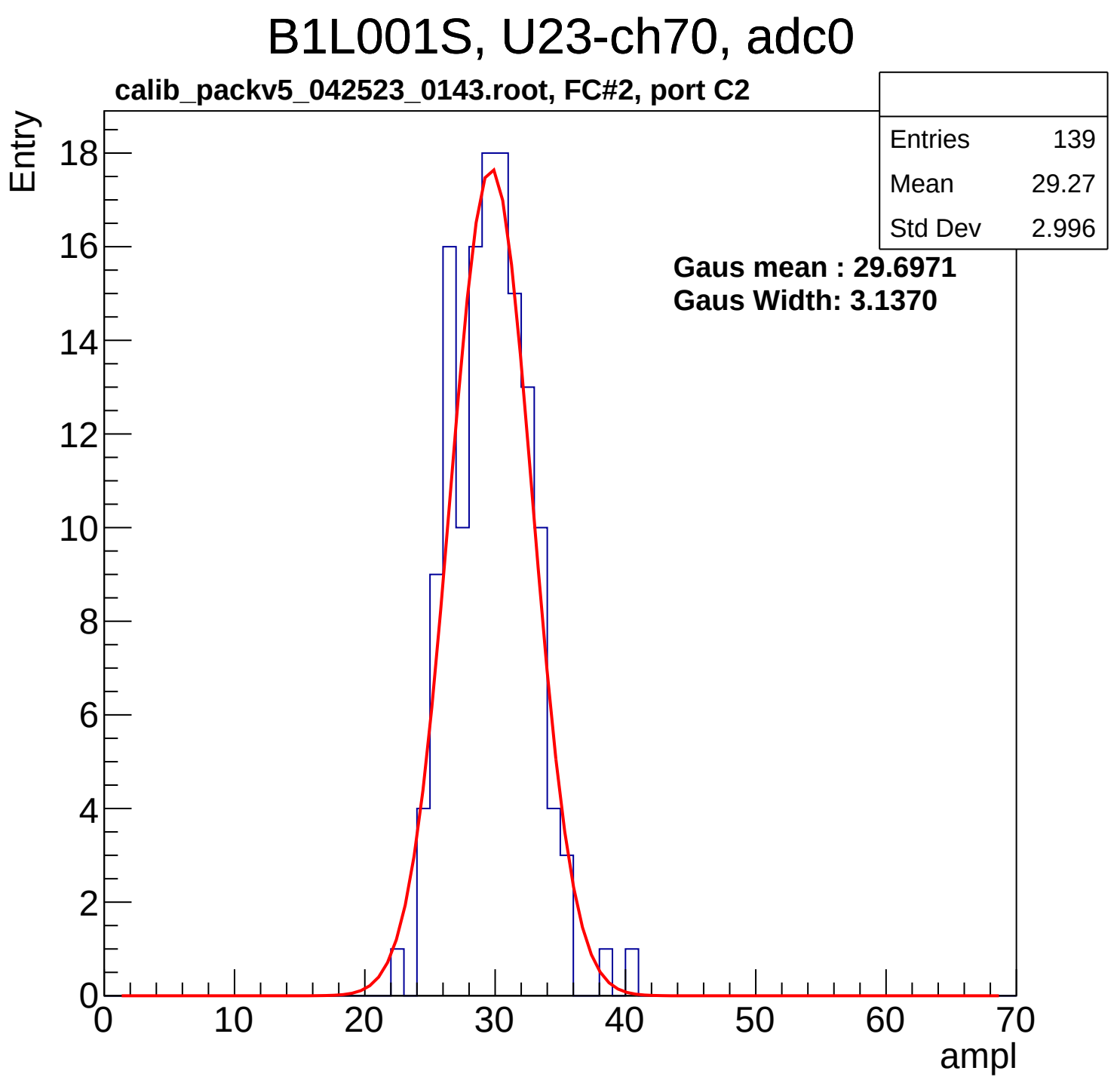

B1L001S, U23-ch69, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 49.45
Std Dev 3.596
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

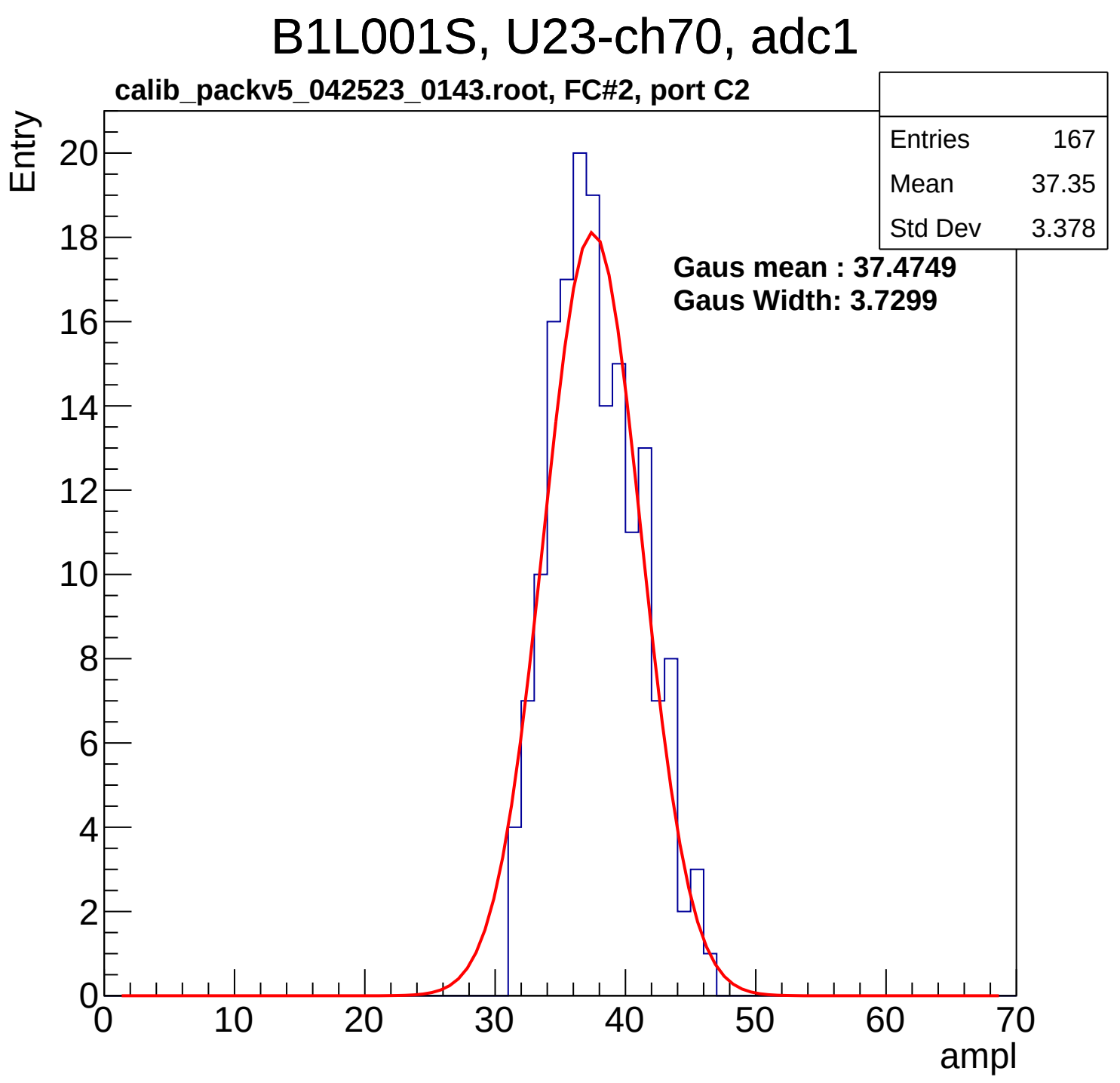

B1L001S, U23-ch69, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 54.9
Std Dev 3.262
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl

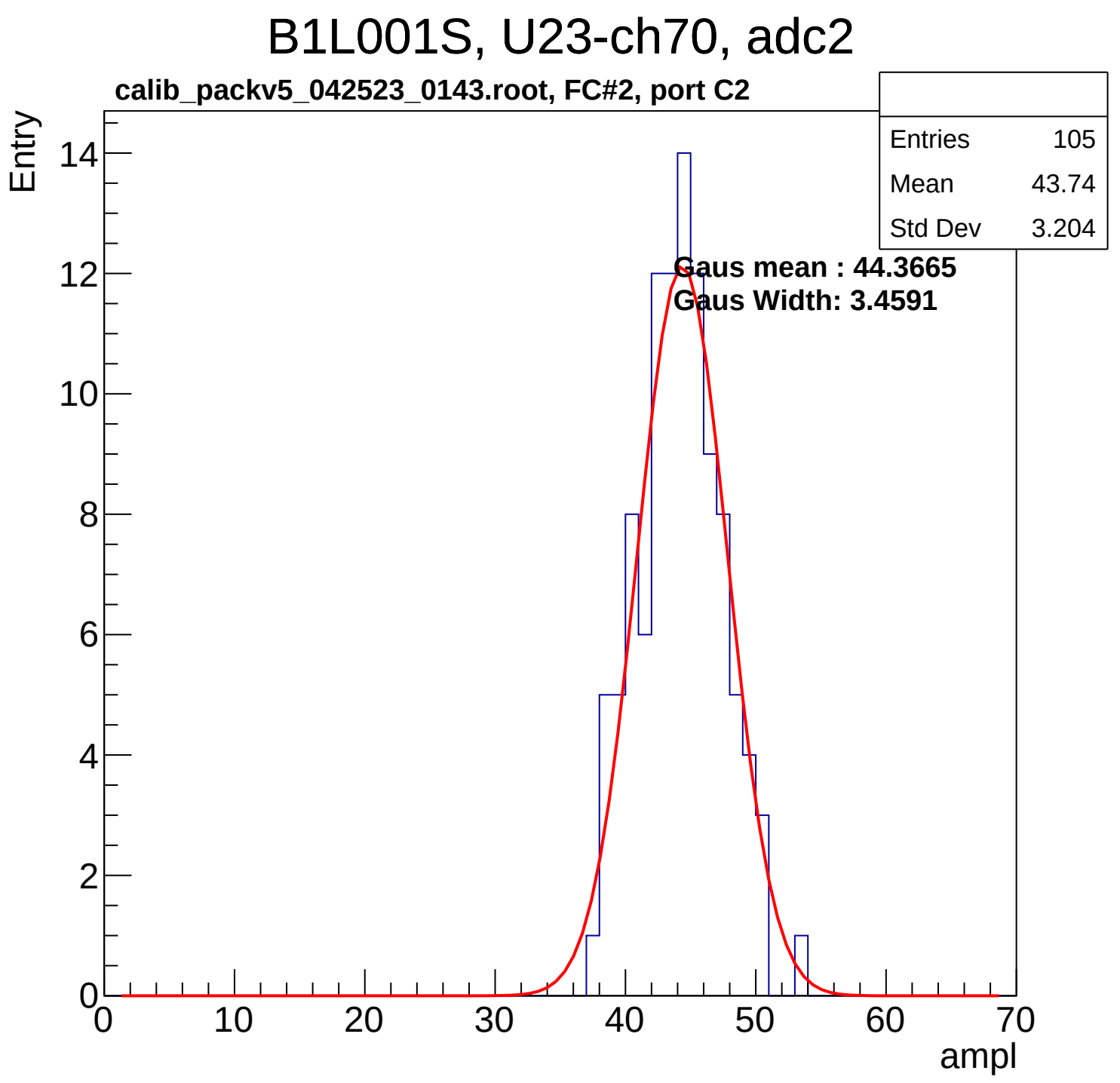

B1L001S, U23-ch69, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 58.89
Std Dev 8.53
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl

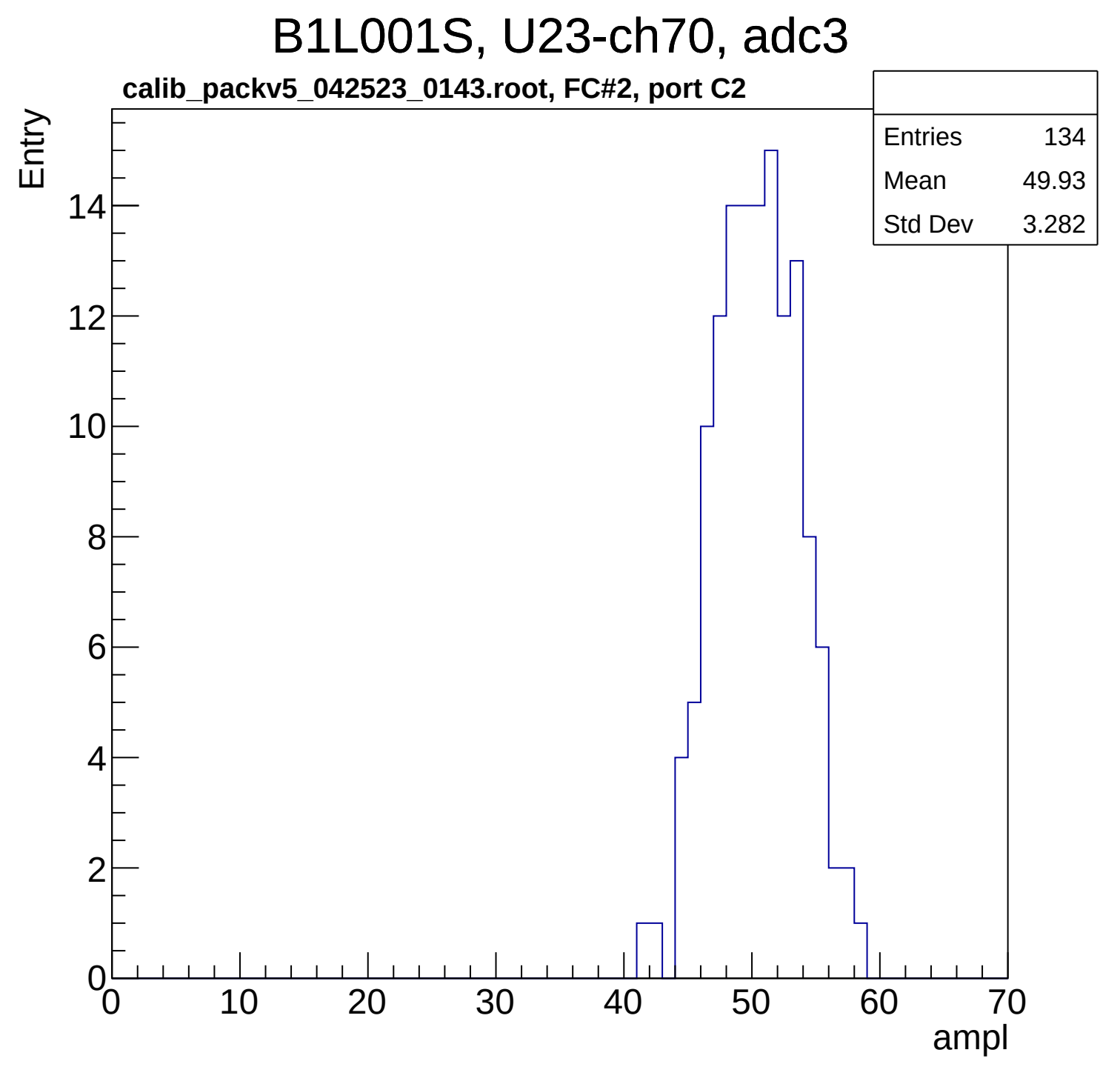

B1L001S, U23-ch69, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 61.5
Std Dev 1.607
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

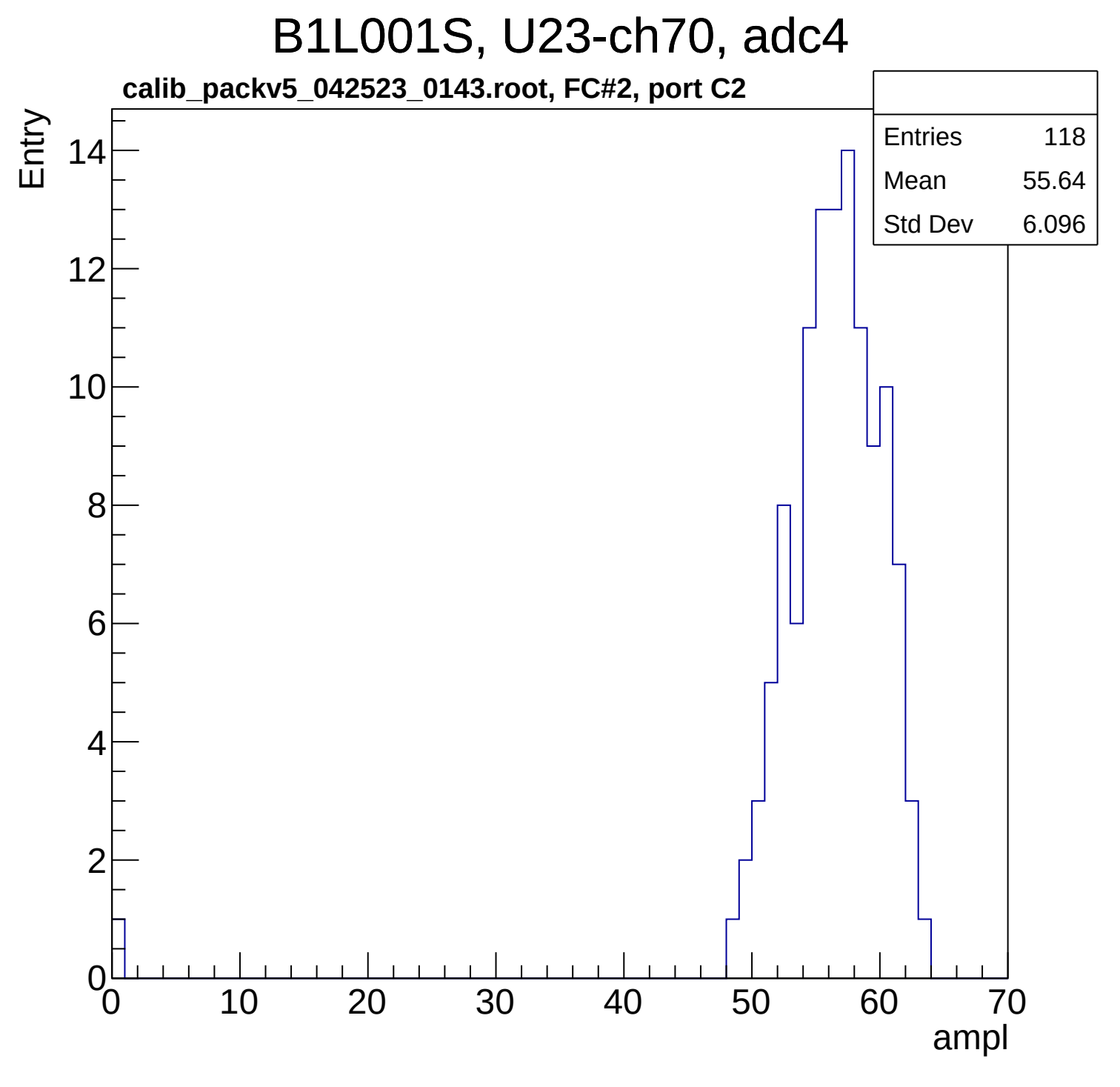

B1L001S, U23-ch69, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch70, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 29.27
Std Dev 2.996
Gaus mean : 29.6971
Gaus Width: 3.1370
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch70, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 167
Mean 37.35
Std Dev 3.378
Gaus mean : 37.4749
Gaus Width: 3.7299
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch70, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 43.74
Std Dev 3.204
Gaus mean : 44.3665
Gaus Width: 3.4591
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch70, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 49.93
Std Dev 3.282
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch70, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 55.64
Std Dev 6.096
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
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60
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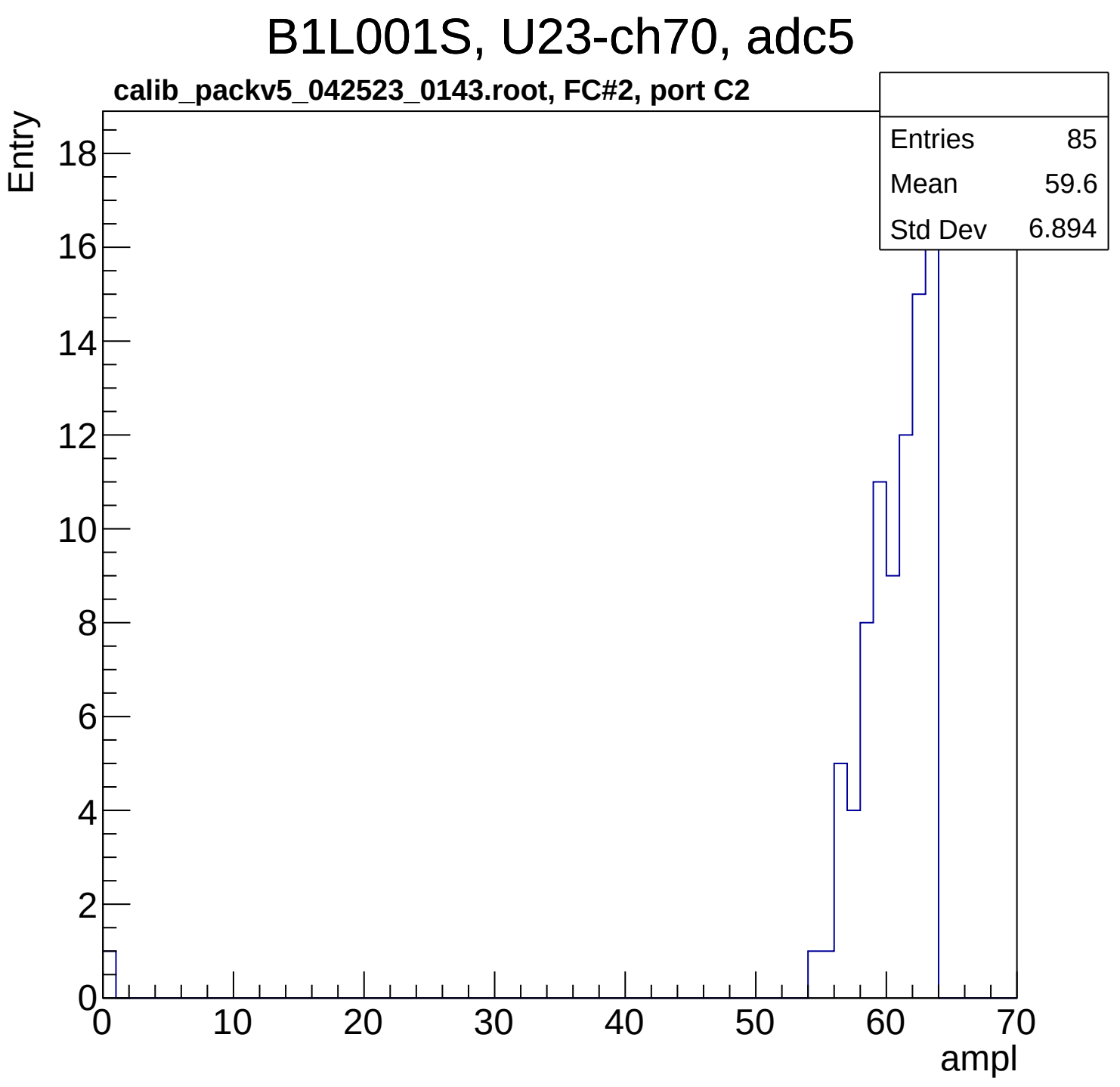
B1L001S, U23-ch70, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 59.6
Std Dev 6.894
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
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40
50
60
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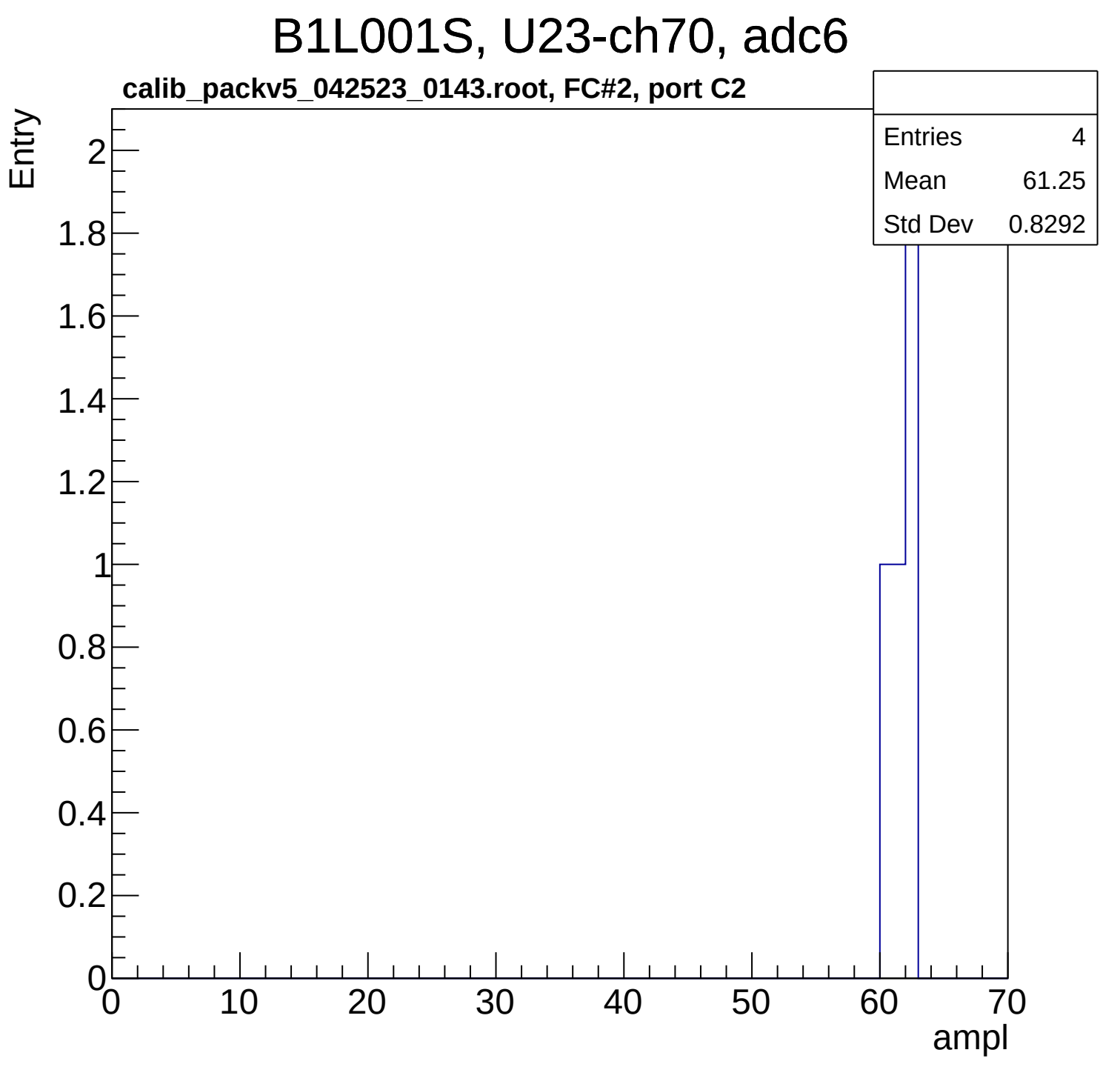
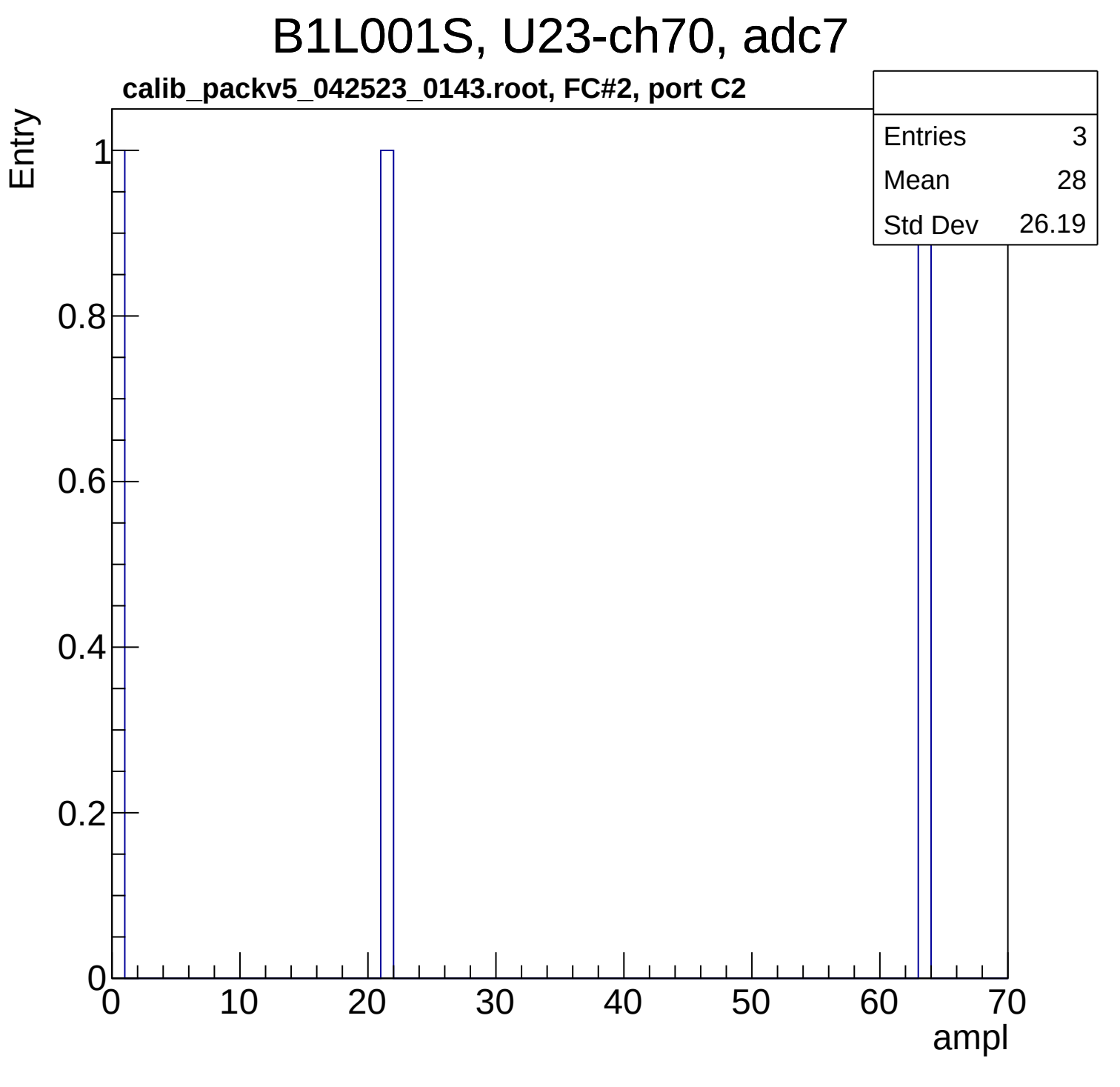
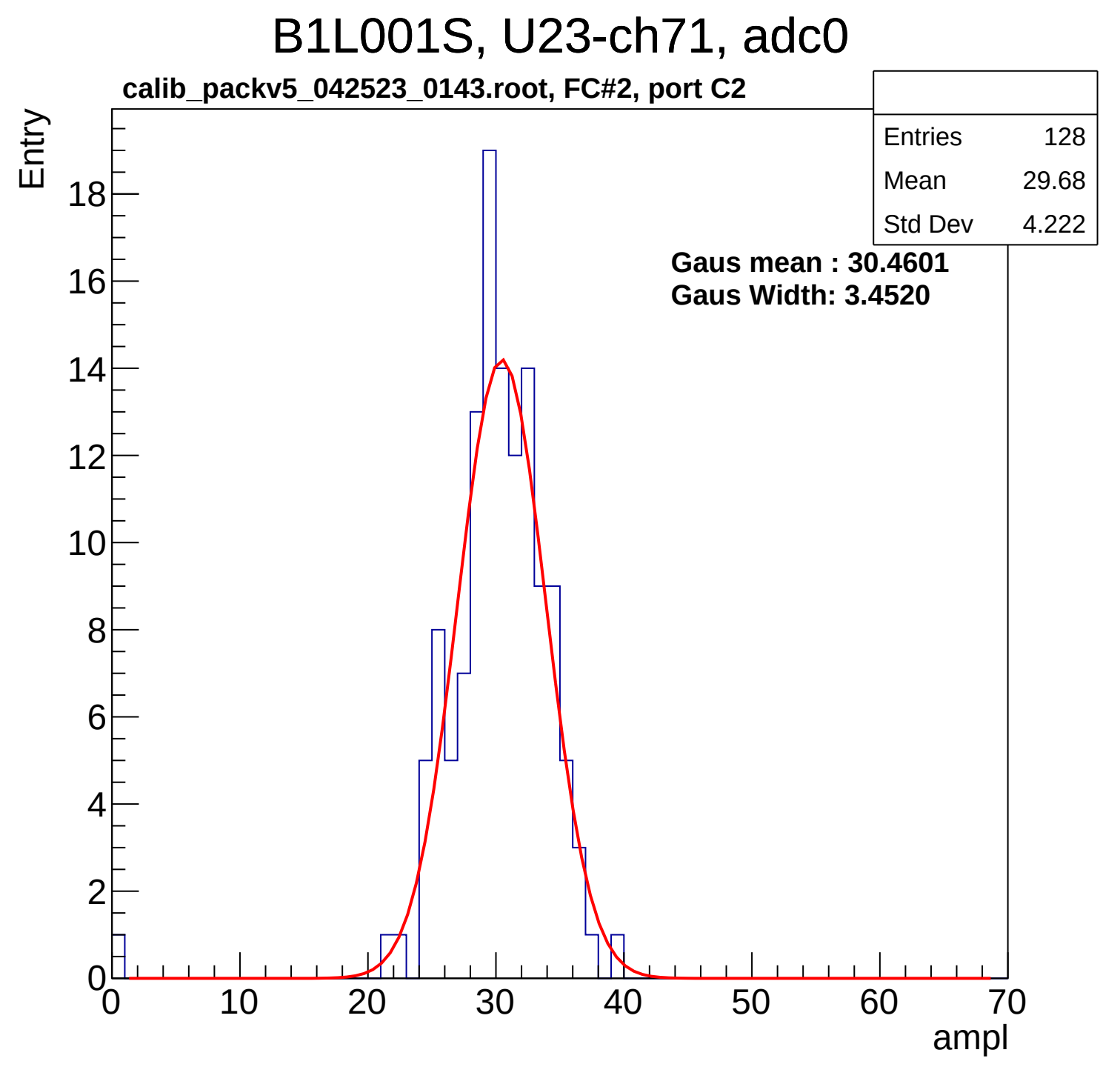
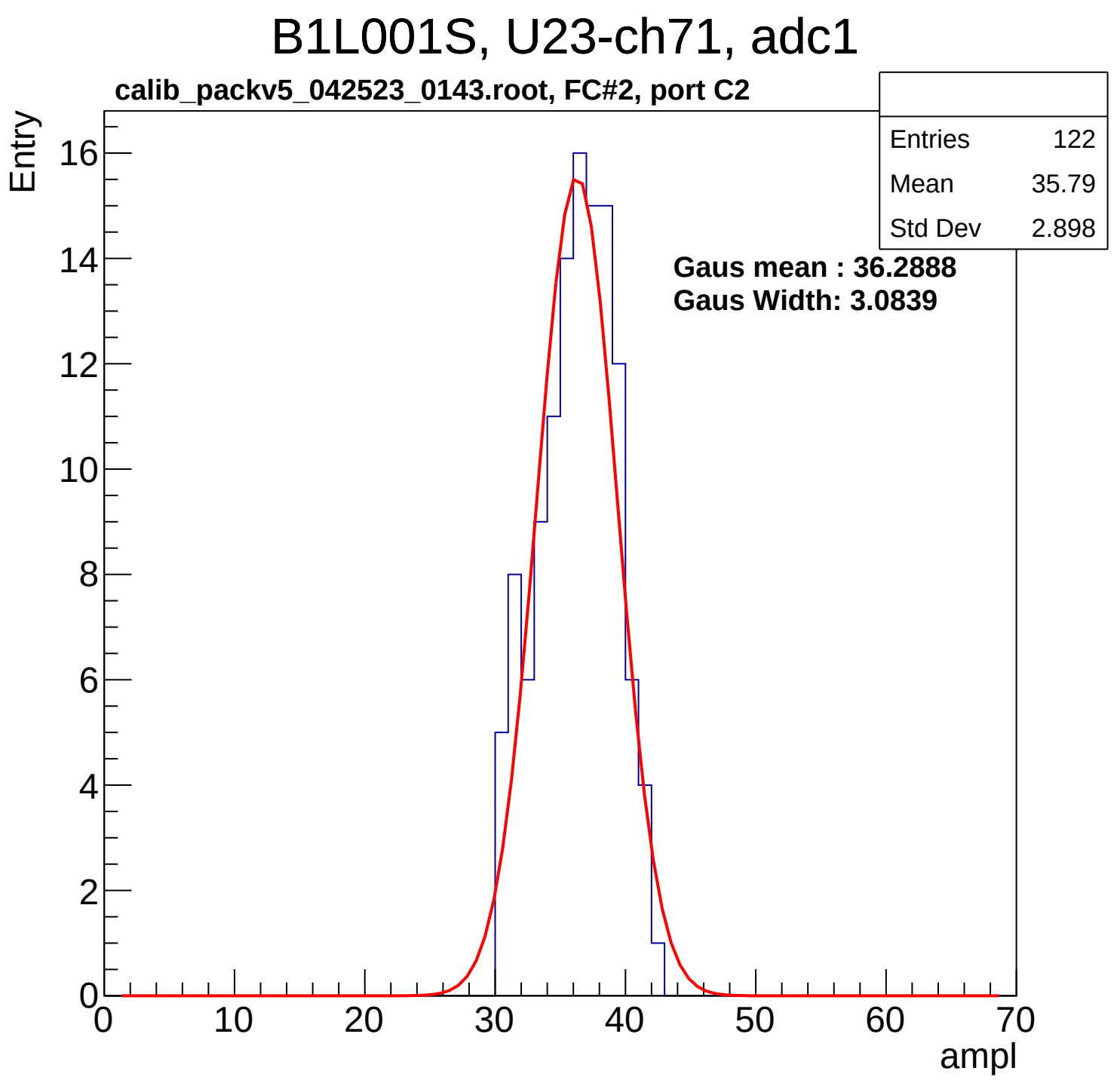
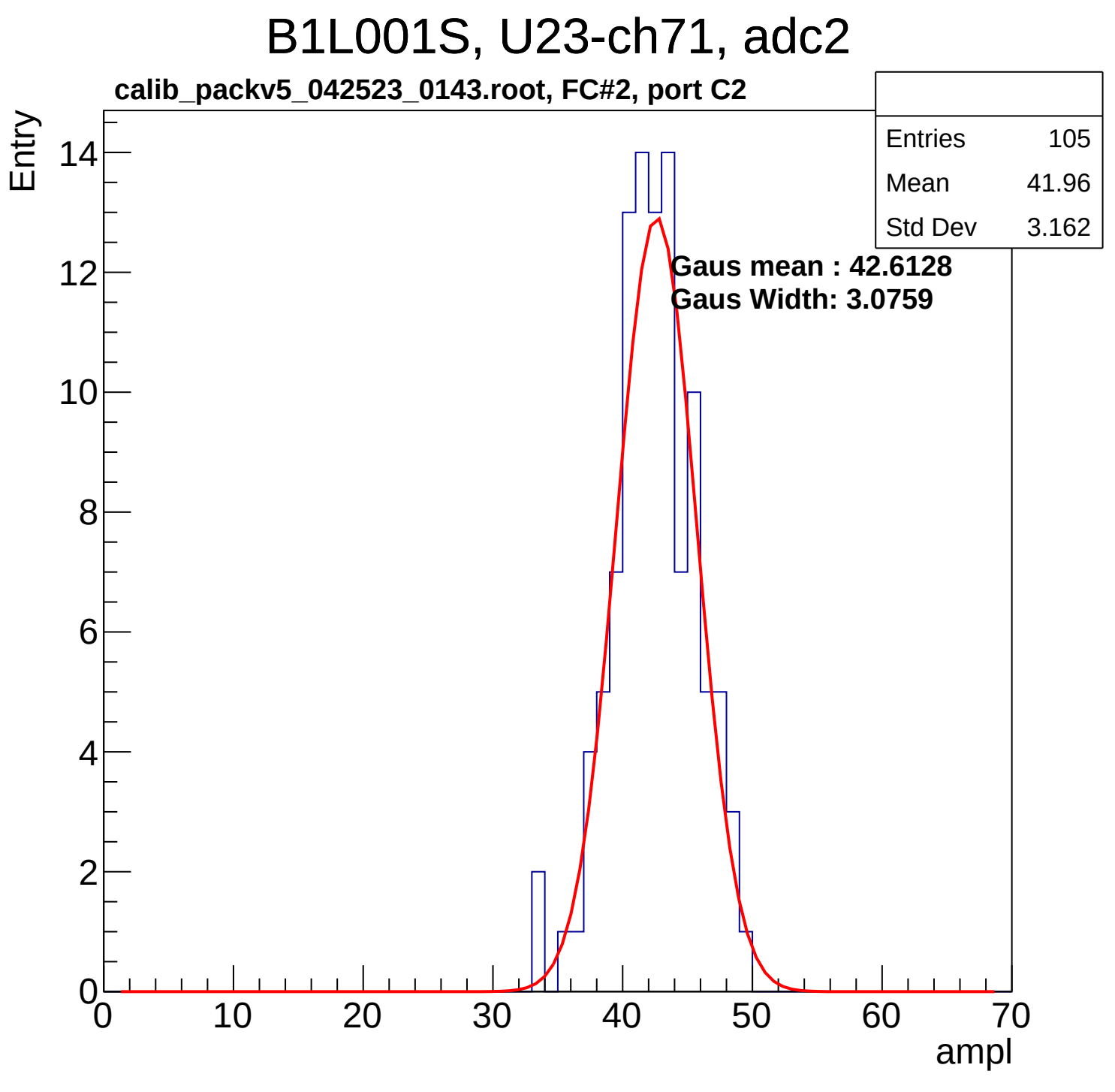

B1L001S, U23-ch70, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch70, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch71, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 29.68
Std Dev 4.222
Gaus mean : 30.4601
Gaus Width: 3.4520
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch71, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 35.79
Std Dev 2.898
Gaus mean : 36.2888
Gaus Width: 3.0839
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch71, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 41.96
Std Dev 3.162
Gaus mean : 42.6128
Gaus Width: 3.0759
Entry
ampl
0
2
4
6
8
10
12
14
0
10
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40
50
60
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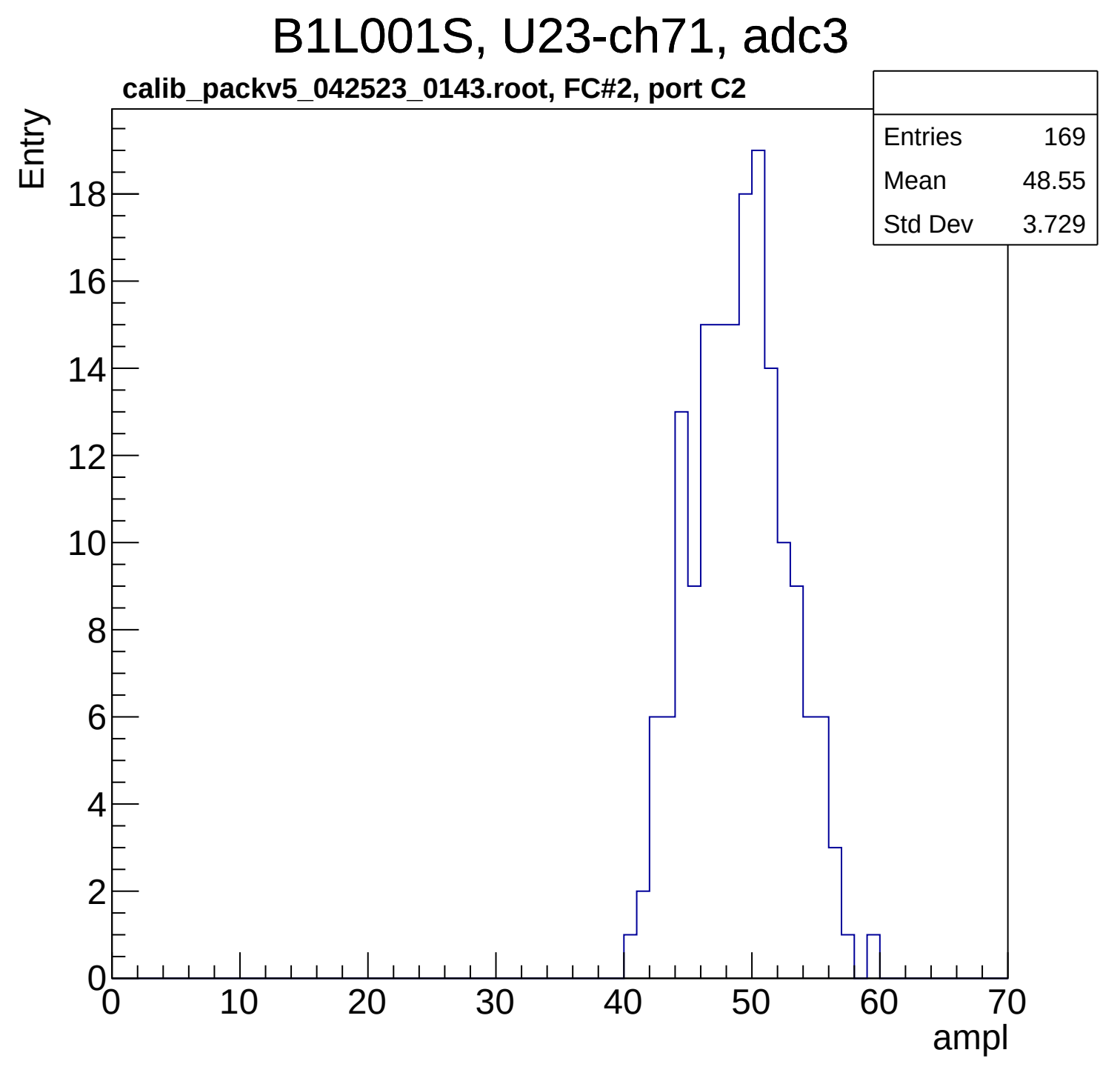
B1L001S, U23-ch71, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 169
Mean 48.55
Std Dev 3.729
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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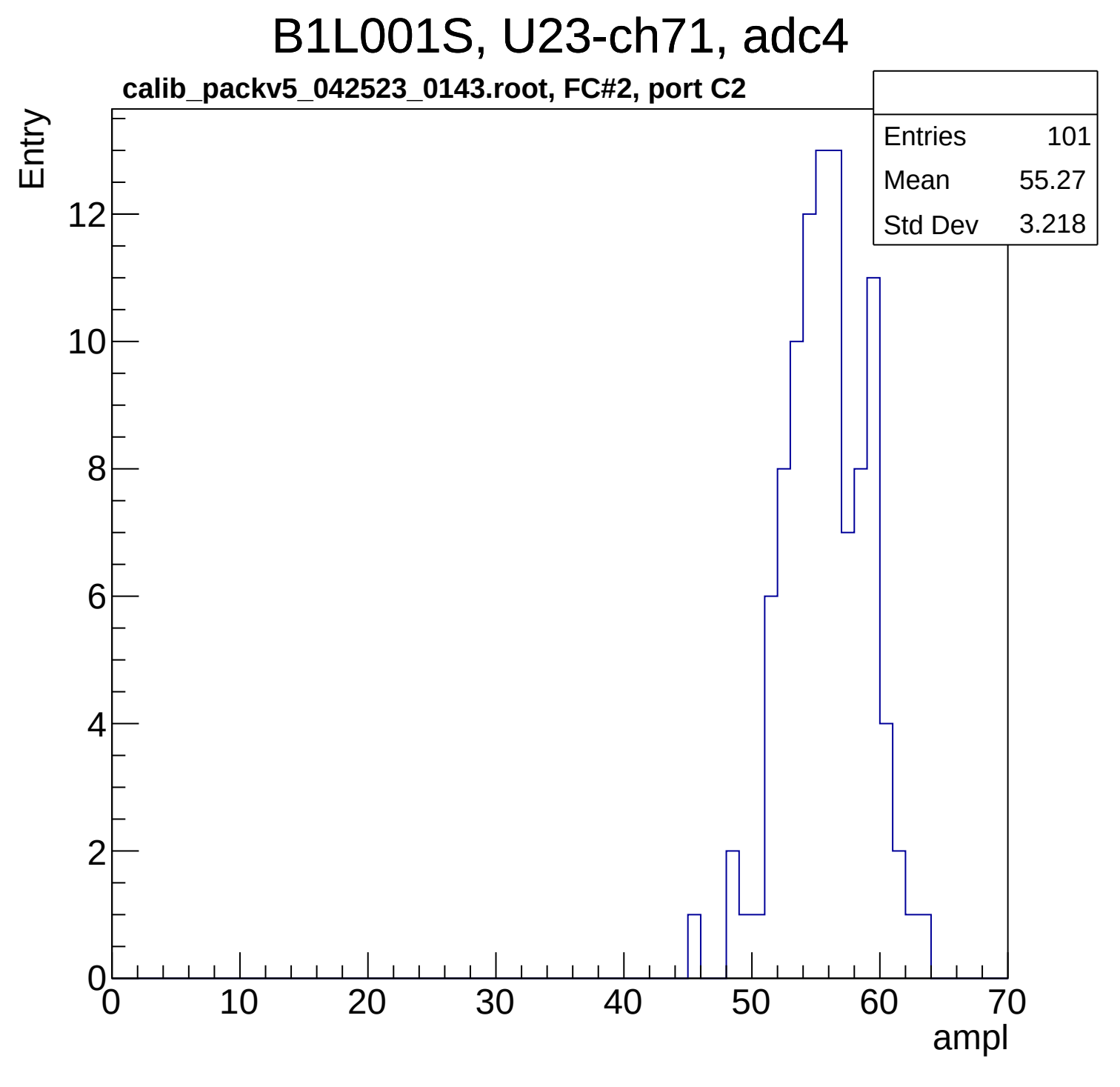
20
30
40
50
60
70


B1L001S, U23-ch71, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 55.27
Std Dev 3.218
Entry
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2
4
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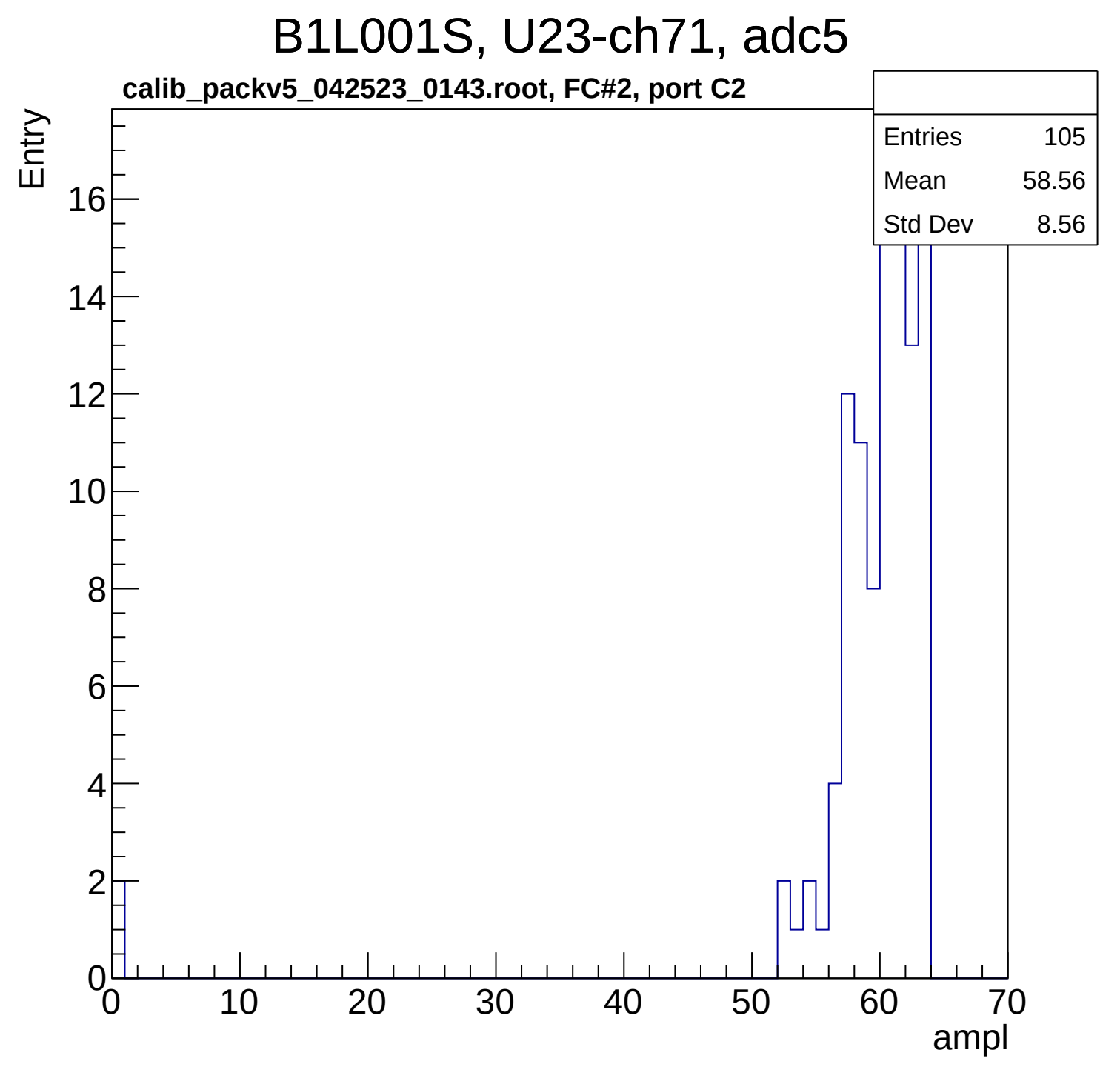
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch71, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 58.56
Std Dev 8.56
Entry
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4
6
8
10
12
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10
20
30
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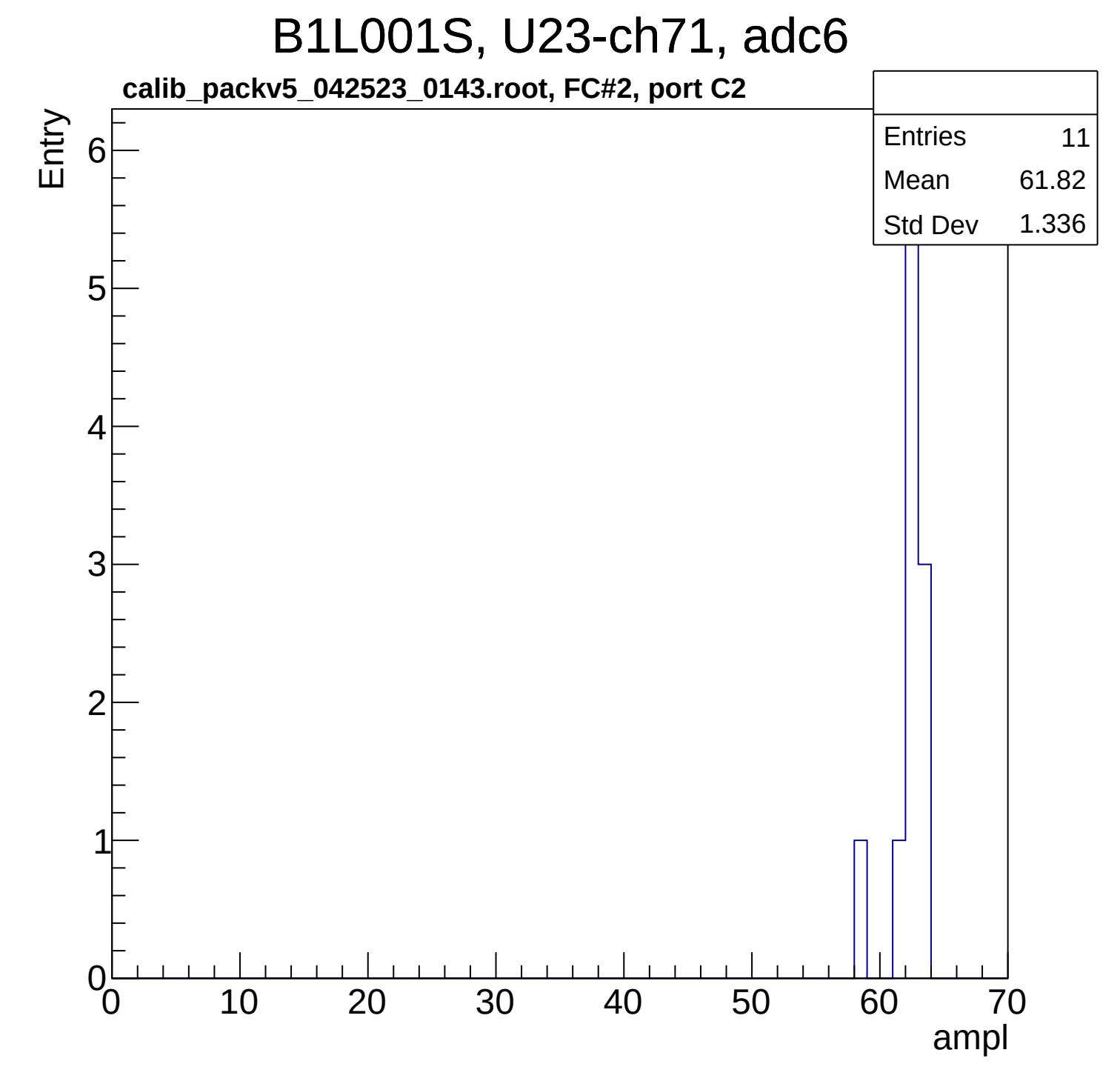
50
60
70
ampl


B1L001S, U23-ch71, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 61.82
Std Dev 1.336
Entry
ampl
0
1
2
3
4
5
6
0
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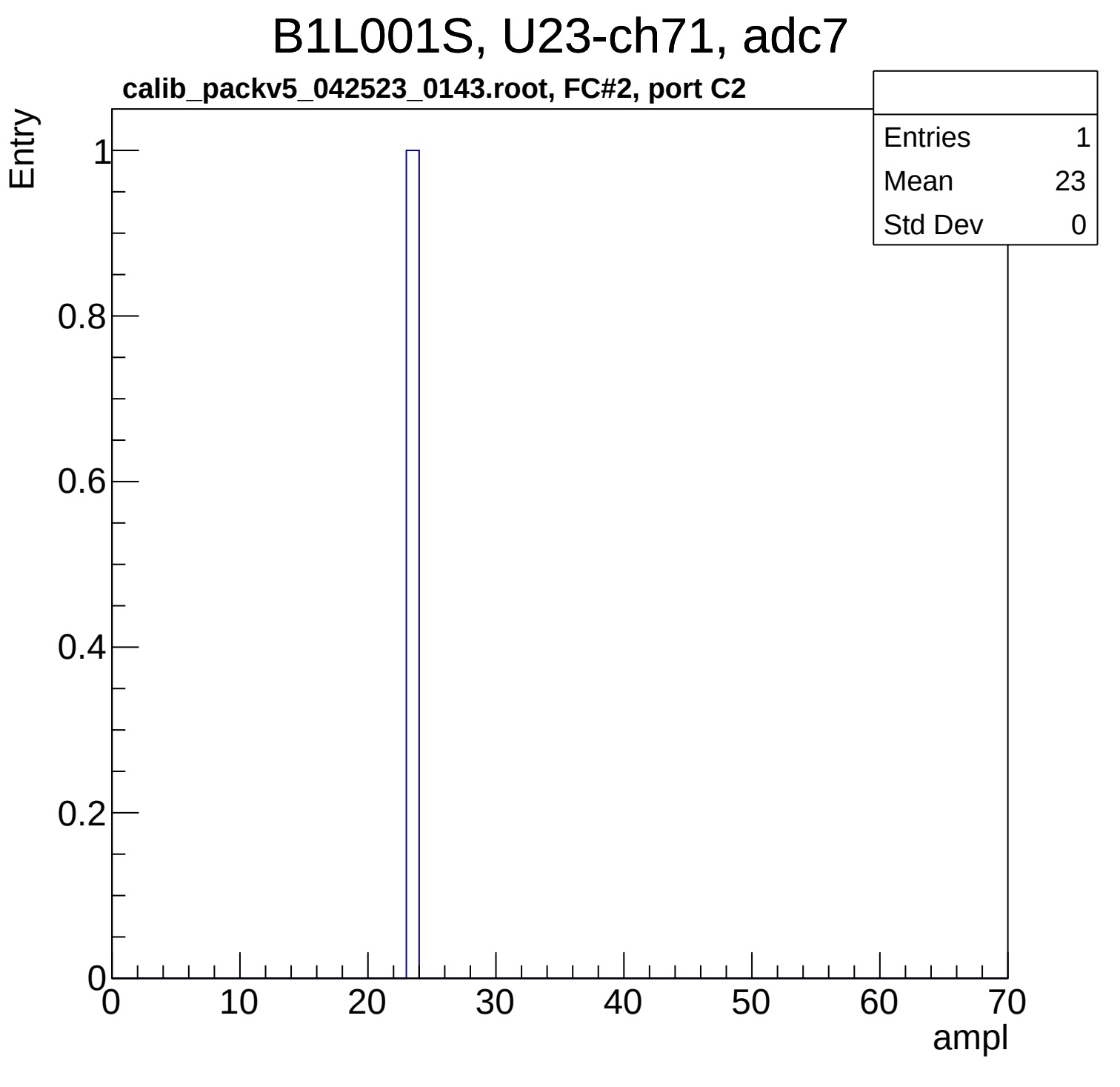
20
30
40
50
60
70


B1L001S, U23-ch71, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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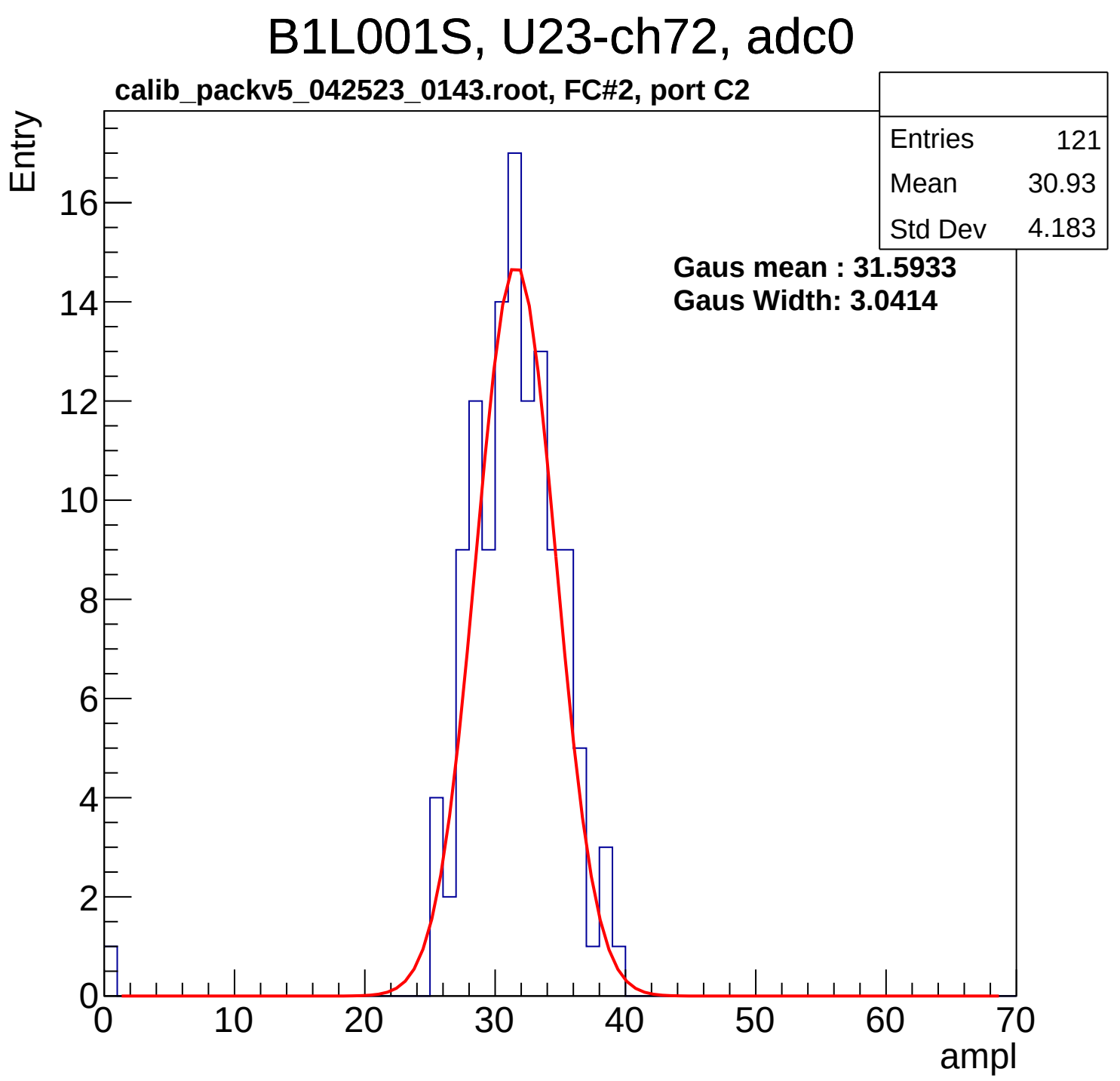
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch72, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 30.93
Std Dev 4.183
Gaus mean : 31.5933
Gaus Width: 3.0414
Entry
ampl
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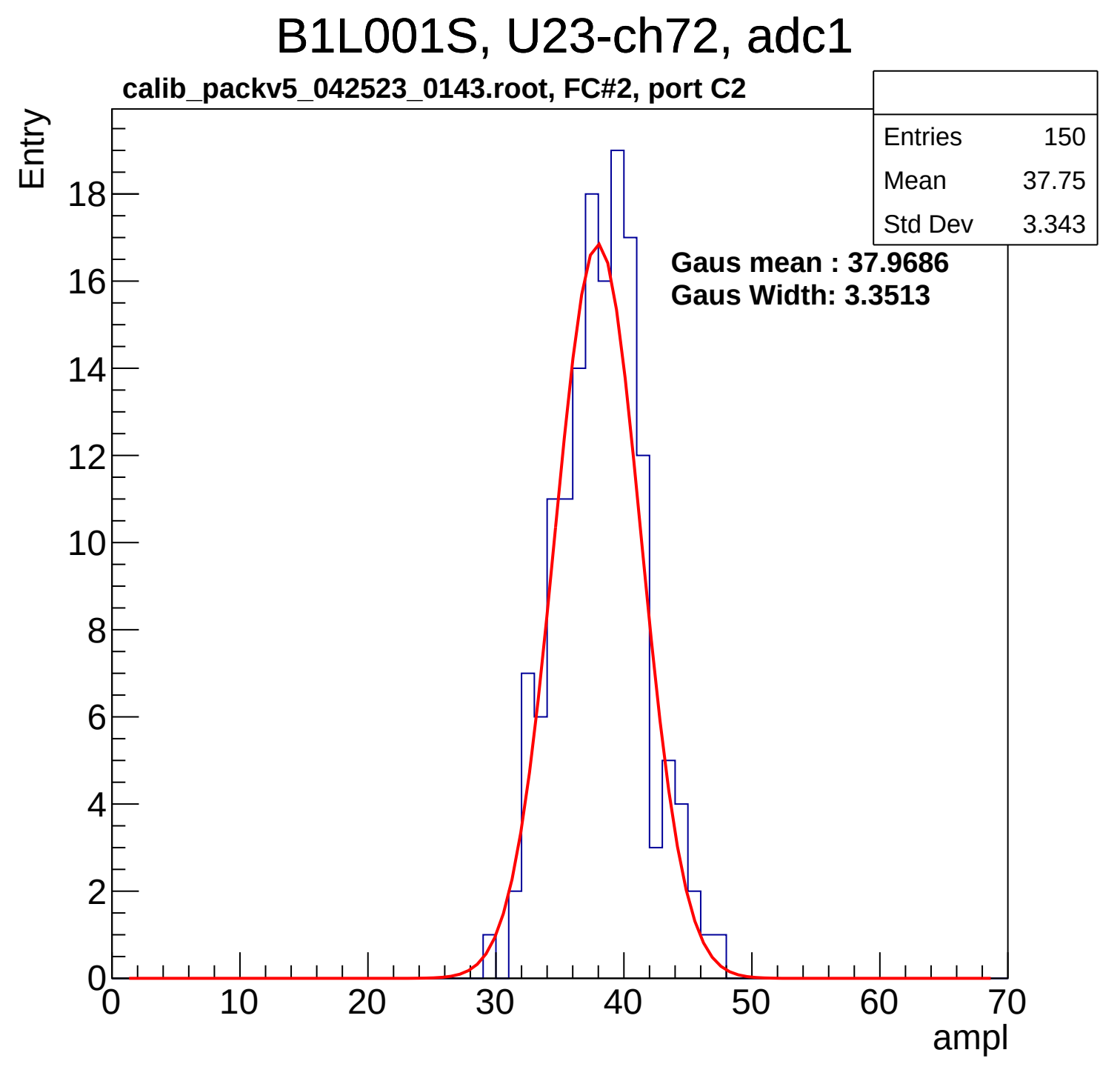
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch72, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 37.75
Std Dev 3.343
Gaus mean : 37.9686
Gaus Width: 3.3513
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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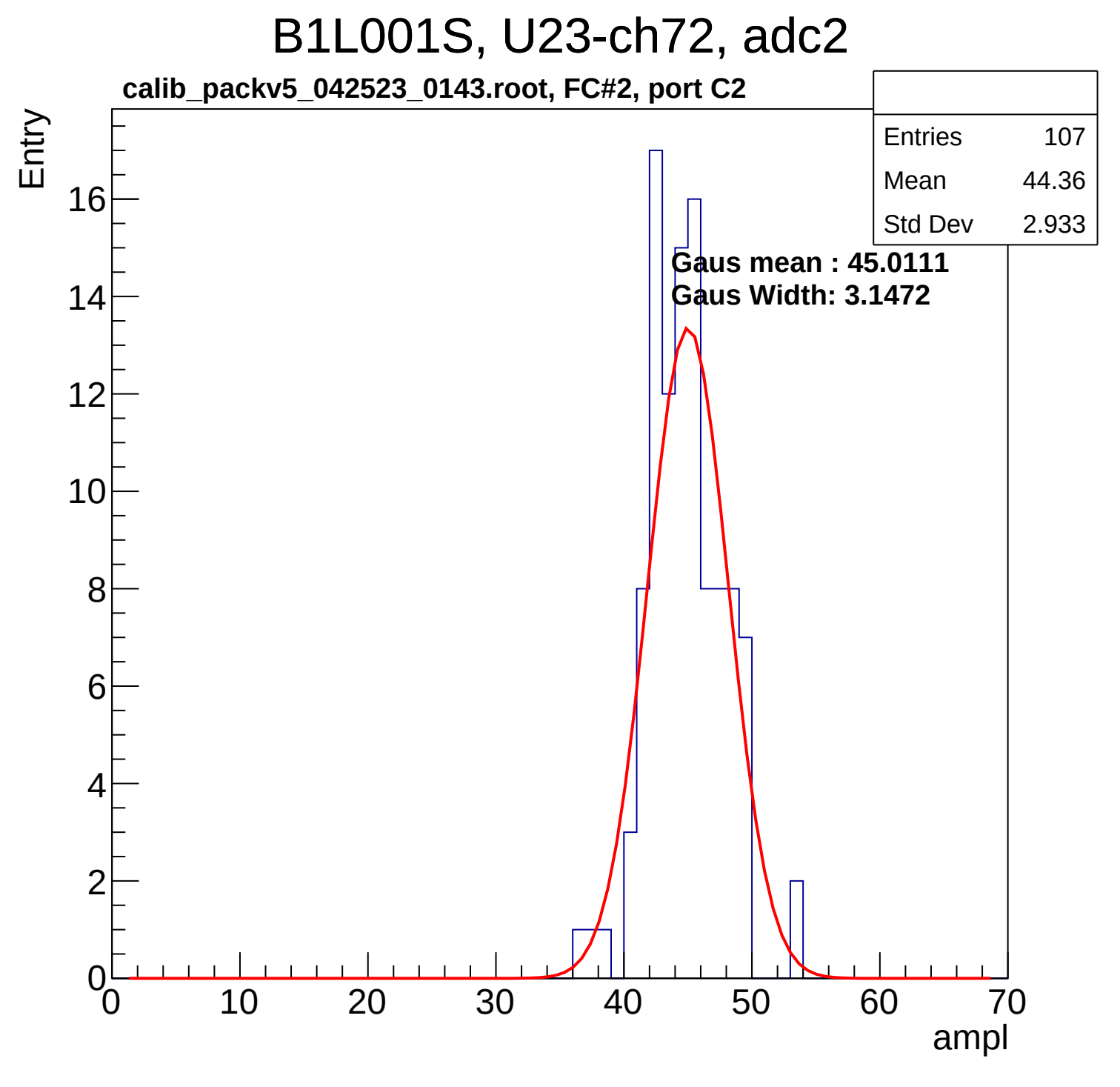
B1L001S, U23-ch72, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 44.36
Std Dev 2.933
Gaus mean : 45.0111
Gaus Width: 3.1472
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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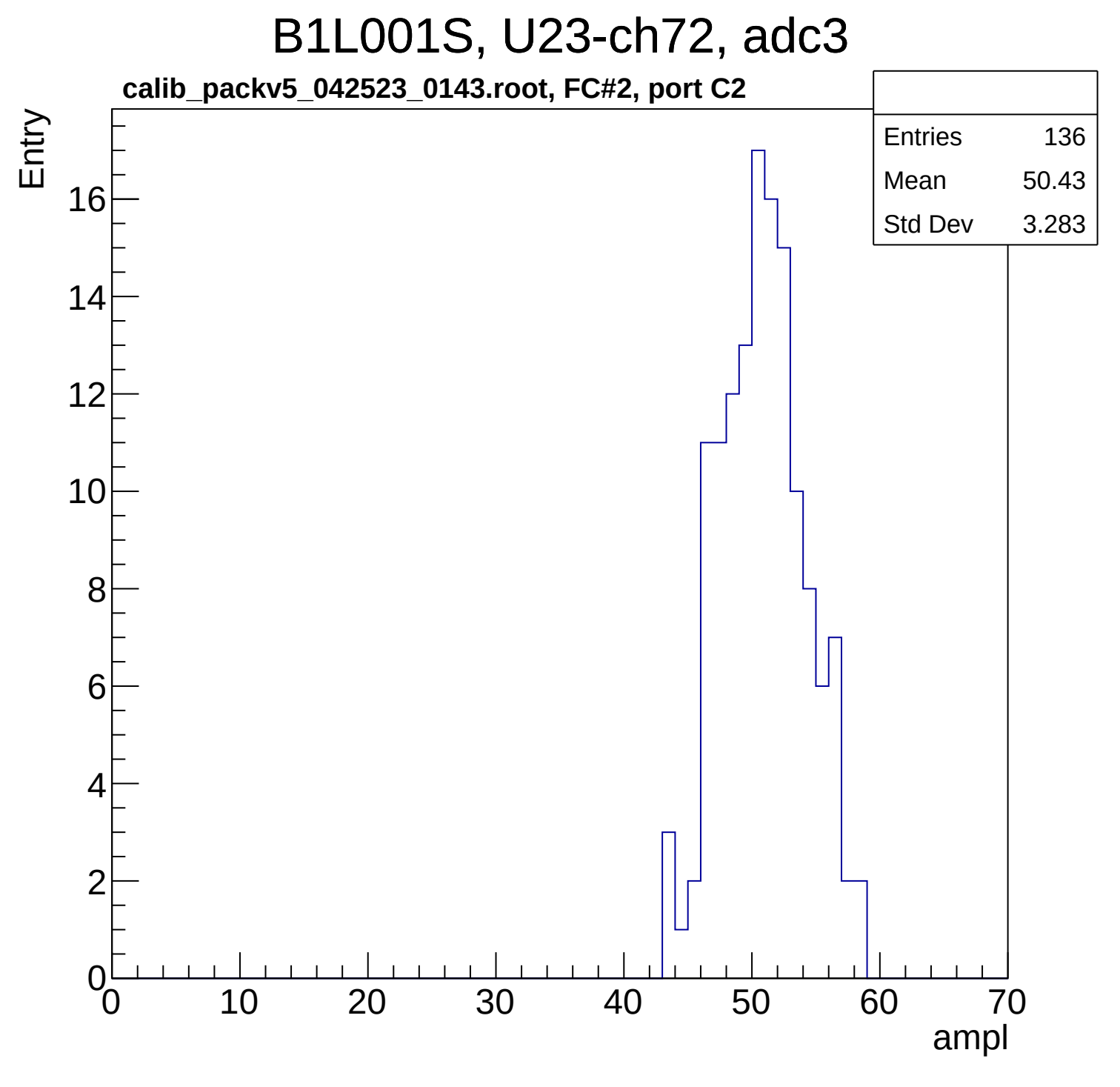
B1L001S, U23-ch72, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 50.43
Std Dev 3.283
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
30
40
50
60
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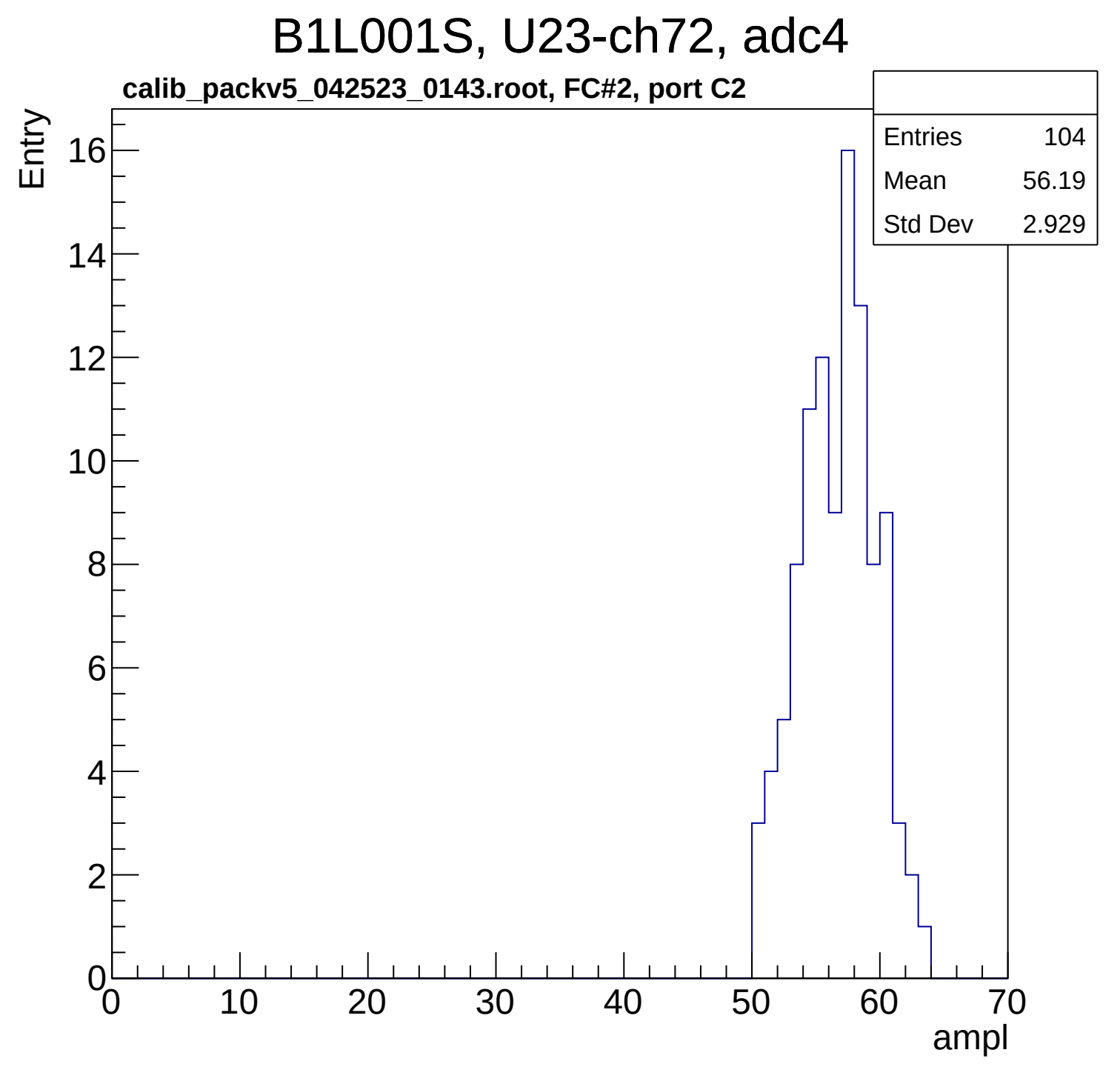
B1L001S, U23-ch72, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 56.19
Std Dev 2.929
Entry
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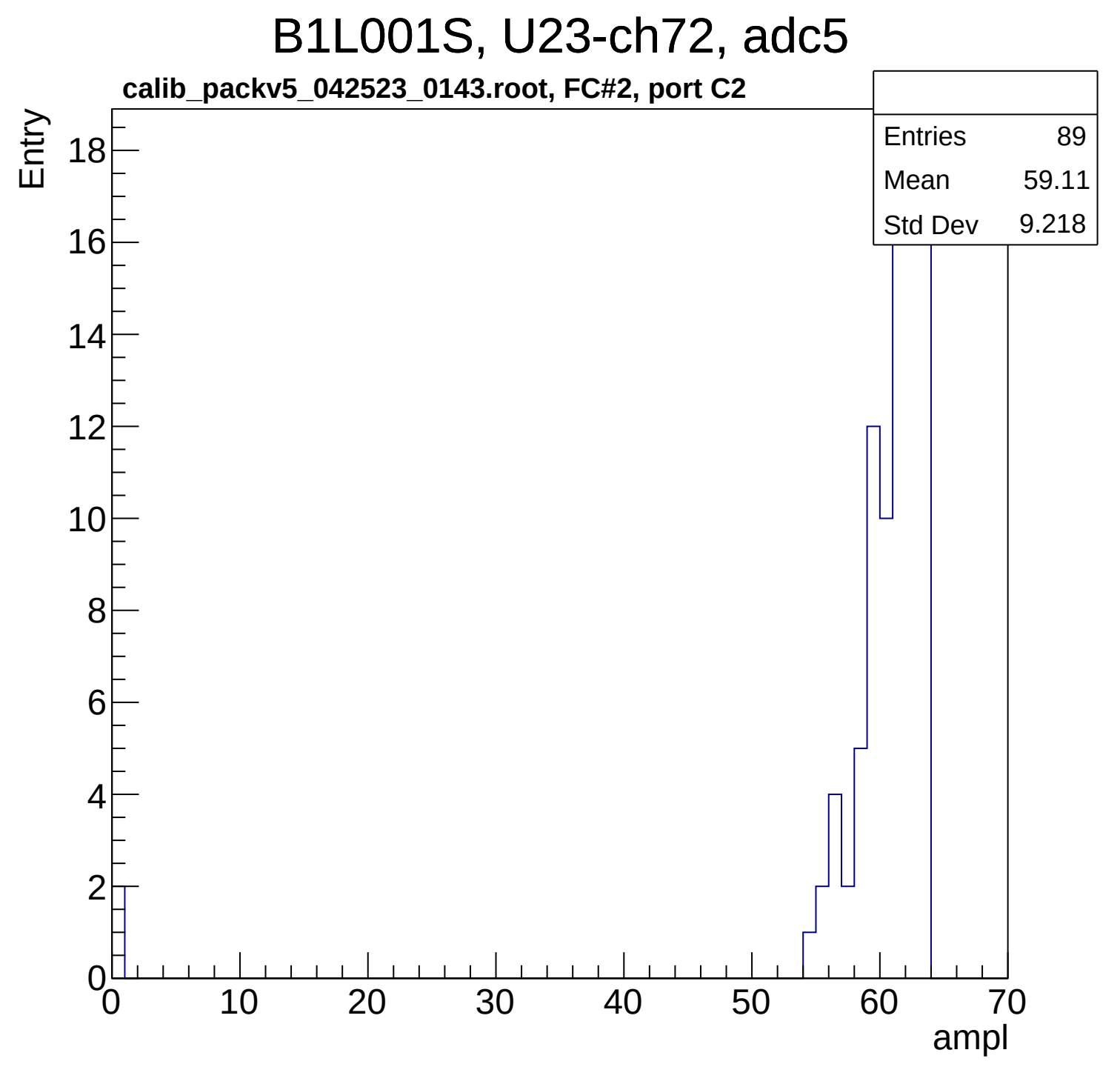
70
ampl


B1L001S, U23-ch72, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 59.11
Std Dev 9.218
Entry
ampl
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4
6
8
10
12
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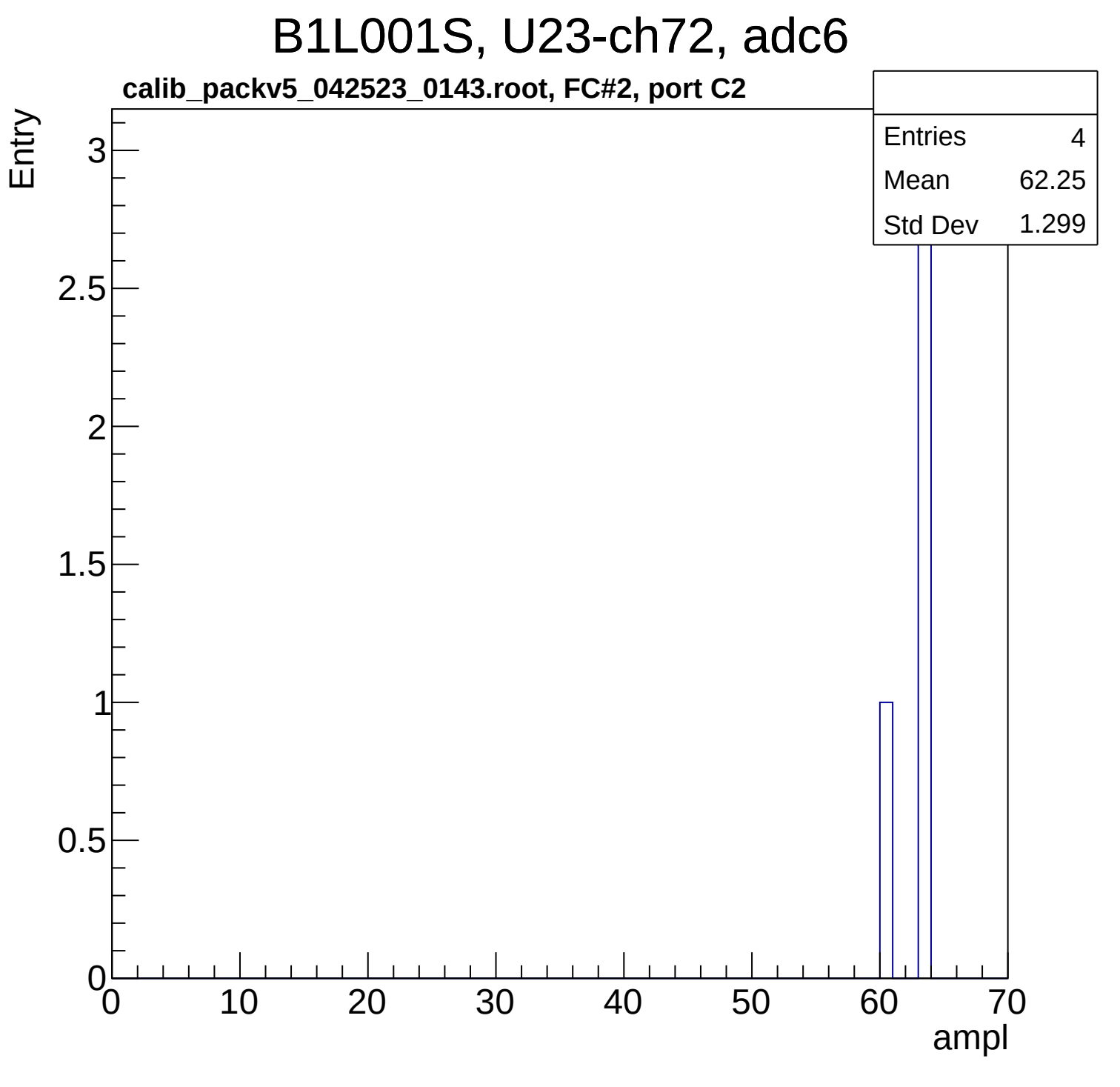
B1L001S, U23-ch72, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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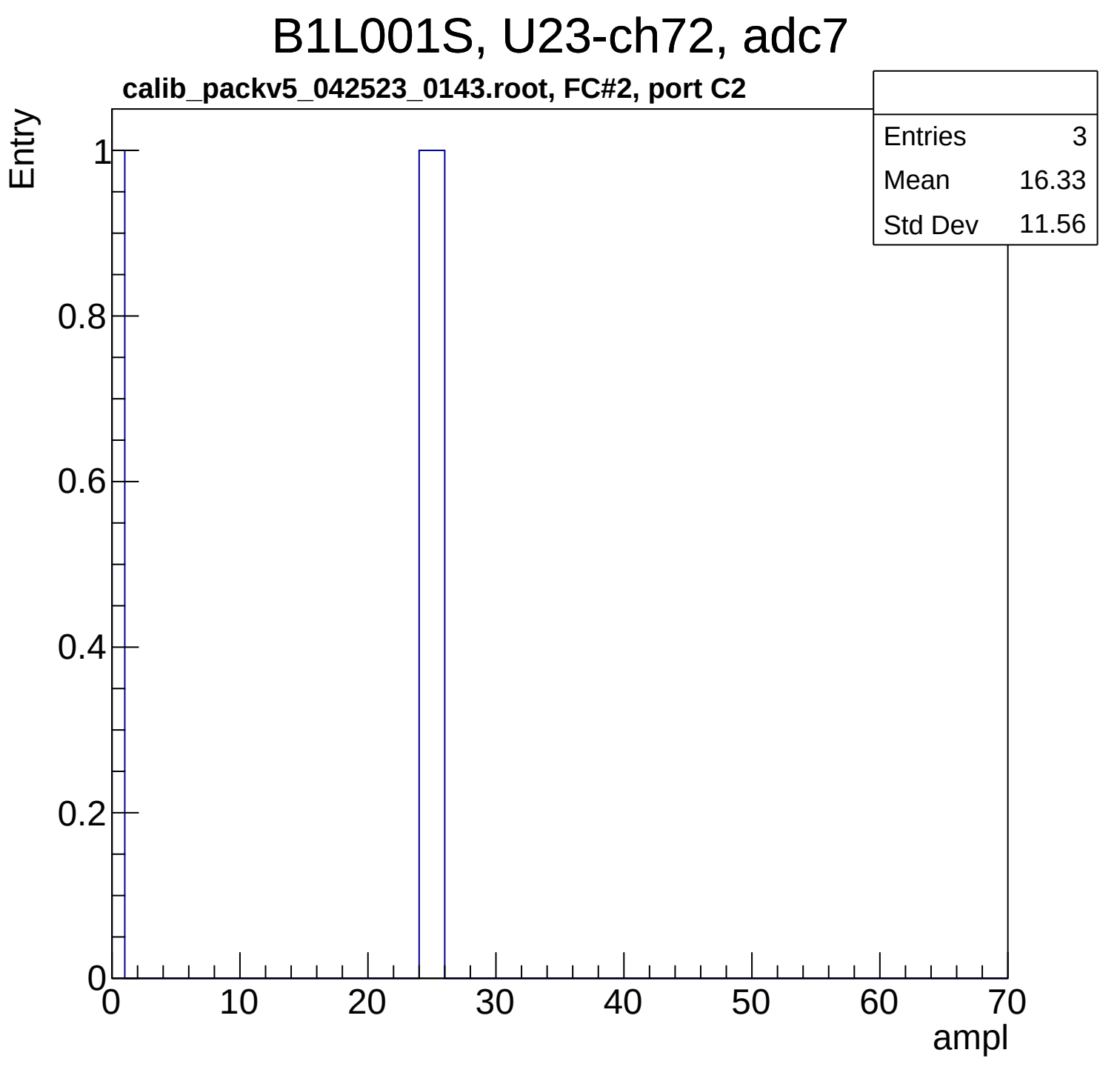
60
70


B1L001S, U23-ch72, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 16.33
Std Dev 11.56
Entry
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0.6
0.4
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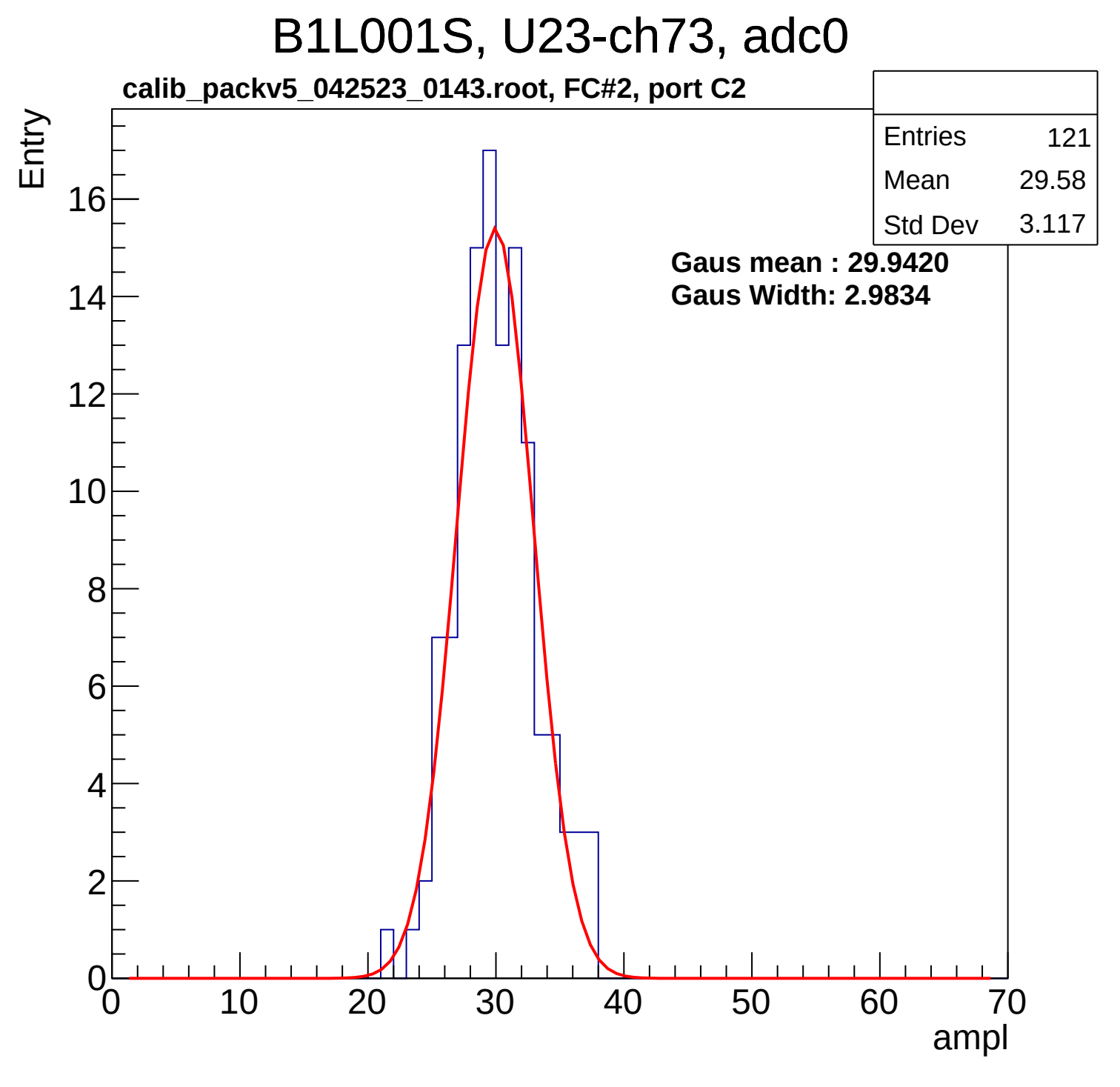
70
ampl


B1L001S, U23-ch73, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 29.58
Std Dev 3.117
Gaus mean : 29.9420
Gaus Width: 2.9834
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
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60
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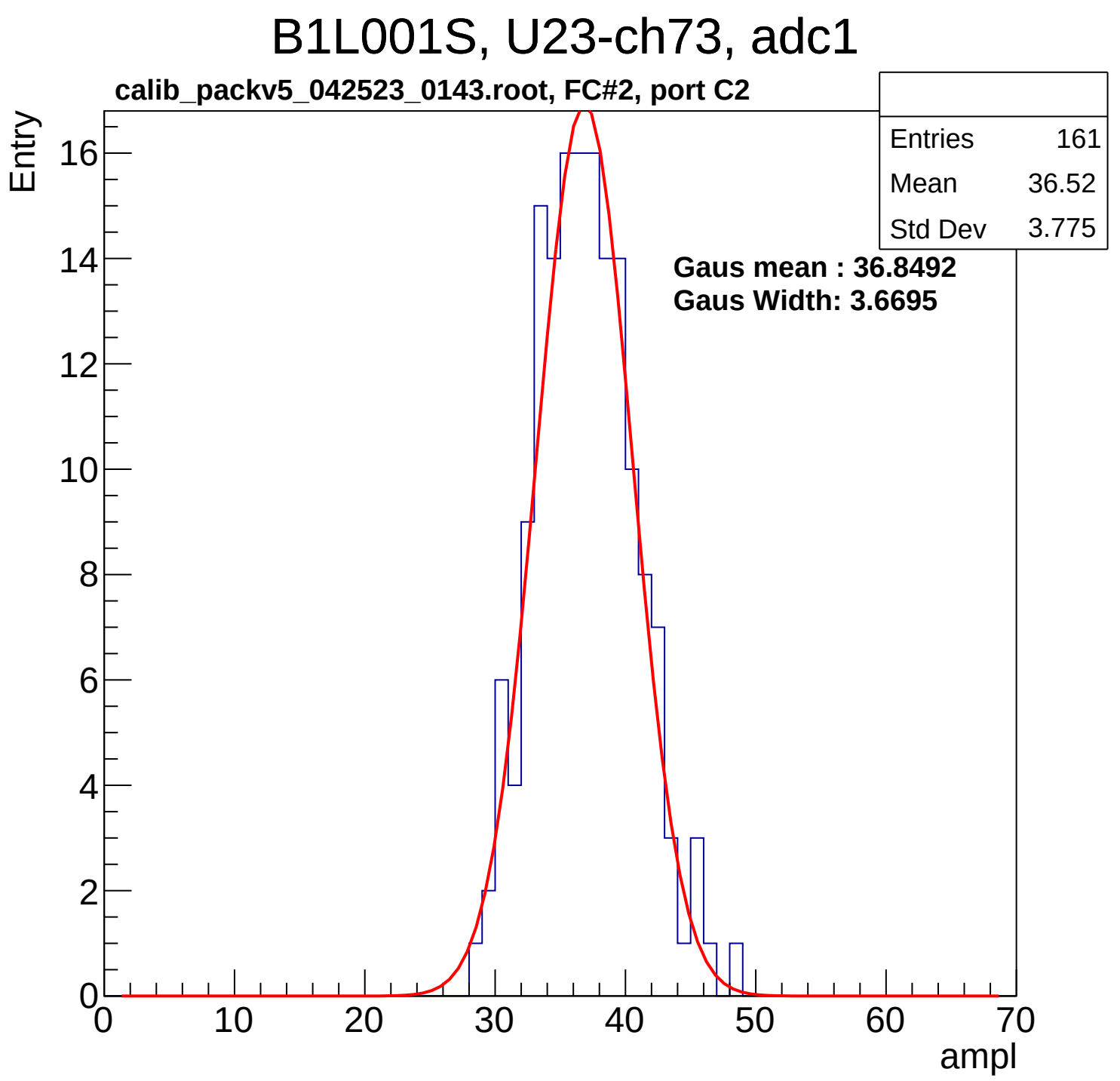
B1L001S, U23-ch73, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 36.52
Std Dev 3.775
Gaus mean : 36.8492
Gaus Width: 3.6695
Entry
ampl
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2
4
6
8
10
12
14
16
0
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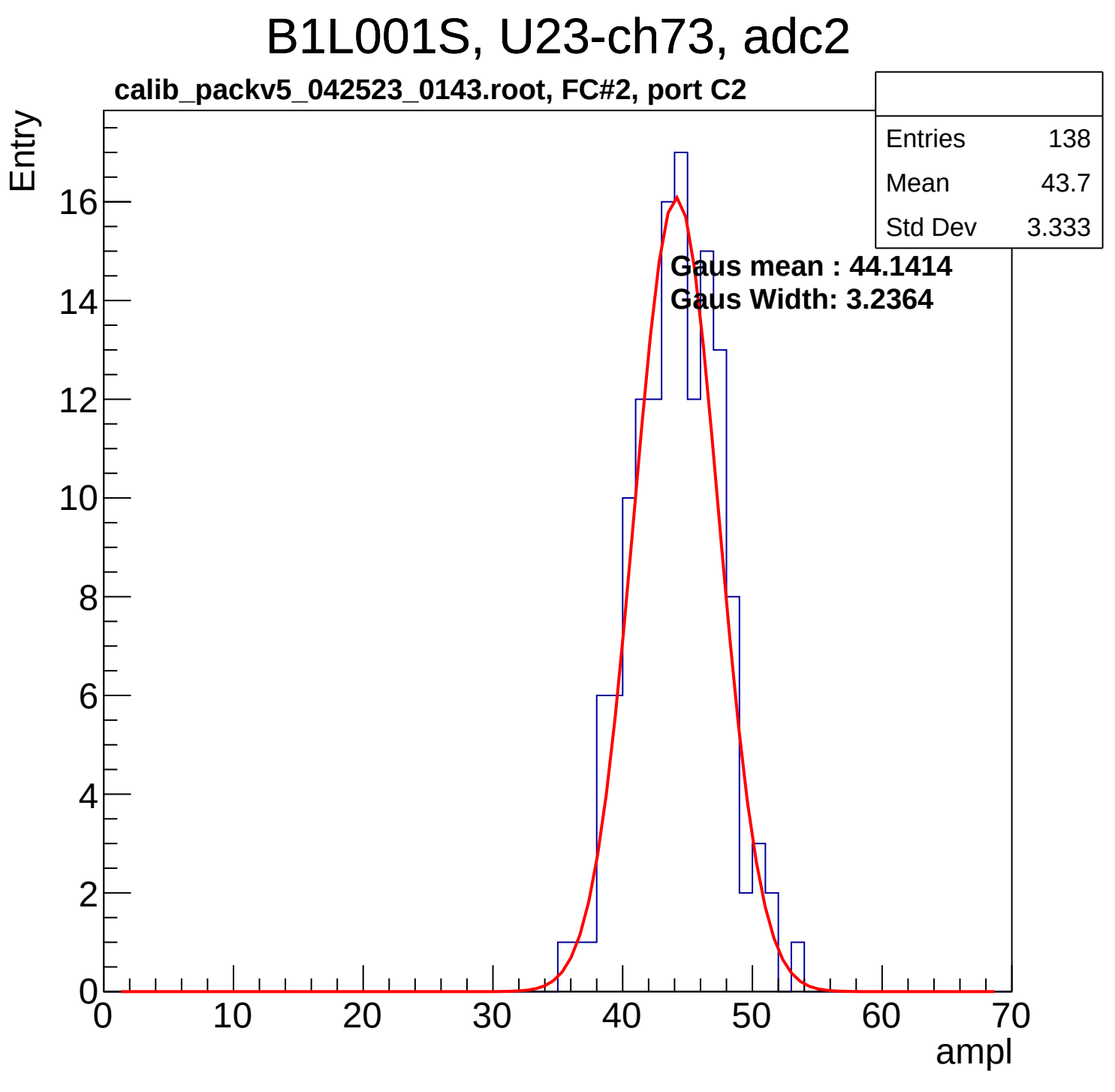
B1L001S, U23-ch73, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 43.7
Std Dev 3.333
Gaus mean : 44.1414
Gaus Width: 3.2364
Entry
ampl
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4
6
8
10
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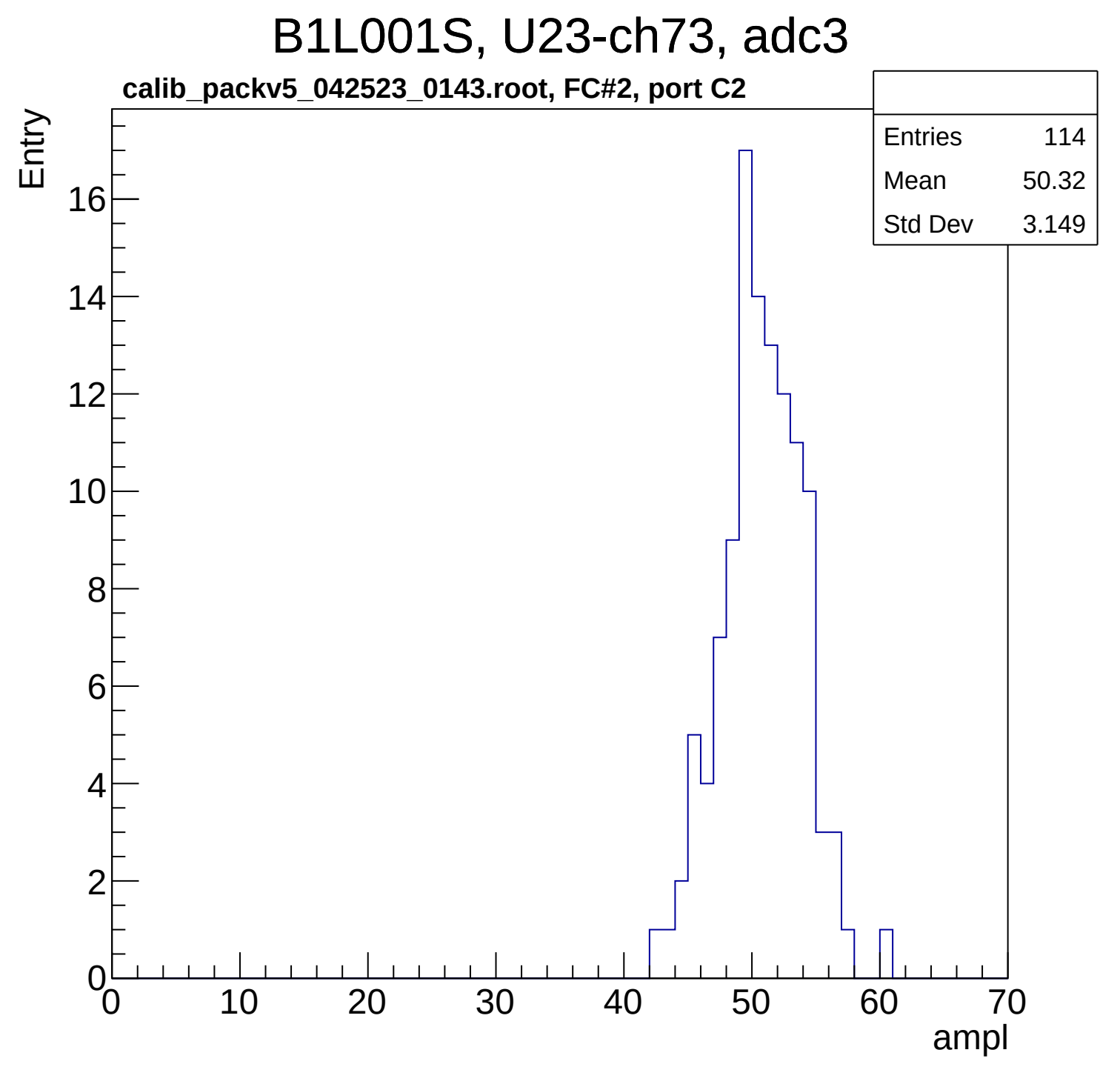
B1L001S, U23-ch73, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 50.32
Std Dev 3.149
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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60
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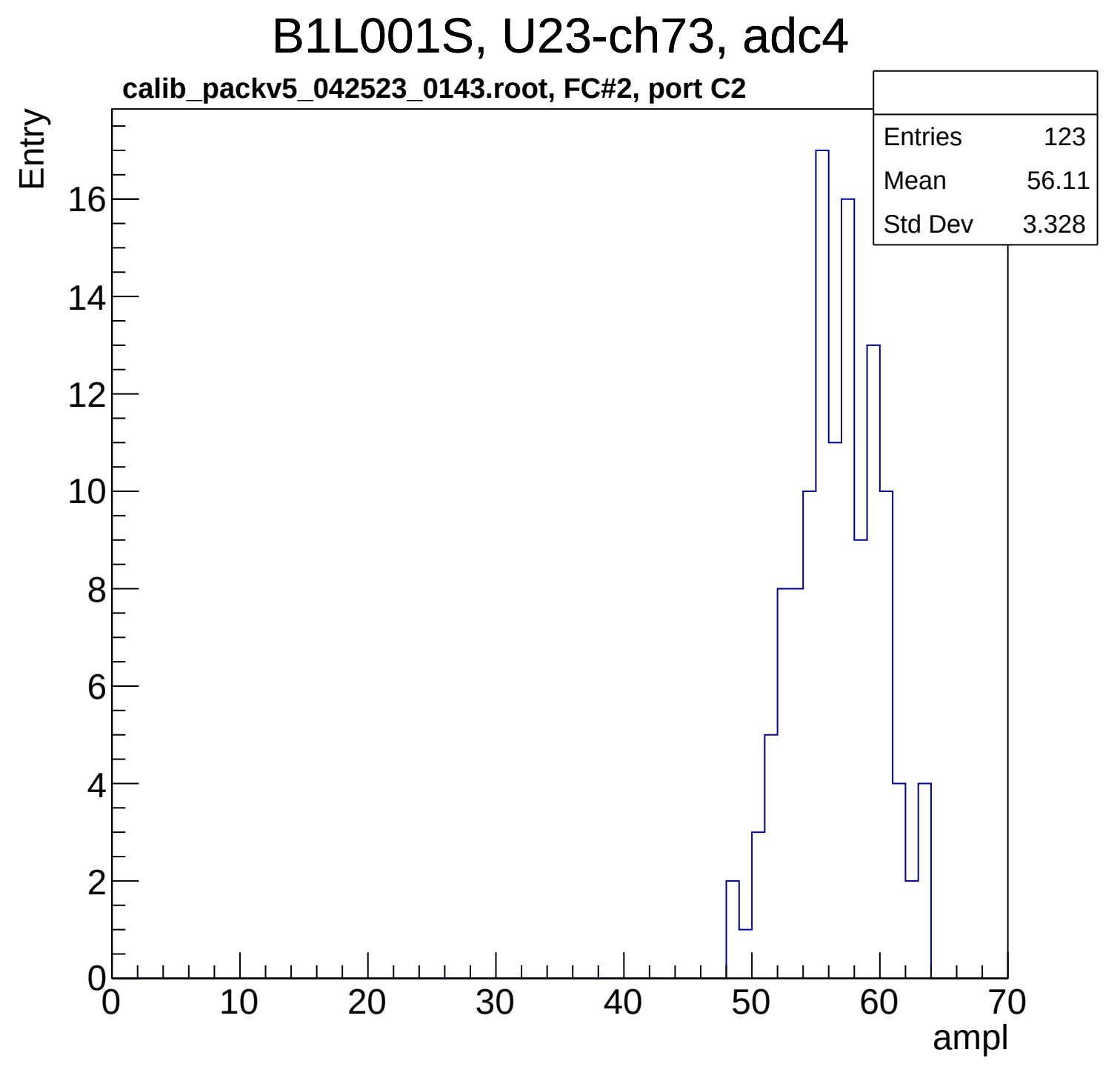
B1L001S, U23-ch73, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 56.11
Std Dev 3.328
Entry
ampl
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4
6
8
10
12
14
16
0
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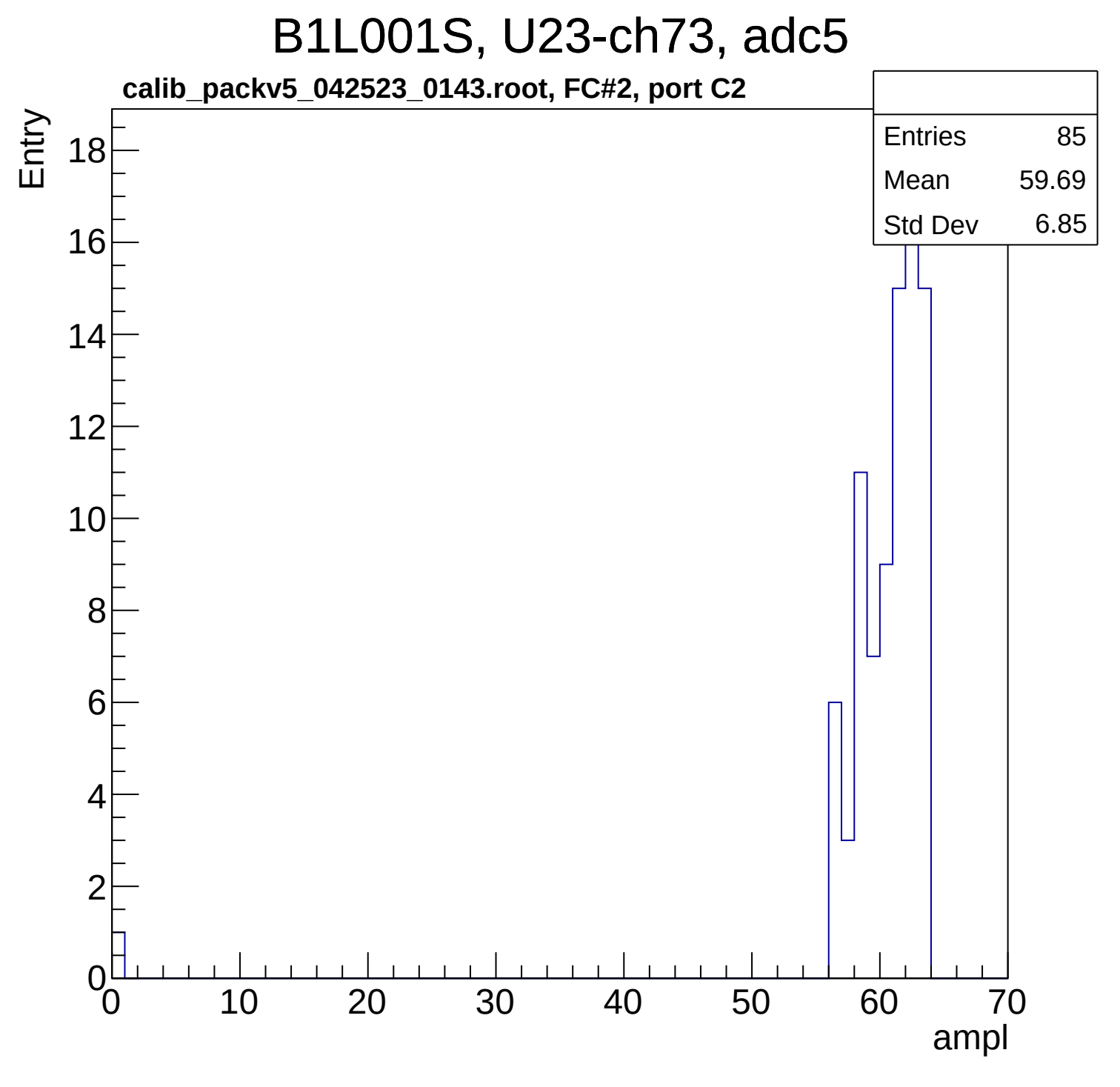
B1L001S, U23-ch73, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 59.69
Std Dev 6.85
Entry
ampl
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6
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10
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0
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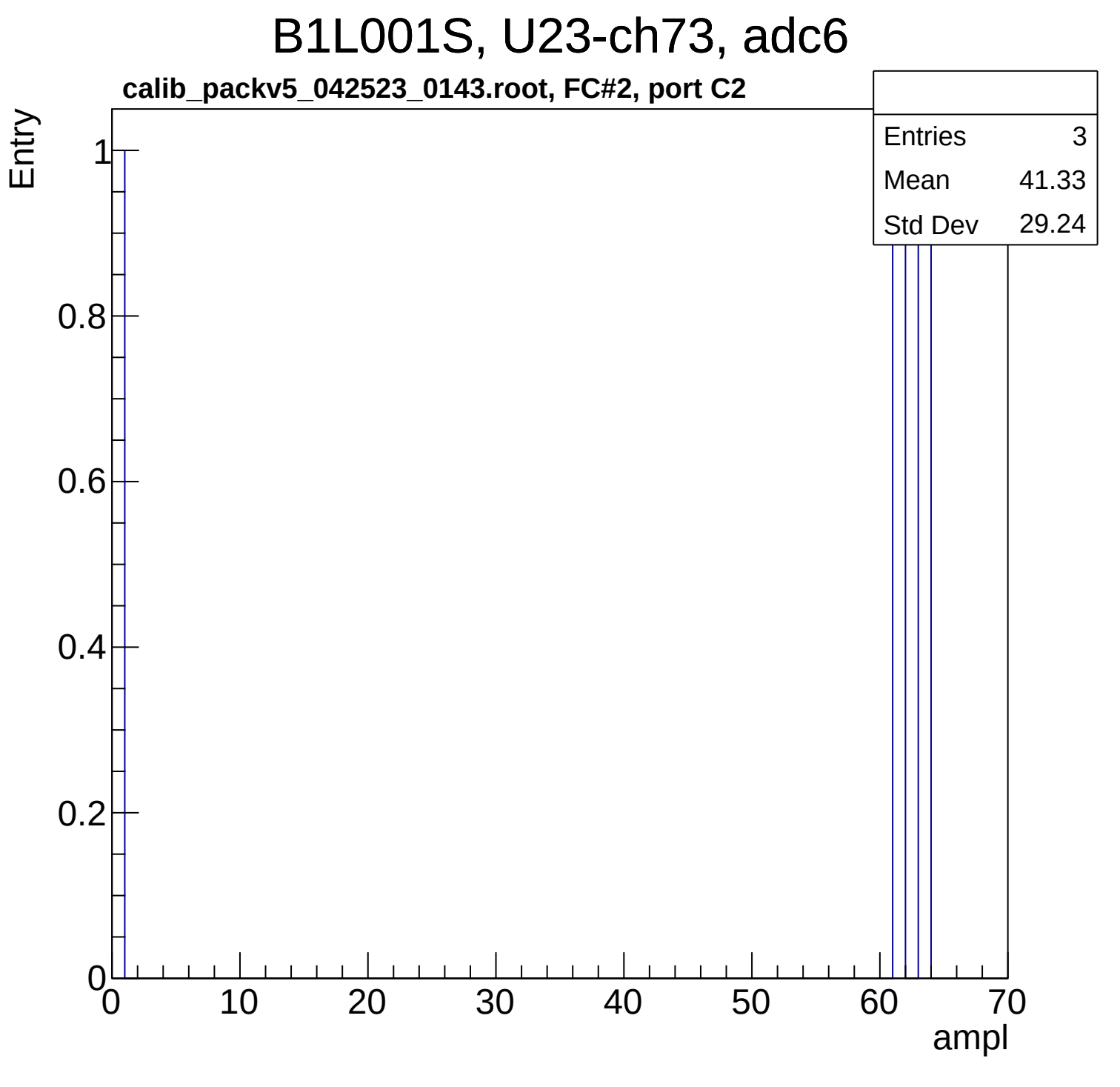
B1L001S, U23-ch73, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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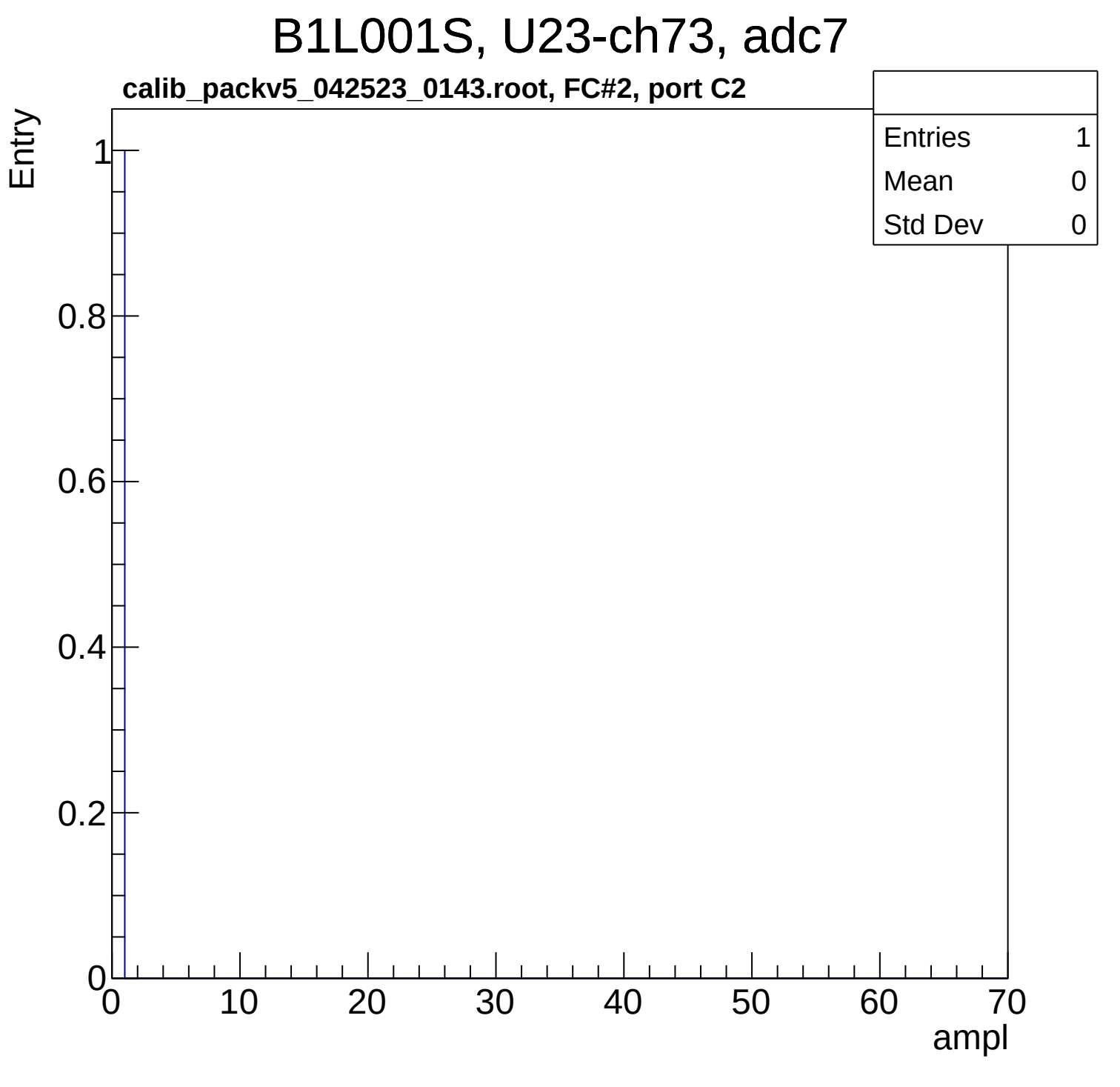
B1L001S, U23-ch73, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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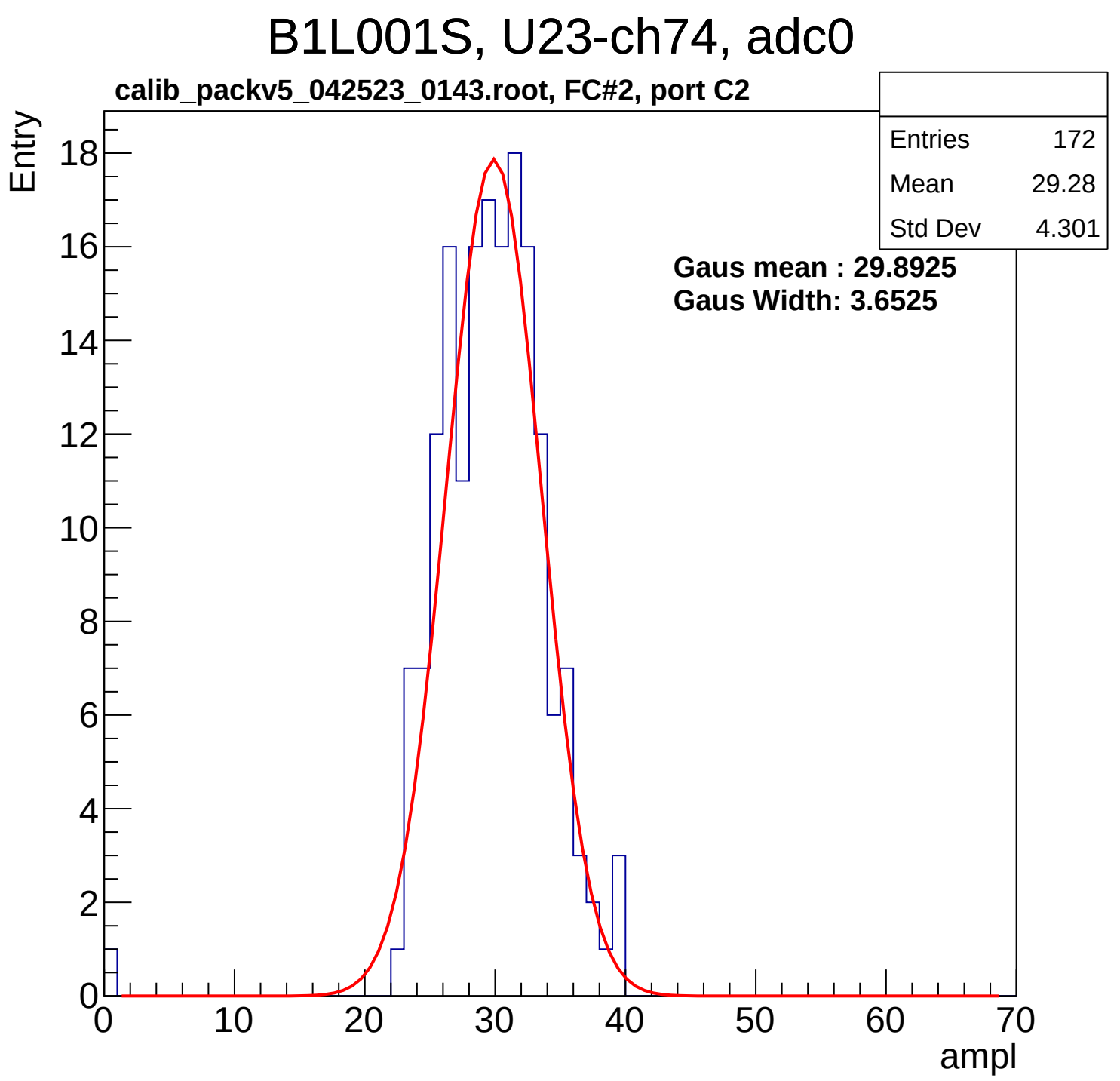
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch74, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 172
Mean 29.28
Std Dev 4.301
Gaus mean : 29.8925
Gaus Width: 3.6525
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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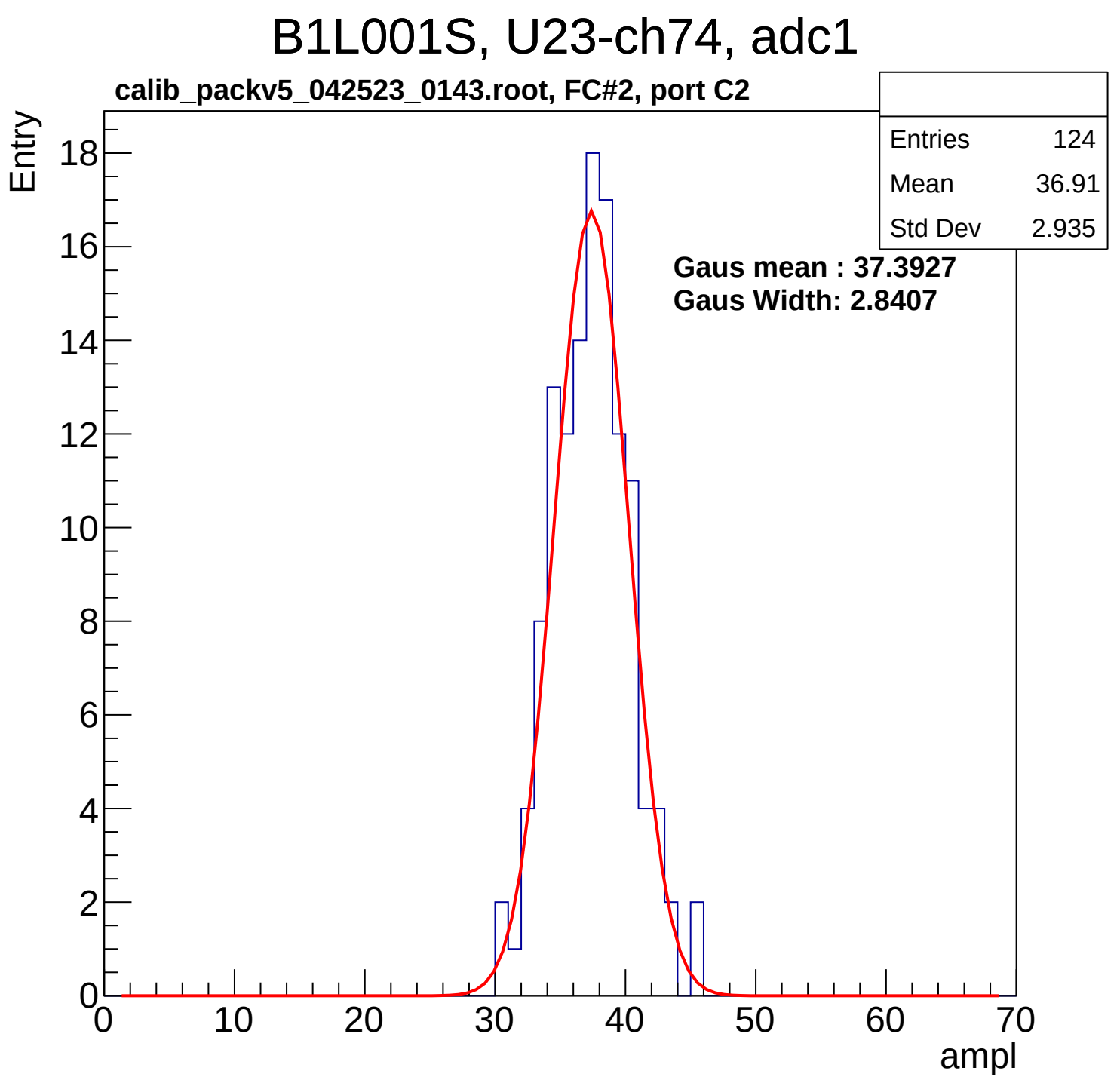
B1L001S, U23-ch74, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 36.91
Std Dev 2.935
Gaus mean : 37.3927
Gaus Width: 2.8407
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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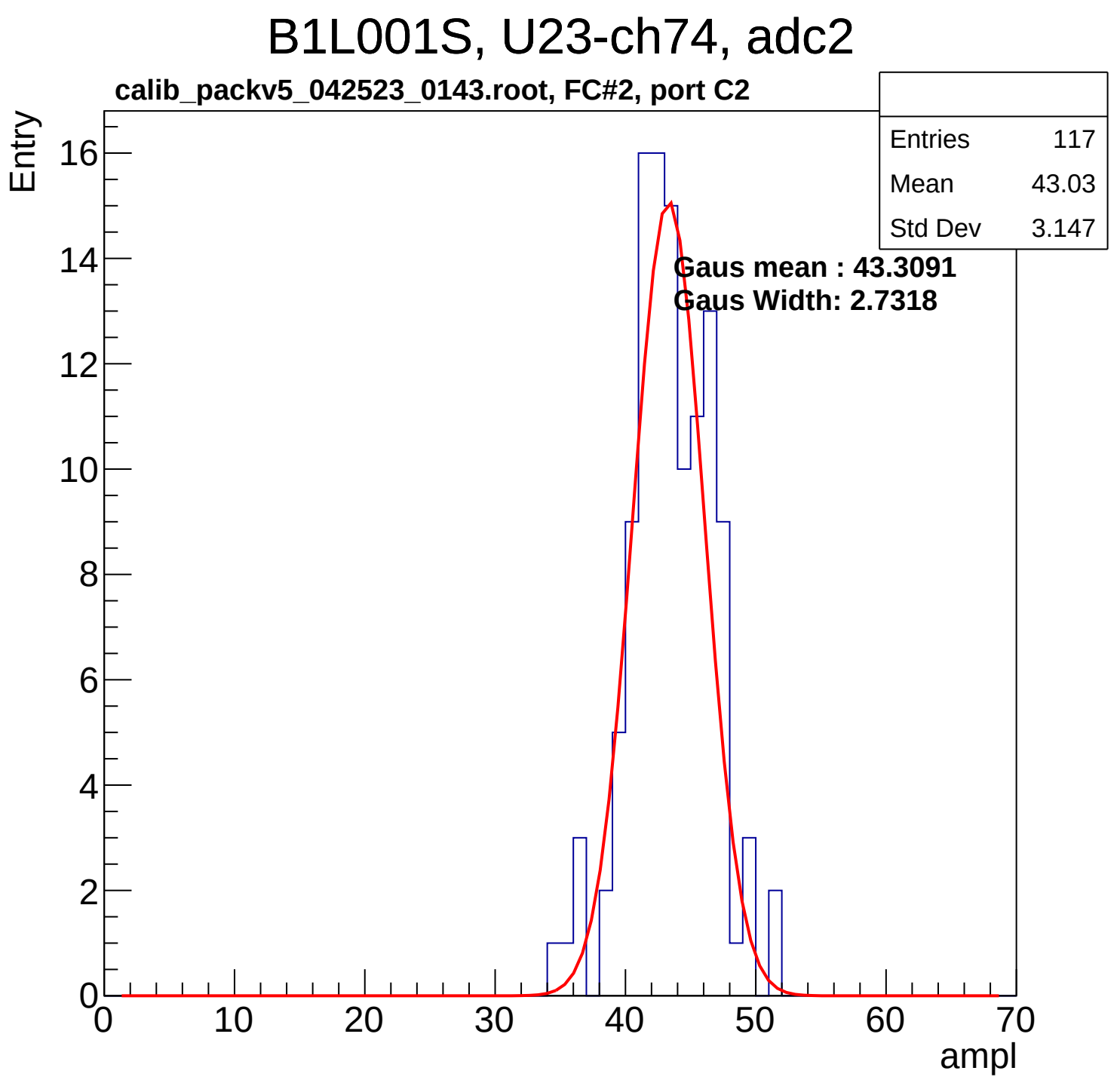
B1L001S, U23-ch74, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 43.03
Std Dev 3.147
Gaus mean : 43.3091
Gaus Width: 2.7318
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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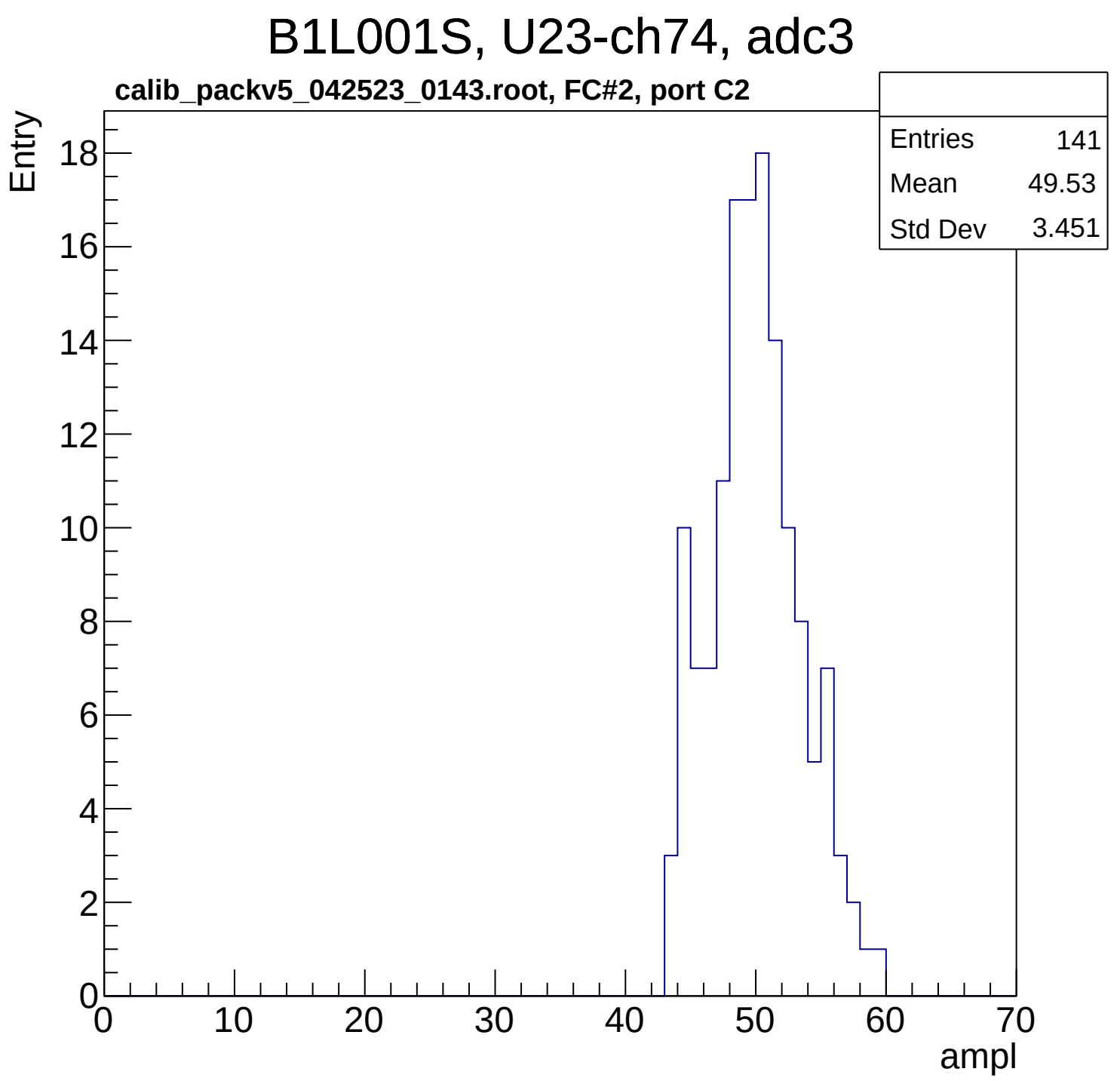
B1L001S, U23-ch74, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 49.53
Std Dev 3.451
Entry
ampl
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4
6
8
10
12
14
16
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0
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60
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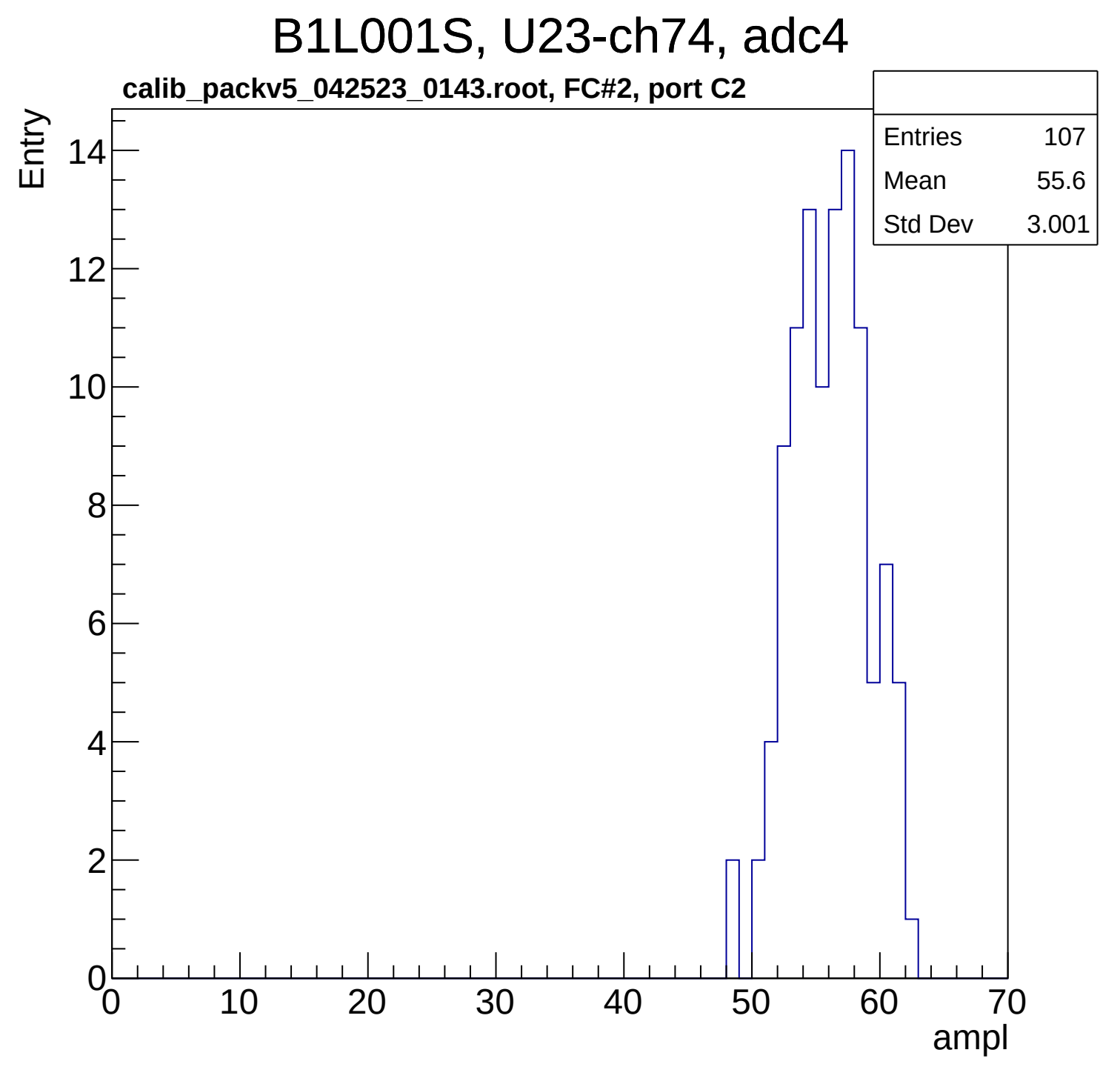
B1L001S, U23-ch74, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 55.6
Std Dev 3.001
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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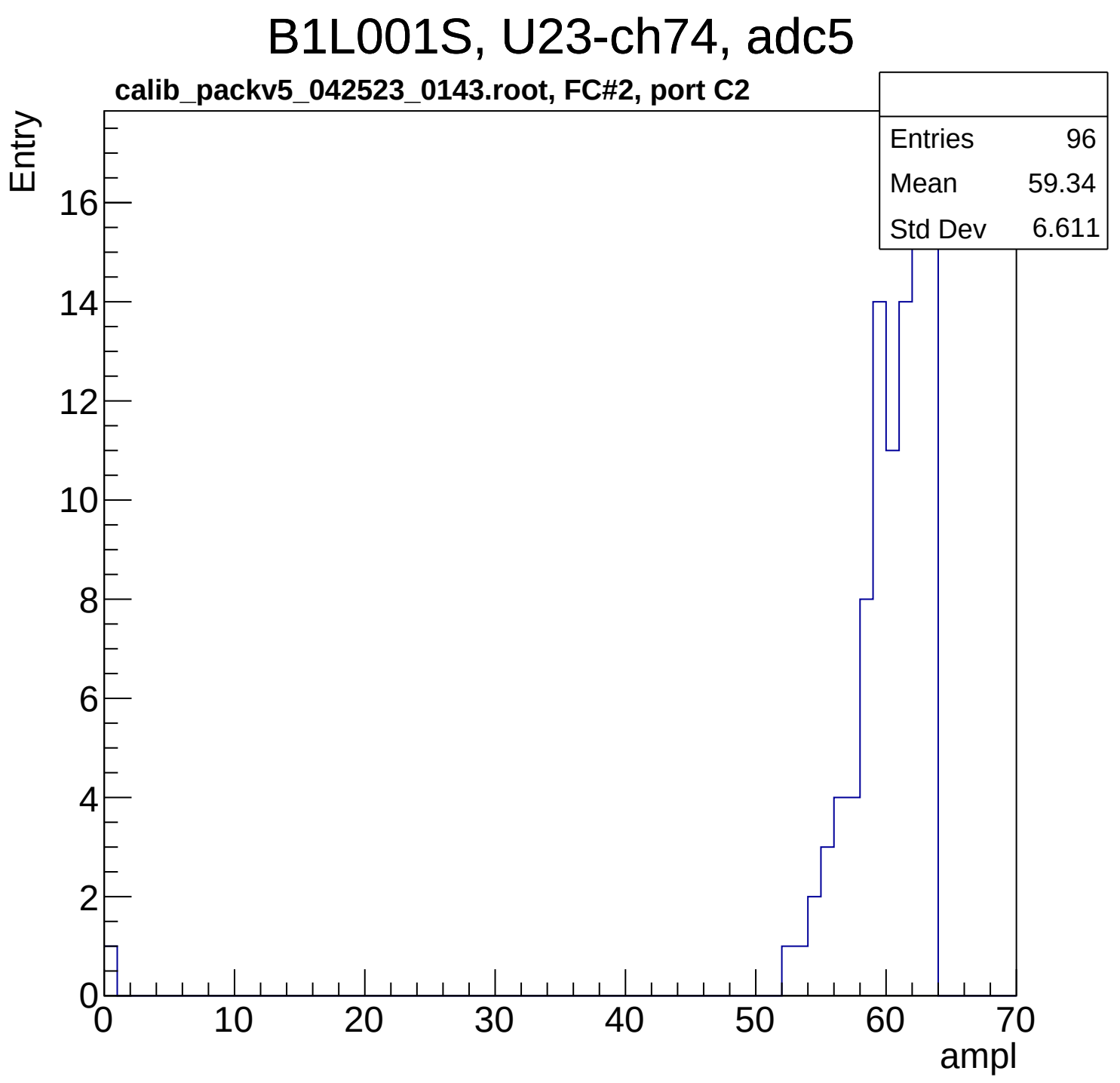
B1L001S, U23-ch74, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 96
Mean 59.34
Std Dev 6.611
Entry
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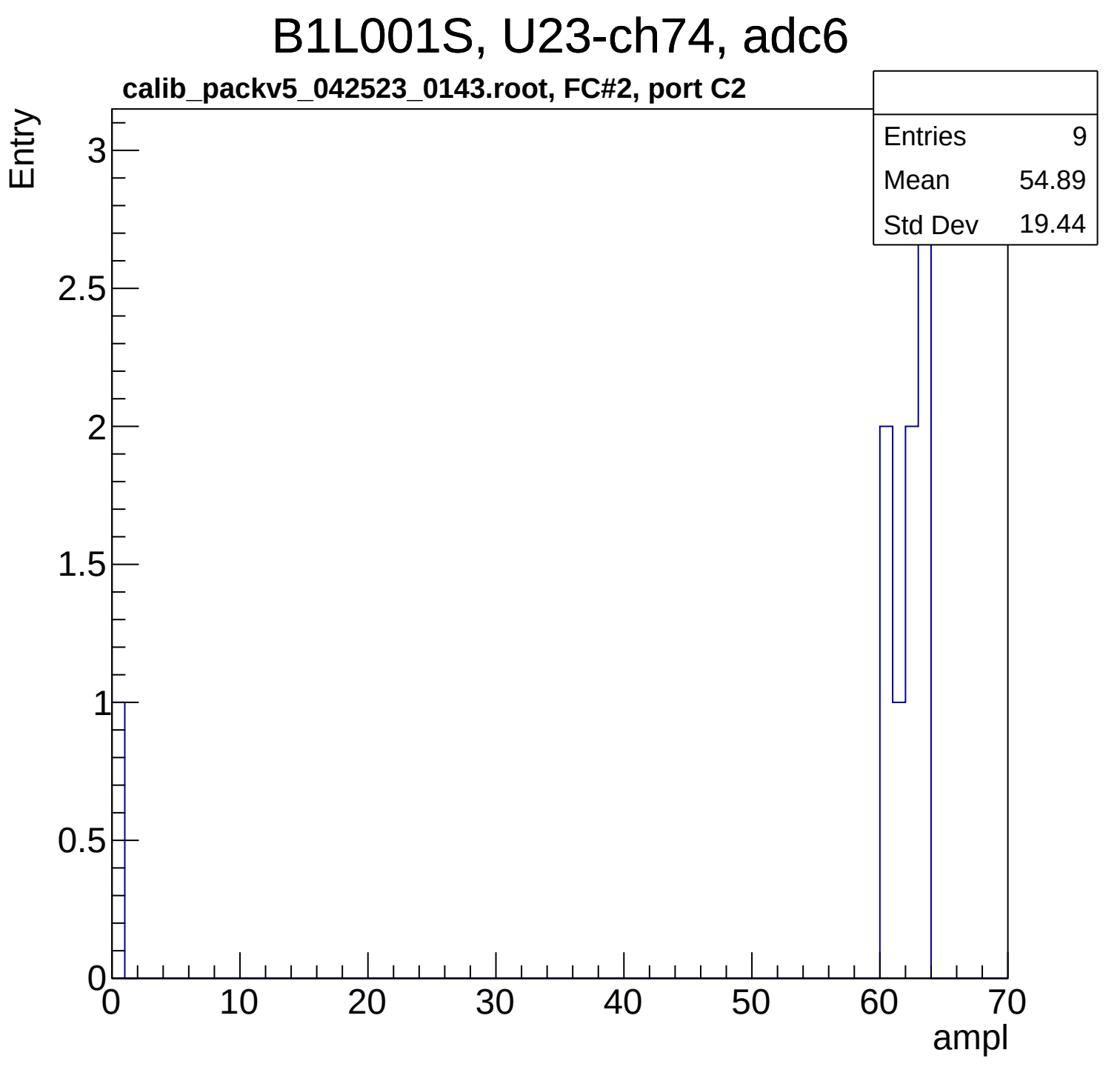
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch74, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
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60
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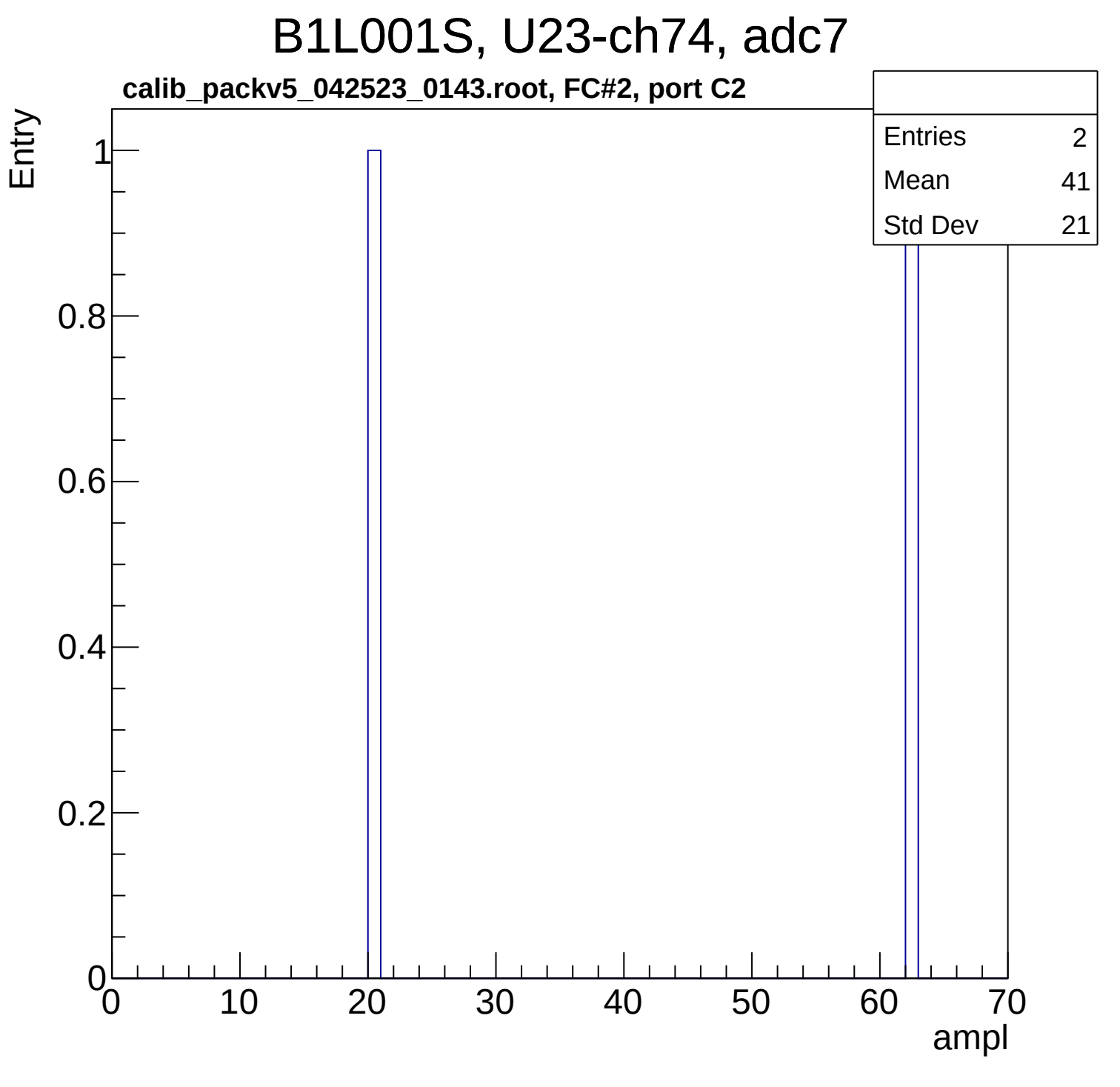
B1L001S, U23-ch74, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 41
Std Dev 21
Entry
ampl
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0.6
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0
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60
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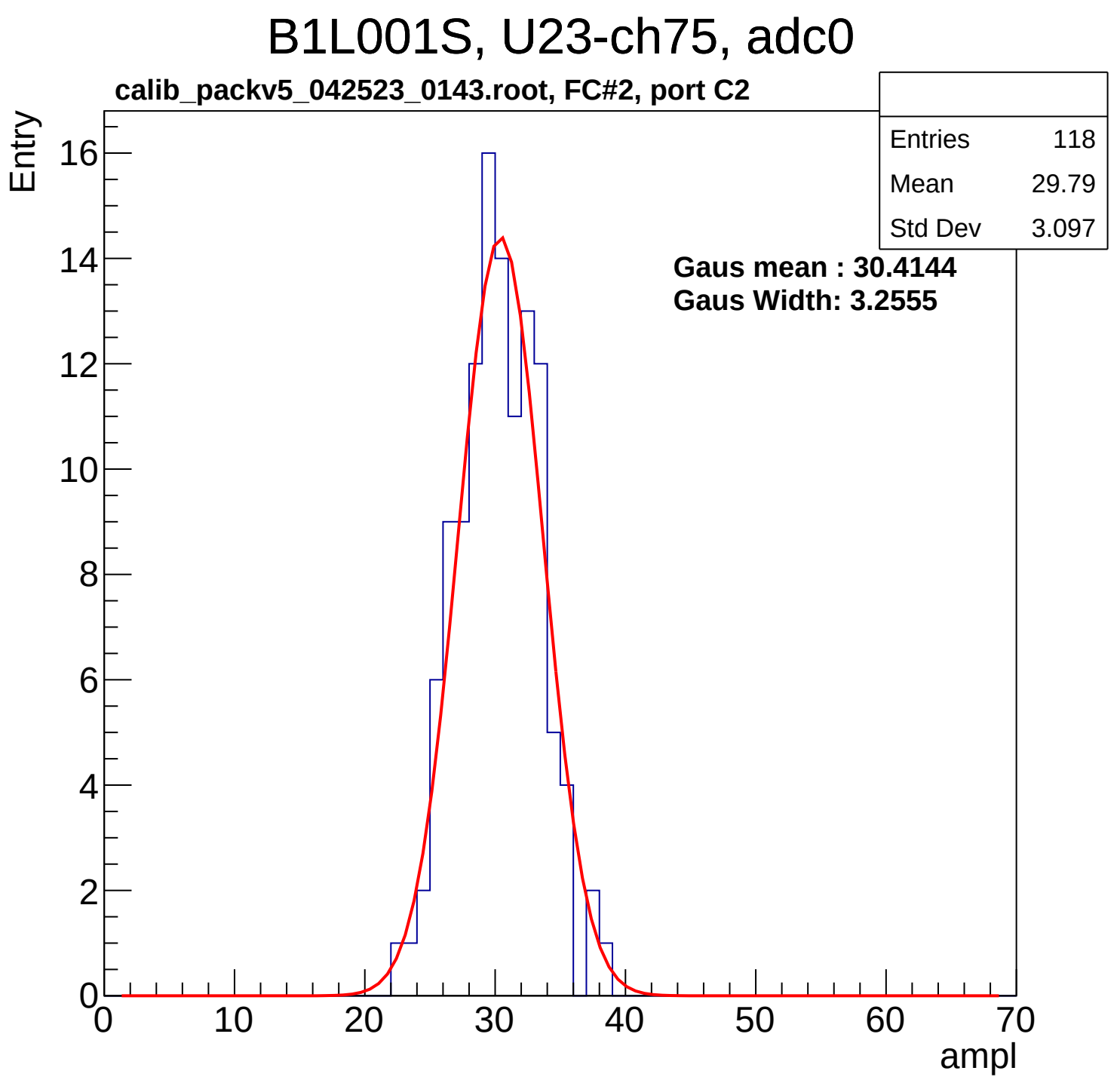
B1L001S, U23-ch75, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 29.79
Std Dev 3.097
Gaus mean : 30.4144
Gaus Width: 3.2555
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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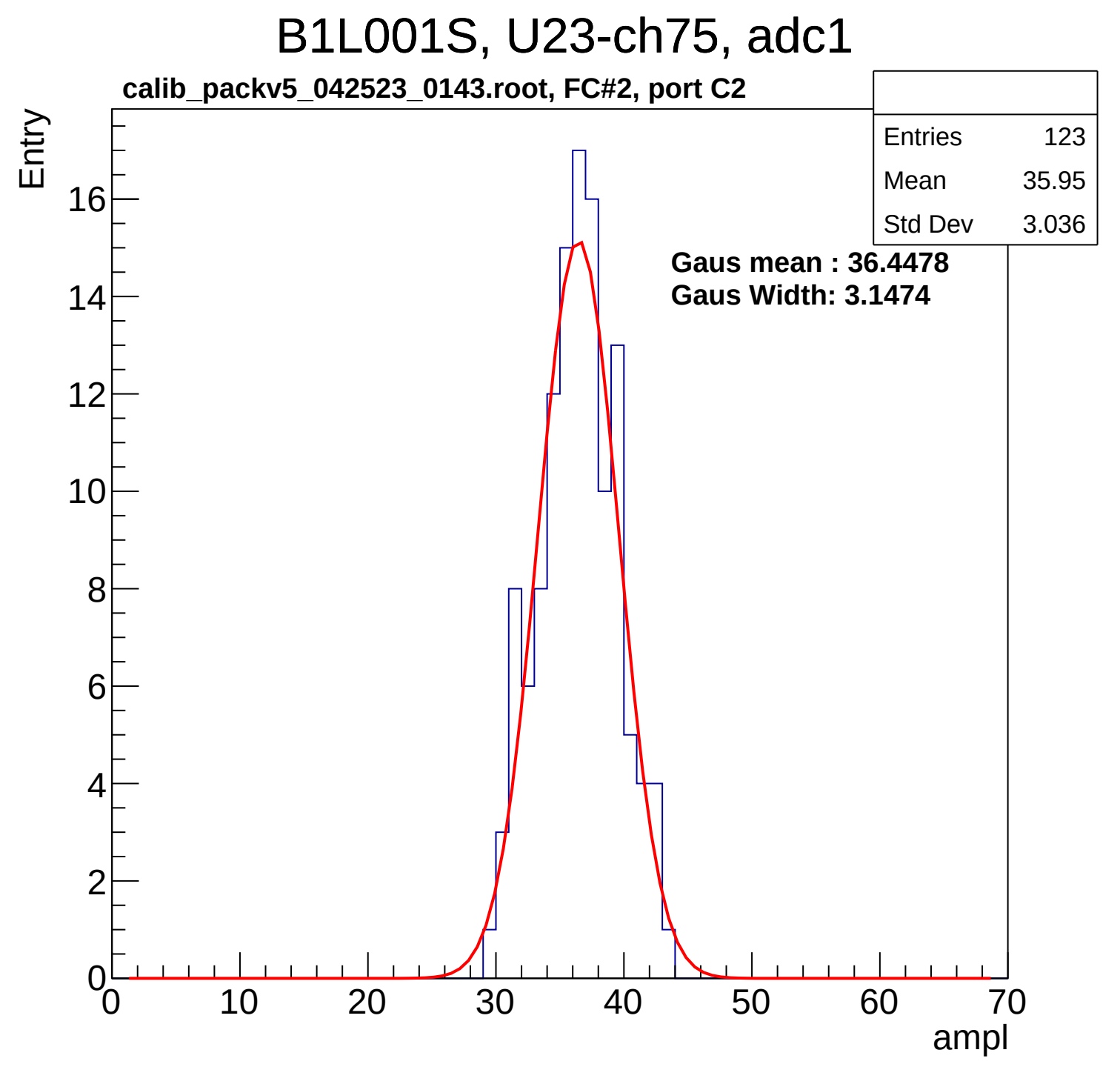
B1L001S, U23-ch75, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 35.95
Std Dev 3.036
Gaus mean : 36.4478
Gaus Width: 3.1474
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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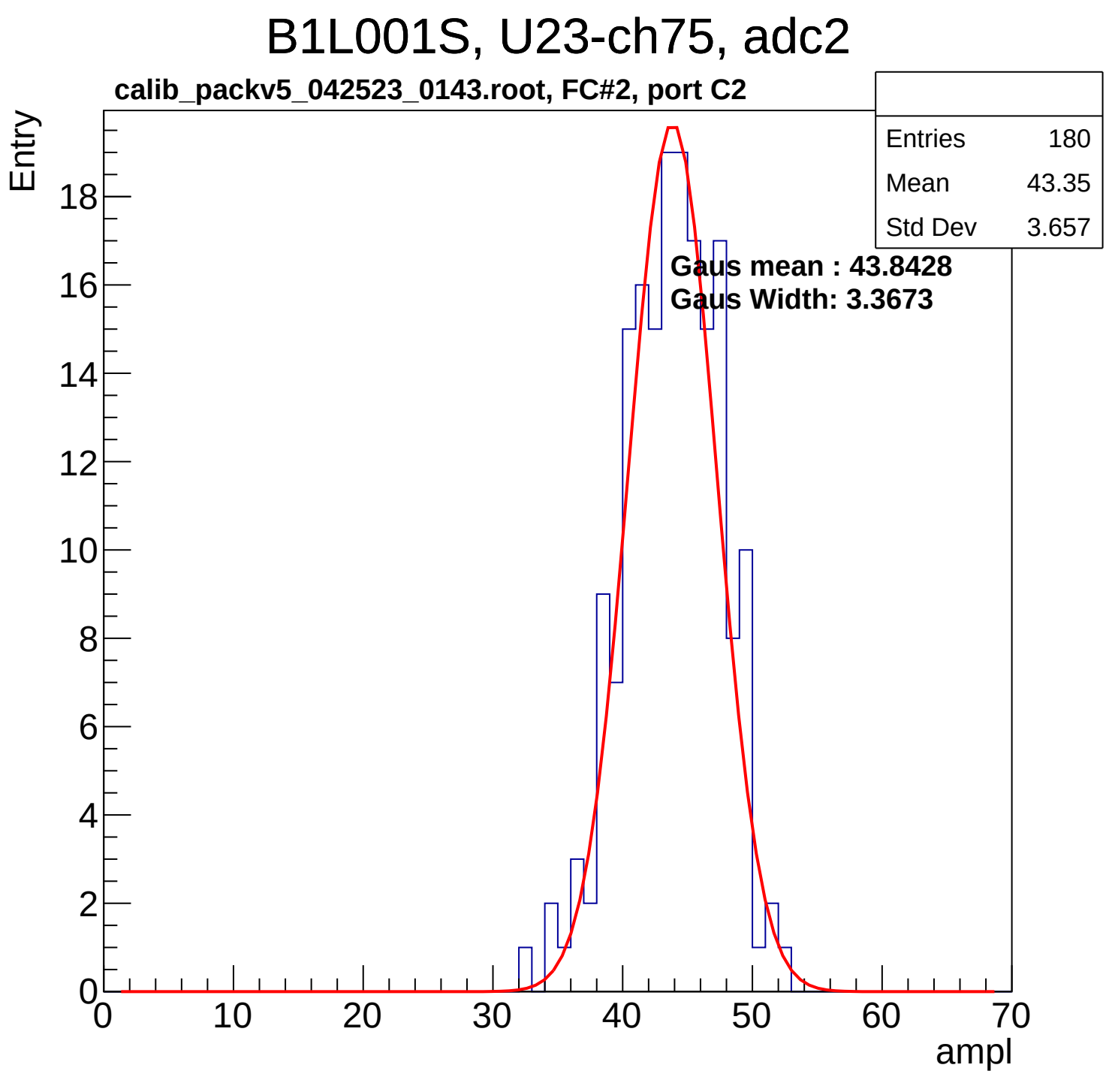
B1L001S, U23-ch75, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 180
Mean 43.35
Std Dev 3.657
Gaus mean : 43.8428
Gaus Width: 3.3673
Entry
ampl
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6
8
10
12
14
16
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0
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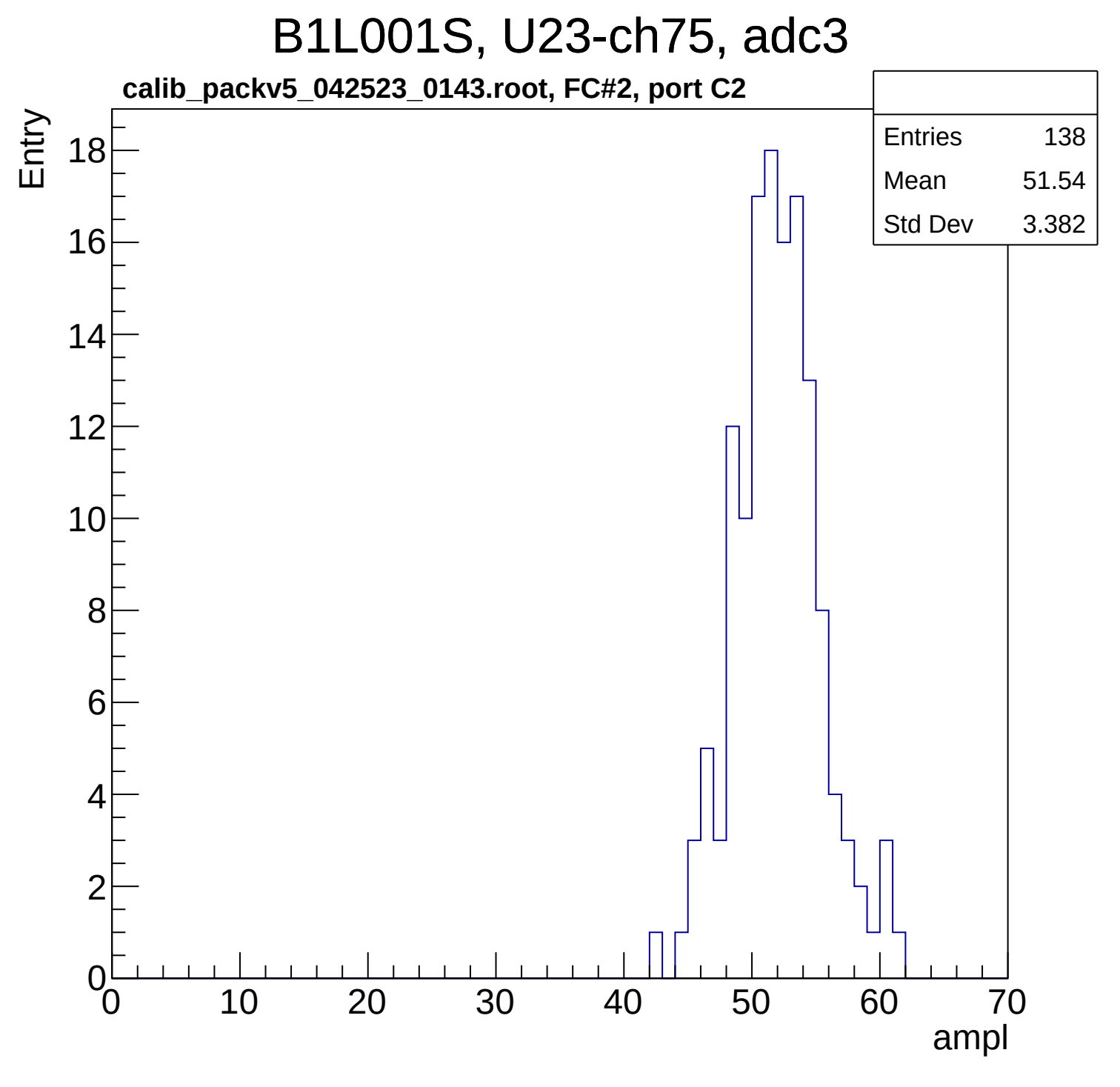
B1L001S, U23-ch75, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 51.54
Std Dev 3.382
Entry
ampl
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0
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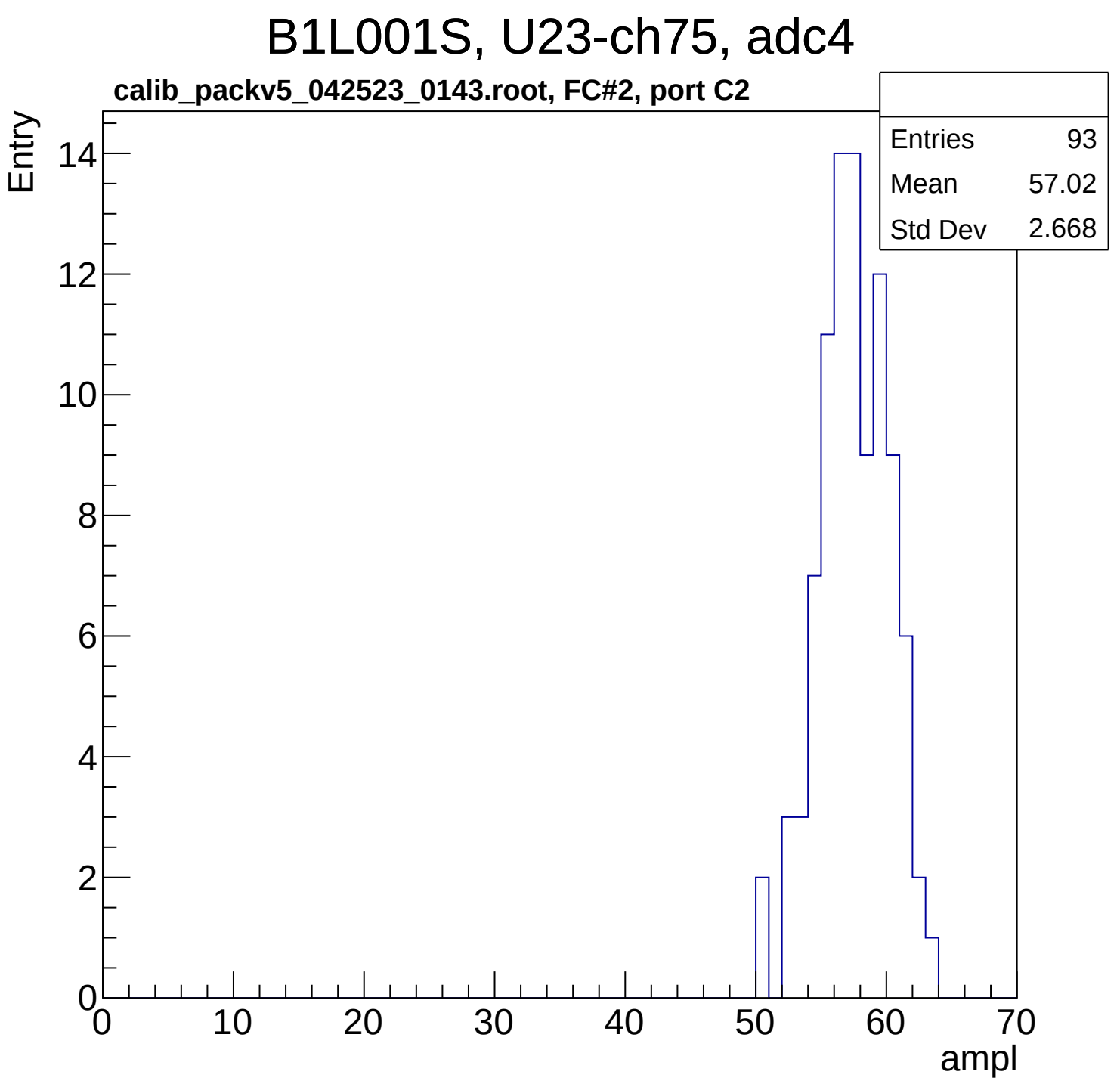
B1L001S, U23-ch75, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 57.02
Std Dev 2.668
Entry
ampl
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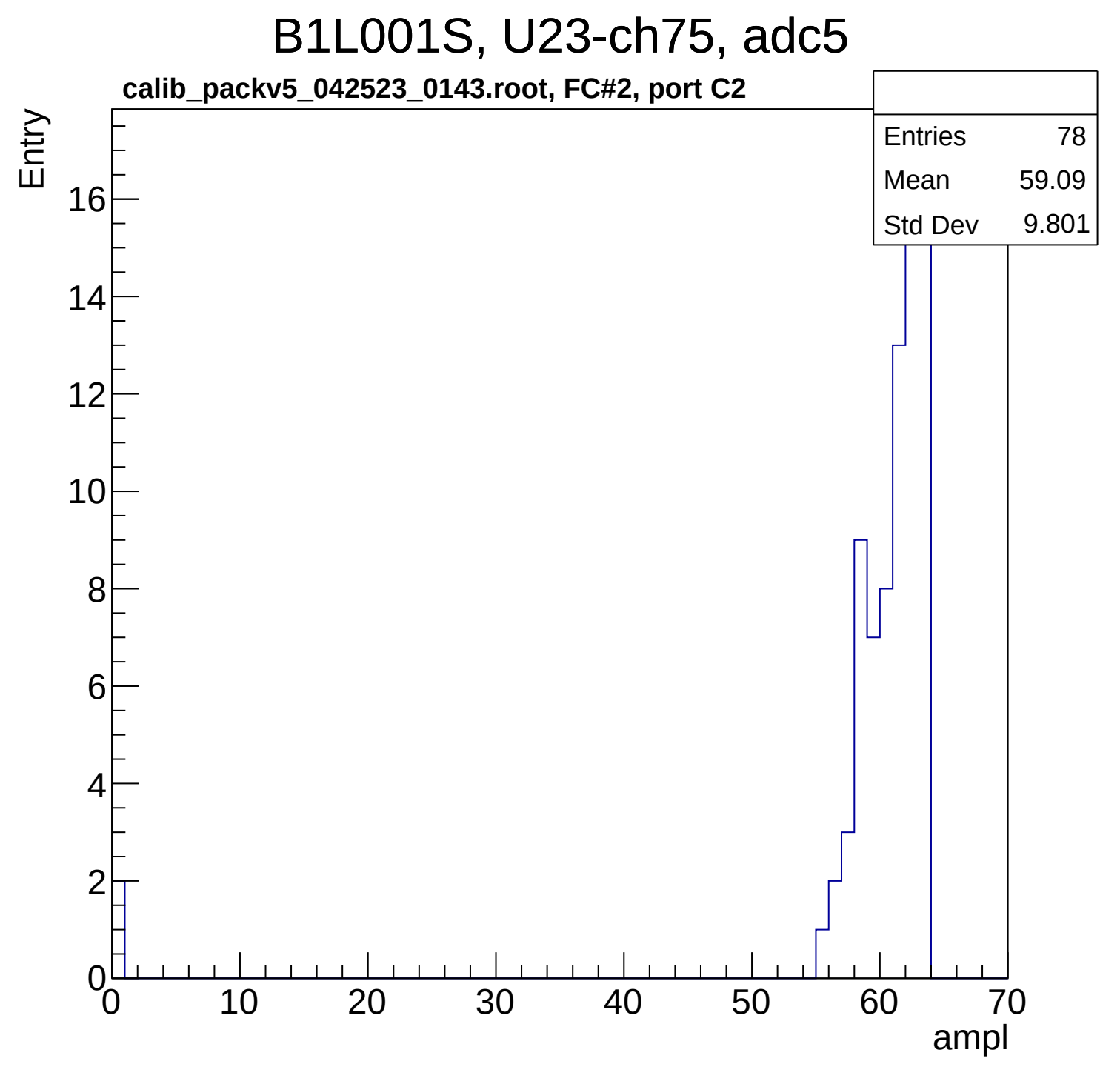
B1L001S, U23-ch75, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 78
Mean 59.09
Std Dev 9.801
Entry
ampl
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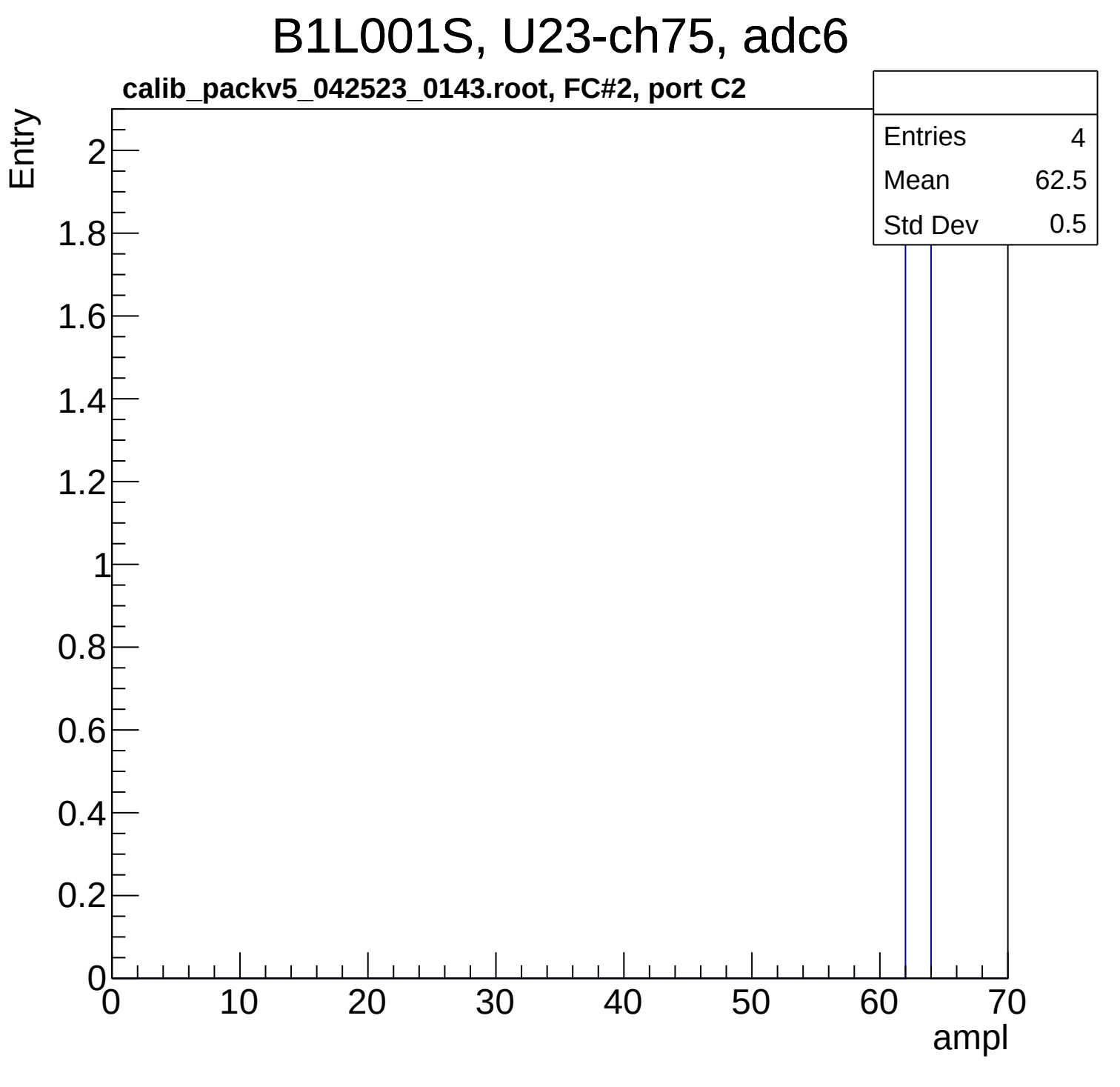
B1L001S, U23-ch75, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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60
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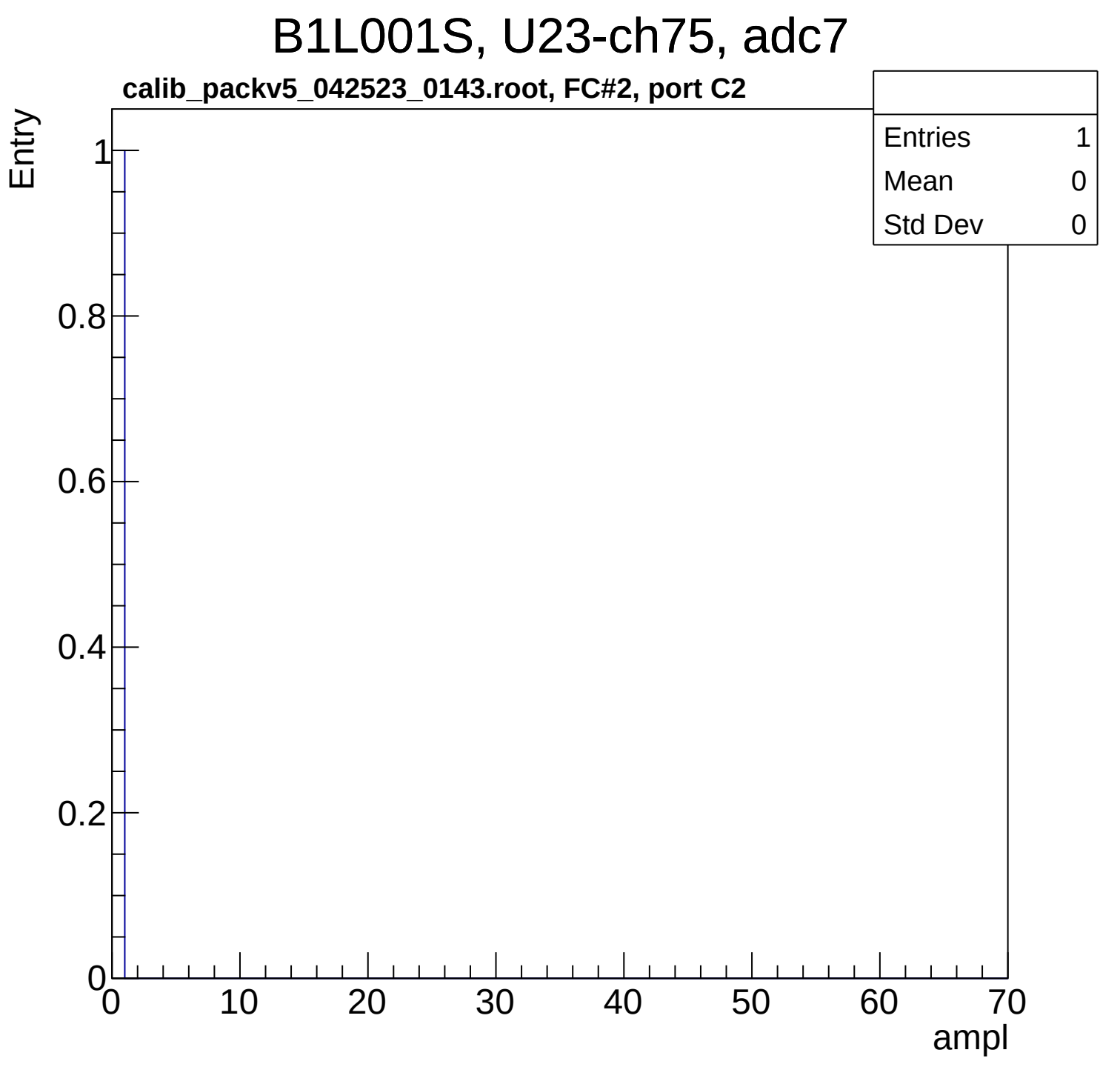
B1L001S, U23-ch75, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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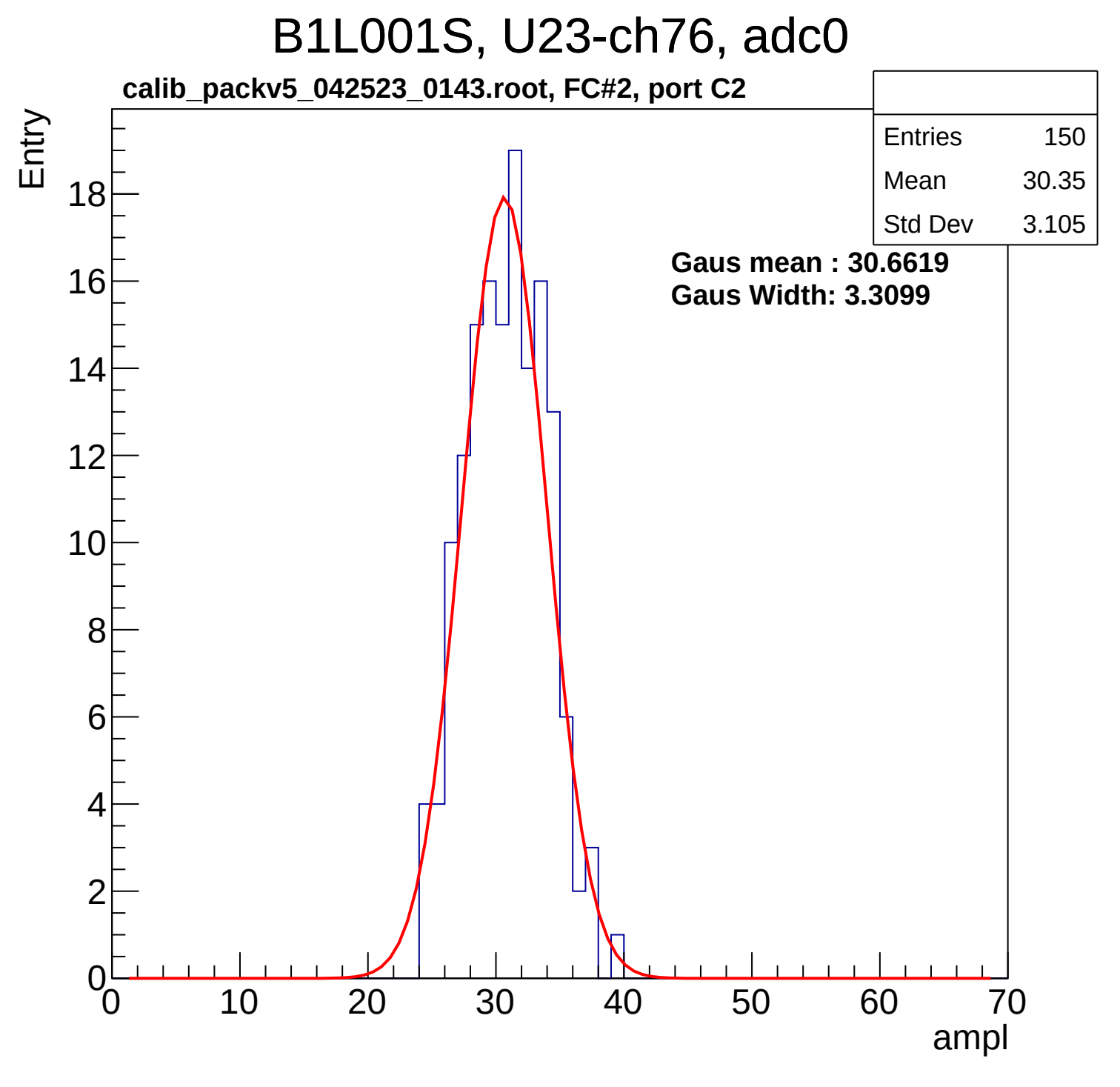
70
ampl


B1L001S, U23-ch76, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 30.35
Std Dev 3.105
Gaus mean : 30.6619
Gaus Width: 3.3099
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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40
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60
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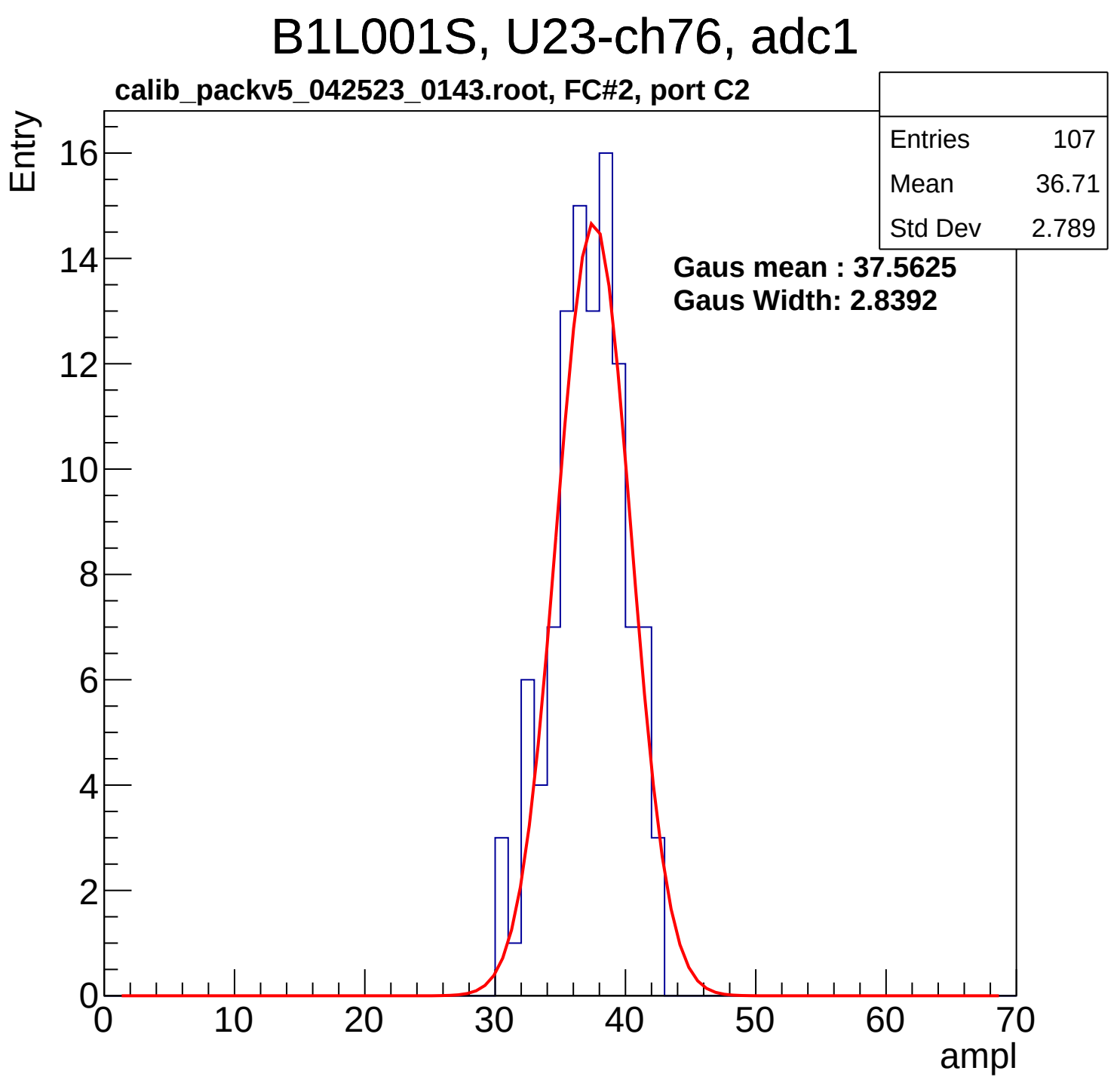
B1L001S, U23-ch76, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 36.71
Std Dev 2.789
Gaus mean : 37.5625
Gaus Width: 2.8392
Entry
ampl
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4
6
8
10
12
14
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0
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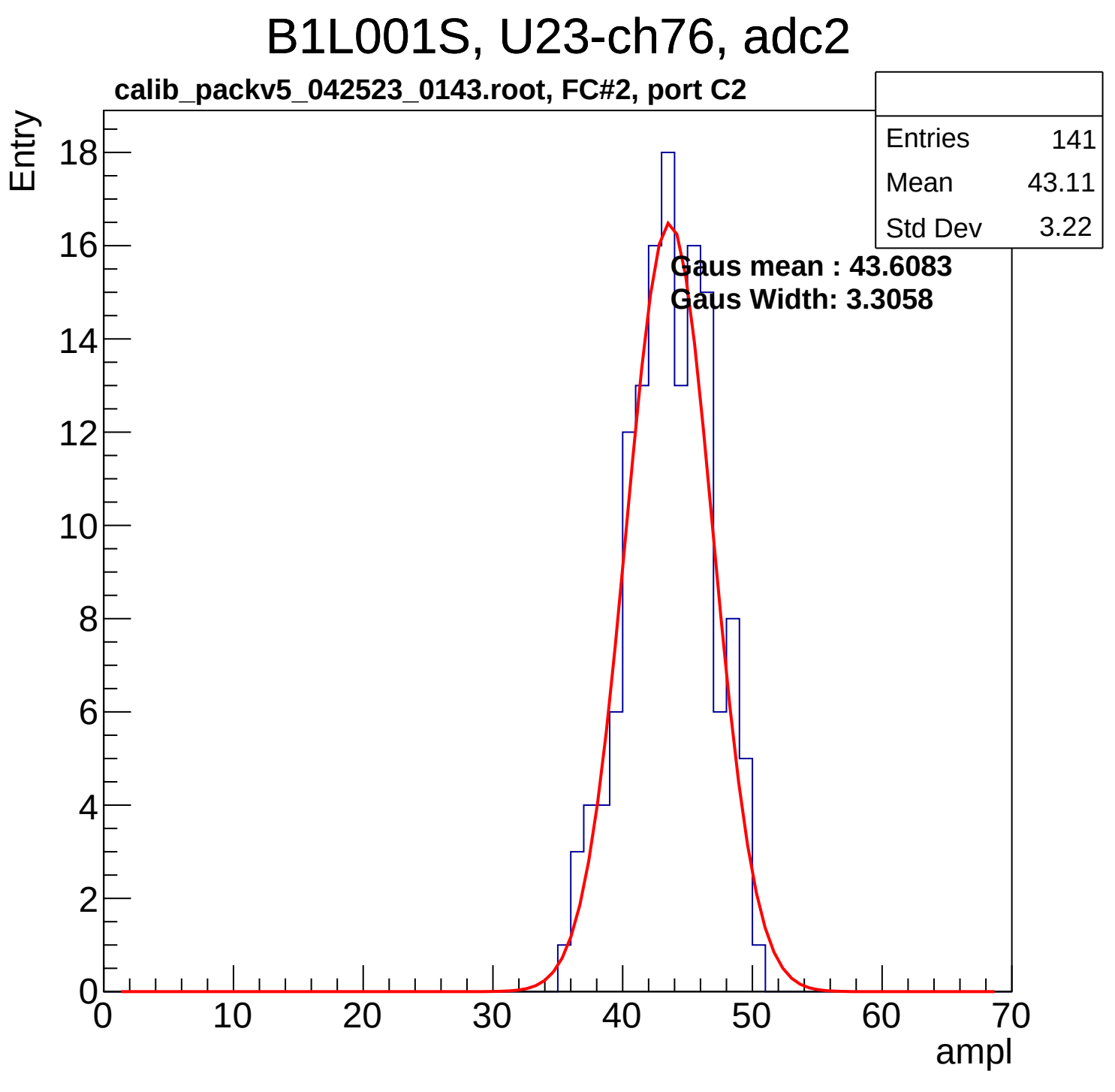
B1L001S, U23-ch76, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 43.11
Std Dev 3.22
Gaus mean : 43.6083
Gaus Width: 3.3058
Entry
ampl
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6
8
10
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14
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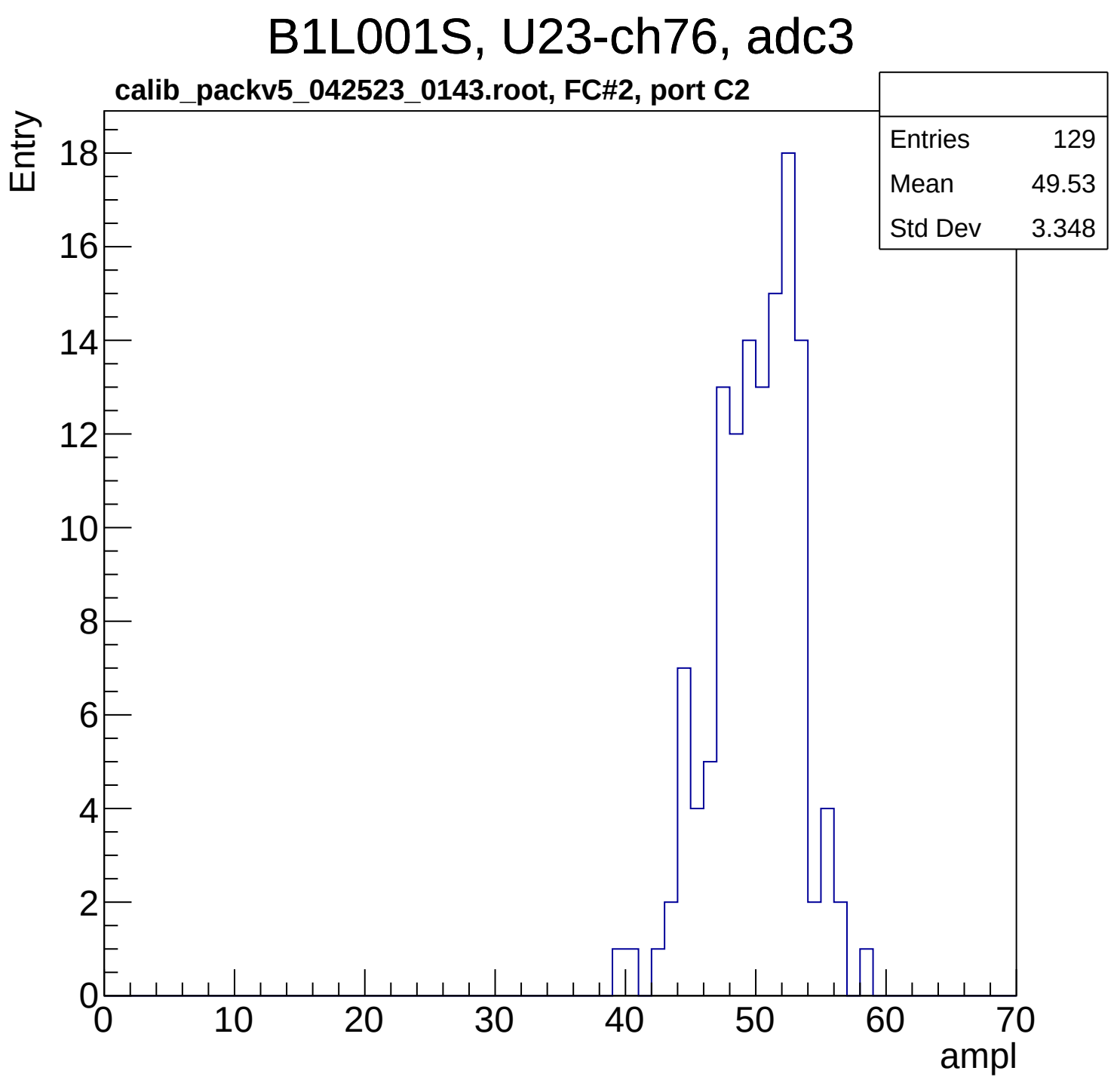
B1L001S, U23-ch76, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 49.53
Std Dev 3.348
Entry
ampl
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6
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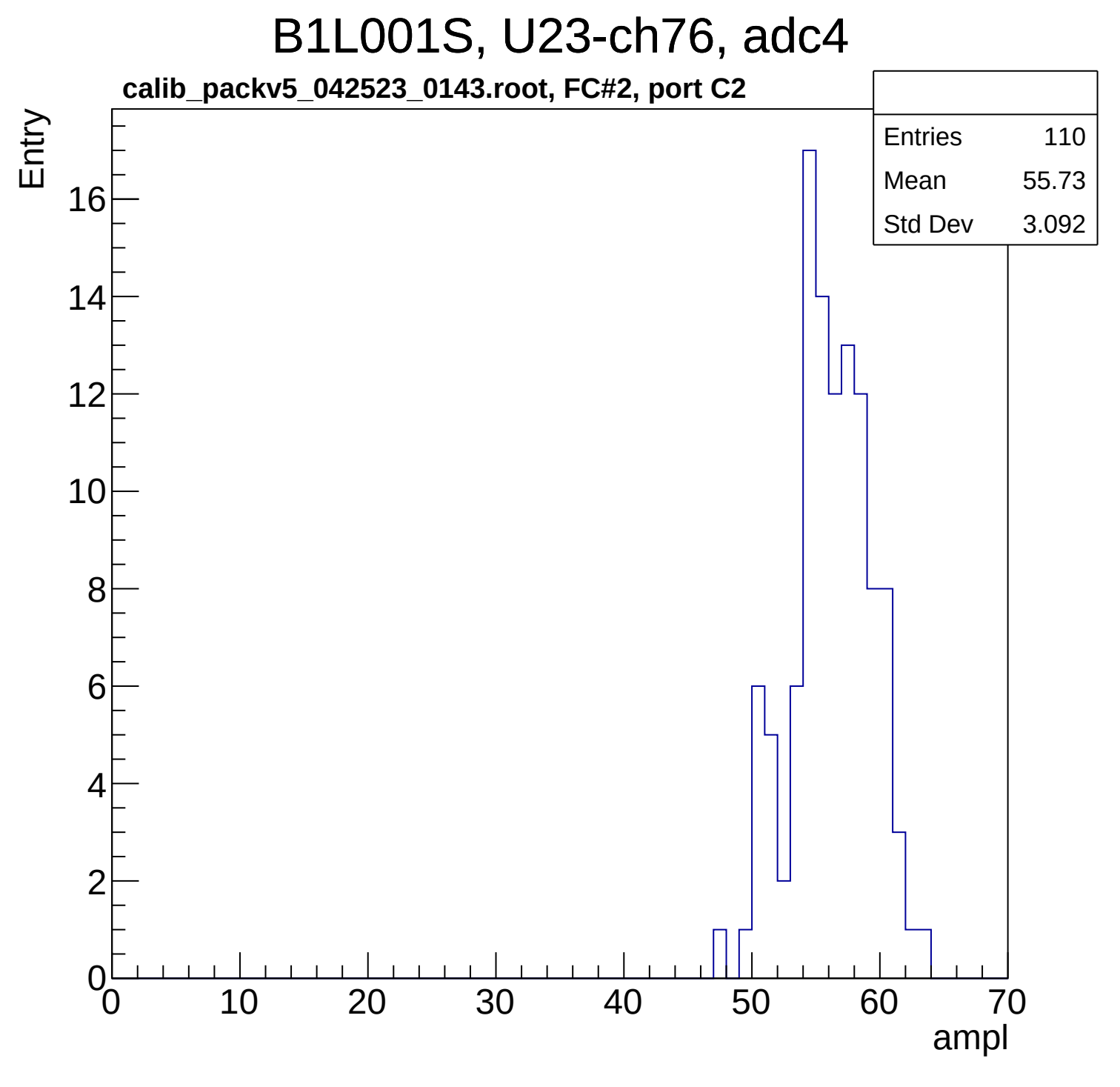
B1L001S, U23-ch76, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 55.73
Std Dev 3.092
Entry
ampl
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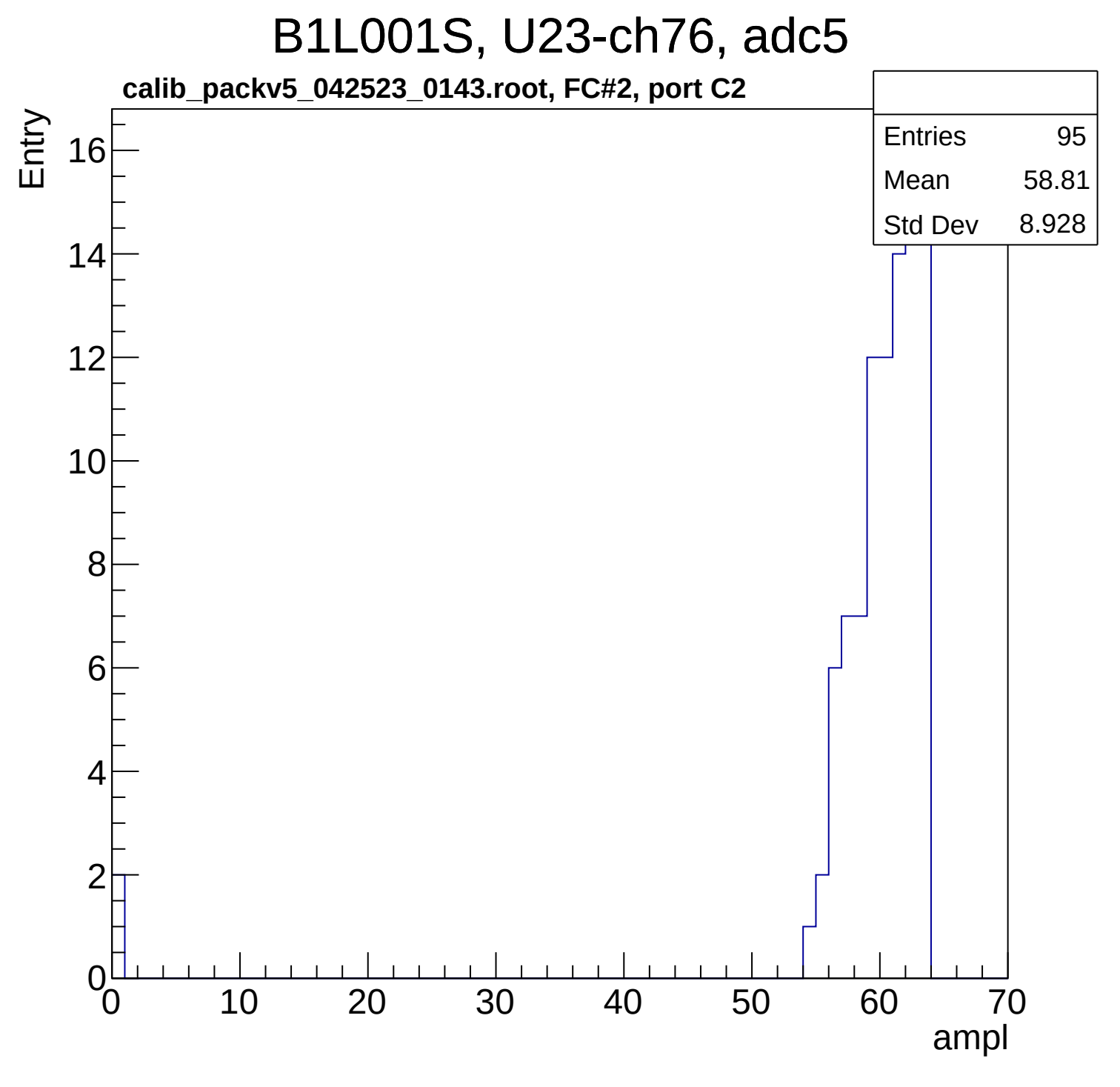
B1L001S, U23-ch76, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 95
Mean 58.81
Std Dev 8.928
Entry
ampl
0
2
4
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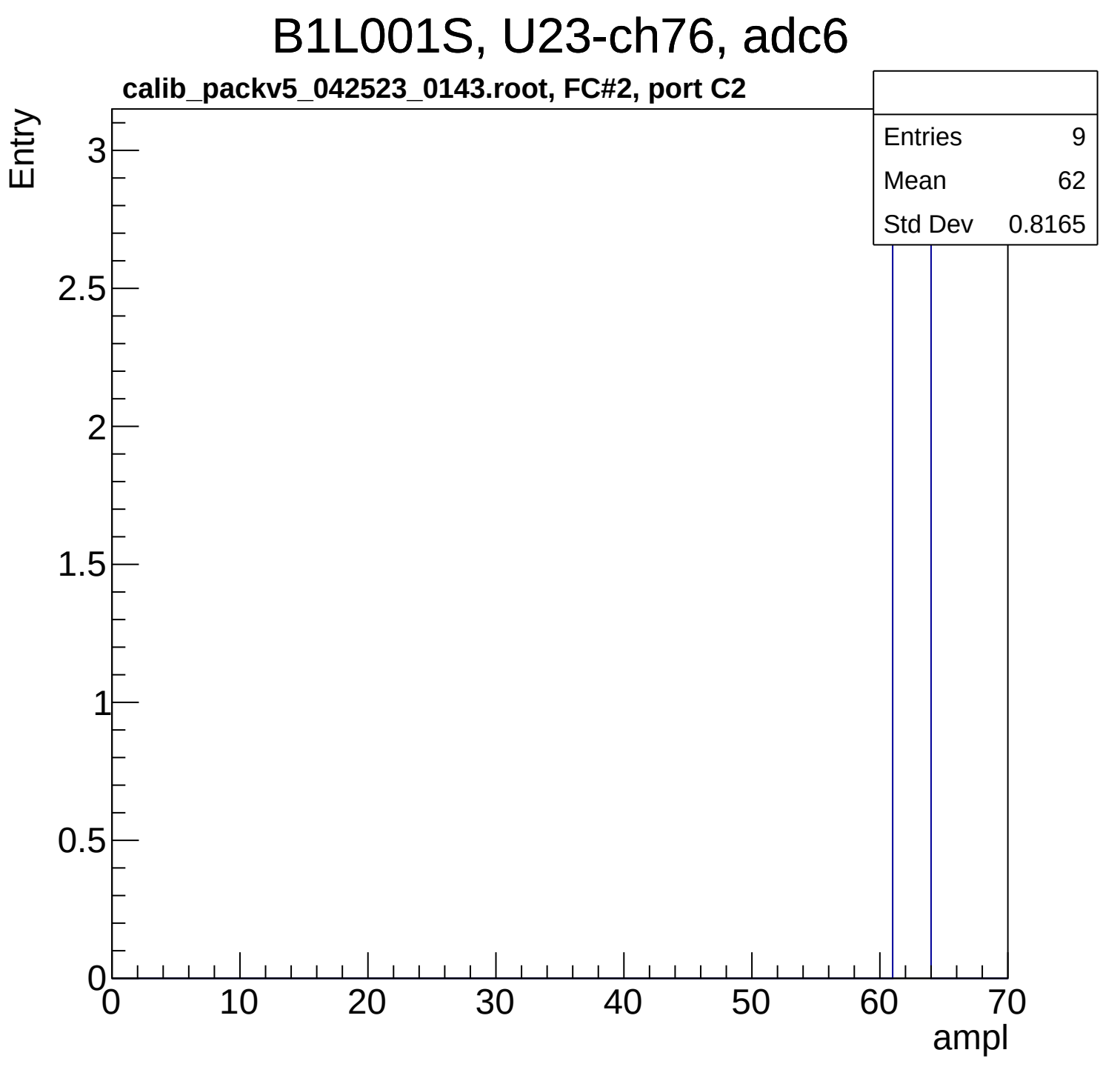
B1L001S, U23-ch76, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 62
Std Dev 0.8165
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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60
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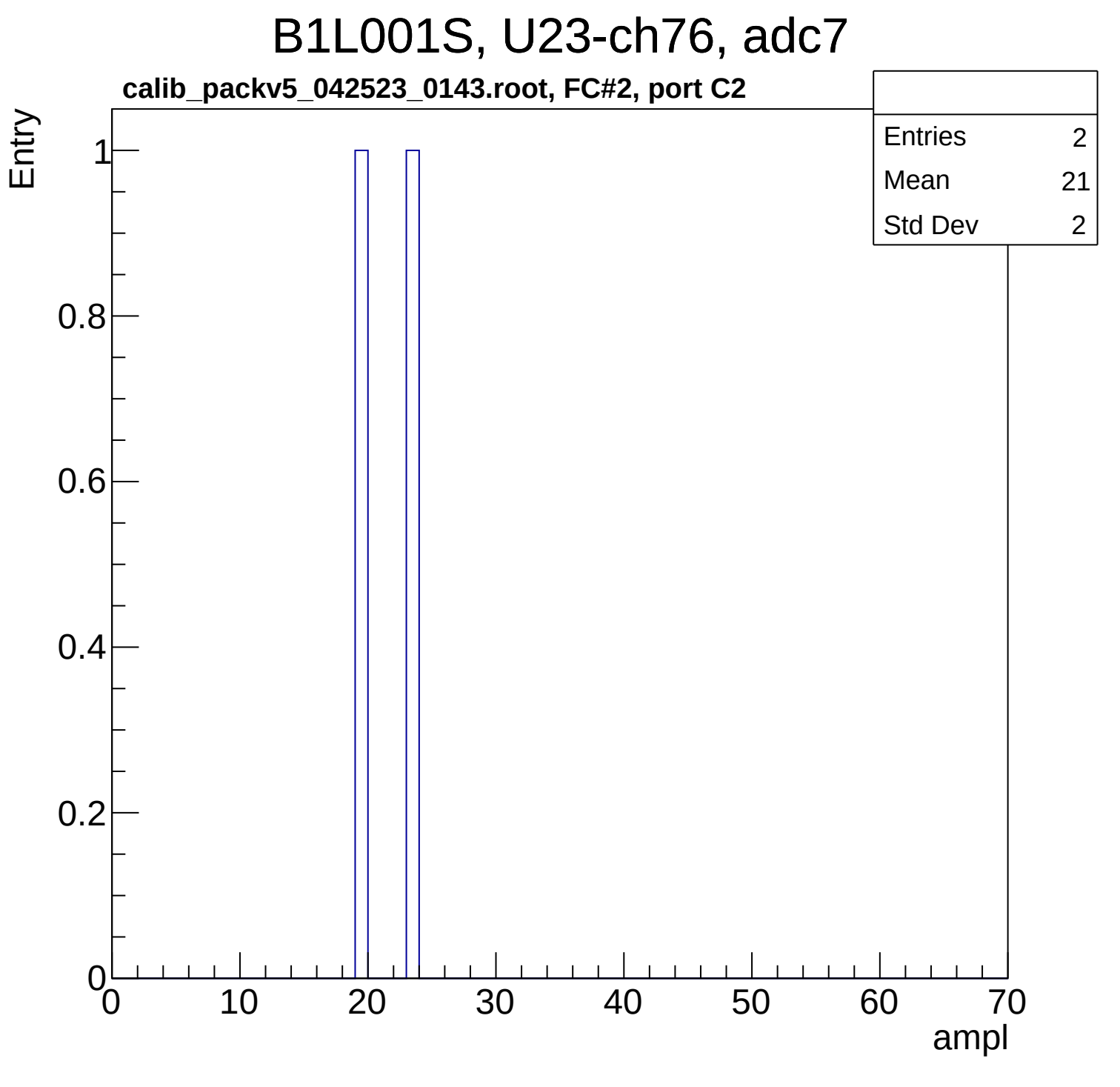
B1L001S, U23-ch76, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 21
Std Dev 2
Entry
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0.6
0.4
0.2
0
0
10
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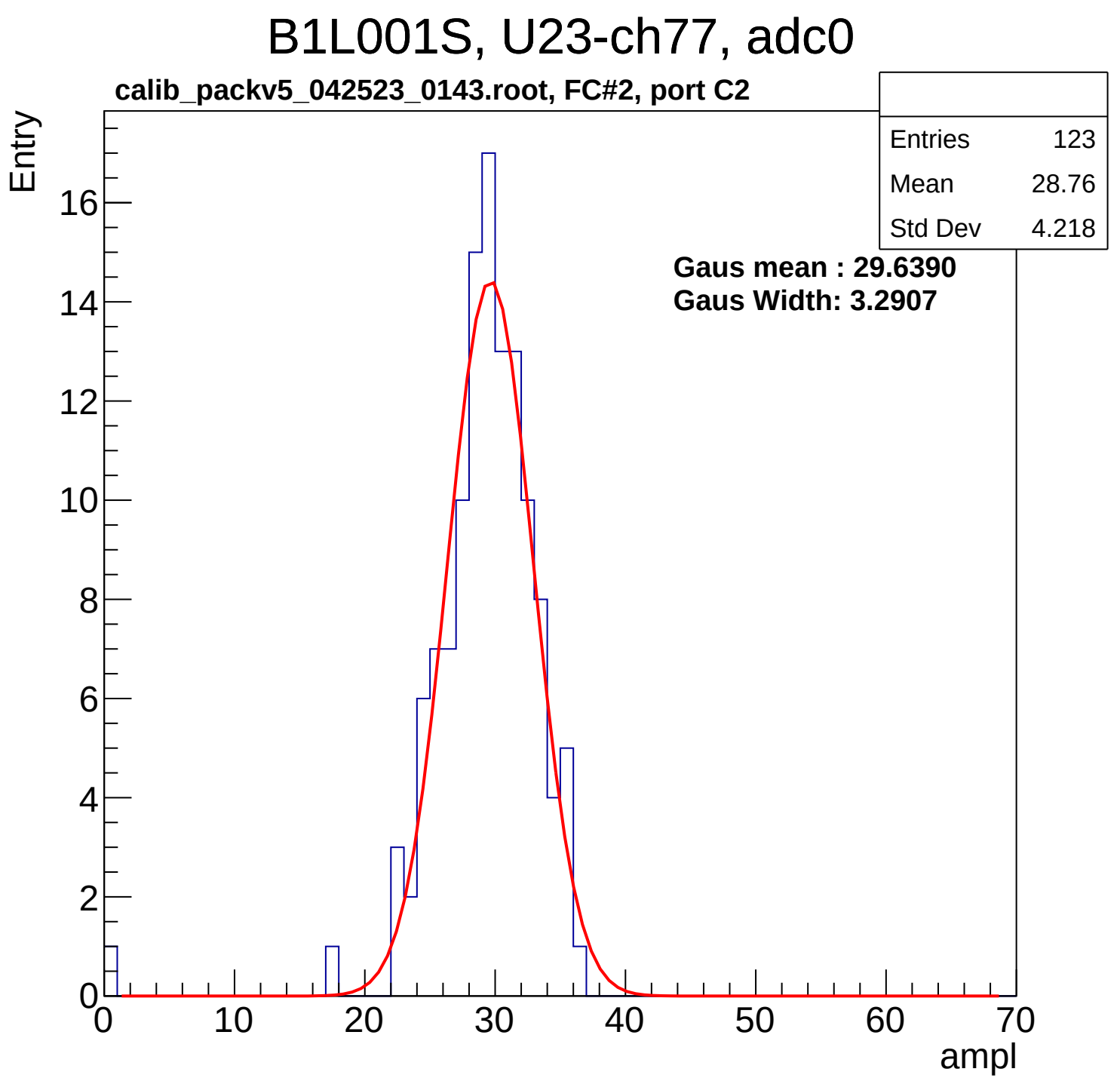
50
60
70
ampl


B1L001S, U23-ch77, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 28.76
Std Dev 4.218
Gaus mean : 29.6390
Gaus Width: 3.2907
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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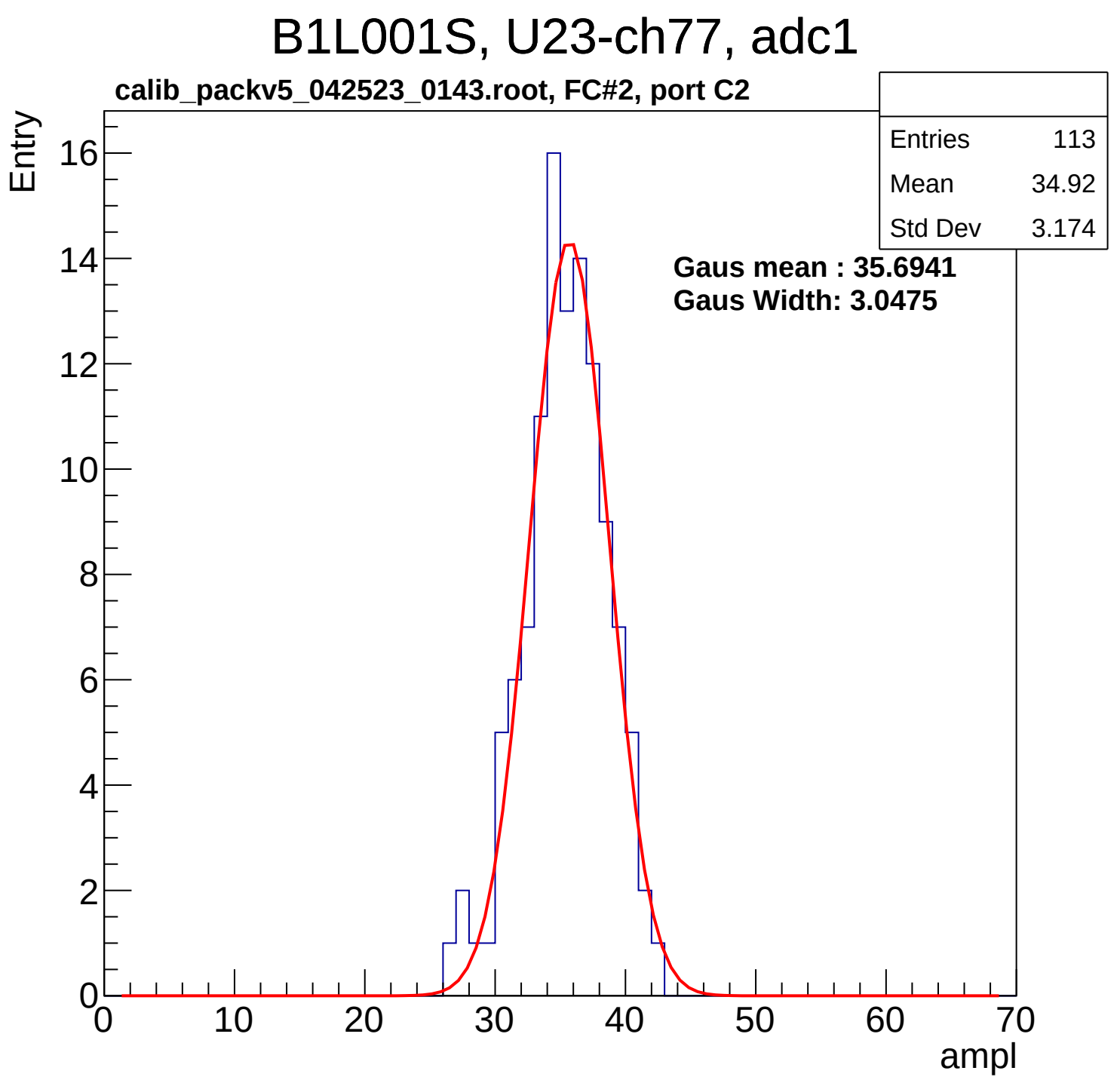
B1L001S, U23-ch77, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 34.92
Std Dev 3.174
Gaus mean : 35.6941
Gaus Width: 3.0475
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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40
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60
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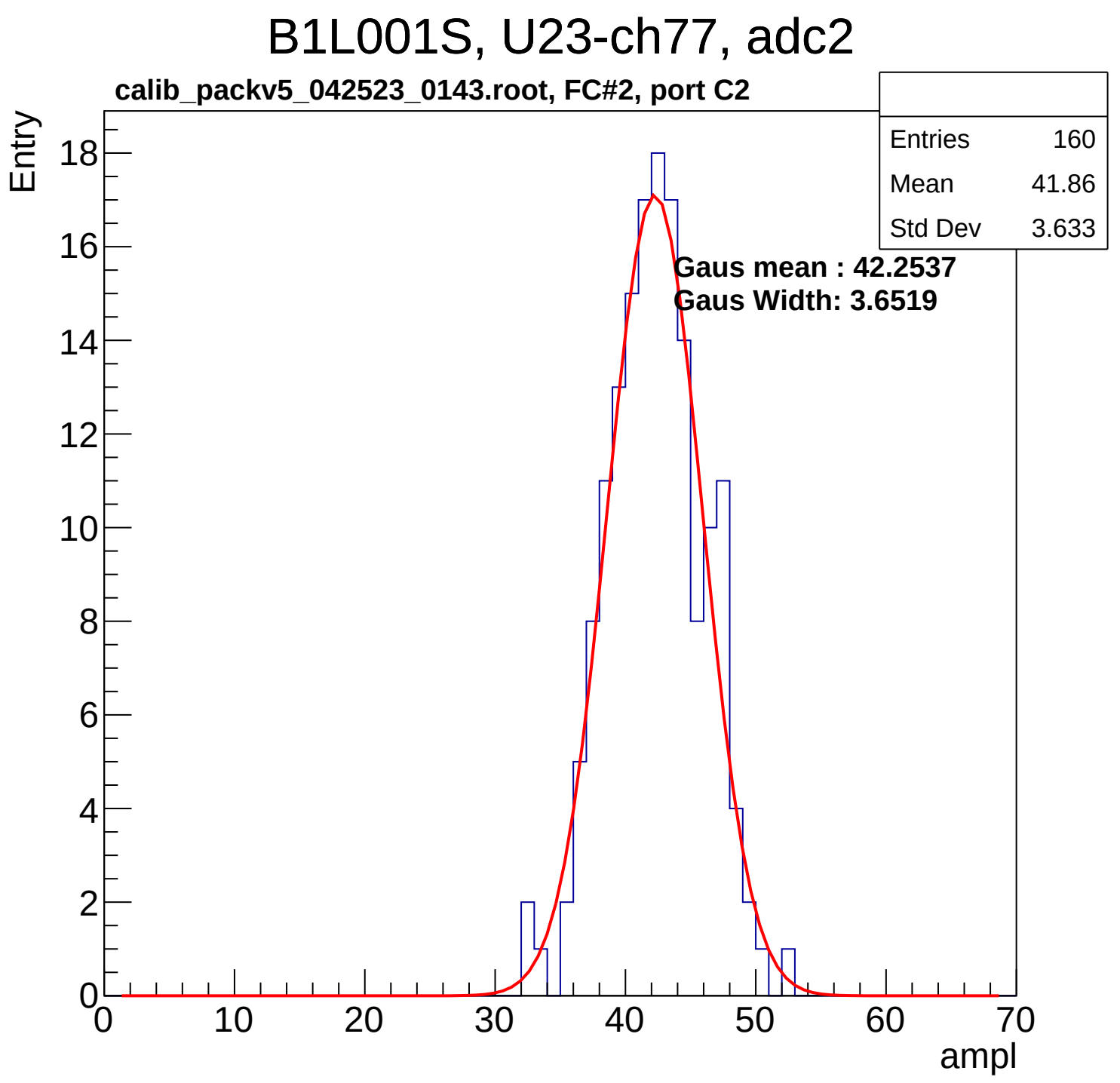
B1L001S, U23-ch77, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 41.86
Std Dev 3.633
Gaus mean : 42.2537
Gaus Width: 3.6519
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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20
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40
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60
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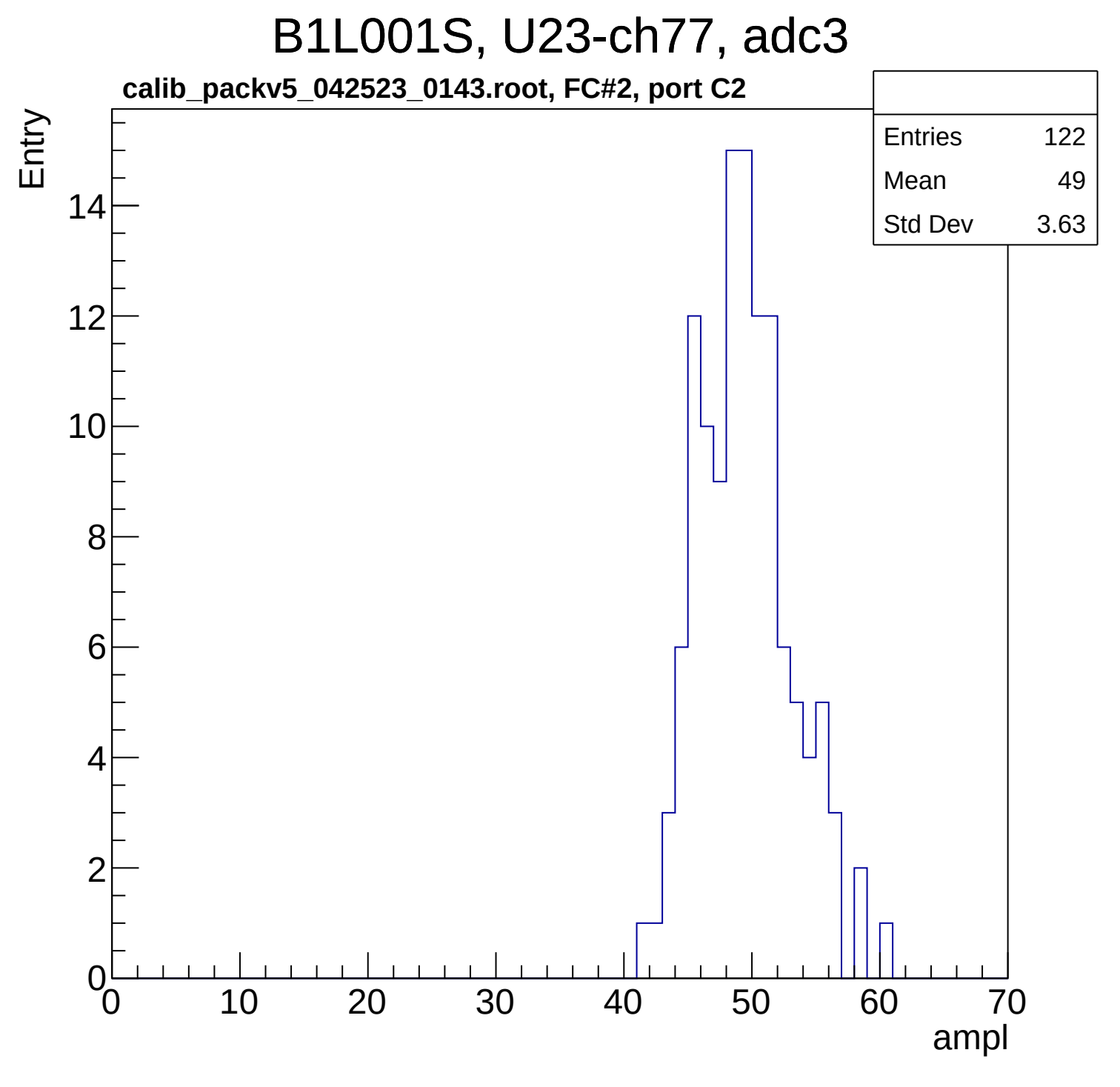
B1L001S, U23-ch77, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 49
Std Dev 3.63
Entry
0
2
4
6
8
10
12
14
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10
20
30
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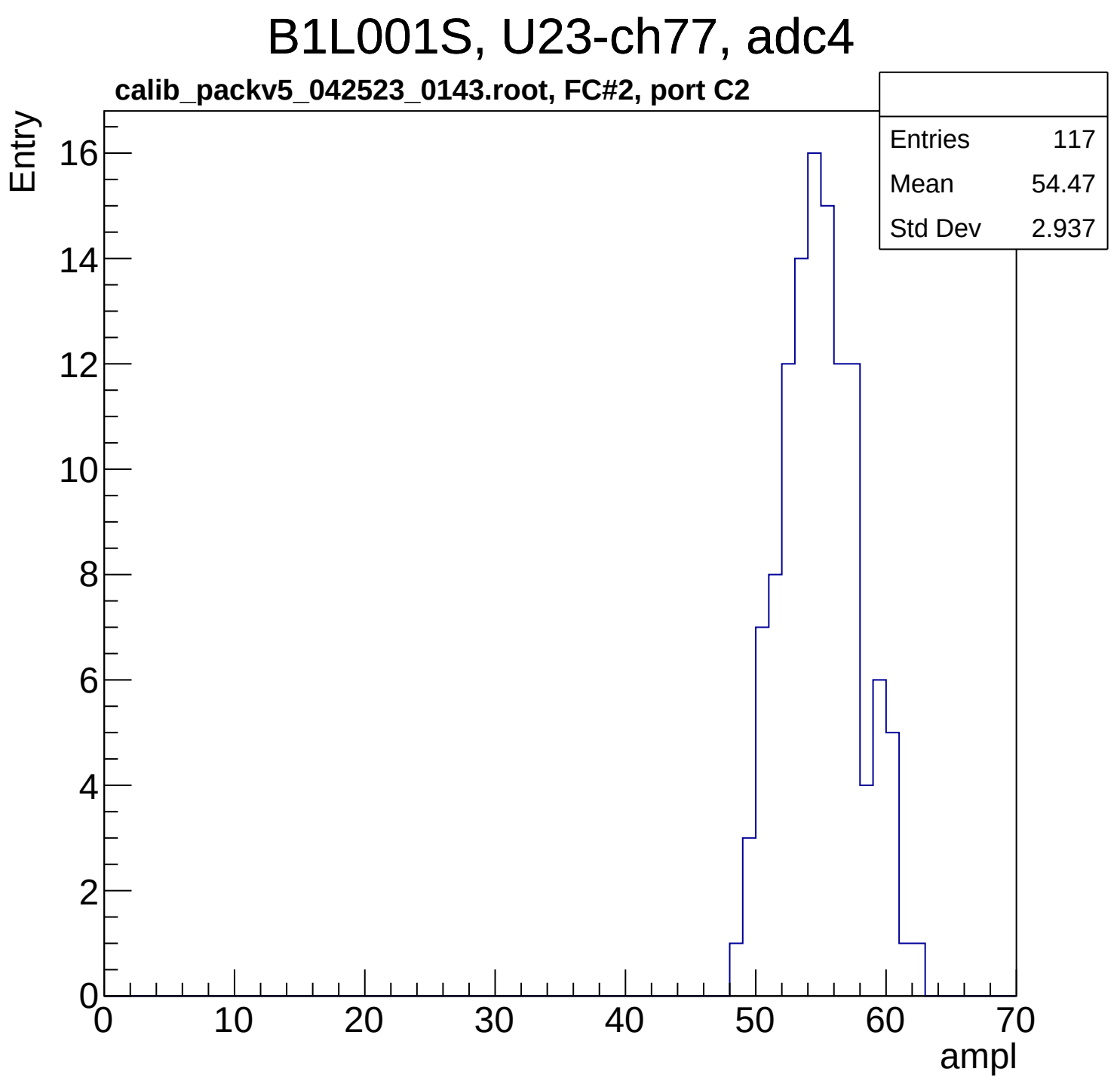
50
60
70
ampl


B1L001S, U23-ch77, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 54.47
Std Dev 2.937
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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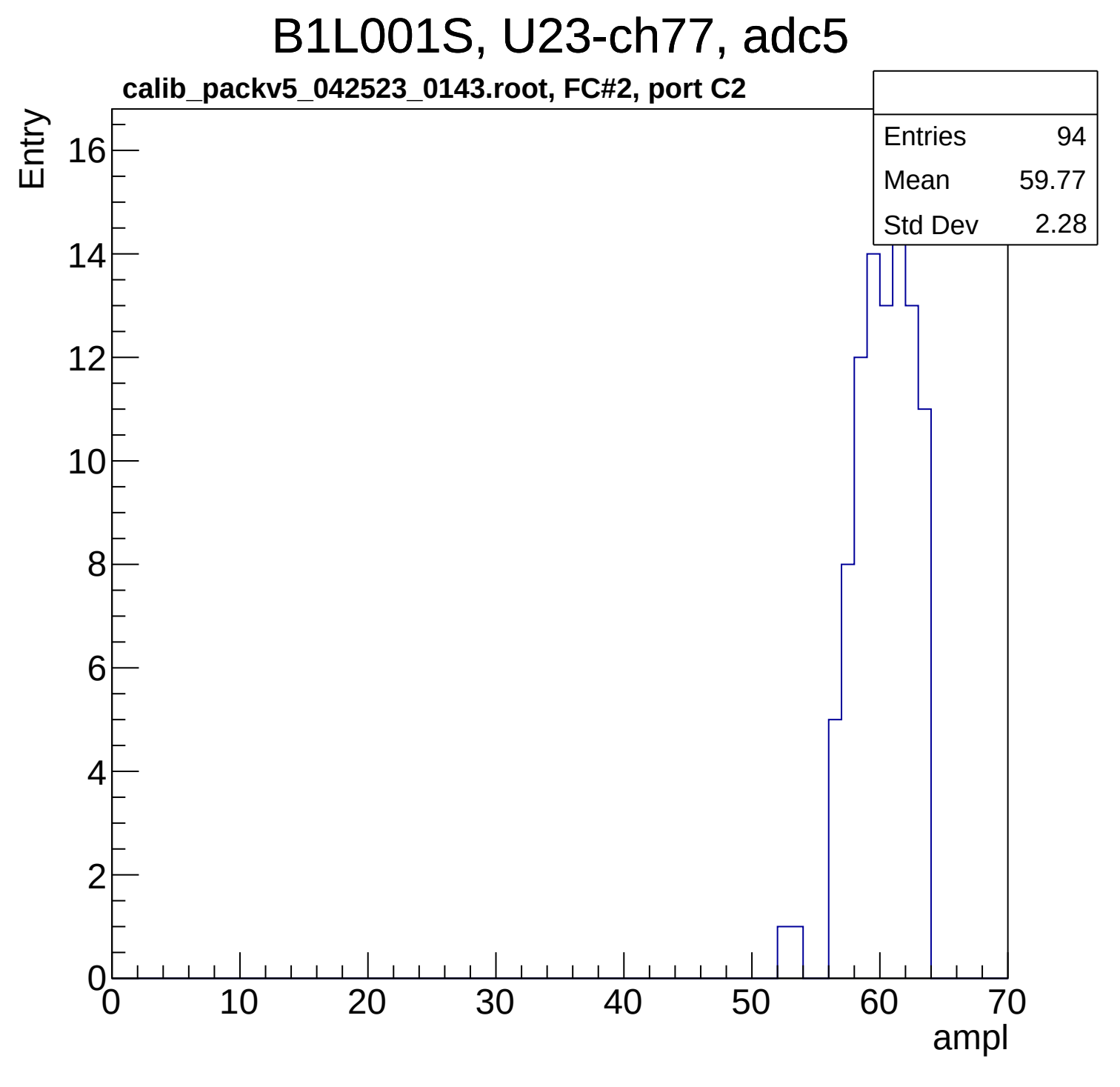
B1L001S, U23-ch77, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.77
Std Dev 2.28
Entry
ampl
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2
4
6
8
10
12
14
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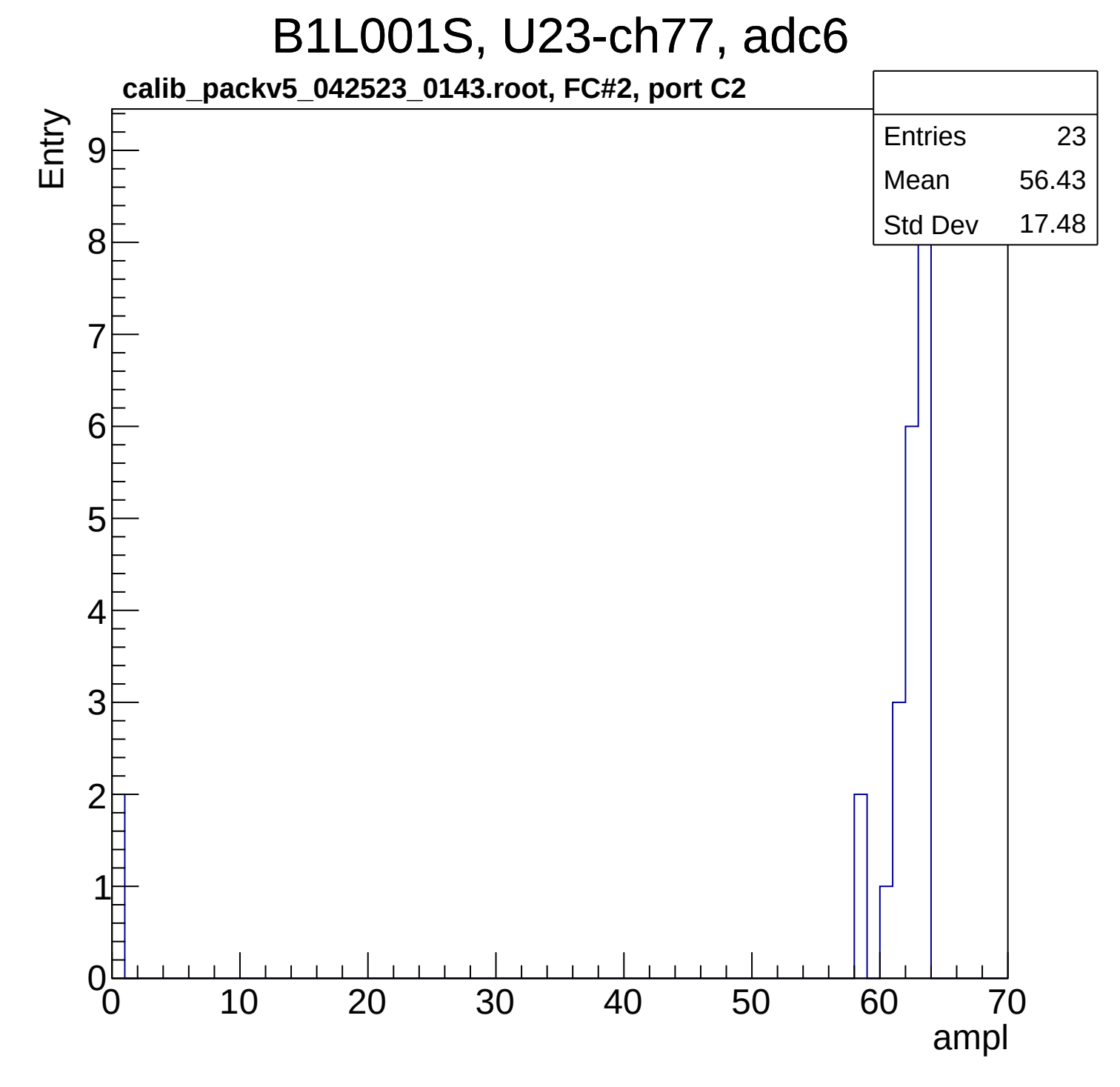
B1L001S, U23-ch77, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 23
Mean 56.43
Std Dev 17.48
Entry
ampl
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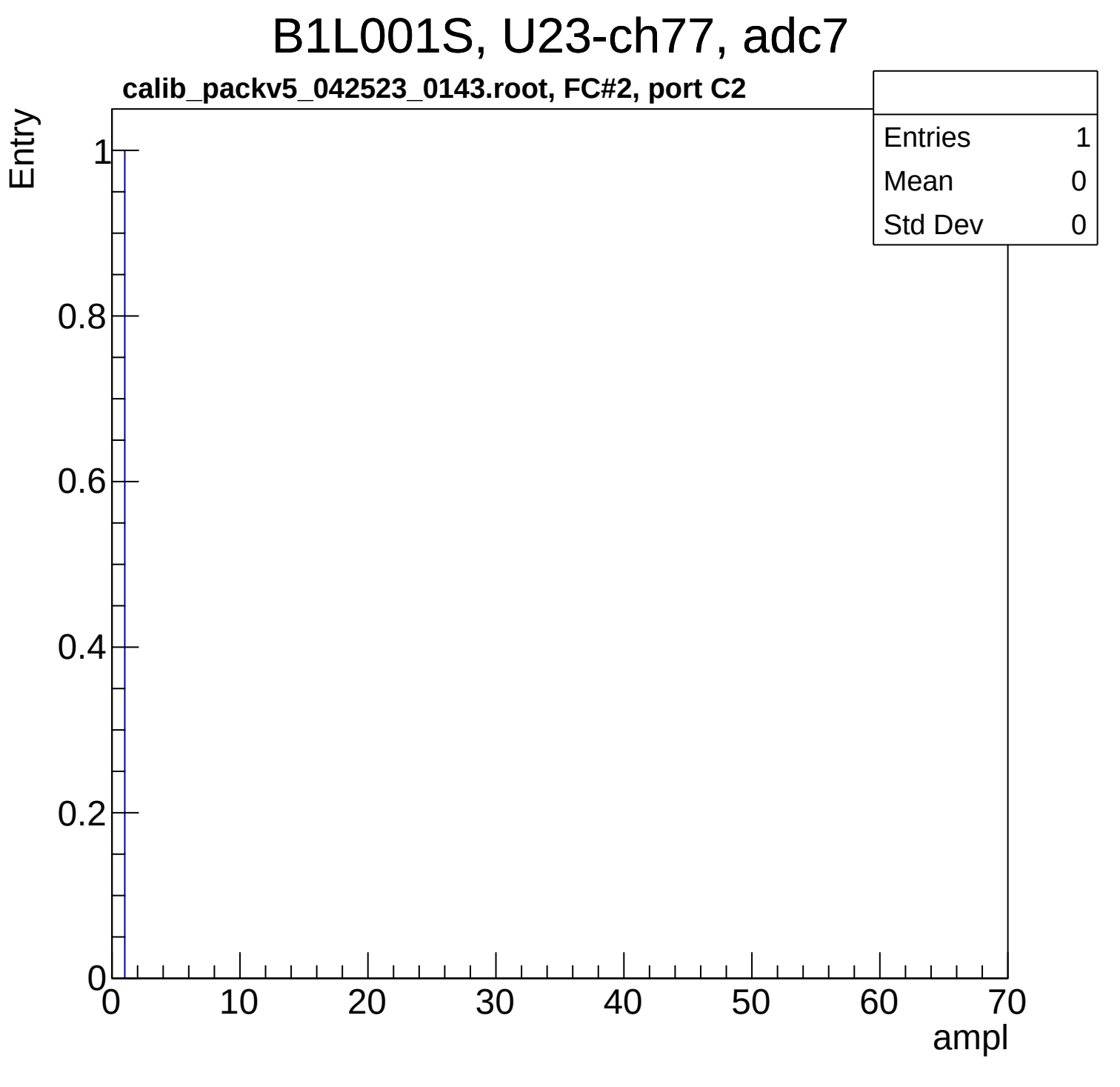
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L001S, U23-ch77, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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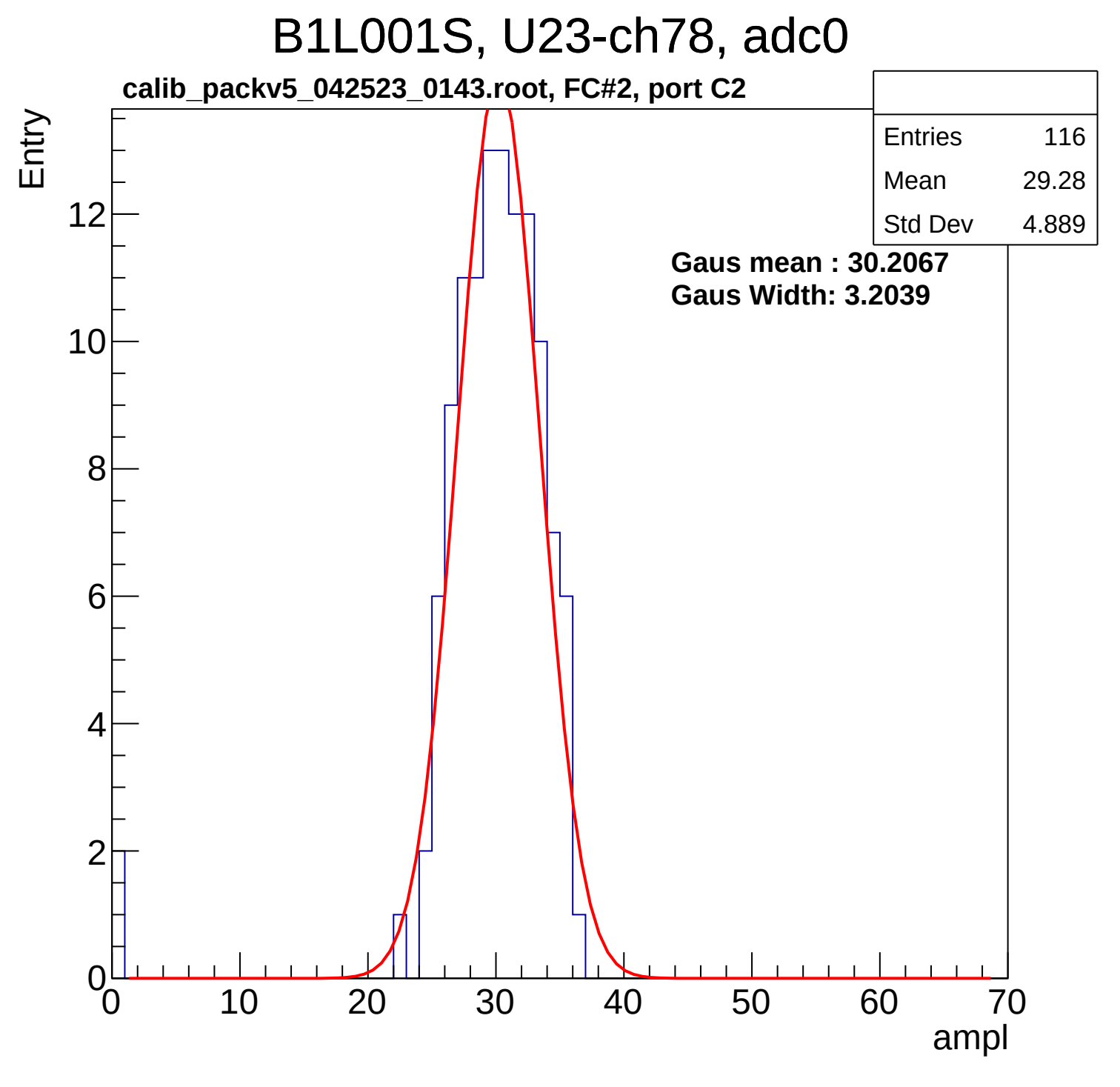
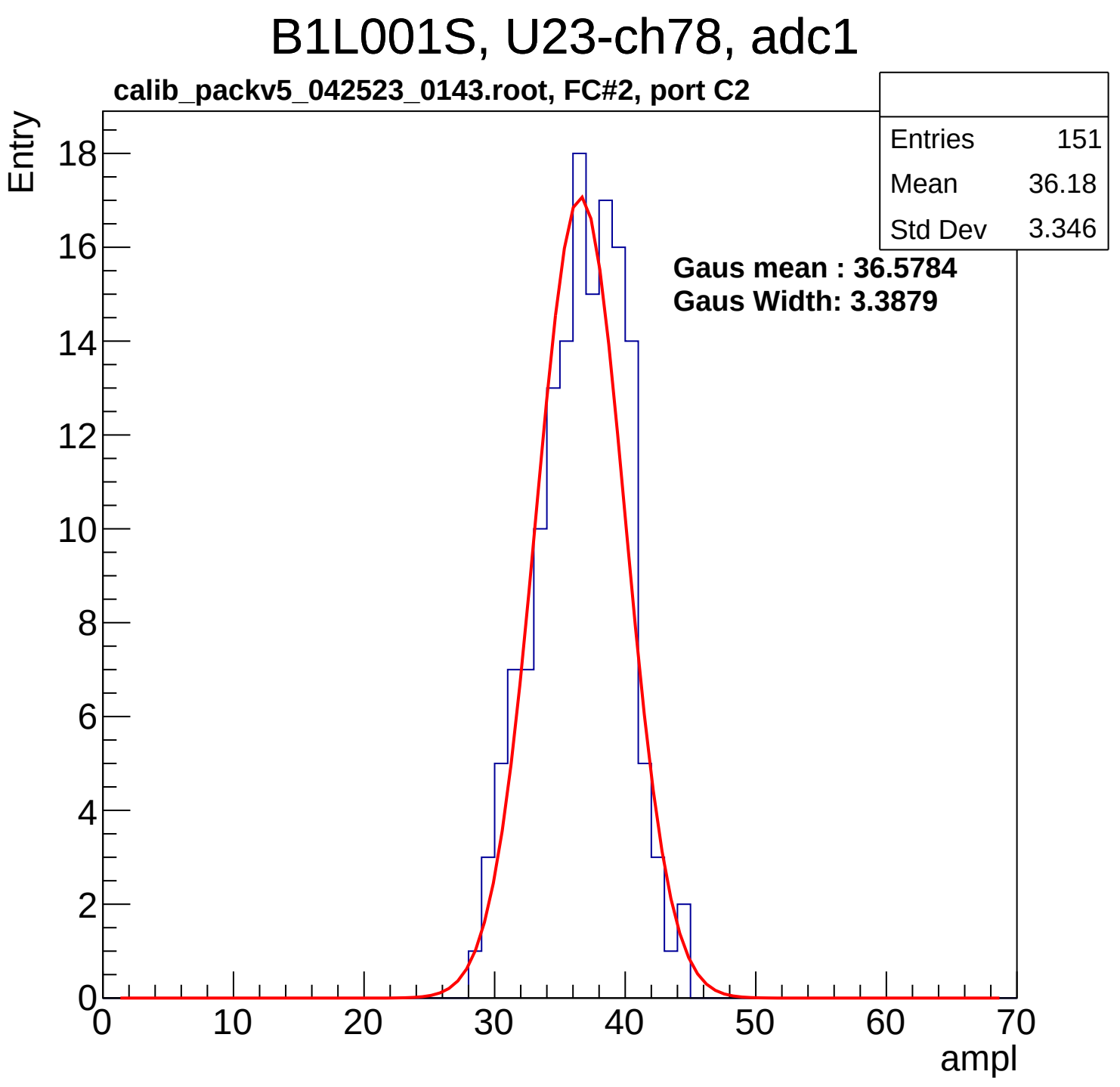
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch78, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 29.28
Std Dev 4.889
Gaus mean : 30.2067
Gaus Width: 3.2039
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U23-ch78, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 36.18
Std Dev 3.346
Gaus mean : 36.5784
Gaus Width: 3.3879
Entry
ampl
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2
4
6
8
10
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14
16
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0
10
20
30
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60
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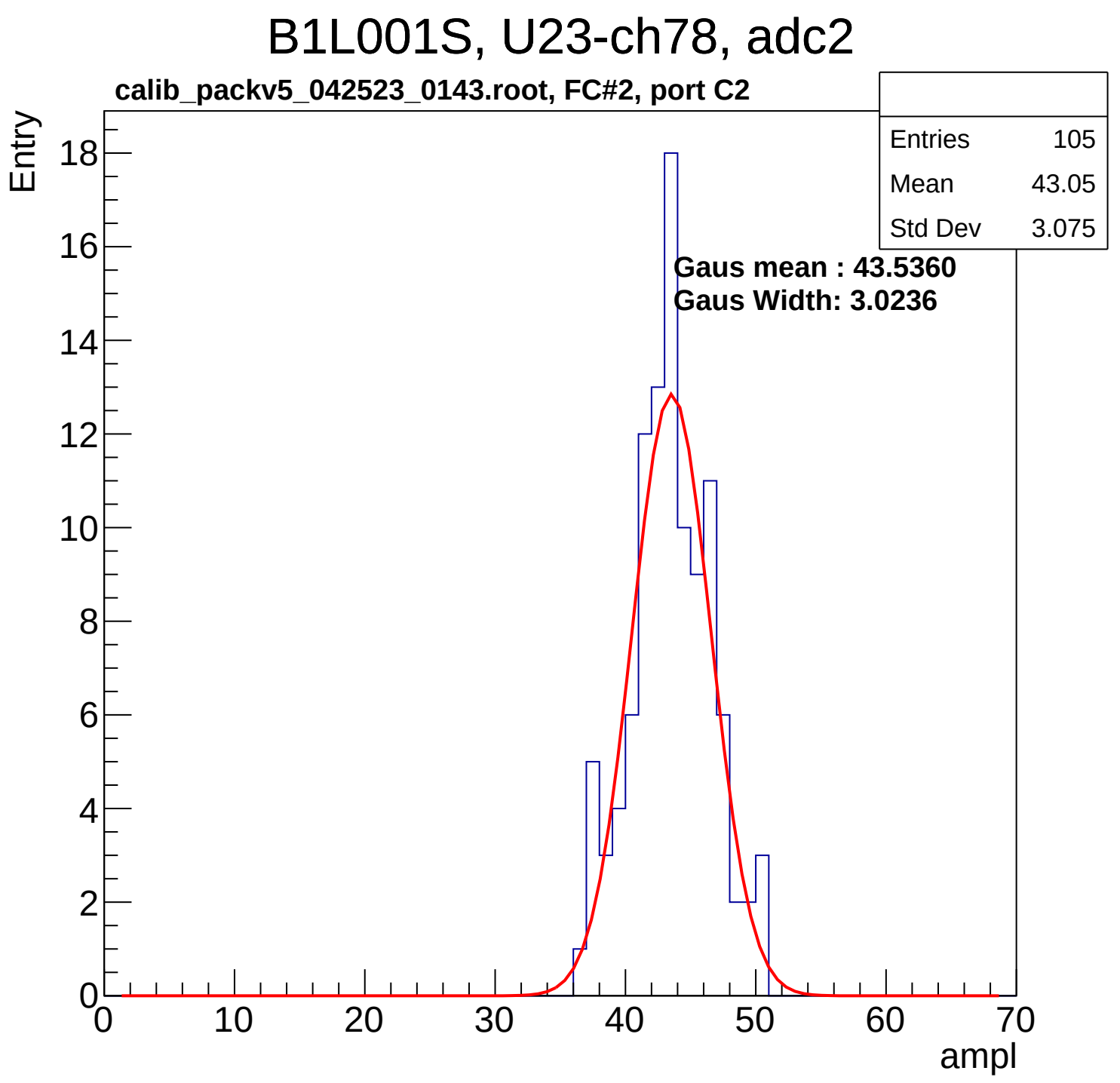
B1L001S, U23-ch78, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 43.05
Std Dev 3.075
Gaus mean : 43.5360
Gaus Width: 3.0236
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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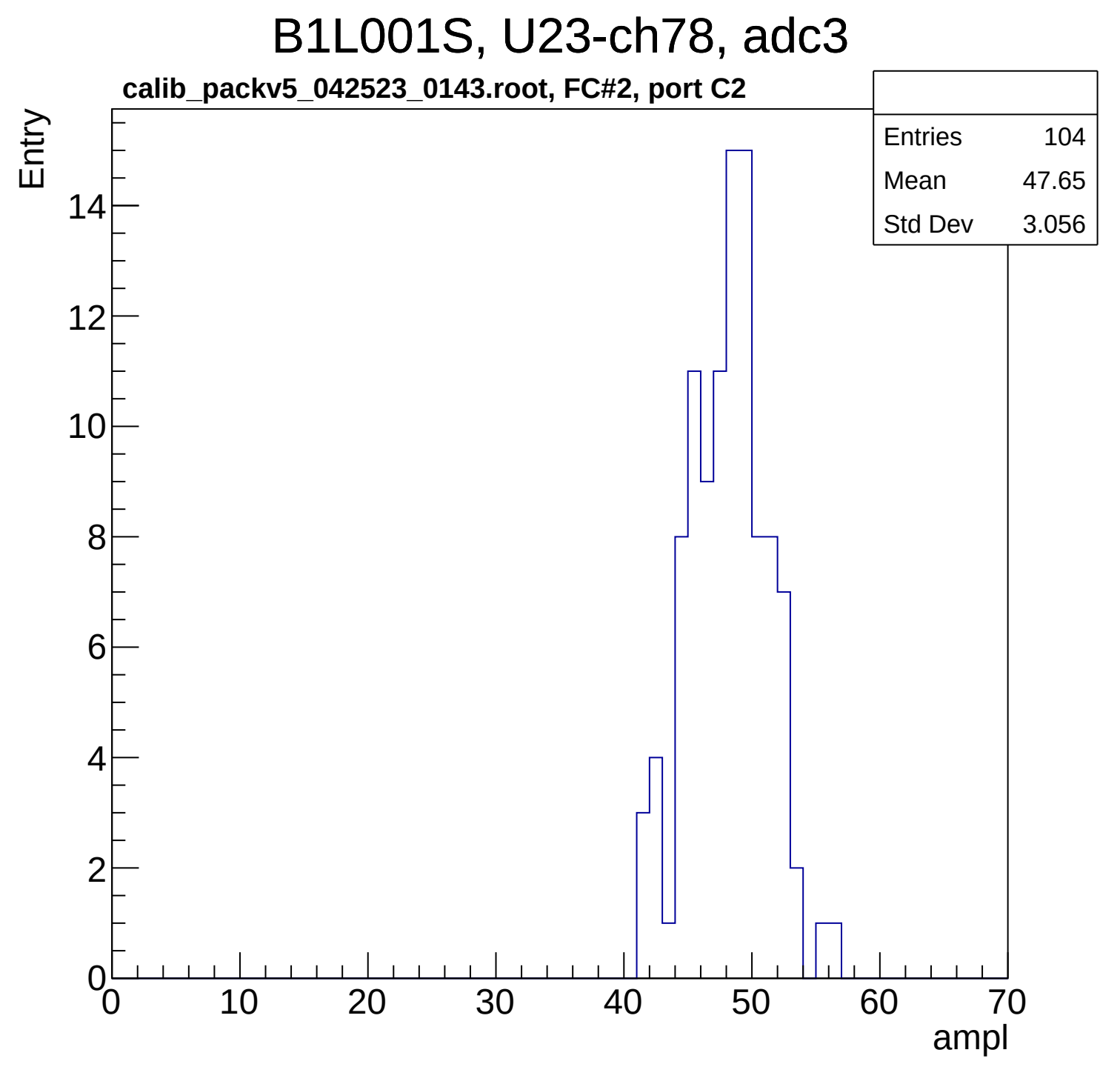
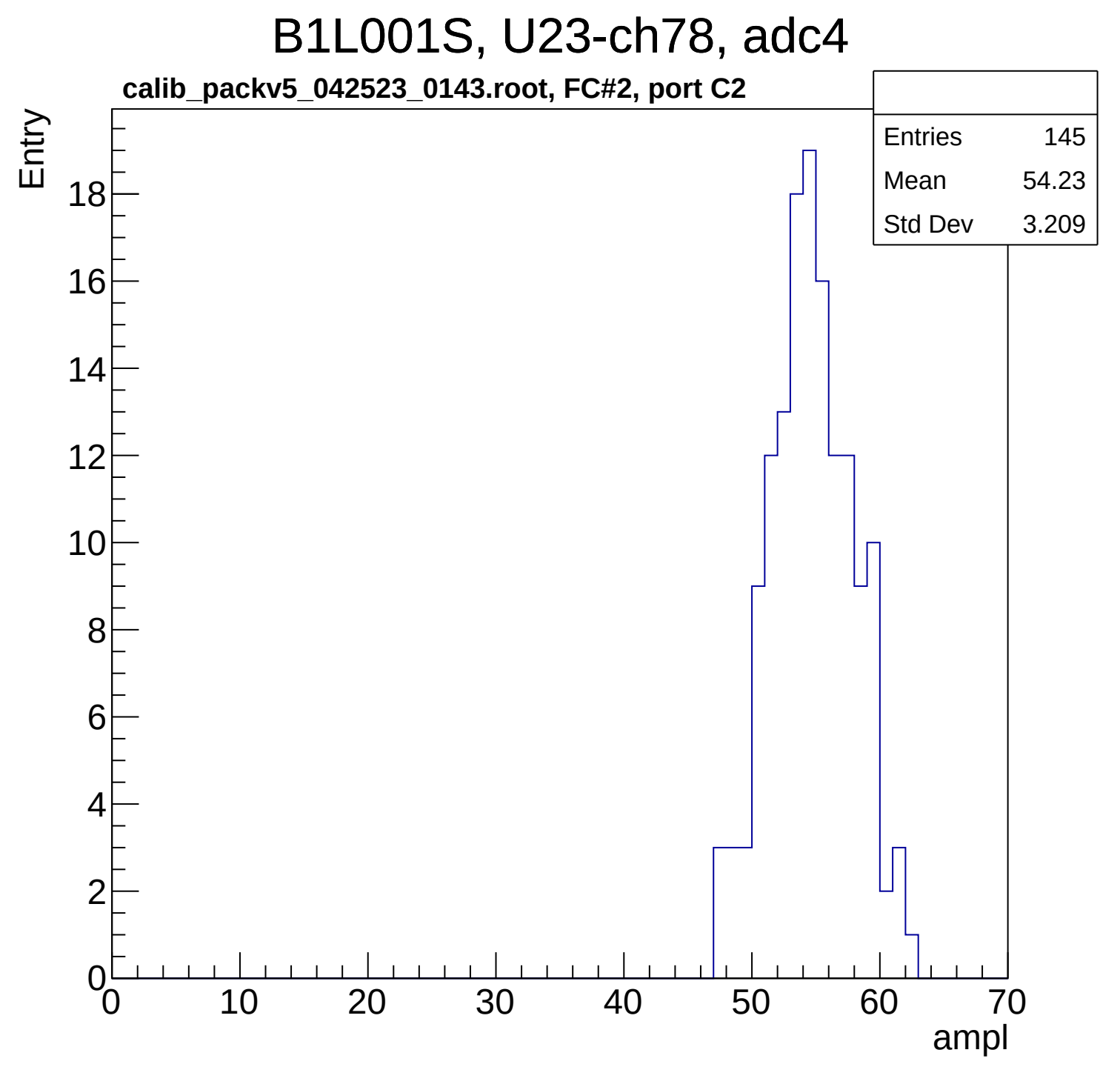

B1L001S, U23-ch78, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 47.65
Std Dev 3.056
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch78, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 54.23
Std Dev 3.209
Entry
ampl
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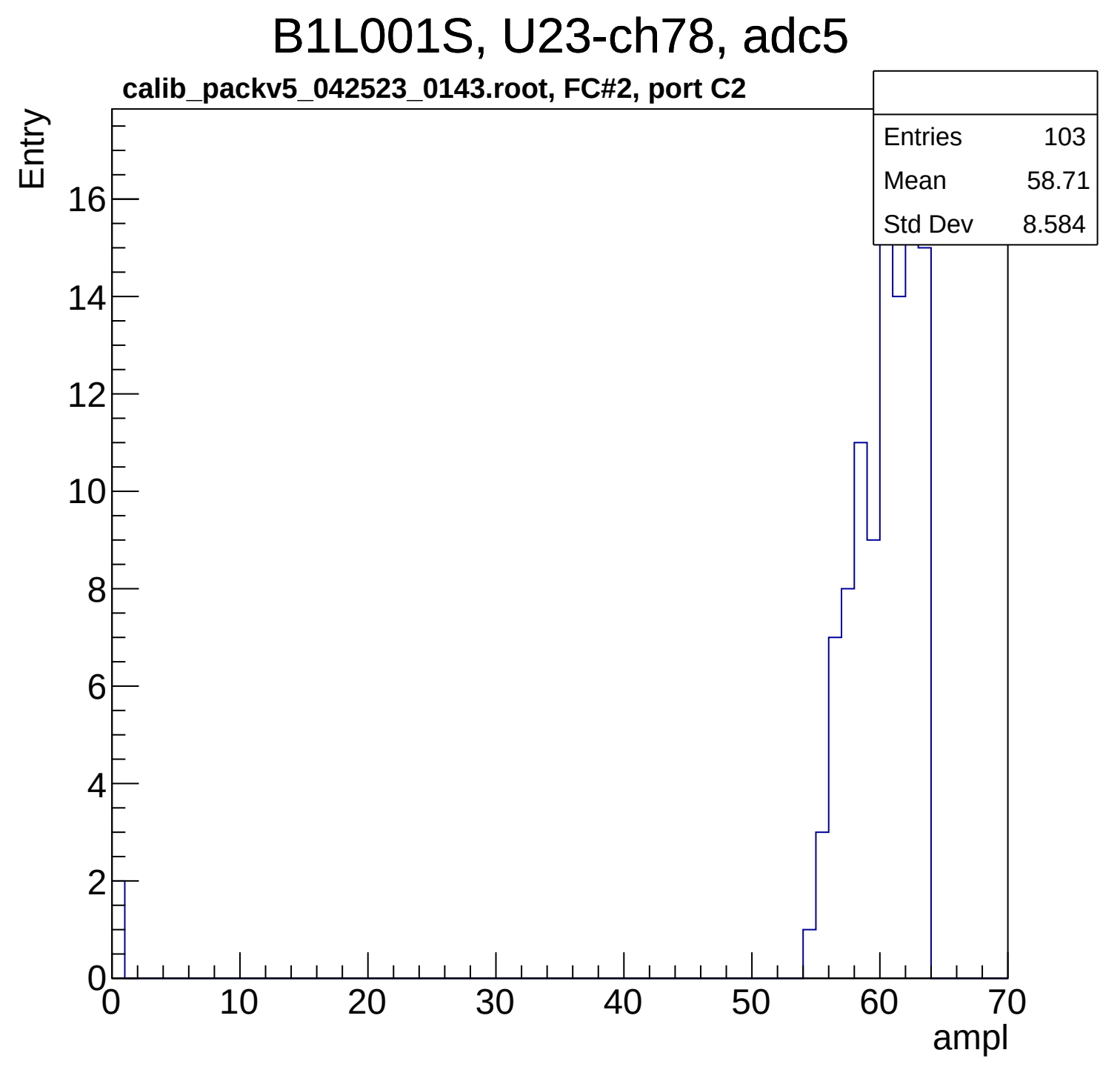
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch78, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 58.71
Std Dev 8.584
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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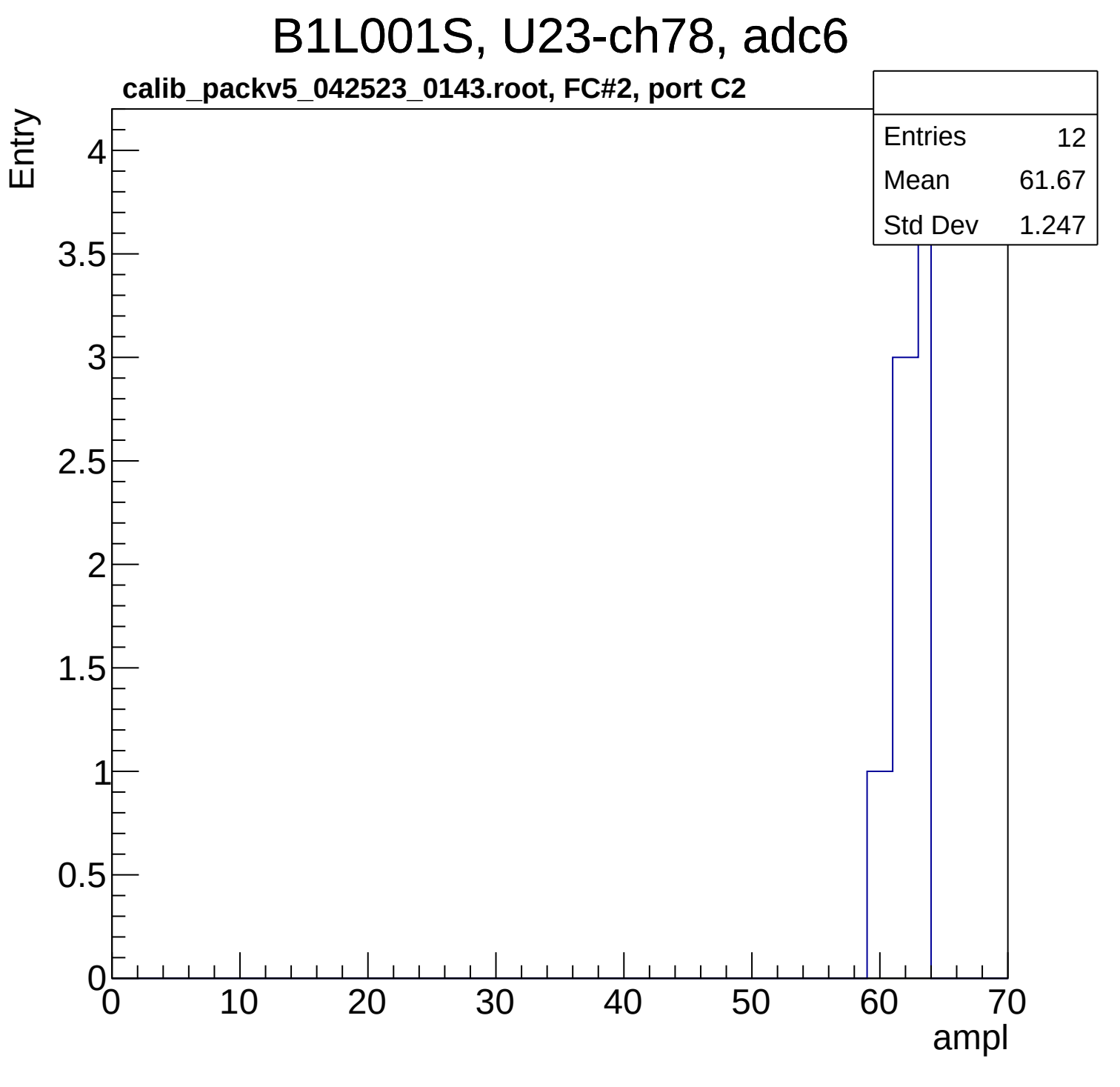
B1L001S, U23-ch78, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 61.67
Std Dev 1.247
Entry
ampl
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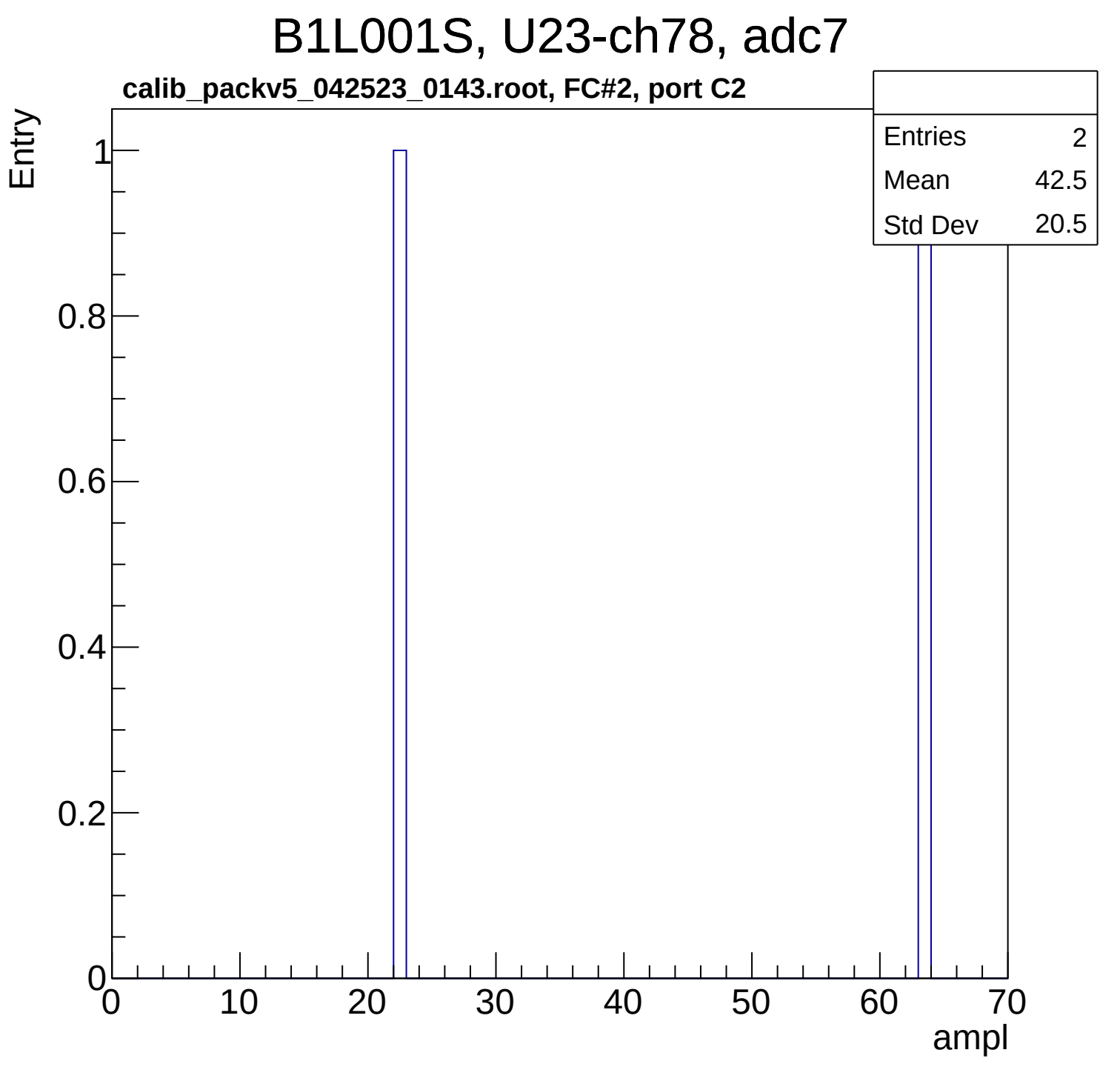
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch78, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
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40
50
60
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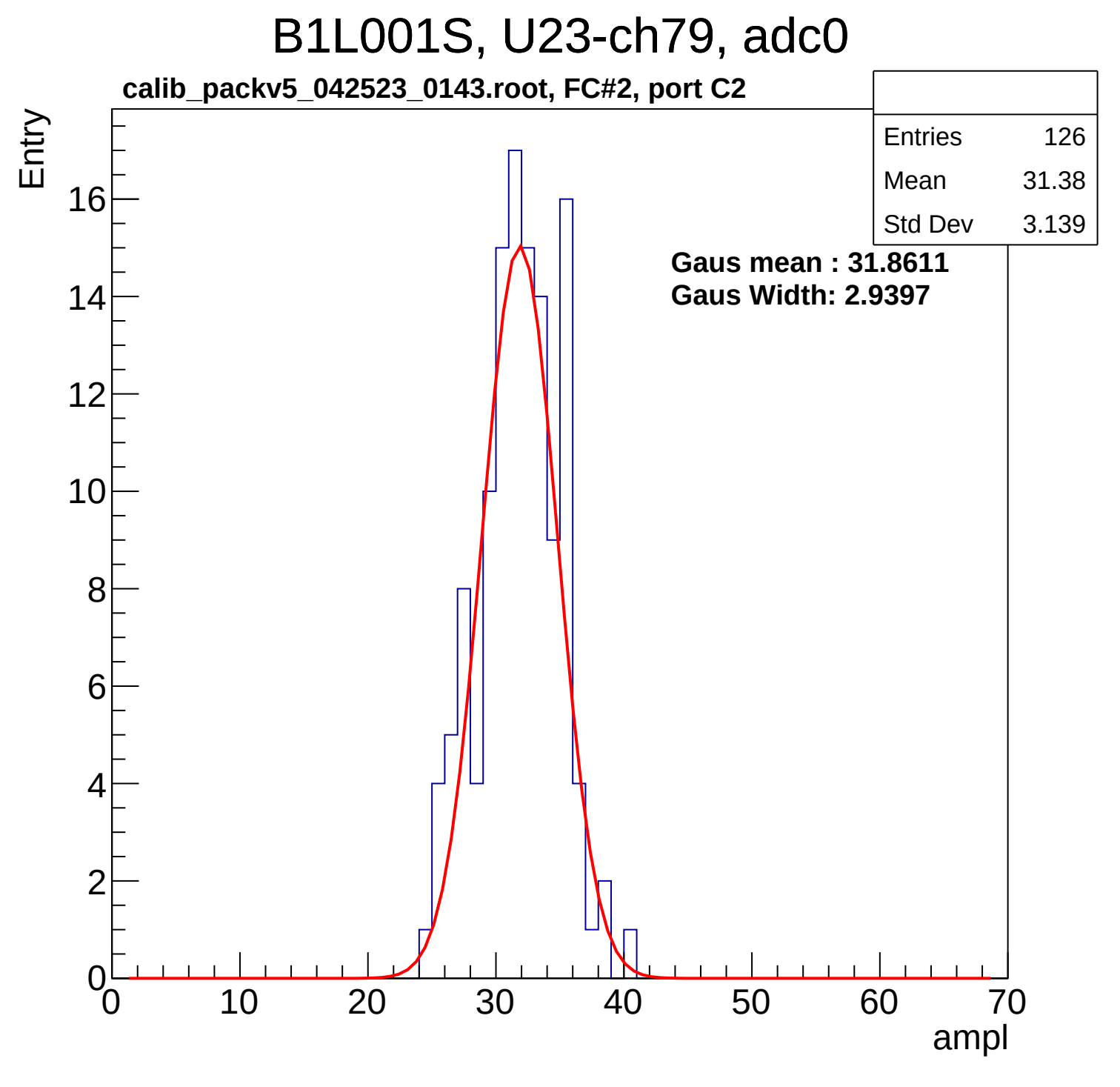
B1L001S, U23-ch79, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 31.38
Std Dev 3.139
Gaus mean : 31.8611
Gaus Width: 2.9397
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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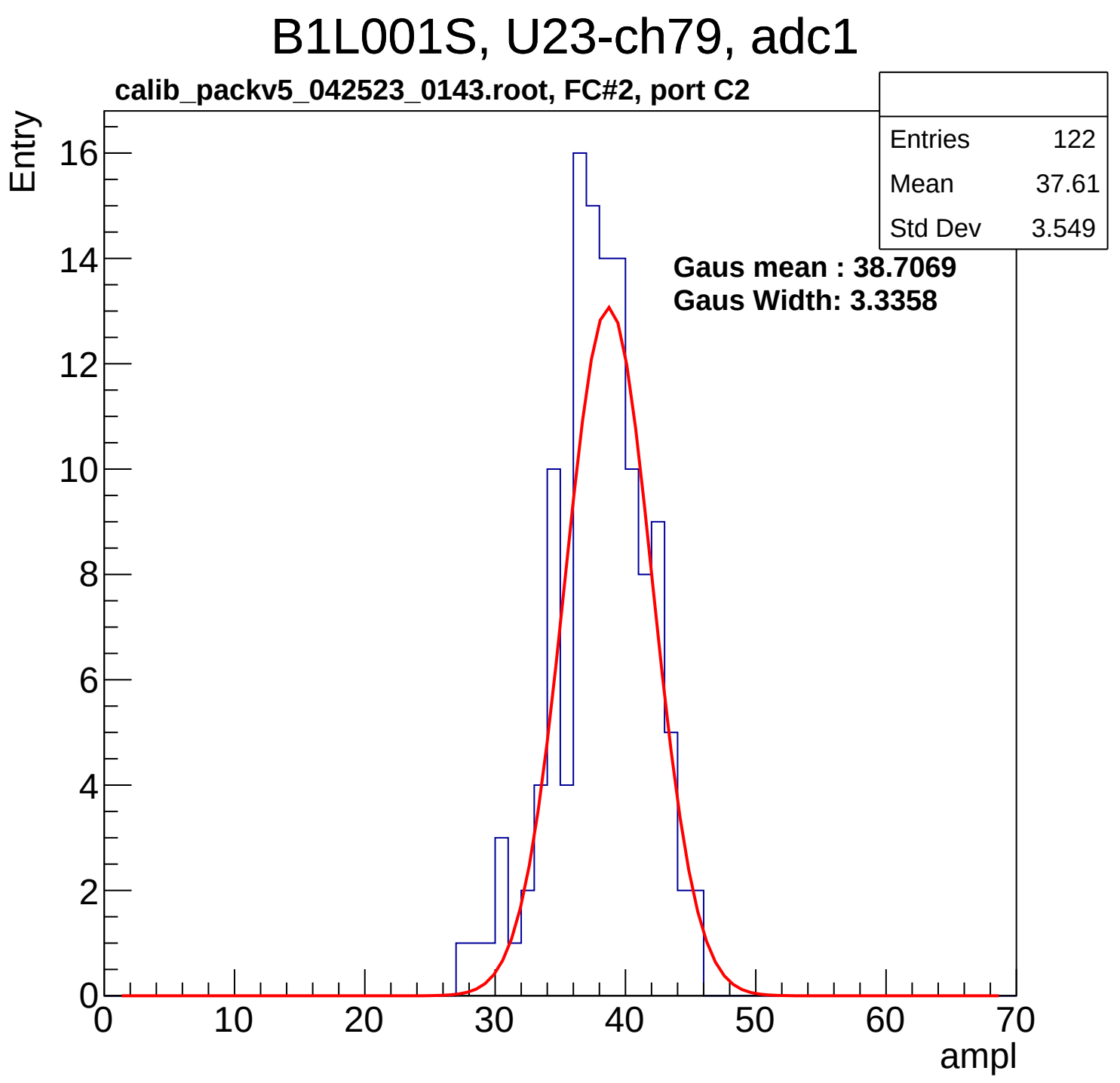
B1L001S, U23-ch79, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 37.61
Std Dev 3.549
Gaus mean : 38.7069
Gaus Width: 3.3358
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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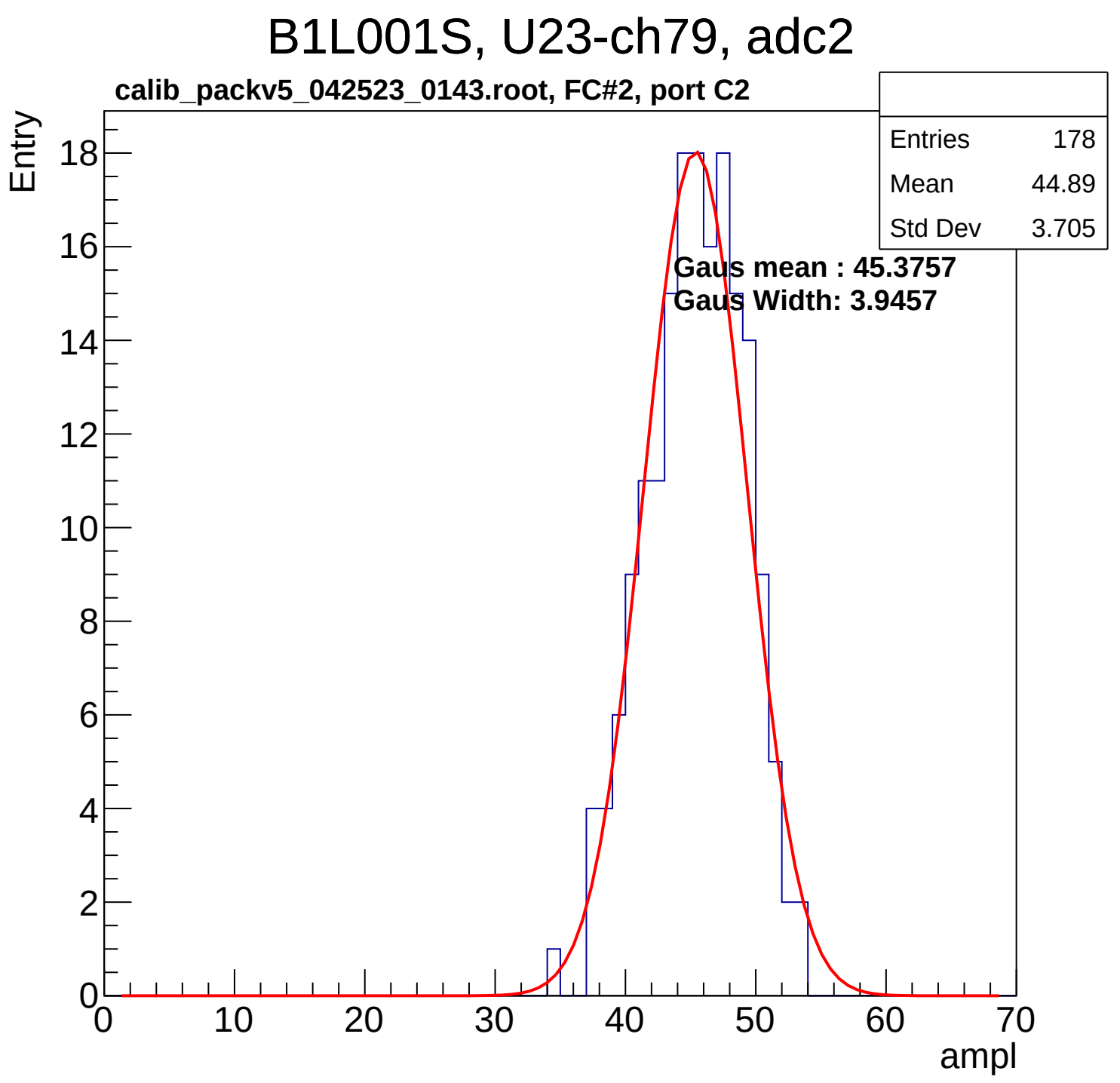
B1L001S, U23-ch79, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 178
Mean 44.89
Std Dev 3.705
Gaus mean : 45.3757
Gaus Width: 3.9457
Entry
ampl
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4
6
8
10
12
14
16
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0
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20
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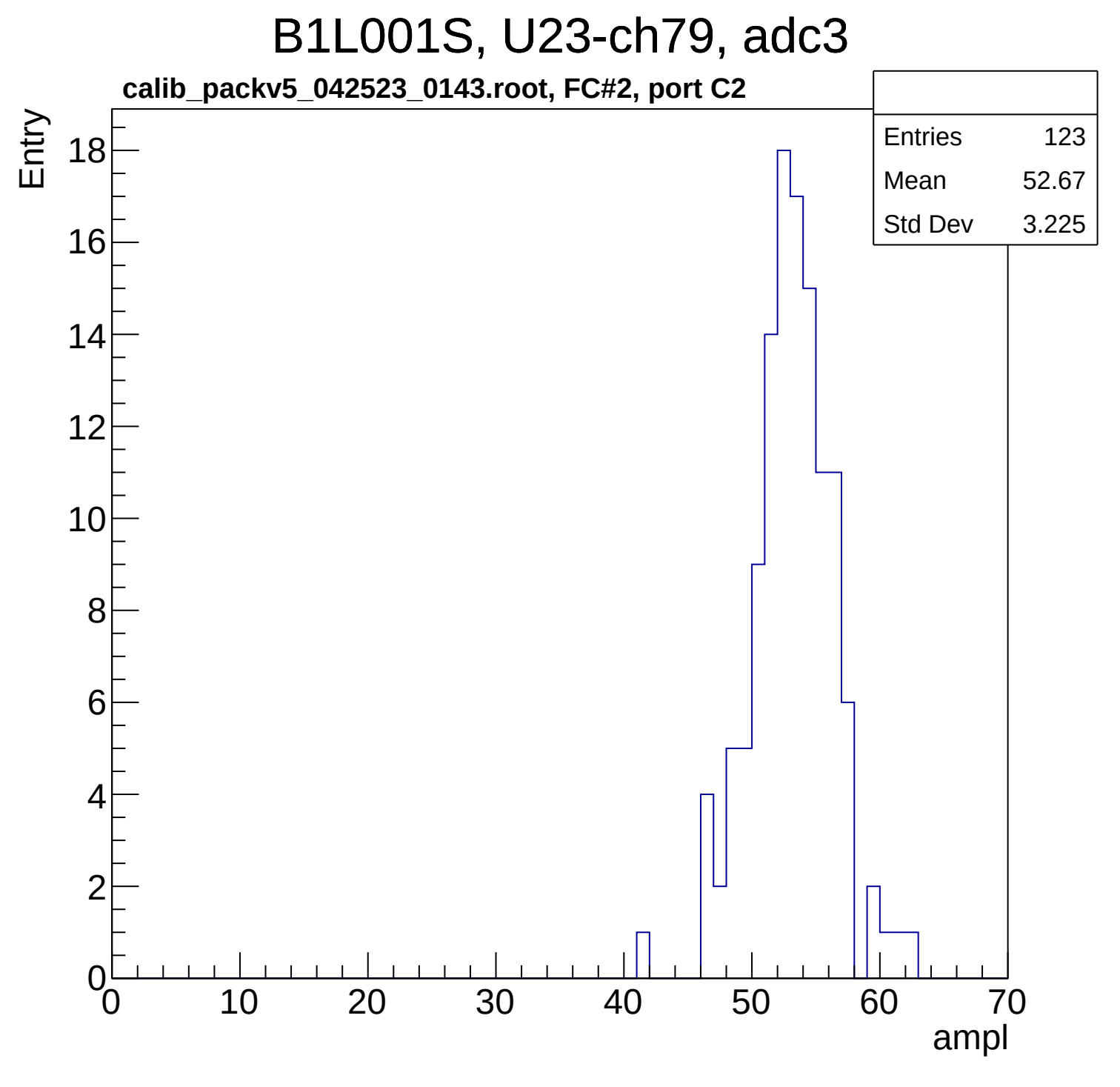
B1L001S, U23-ch79, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 52.67
Std Dev 3.225
Entry
ampl
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6
8
10
12
14
16
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0
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60
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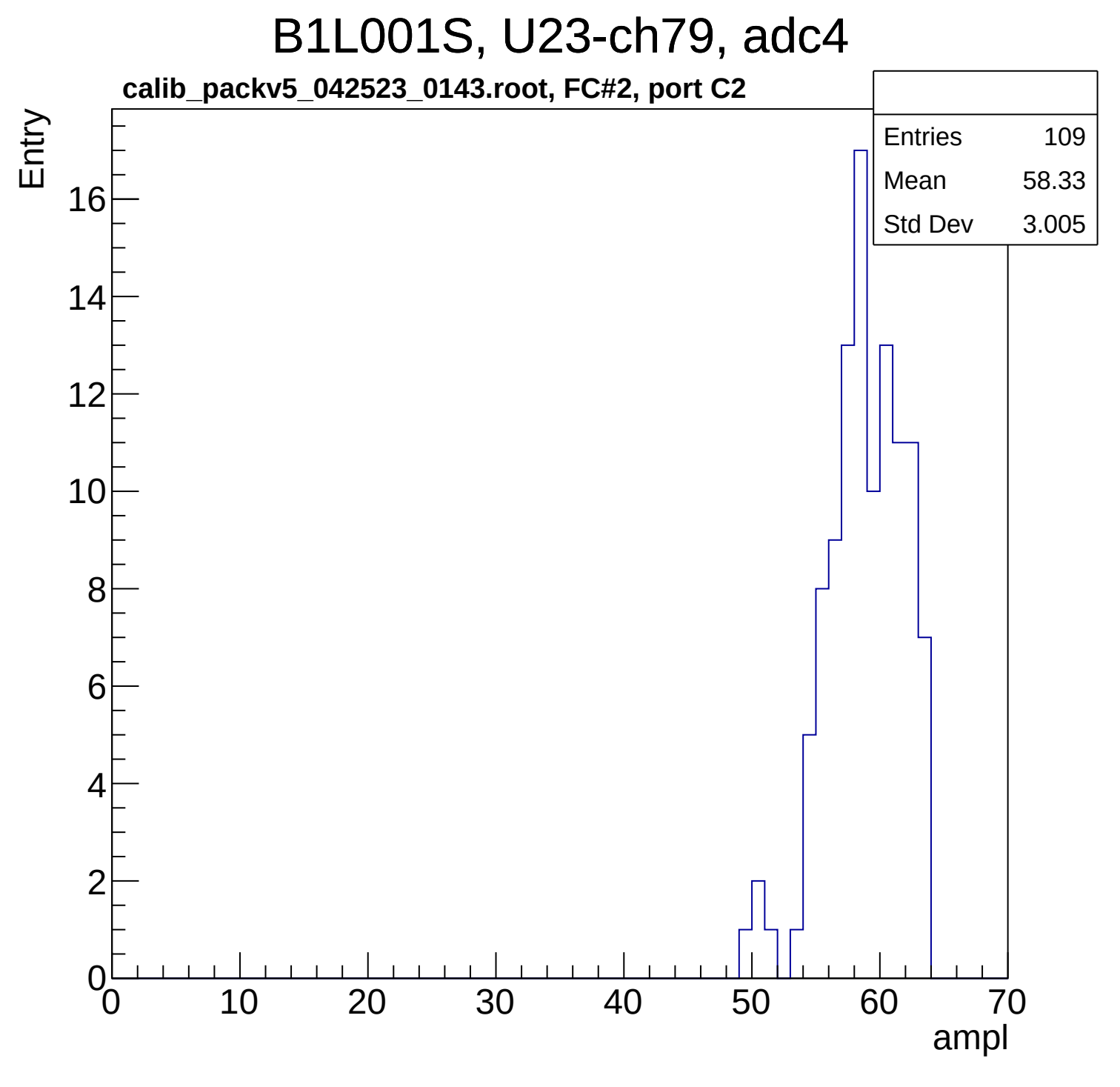
B1L001S, U23-ch79, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 58.33
Std Dev 3.005
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
50
60
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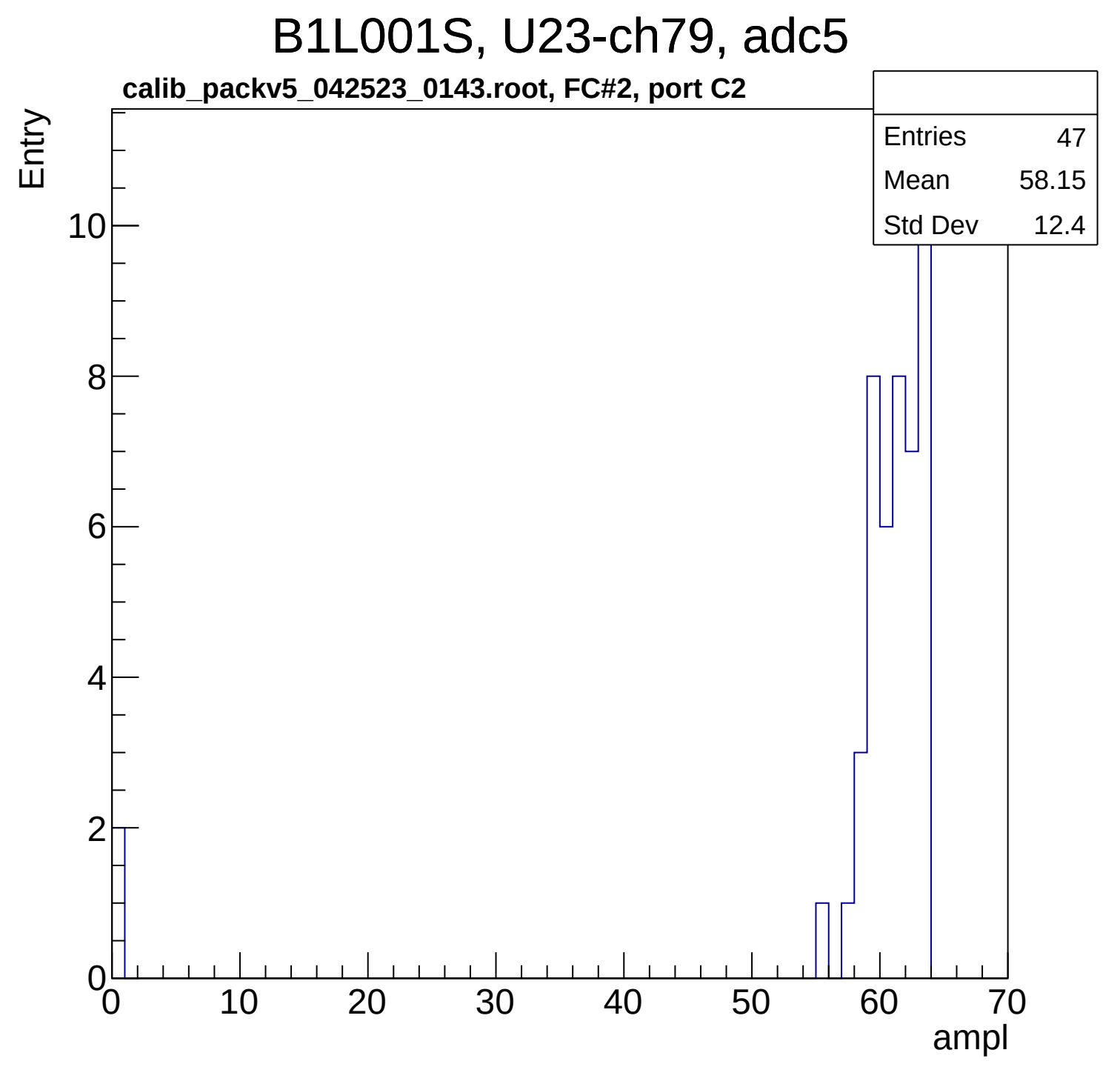
B1L001S, U23-ch79, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 47
Mean 58.15
Std Dev 12.4
Entry
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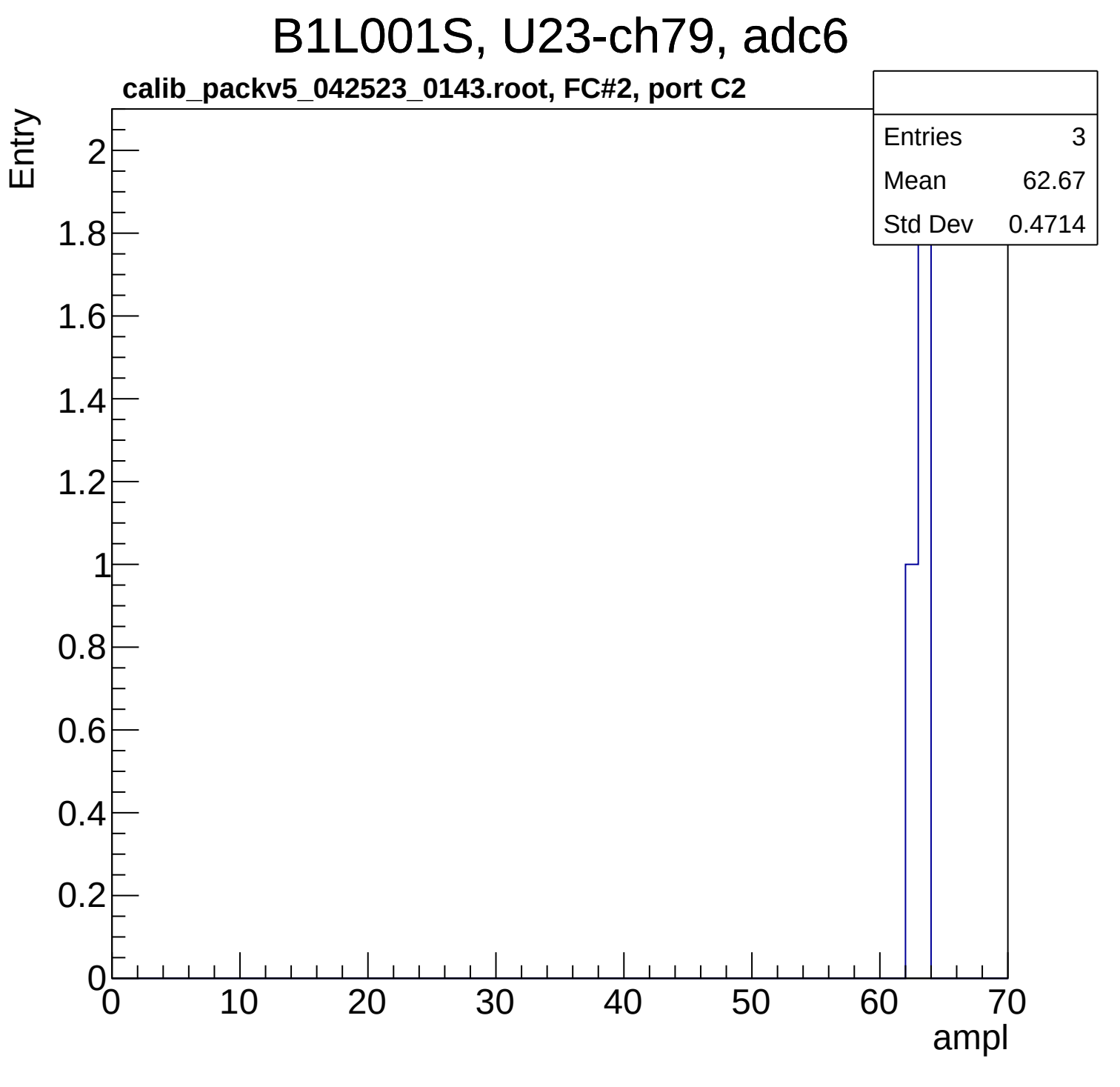
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch79, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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50
60
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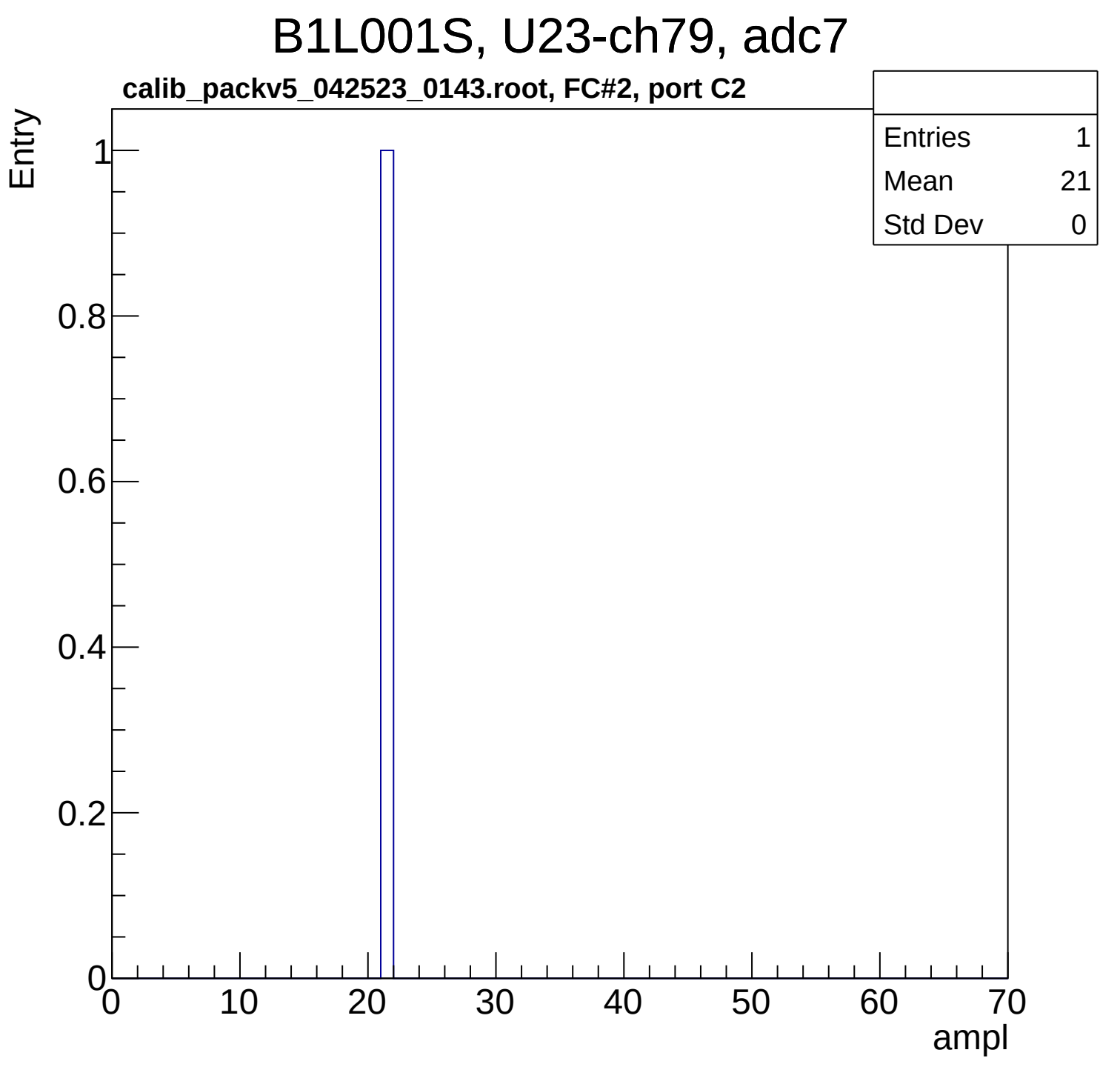
B1L001S, U23-ch79, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
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10
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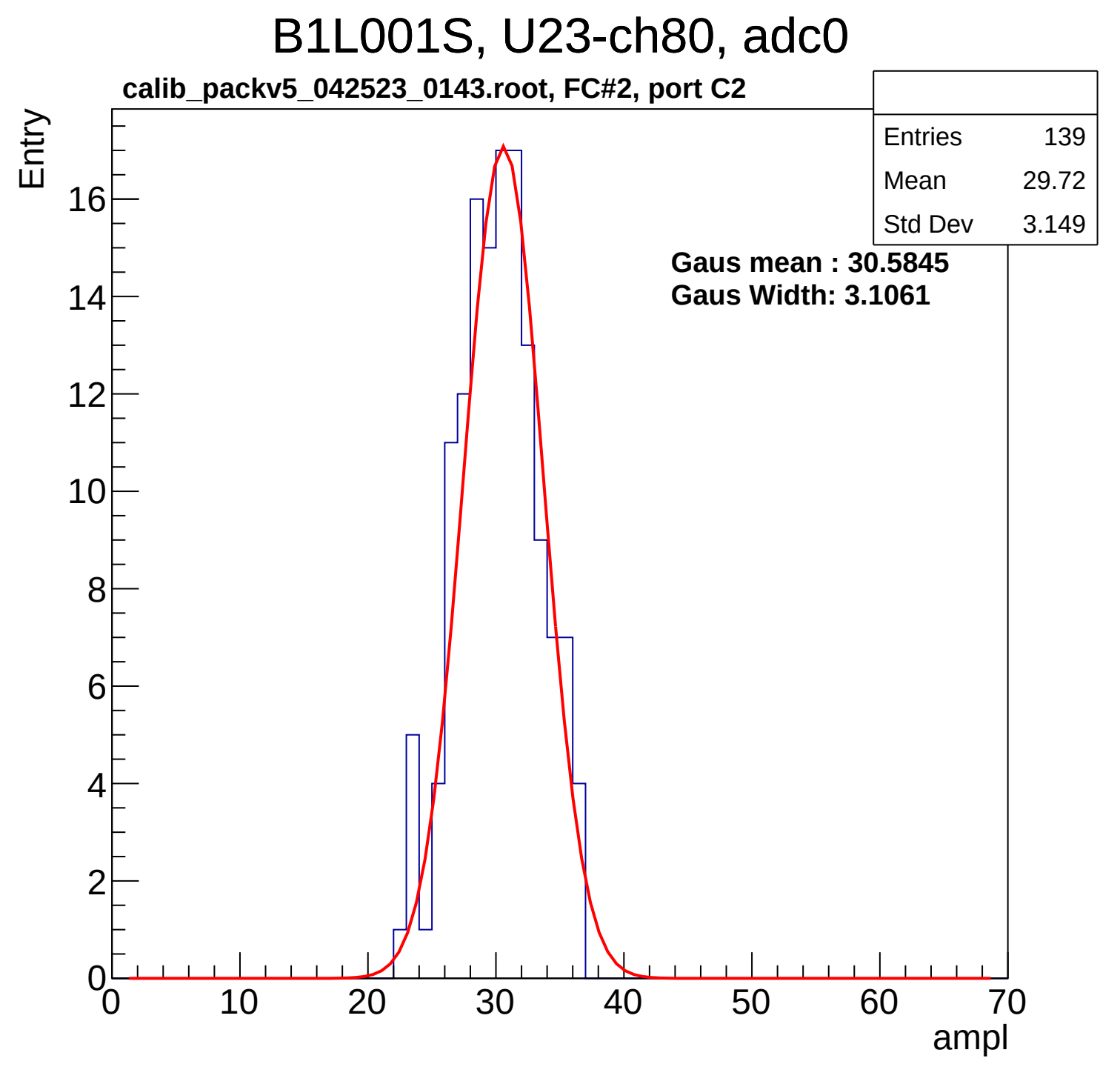
50
60
70
ampl


B1L001S, U23-ch80, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 29.72
Std Dev 3.149
Gaus mean : 30.5845
Gaus Width: 3.1061
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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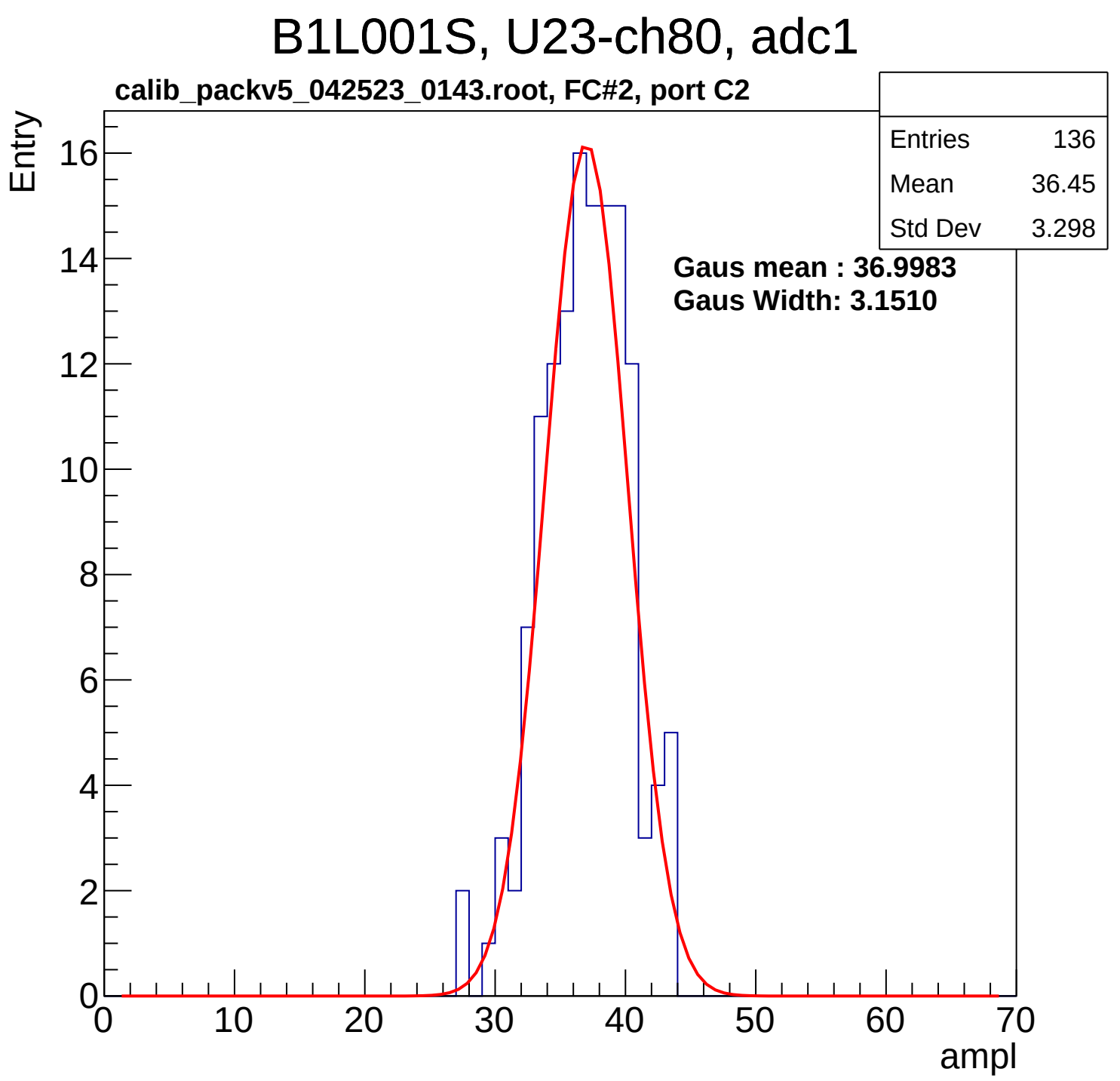
B1L001S, U23-ch80, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 36.45
Std Dev 3.298
Gaus mean : 36.9983
Gaus Width: 3.1510
Entry
ampl
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2
4
6
8
10
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0
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40
50
60
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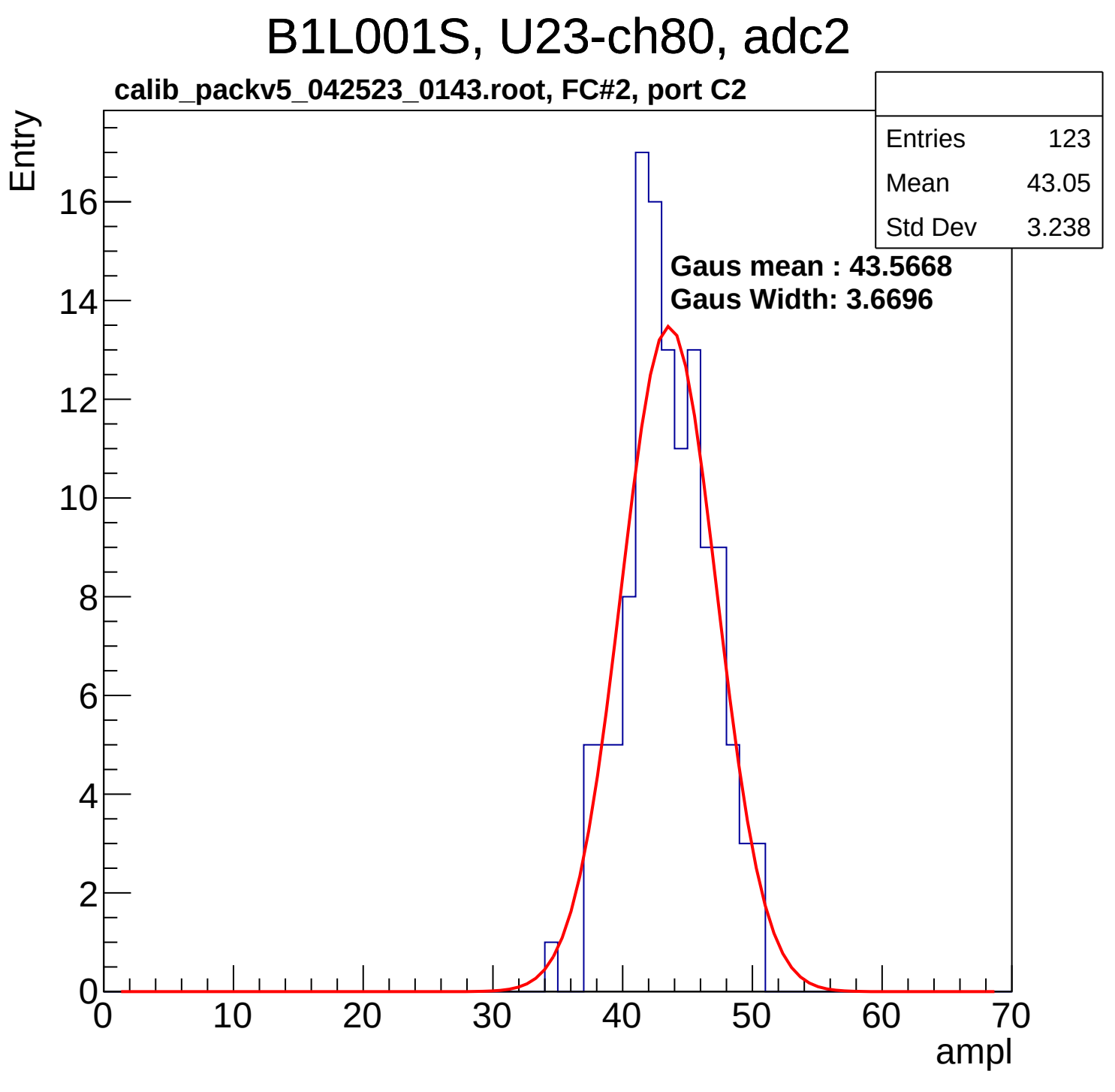
B1L001S, U23-ch80, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 43.05
Std Dev 3.238
Gaus mean : 43.5668
Gaus Width: 3.6696
Entry
ampl
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2
4
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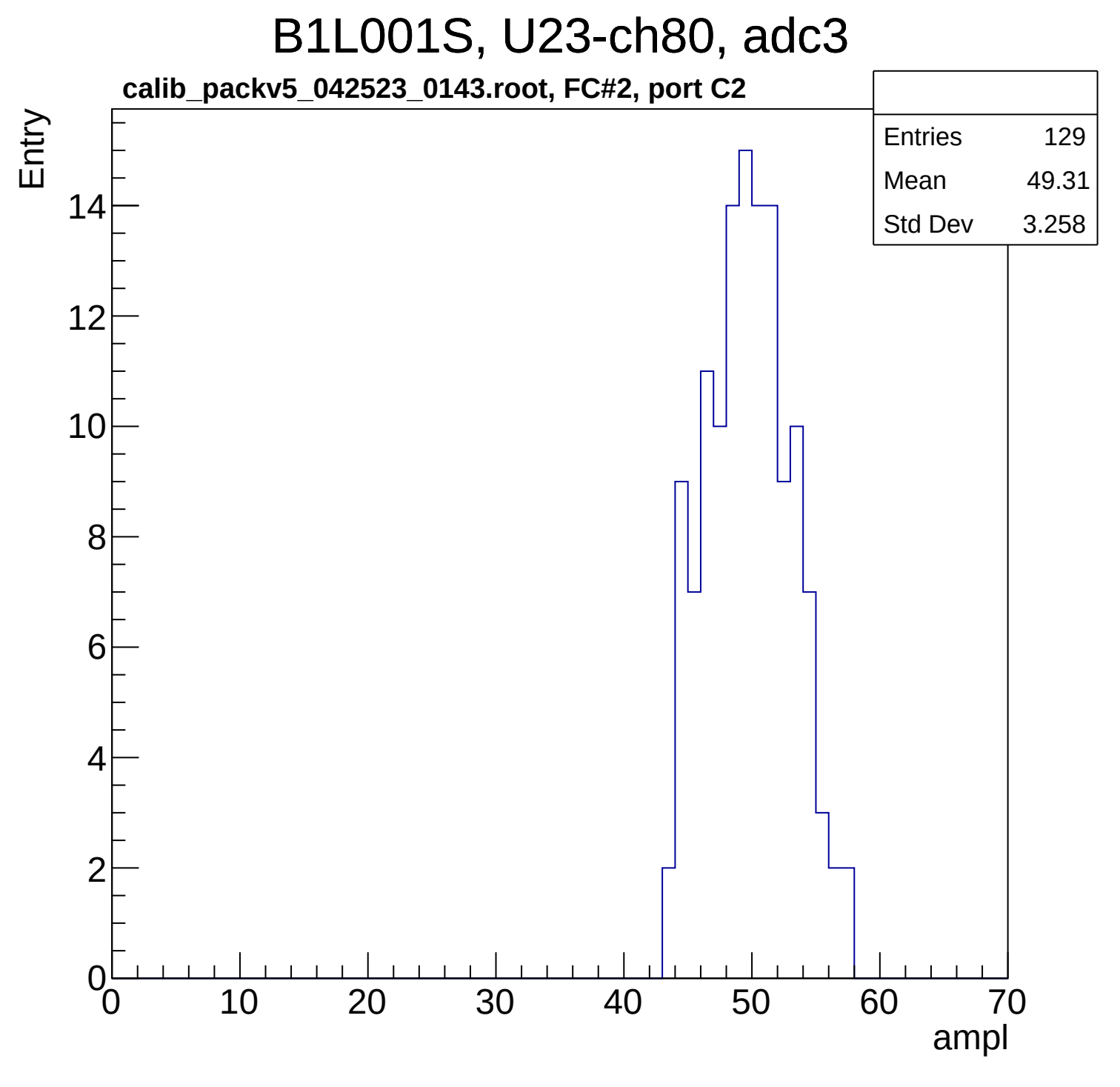
B1L001S, U23-ch80, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 49.31
Std Dev 3.258
Entry
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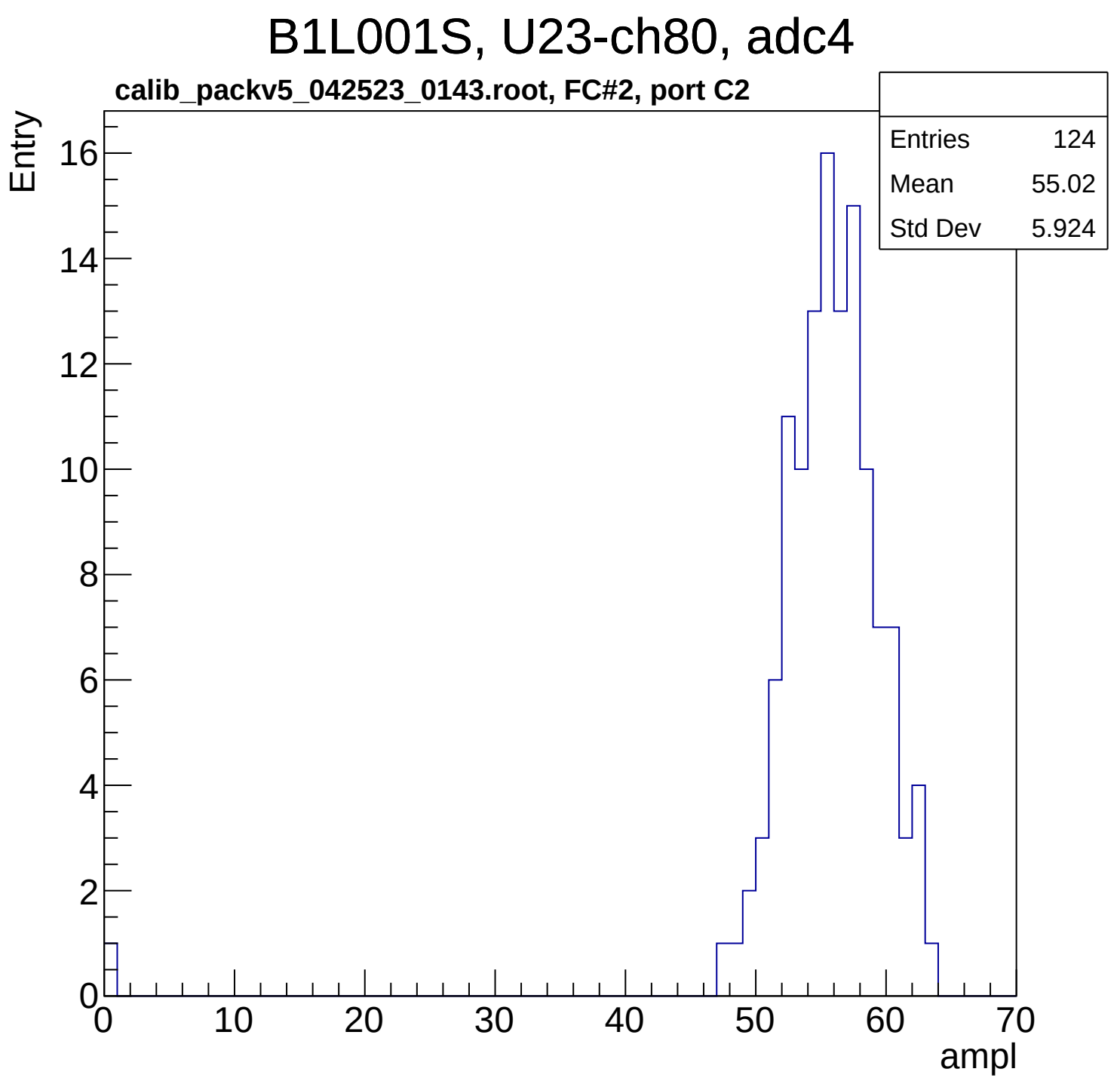
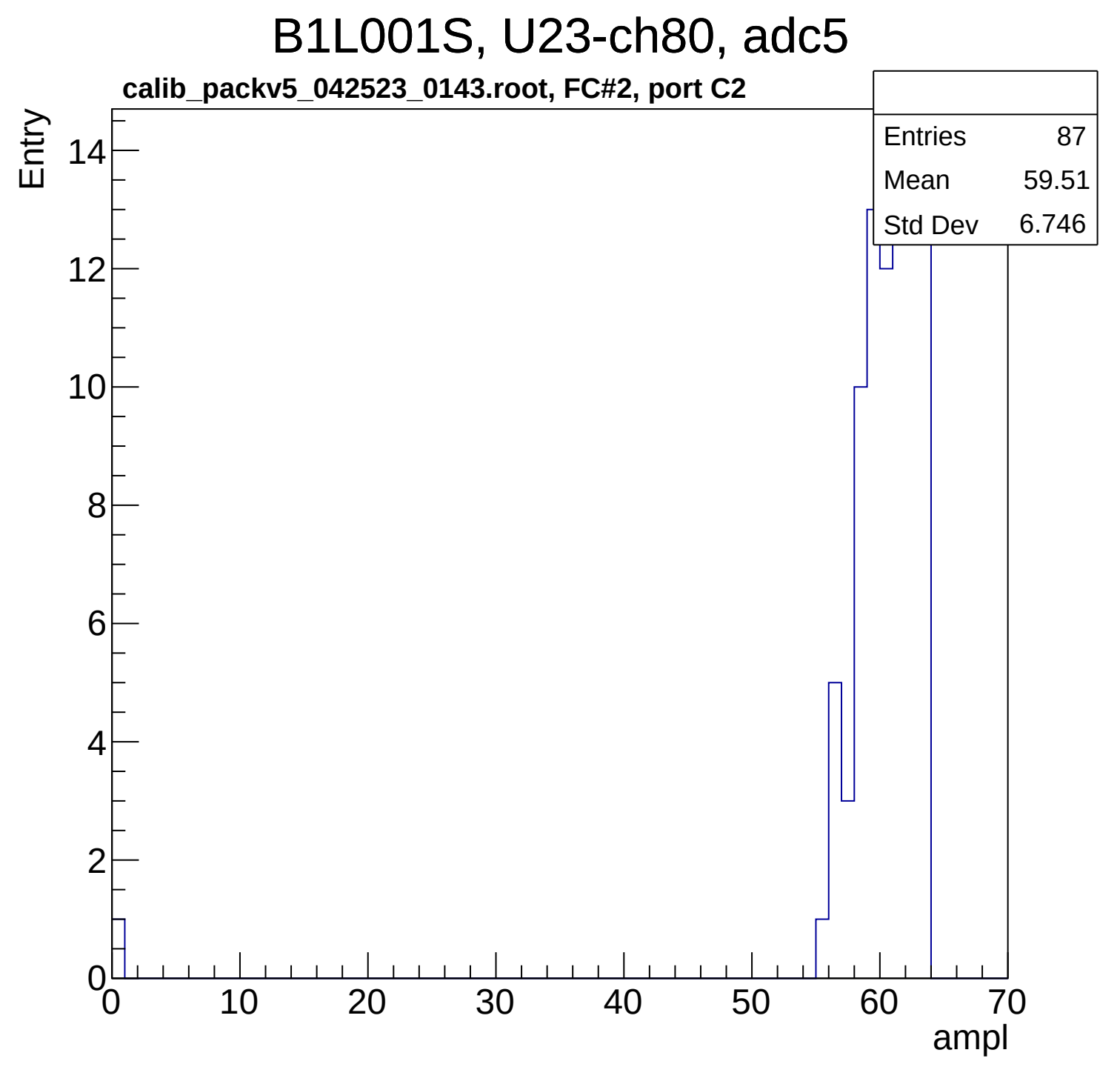
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch80, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.02
Std Dev 5.924
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch80, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 59.51
Std Dev 6.746
Entry
ampl
0
2
4
6
8
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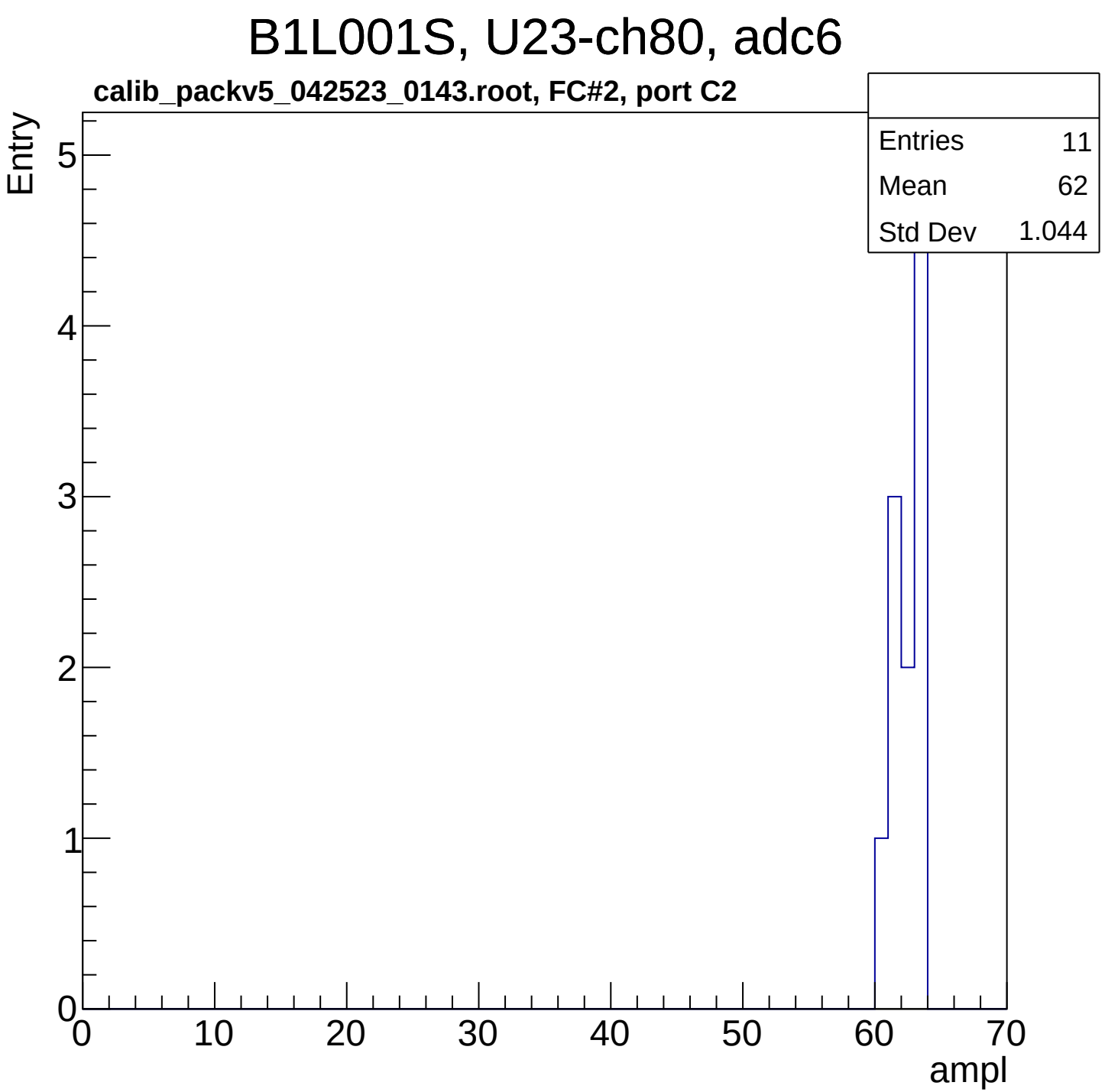
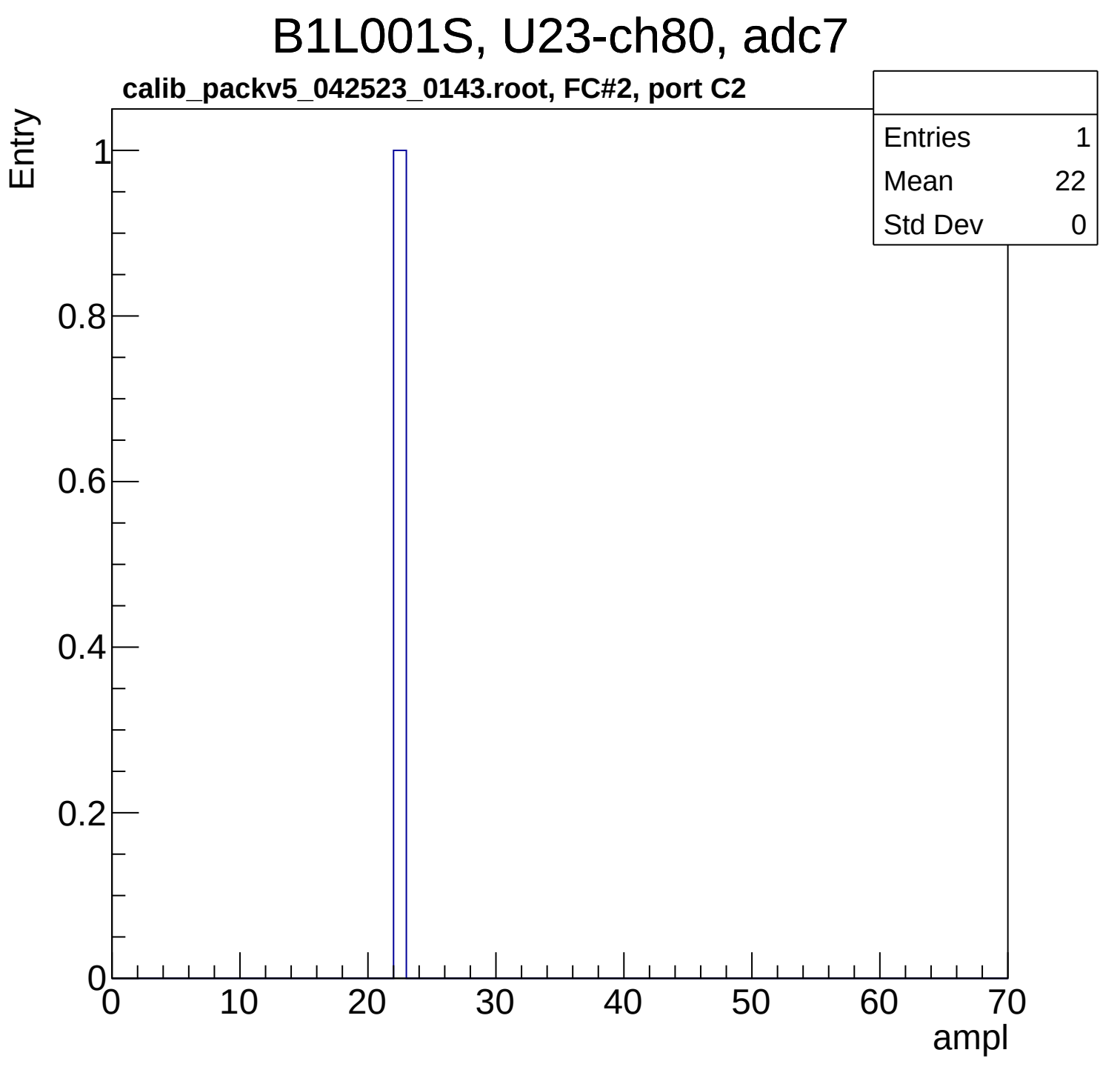
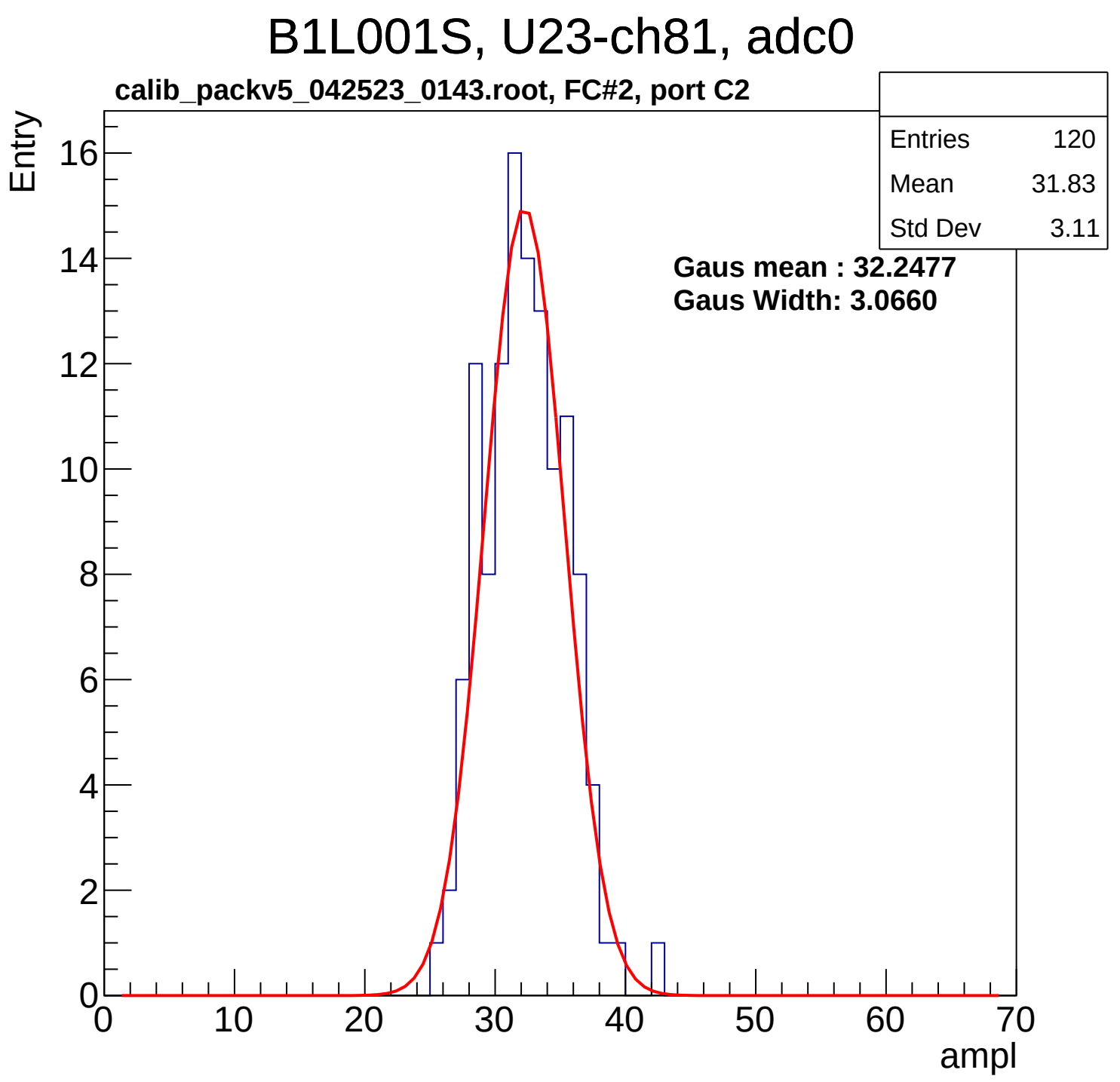
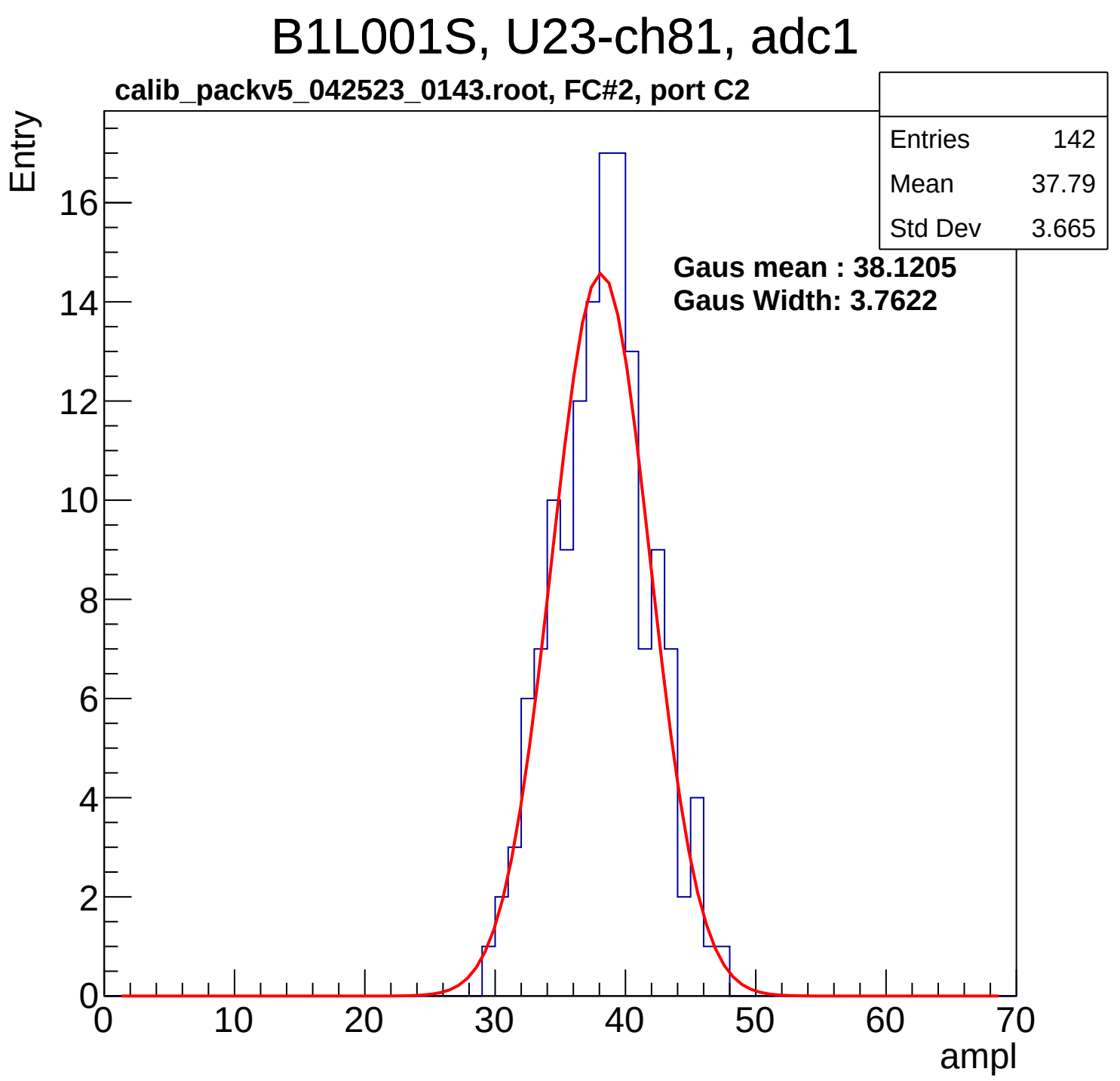

B1L001S, U23-ch80, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 62
Std Dev 1.044
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch80, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch81, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 31.83
Std Dev 3.11
Gaus mean : 32.2477
Gaus Width: 3.0660
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch81, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 37.79
Std Dev 3.665
Gaus mean : 38.1205
Gaus Width: 3.7622
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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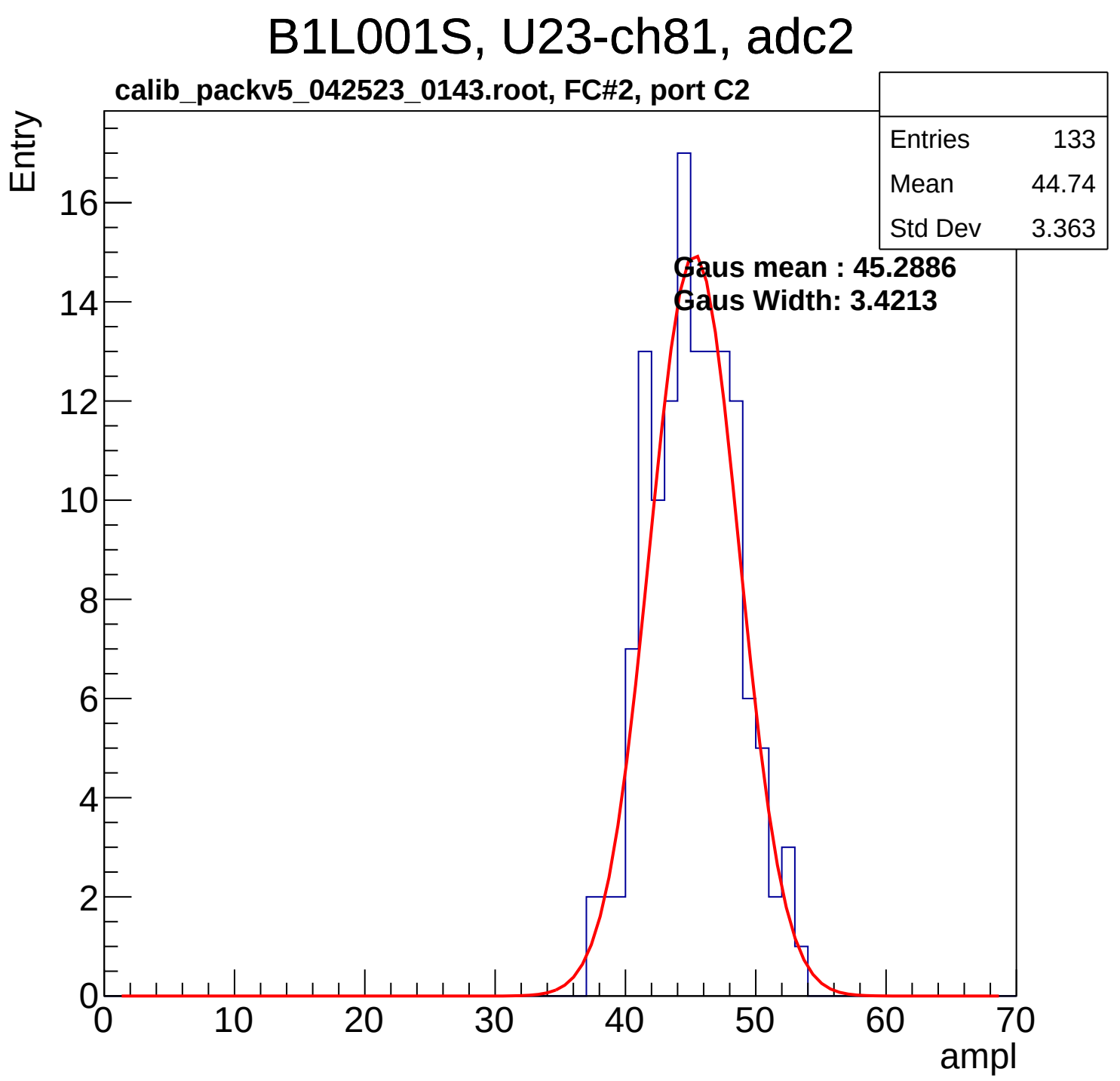
B1L001S, U23-ch81, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 44.74
Std Dev 3.363
Gaus mean : 45.2886
Gaus Width: 3.4213
Entry
ampl
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2
4
6
8
10
12
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16
0
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20
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40
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60
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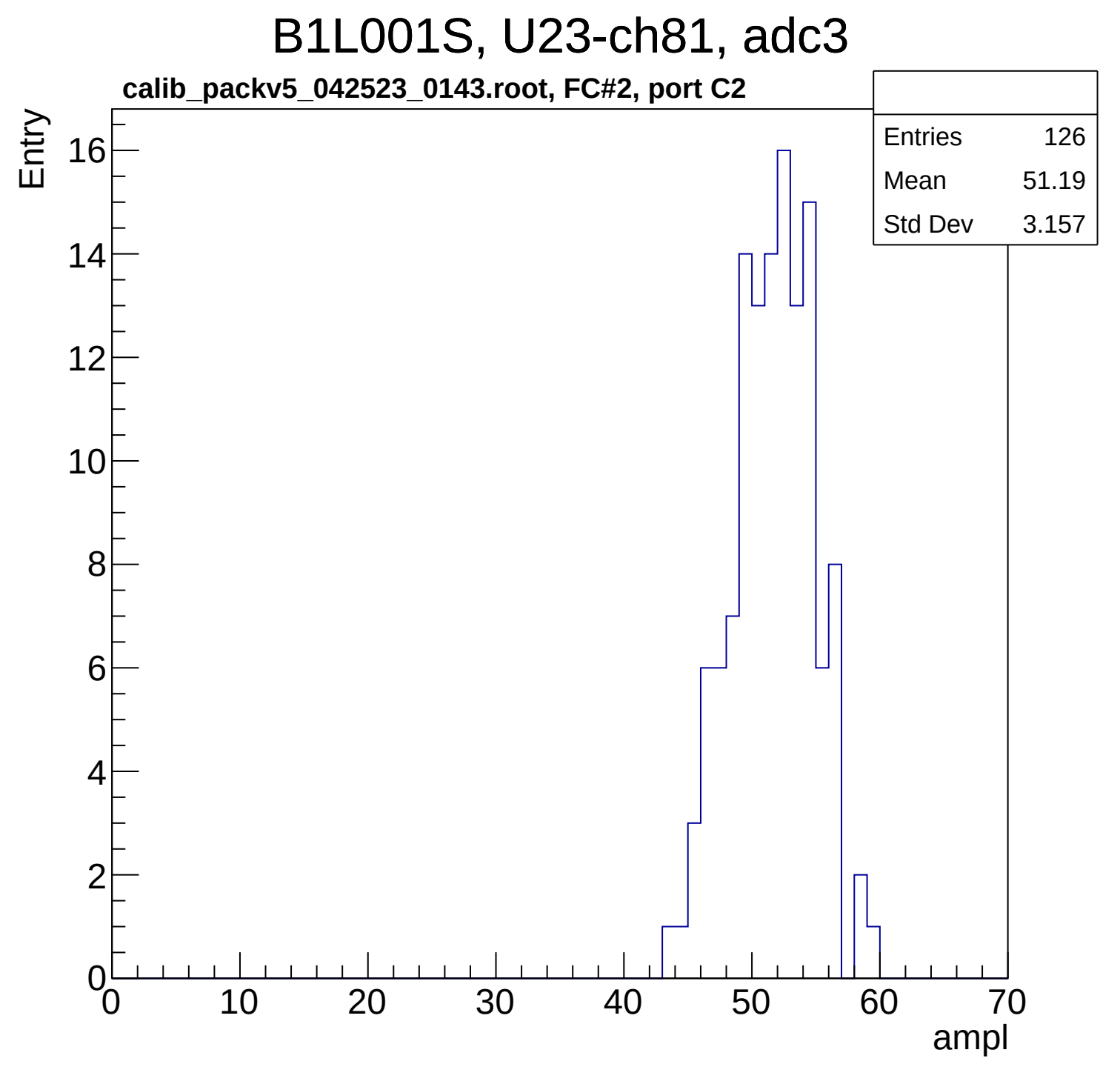
B1L001S, U23-ch81, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 51.19
Std Dev 3.157
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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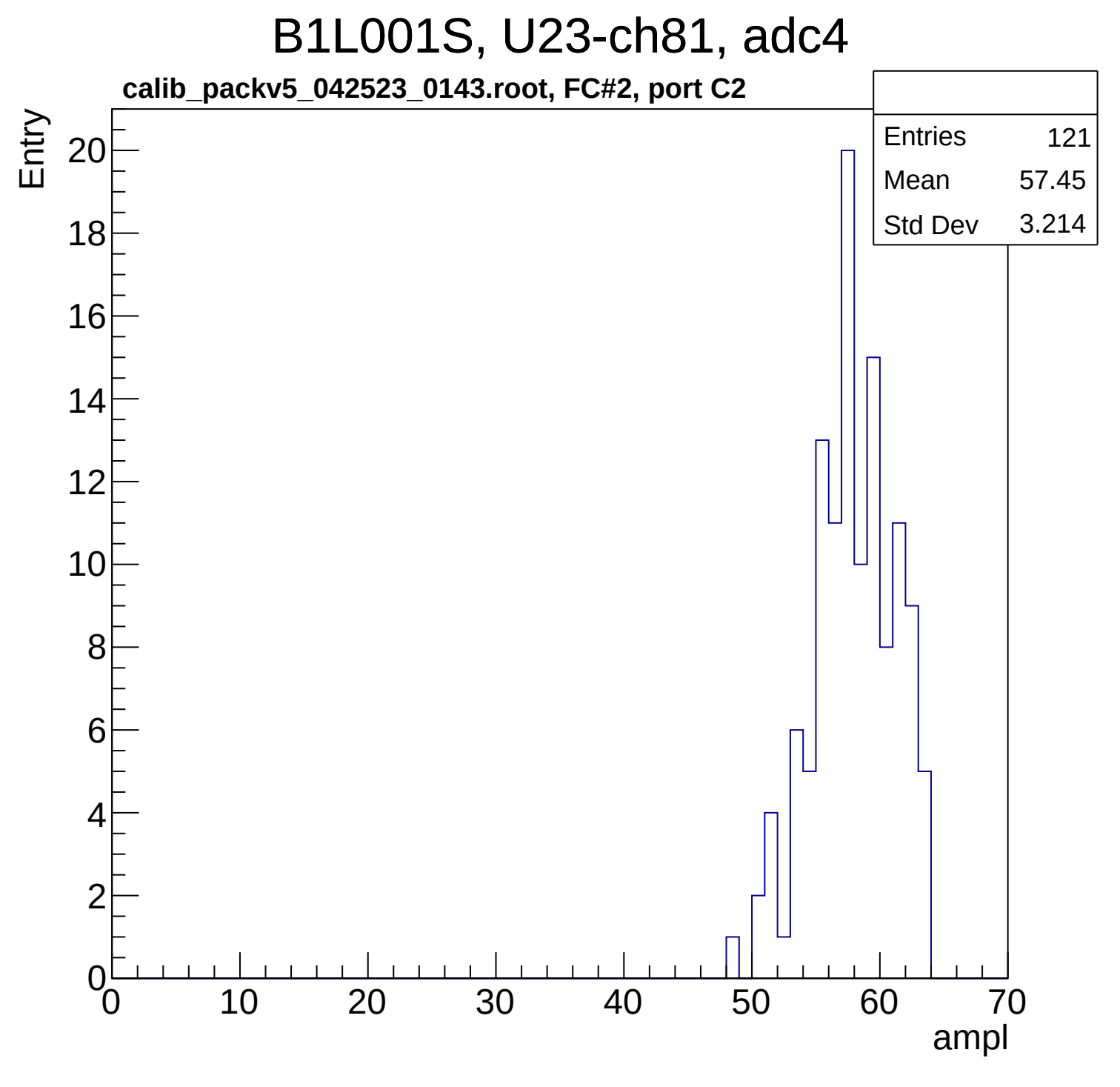
B1L001S, U23-ch81, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 57.45
Std Dev 3.214
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
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40
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60
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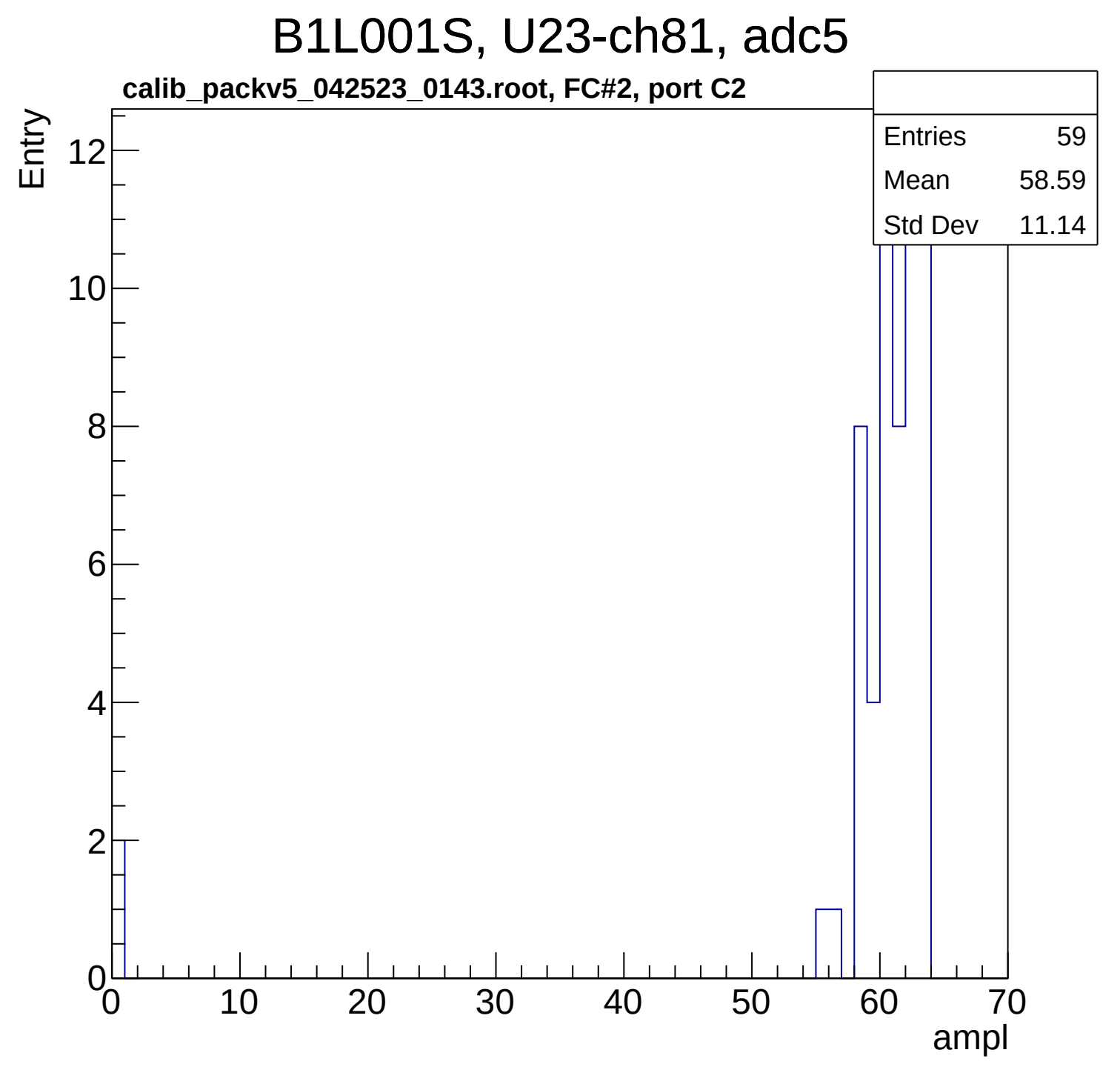
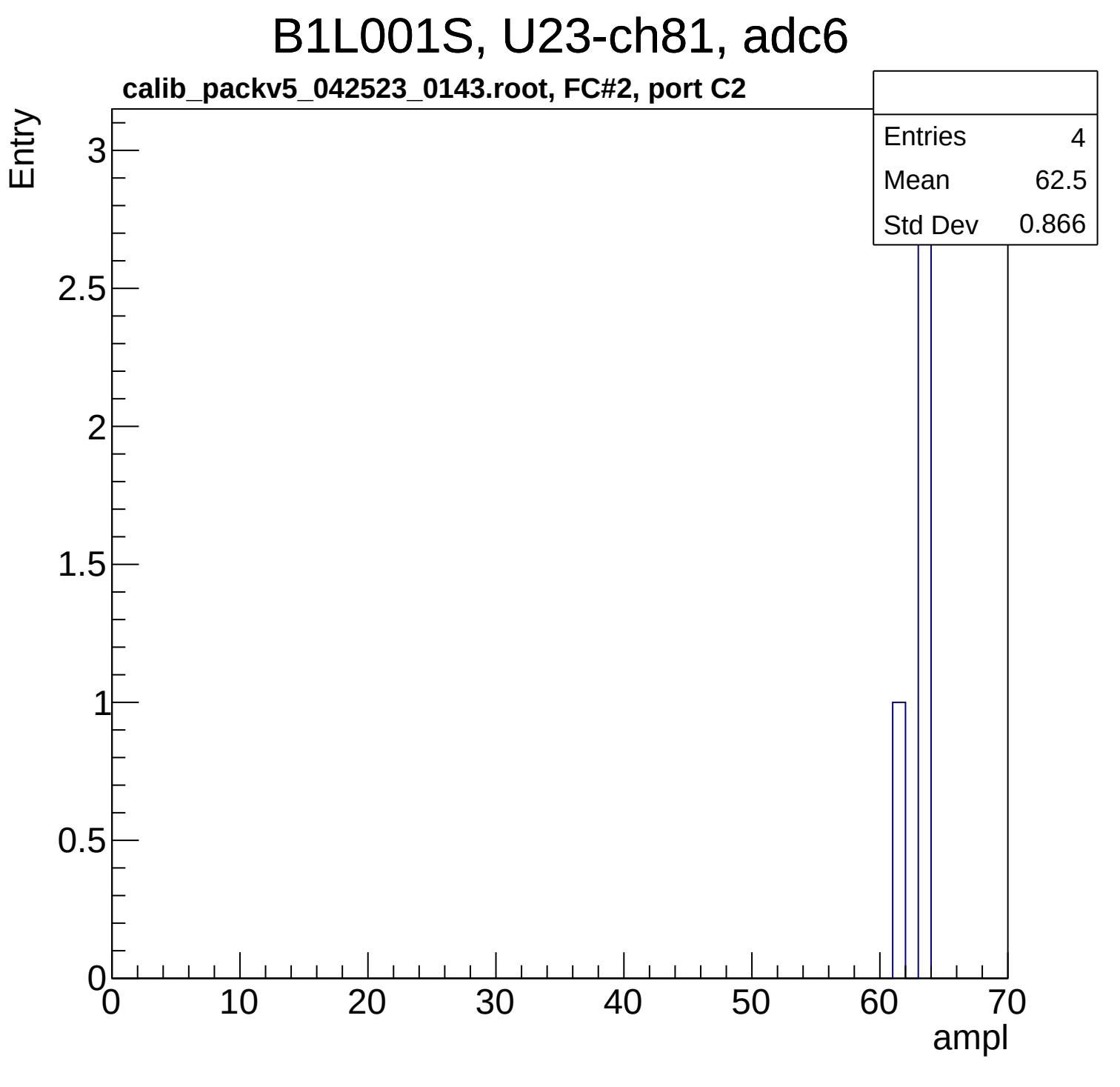

B1L001S, U23-ch81, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 59
Mean 58.59
Std Dev 11.14
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch81, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
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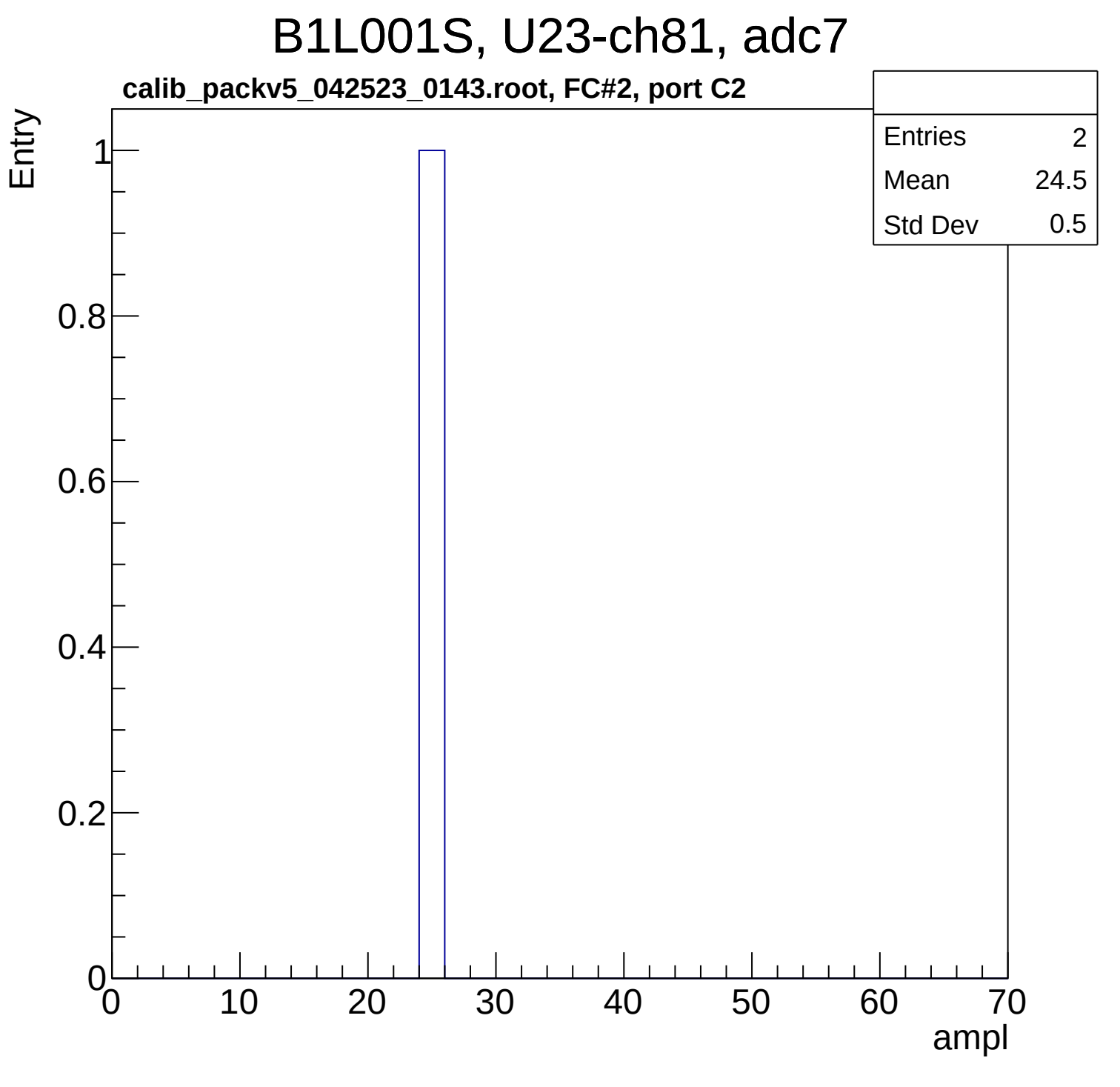
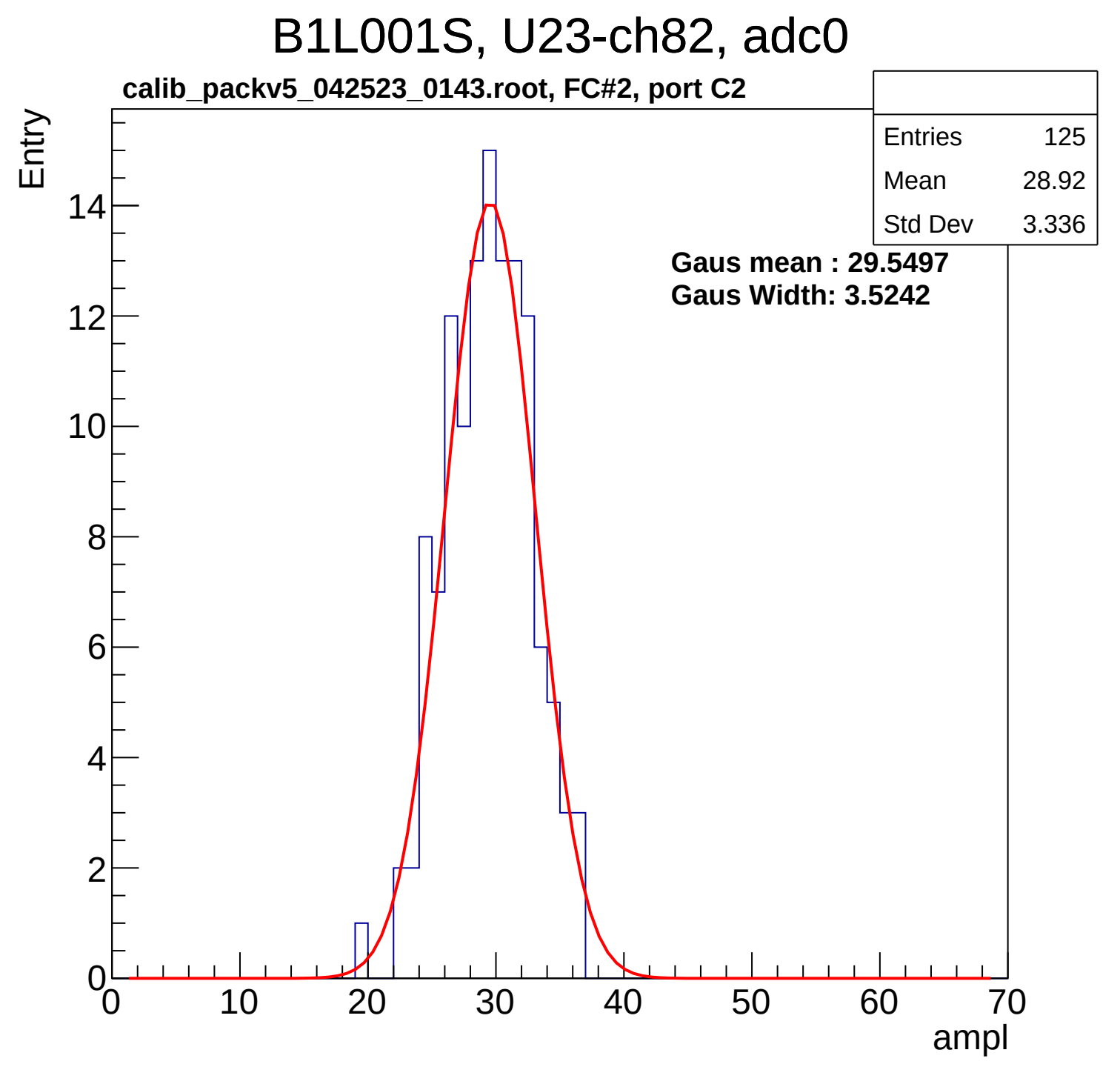
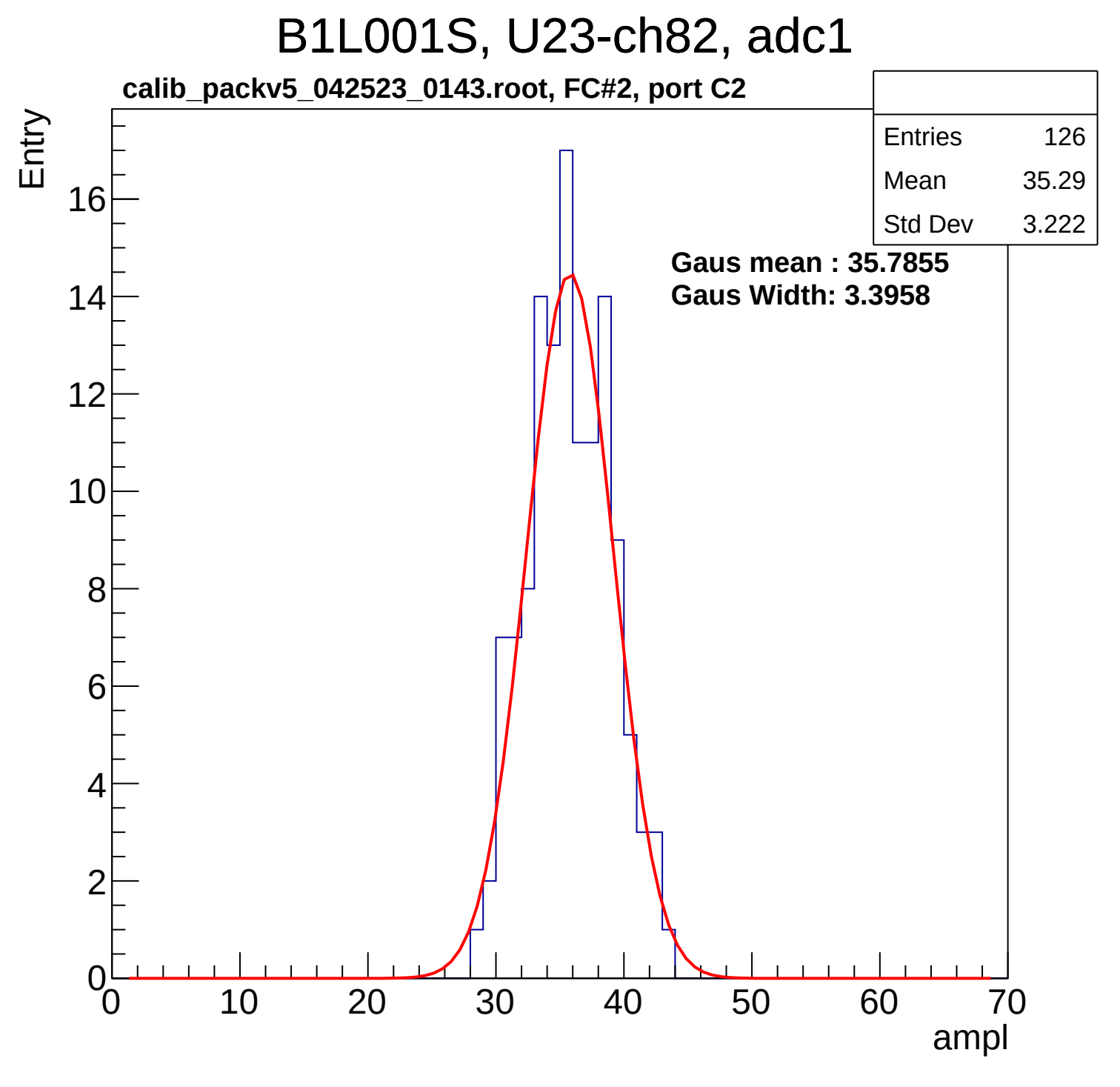
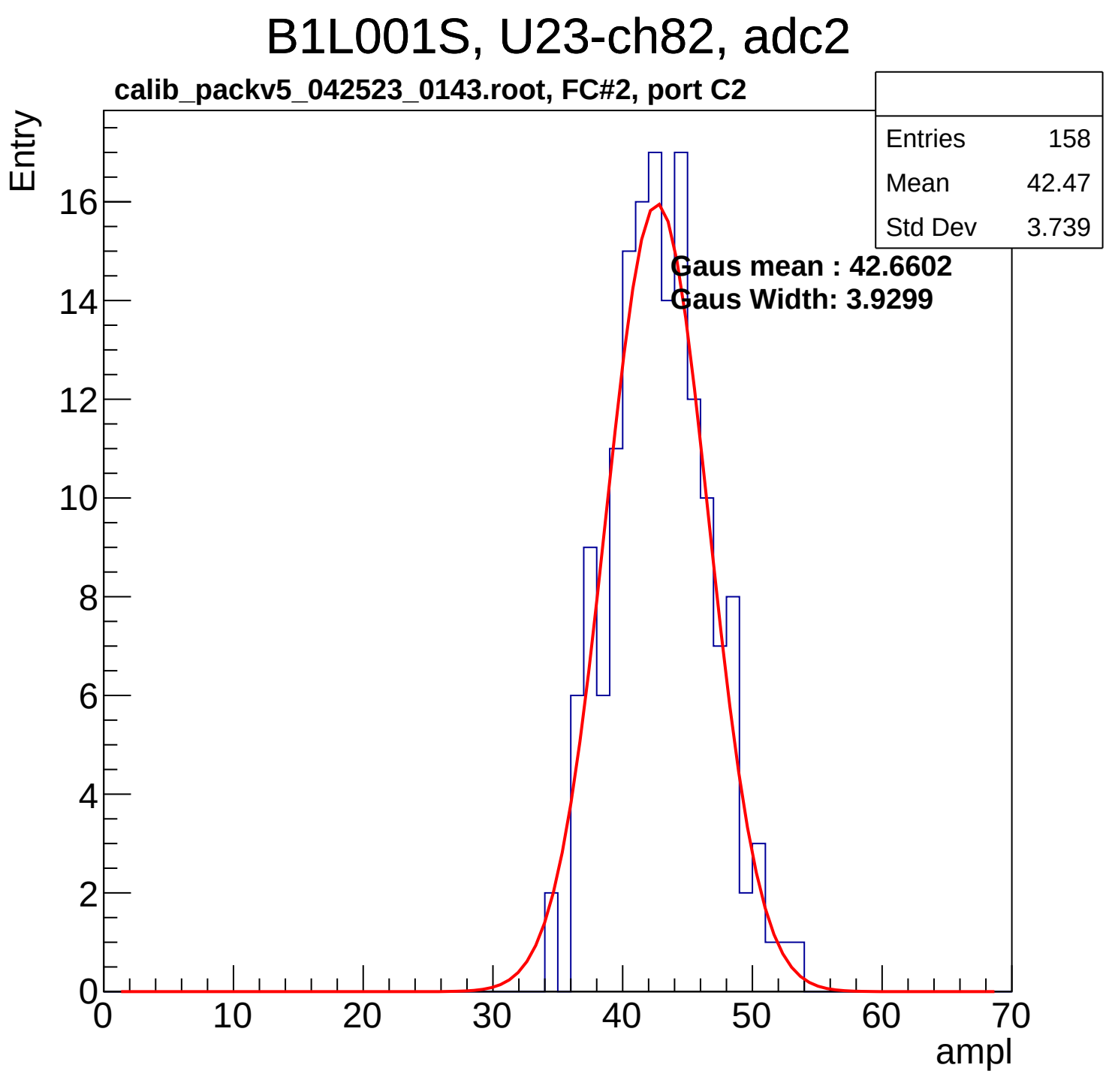
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch81, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 24.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch82, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 28.92
Std Dev 3.336
Gaus mean : 29.5497
Gaus Width: 3.5242
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch82, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 35.29
Std Dev 3.222
Gaus mean : 35.7855
Gaus Width: 3.3958
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch82, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 158
Mean 42.47
Std Dev 3.739
Gaus mean : 42.6602
Gaus Width: 3.9299
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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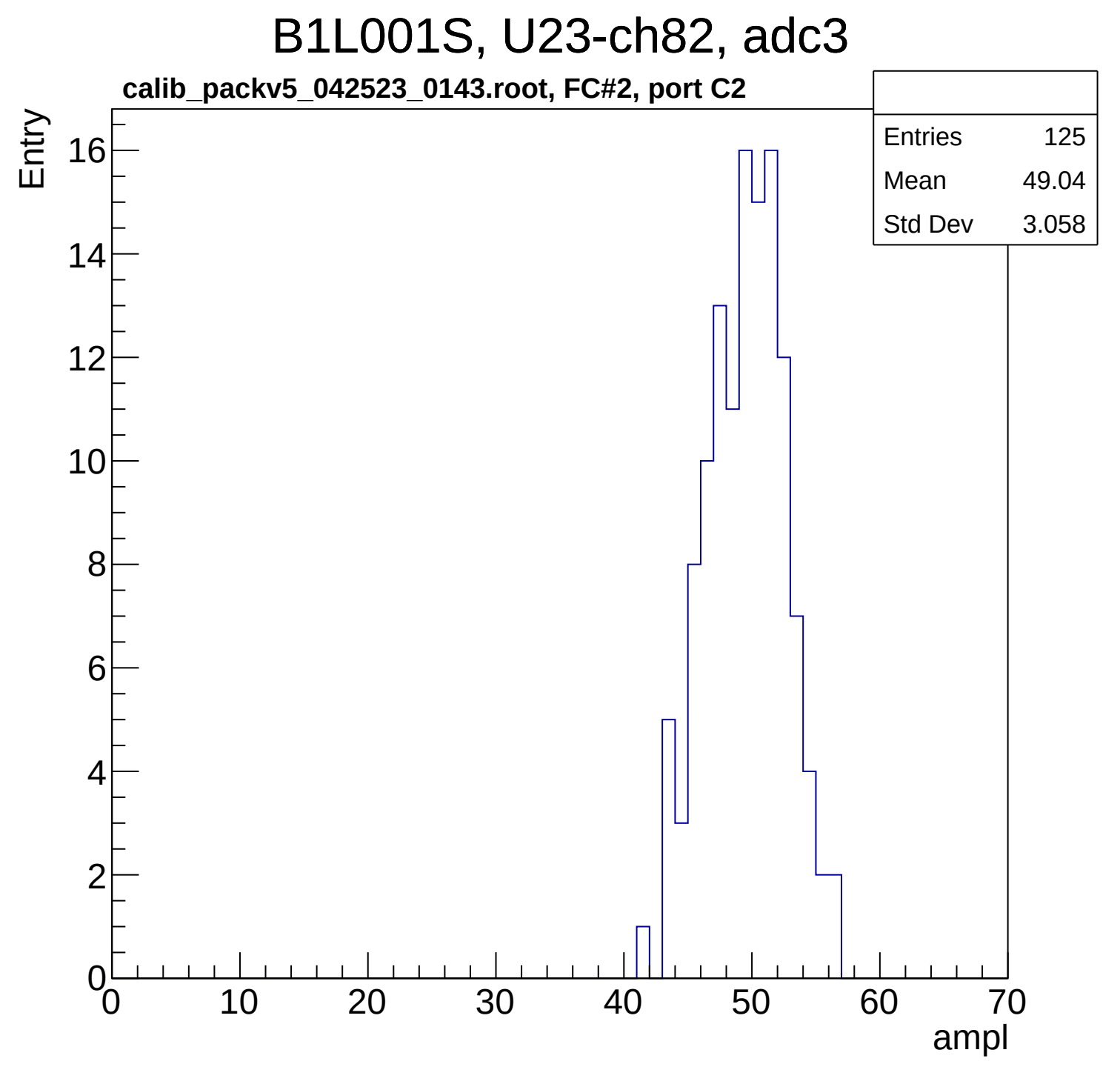
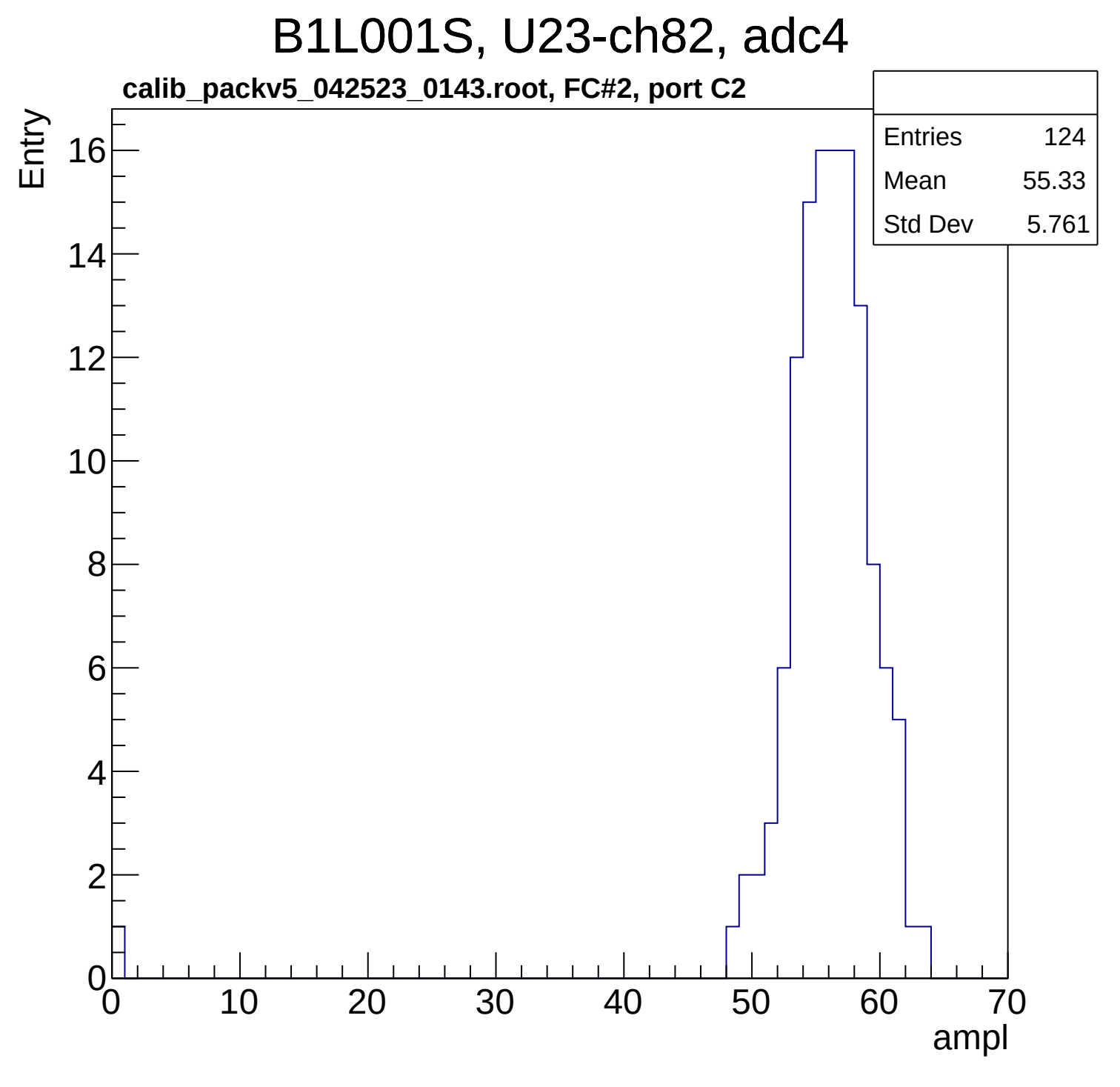

B1L001S, U23-ch82, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 49.04
Std Dev 3.058
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch82, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.33
Std Dev 5.761
Entry
ampl
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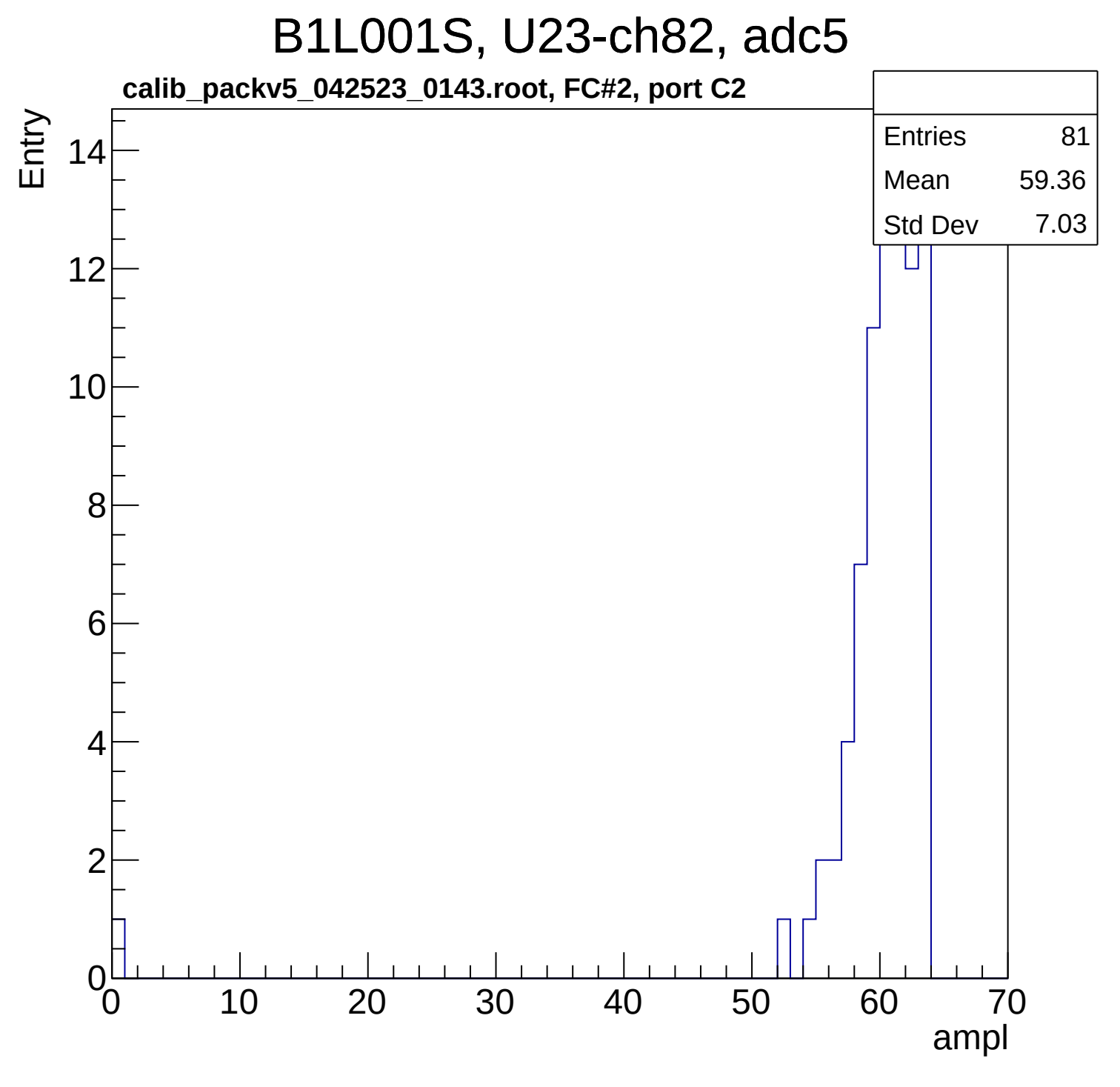
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch82, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 59.36
Std Dev 7.03
Entry
ampl
0
2
4
6
8
10
12
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0
10
20
30
40
50
60
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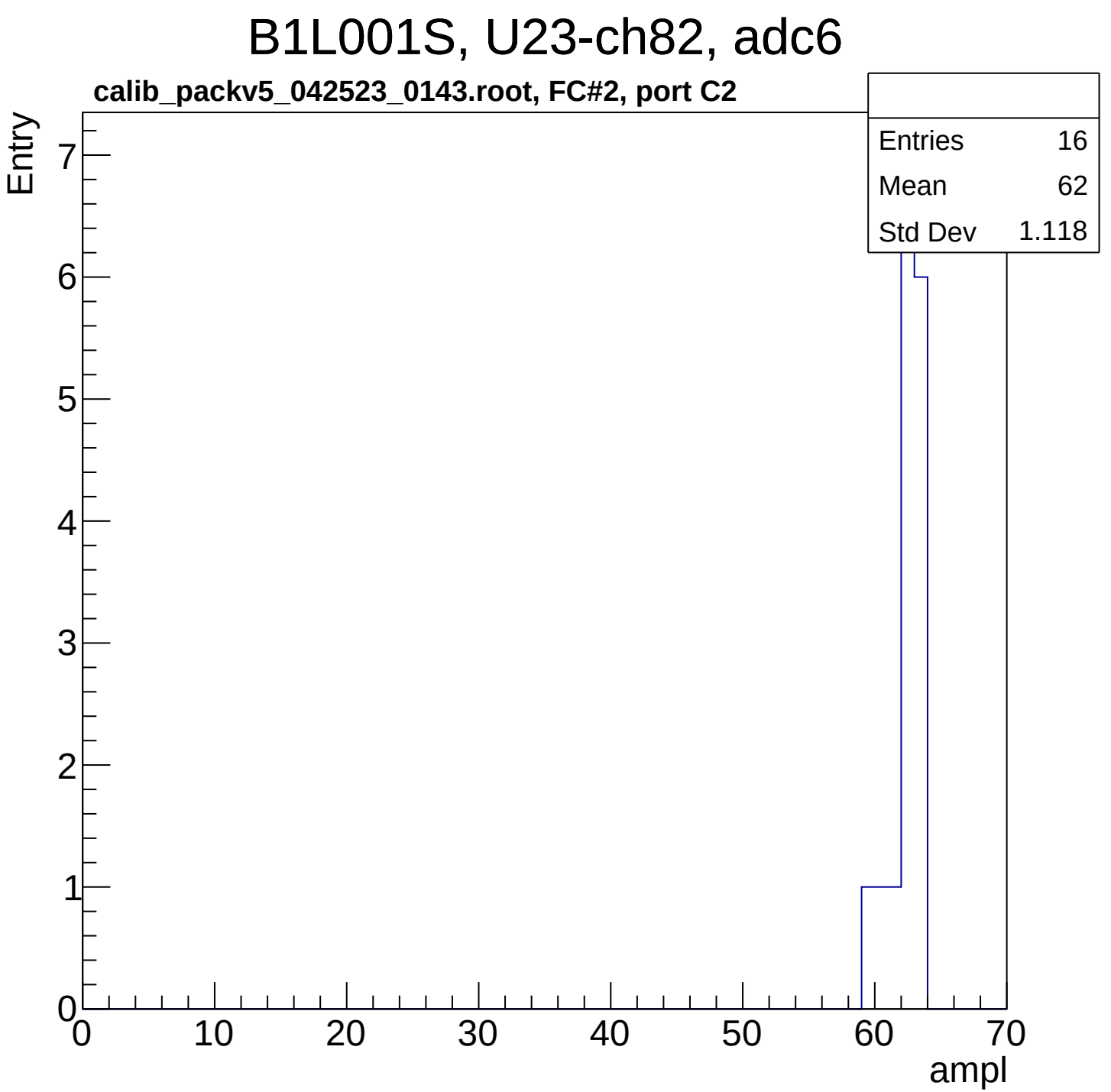
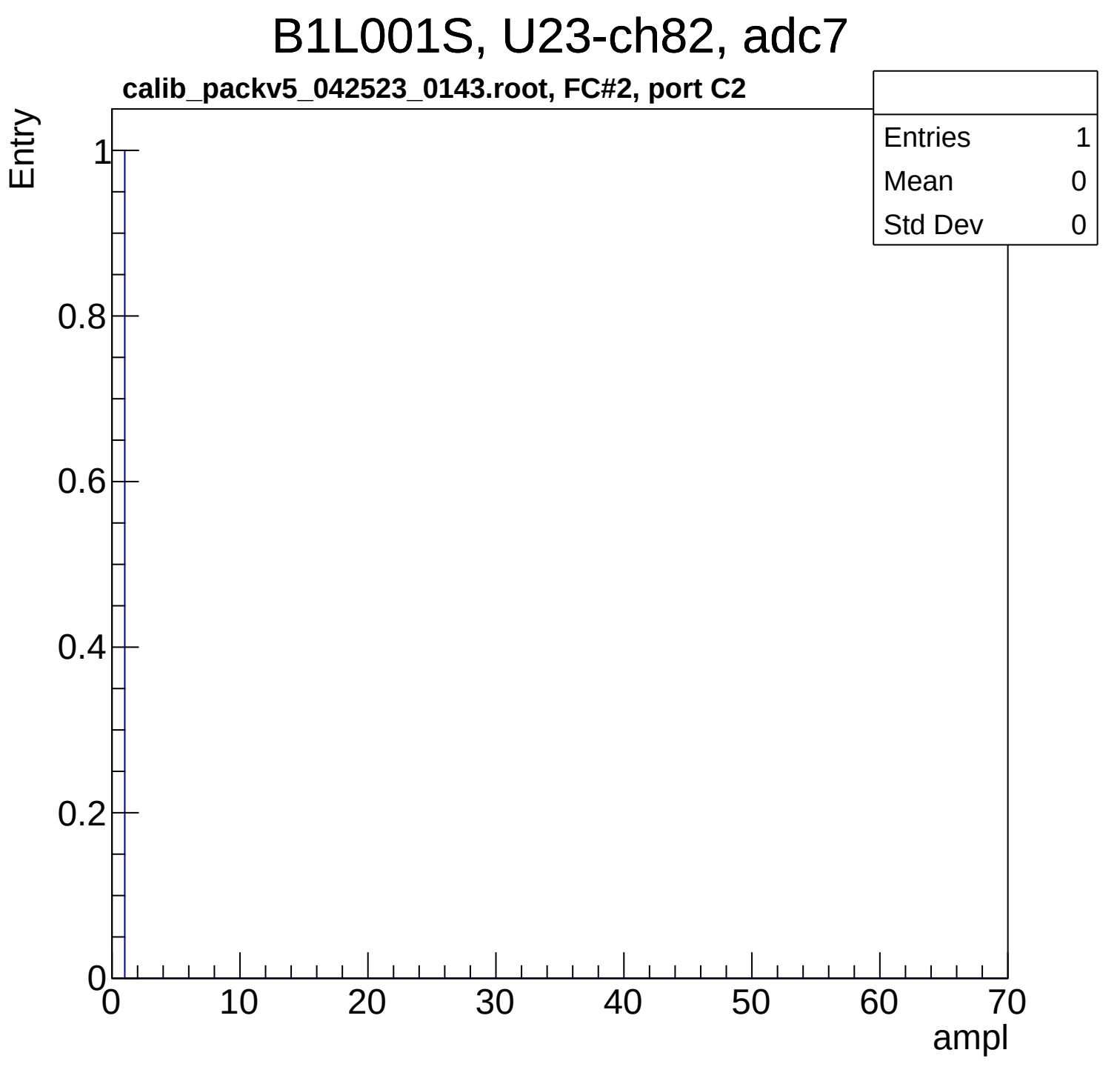

B1L001S, U23-ch82, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 62
Std Dev 1.118
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch82, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
20
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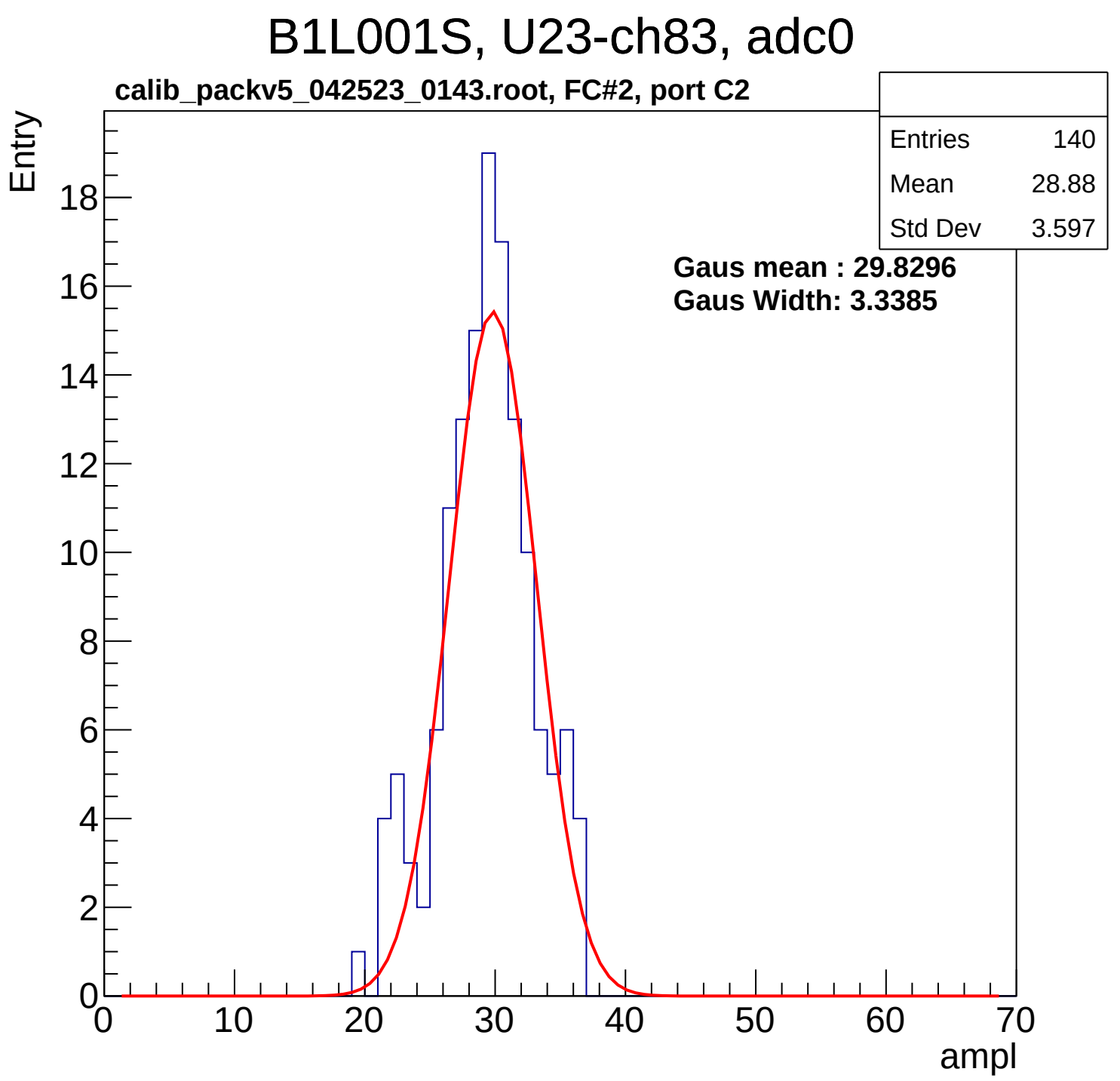
40
50
60
70
ampl


B1L001S, U23-ch83, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 28.88
Std Dev 3.597
Gaus mean : 29.8296
Gaus Width: 3.3385
Entry
ampl
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4
6
8
10
12
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18
0
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20
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60
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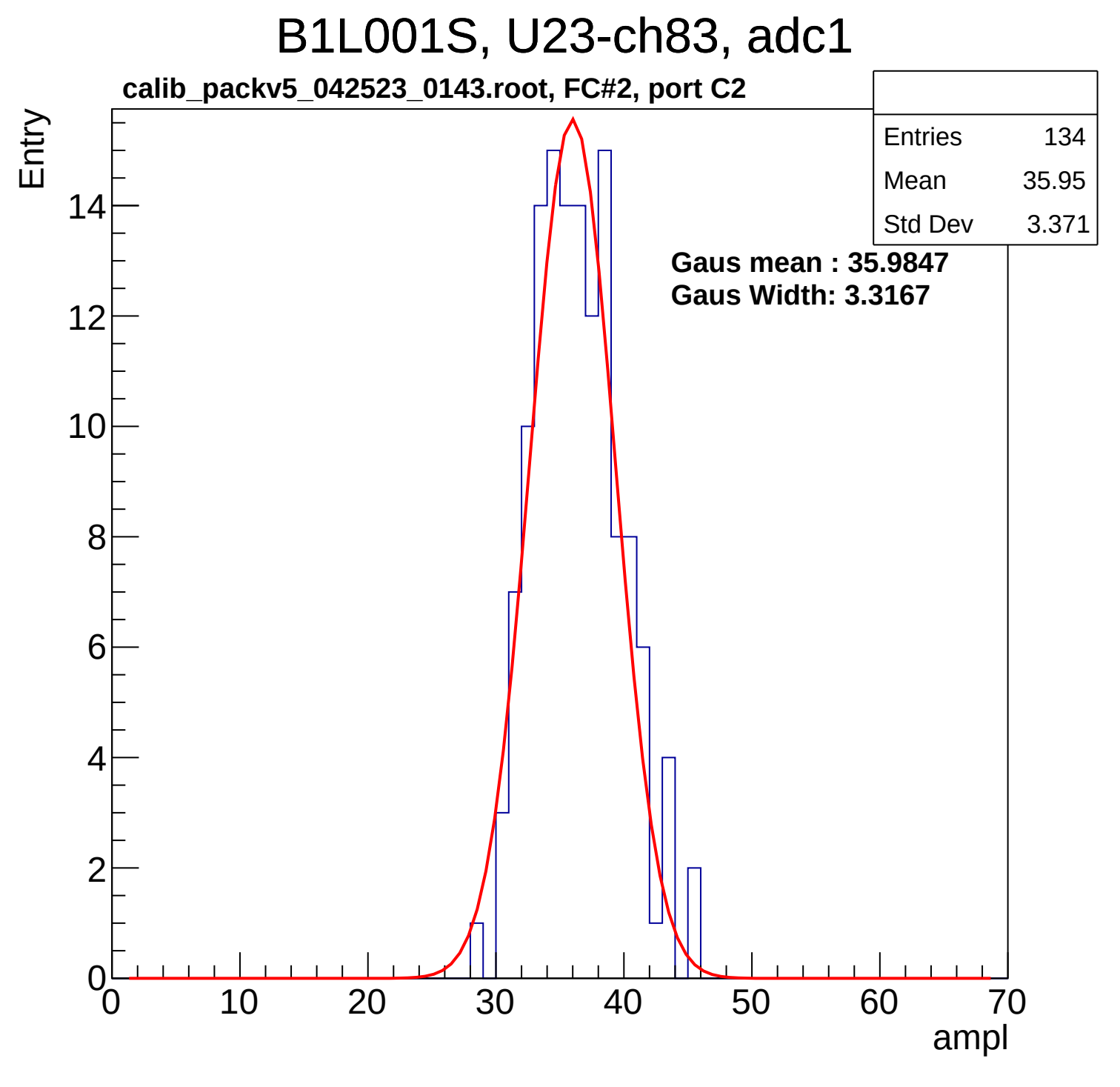
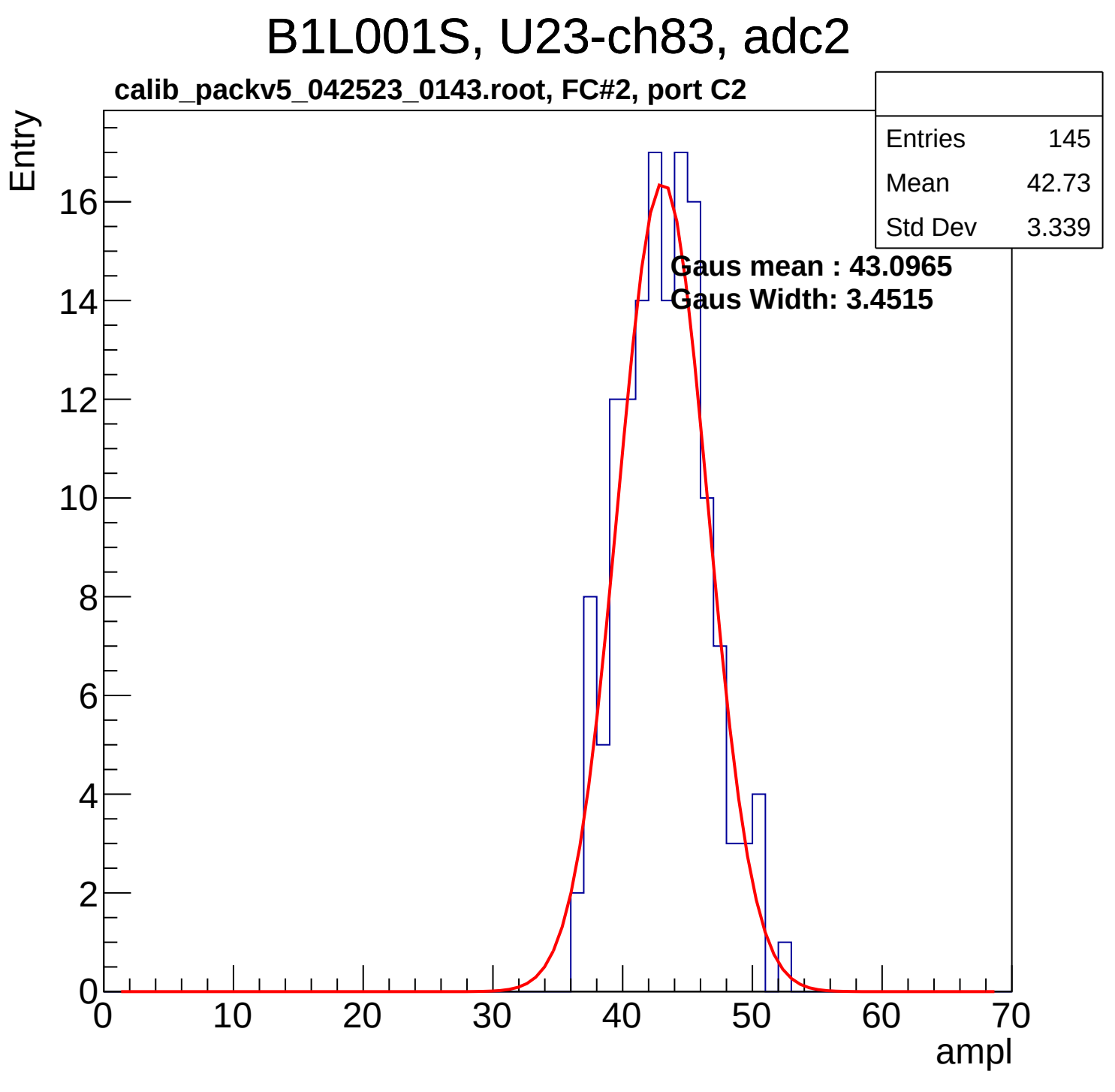
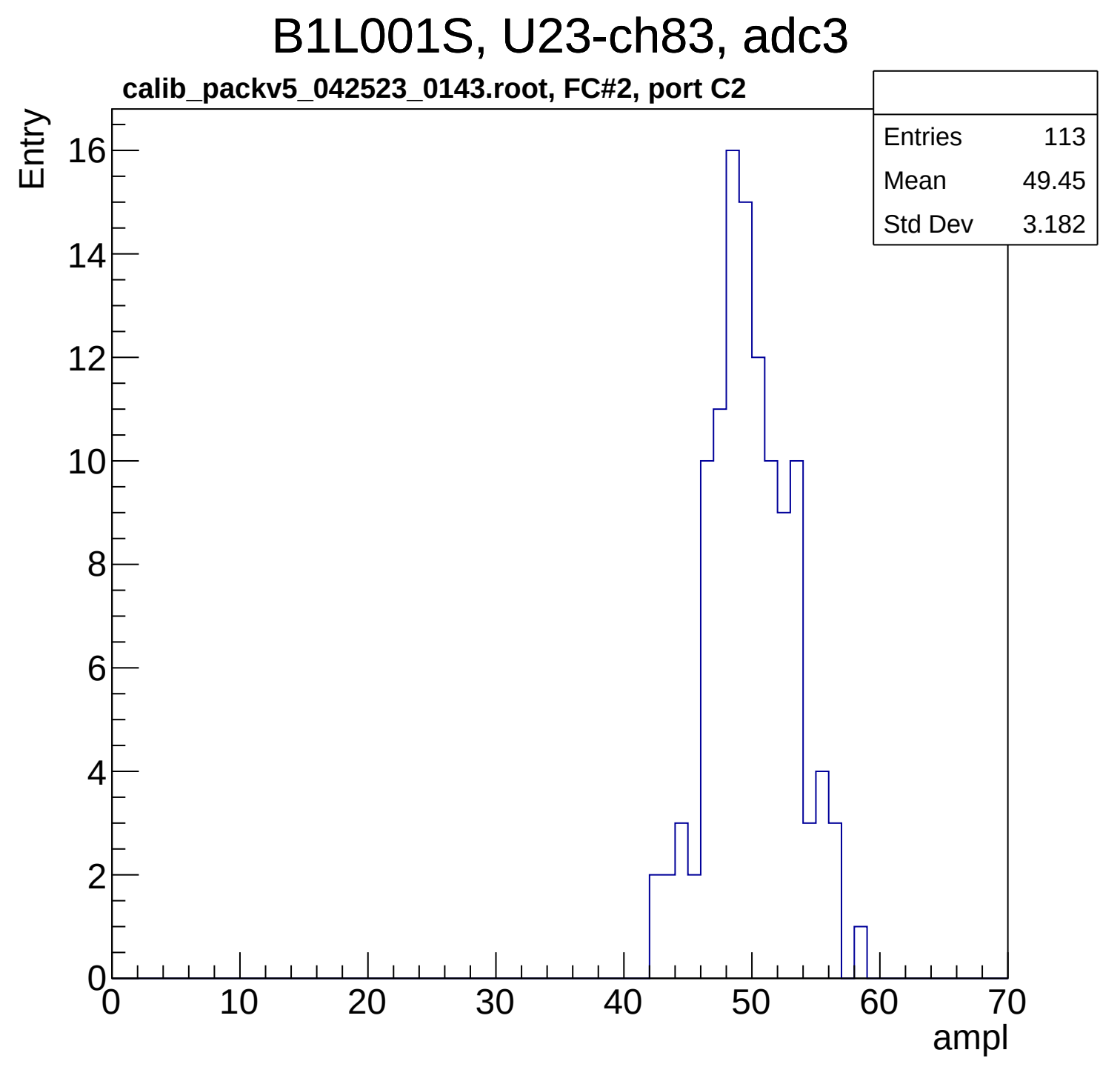

B1L001S, U23-ch83, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 35.95
Std Dev 3.371
Gaus mean : 35.9847
Gaus Width: 3.3167
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch83, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 42.73
Std Dev 3.339
Gaus mean : 43.0965
Gaus Width: 3.4515
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch83, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 49.45
Std Dev 3.182
Entry
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4
6
8
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12
14
16
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10
20
30
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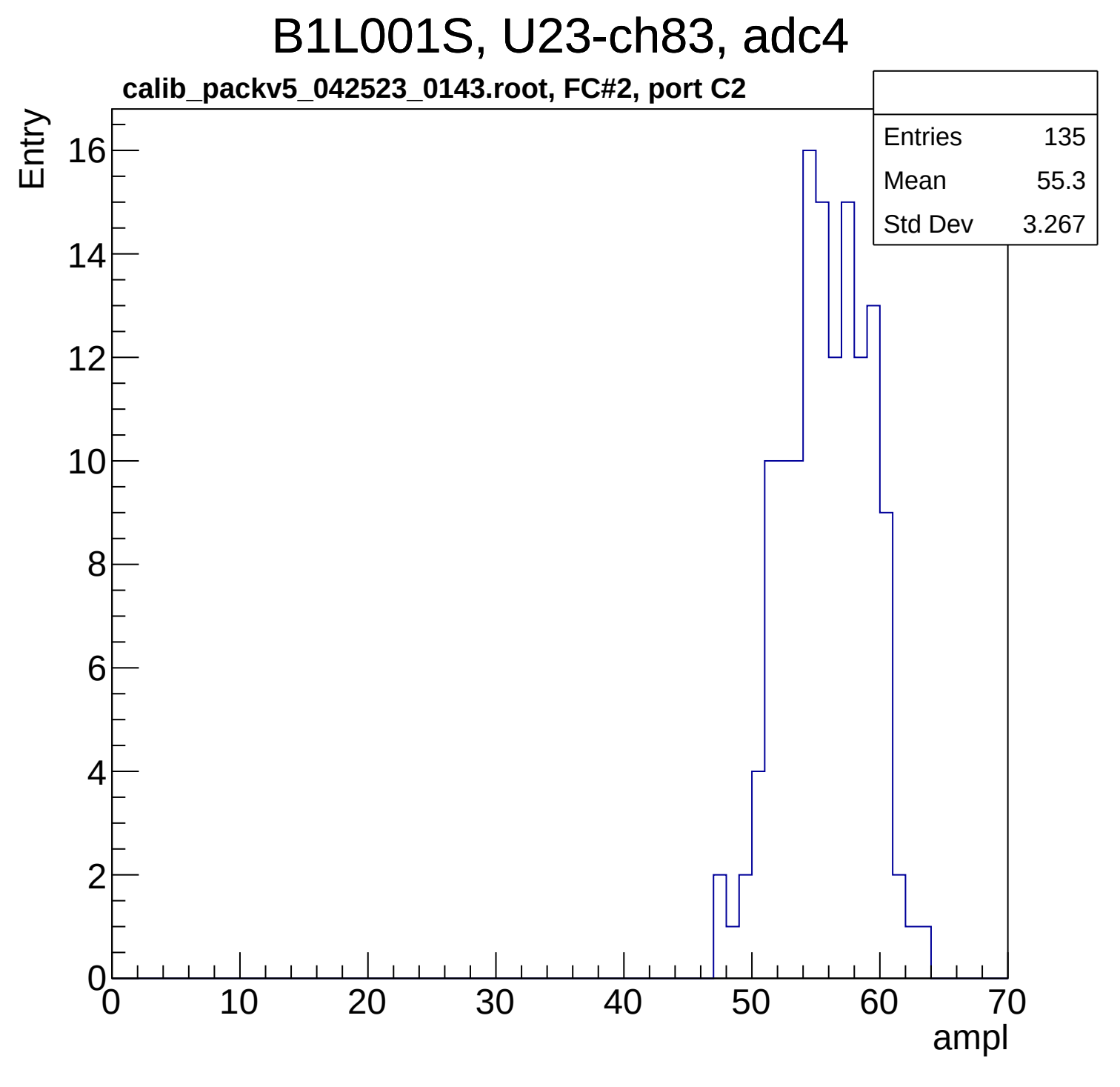
50
60
70
ampl


B1L001S, U23-ch83, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 55.3
Std Dev 3.267
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
50
60
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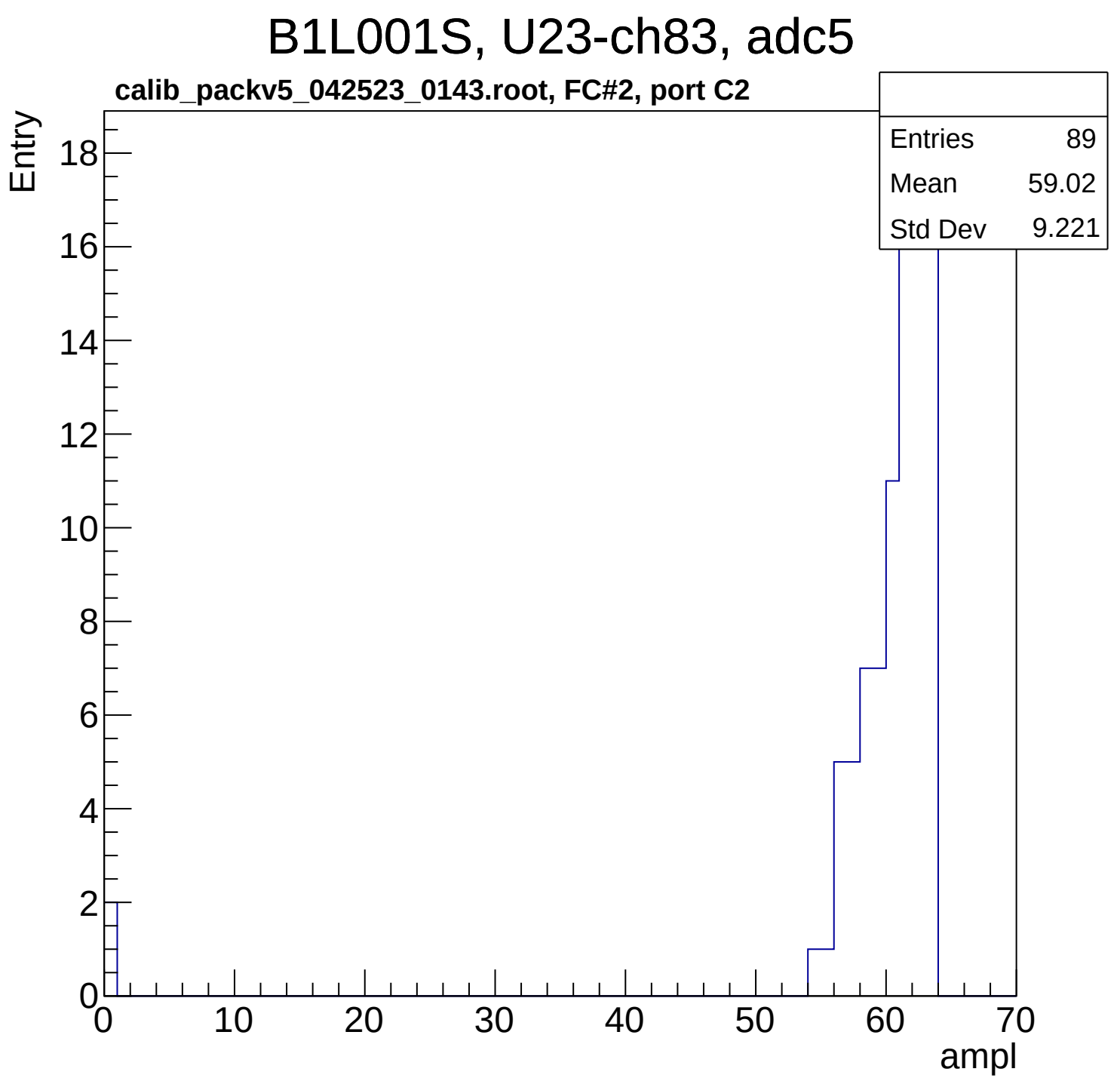
B1L001S, U23-ch83, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 89
Mean 59.02
Std Dev 9.221
Entry
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6
8
10
12
14
16
18
0
10
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30
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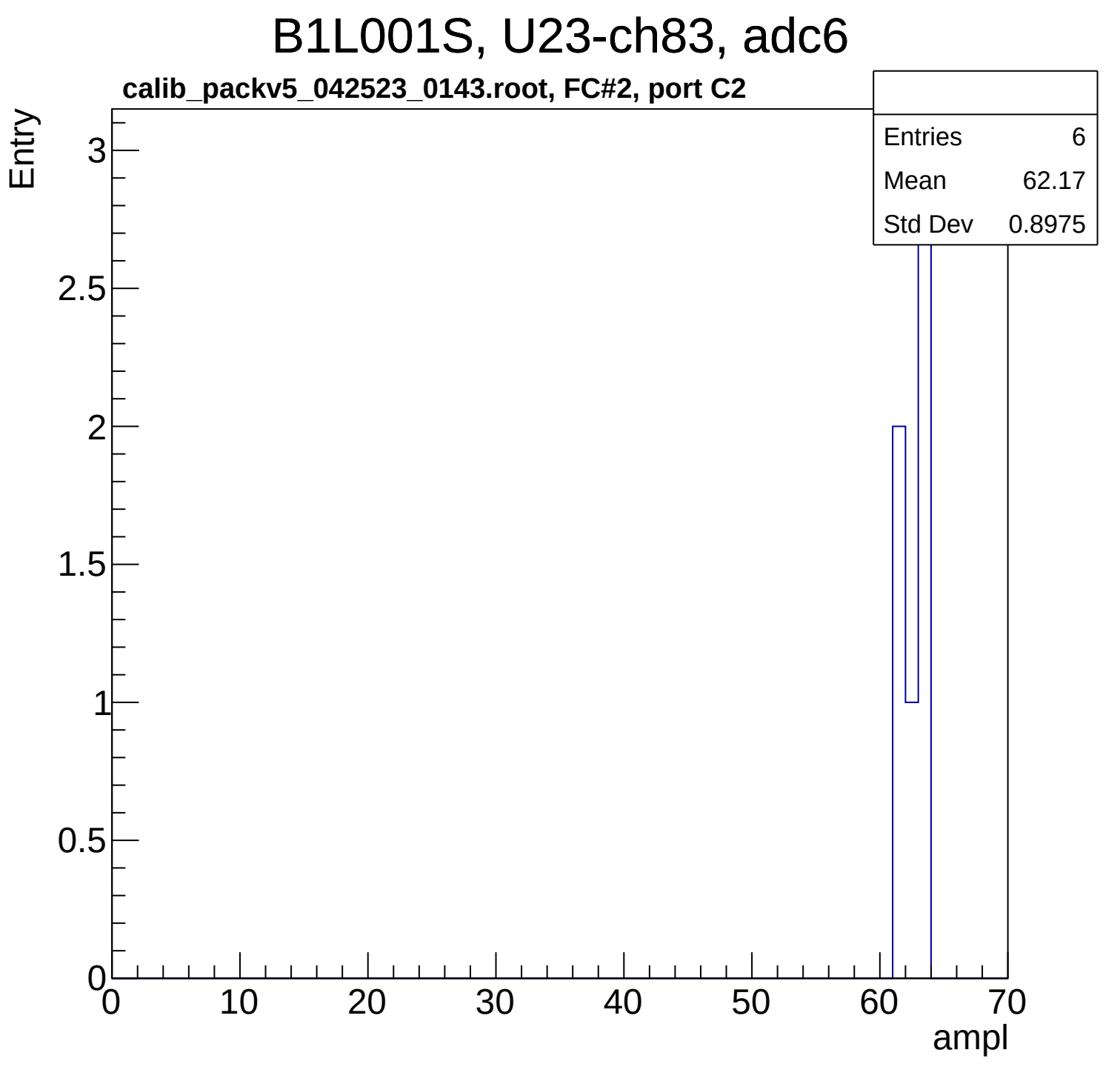
50
60
70
ampl


B1L001S, U23-ch83, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62.17
Std Dev 0.8975
Entry
ampl
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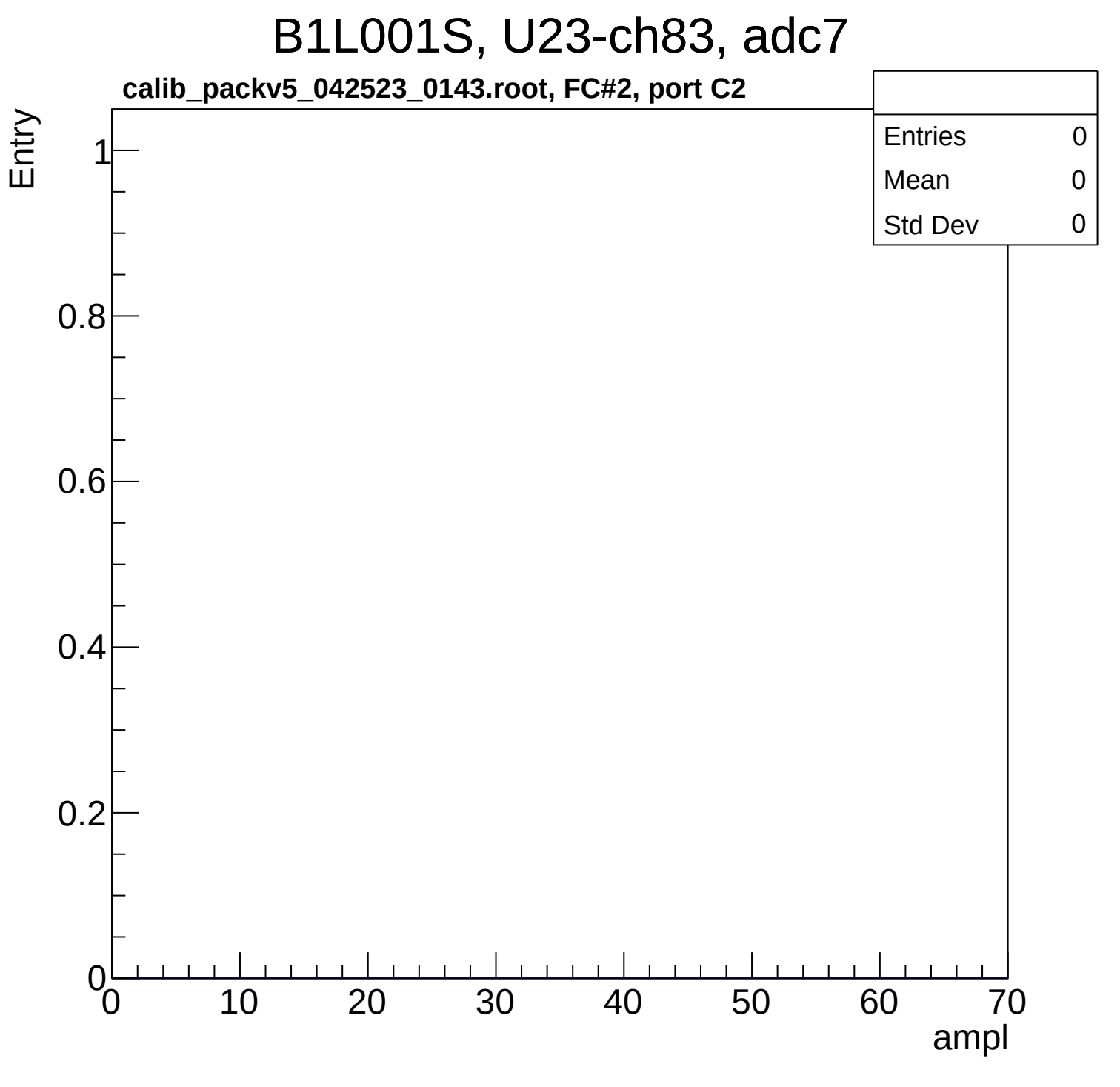
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch83, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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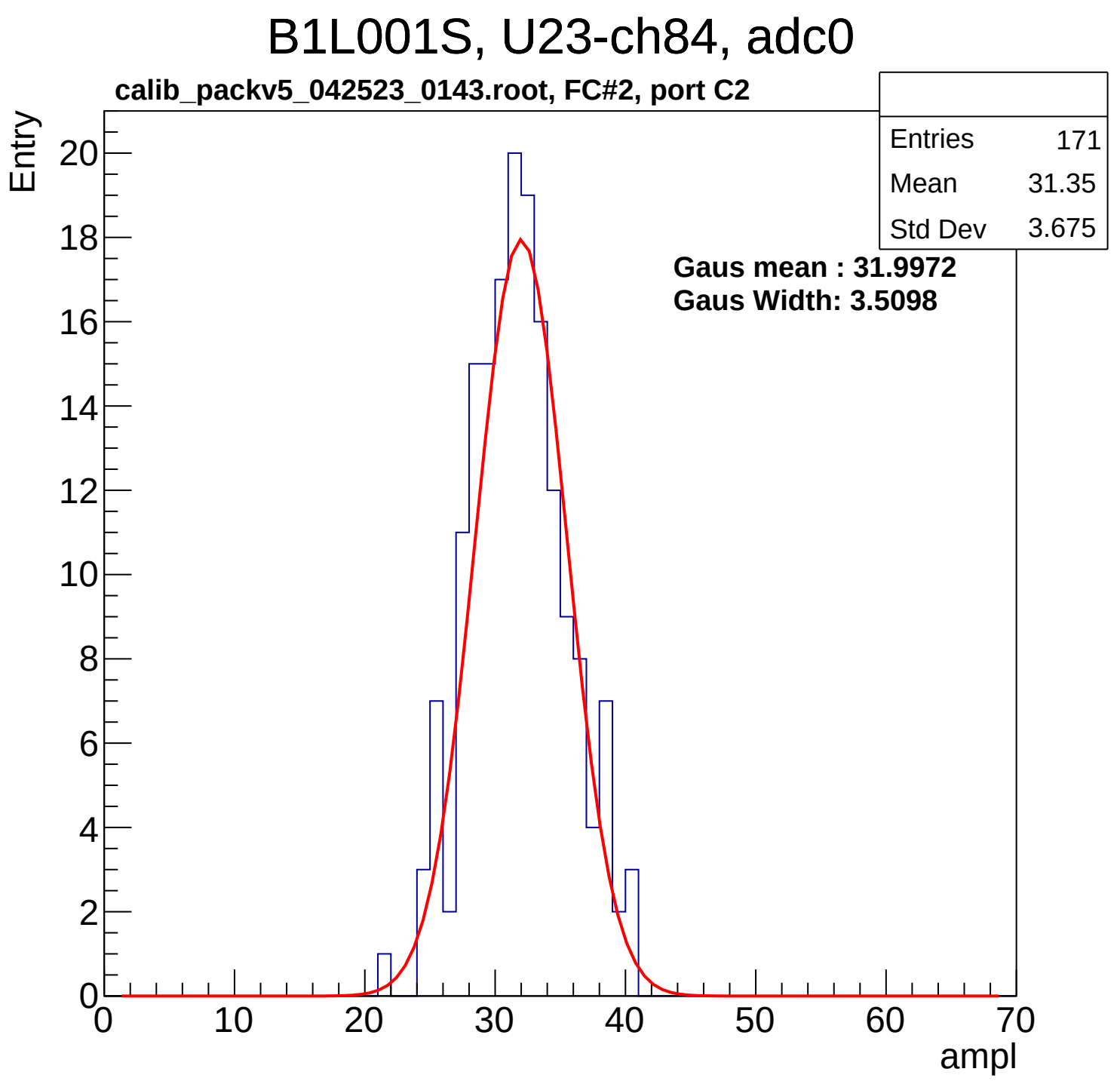
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch84, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 171
Mean 31.35
Std Dev 3.675
Gaus mean : 31.9972
Gaus Width: 3.5098
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
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60
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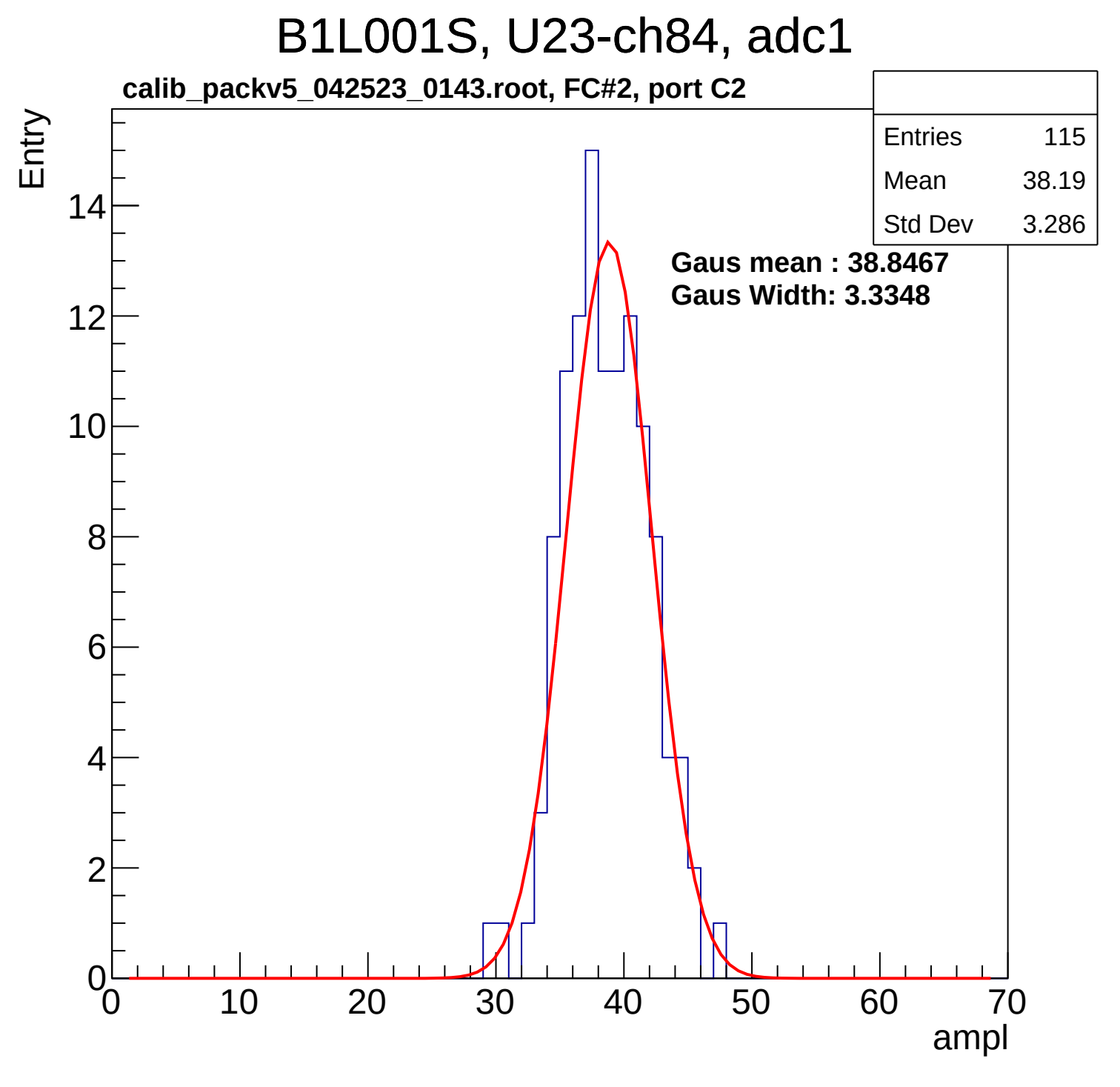
B1L001S, U23-ch84, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 38.19
Std Dev 3.286
Gaus mean : 38.8467
Gaus Width: 3.3348
Entry
ampl
0
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4
6
8
10
12
14
0
10
20
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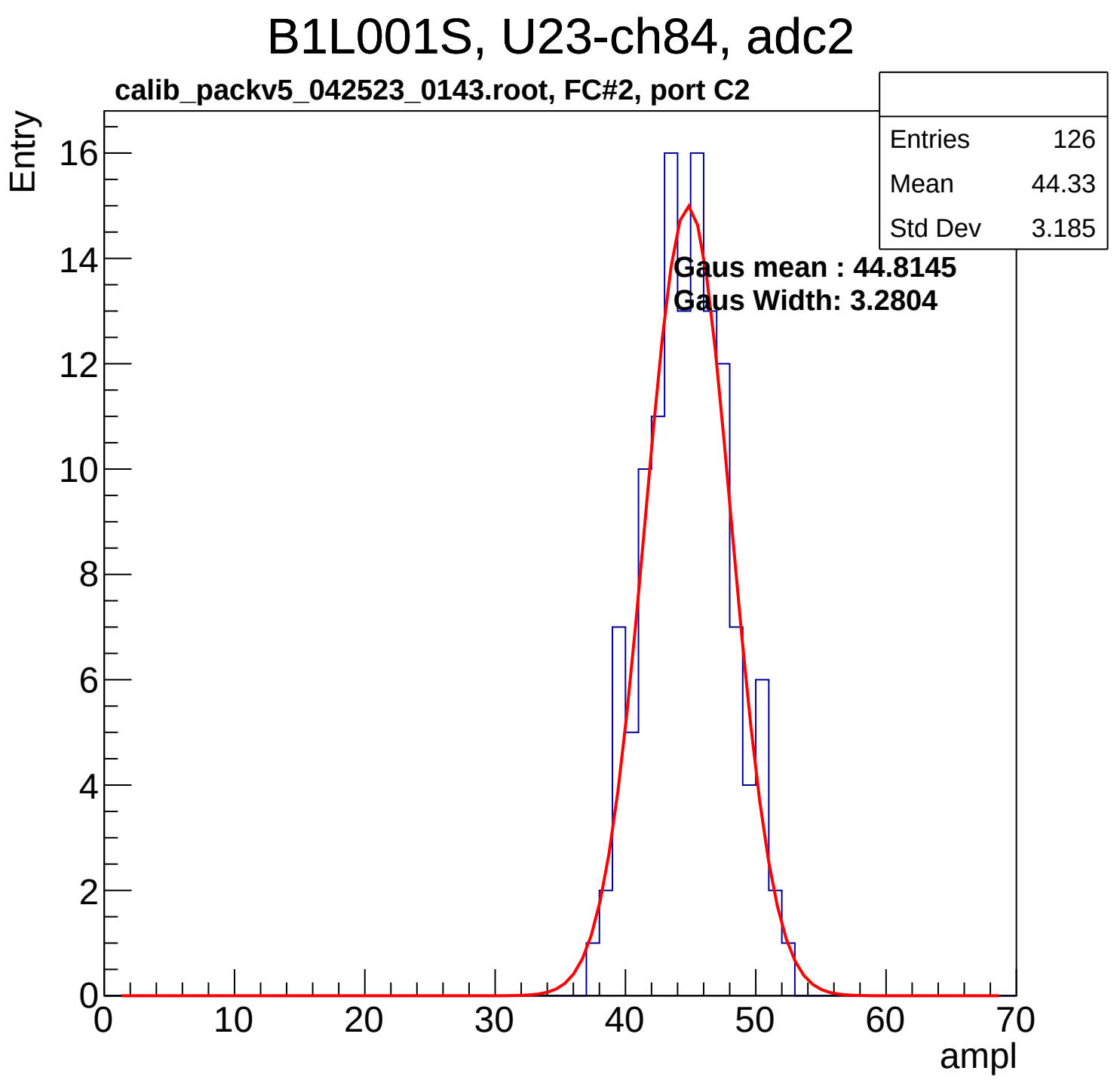
B1L001S, U23-ch84, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 44.33
Std Dev 3.185
Gaus mean : 44.8145
Gaus Width: 3.2804
Entry
ampl
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2
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6
8
10
12
14
16
0
10
20
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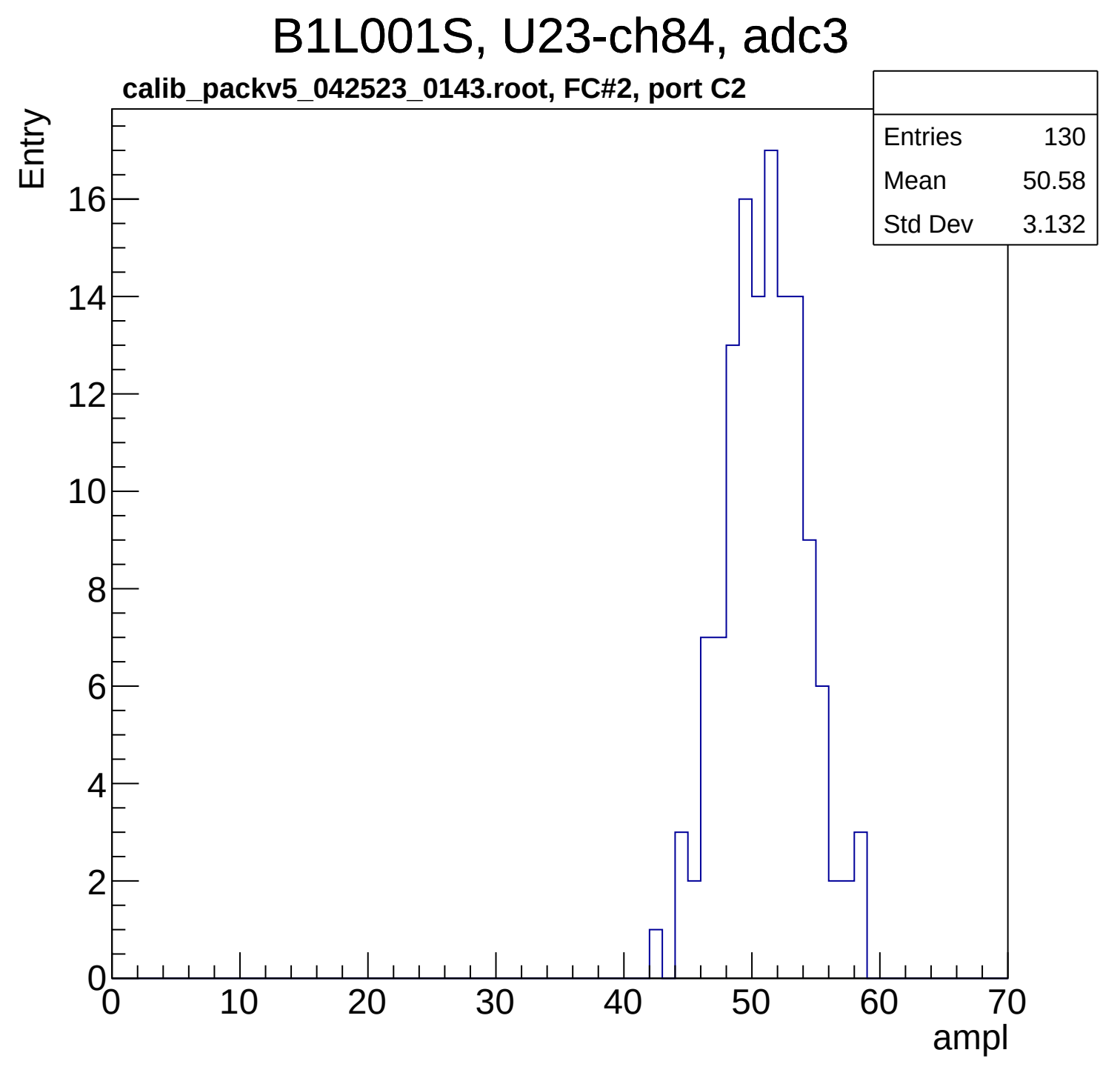
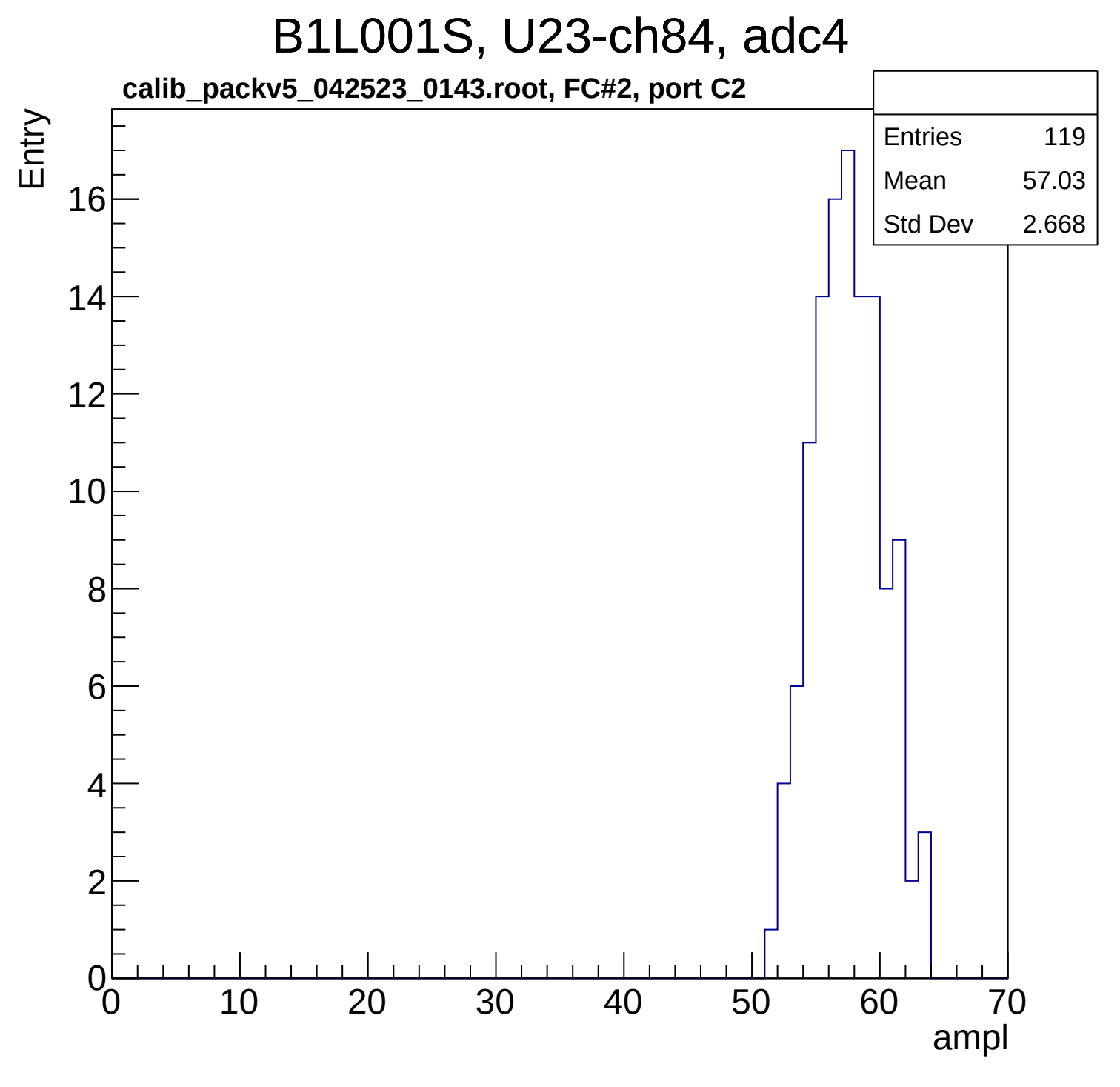

B1L001S, U23-ch84, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 50.58
Std Dev 3.132
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch84, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 57.03
Std Dev 2.668
Entry
0
2
4
6
8
10
12
14
16
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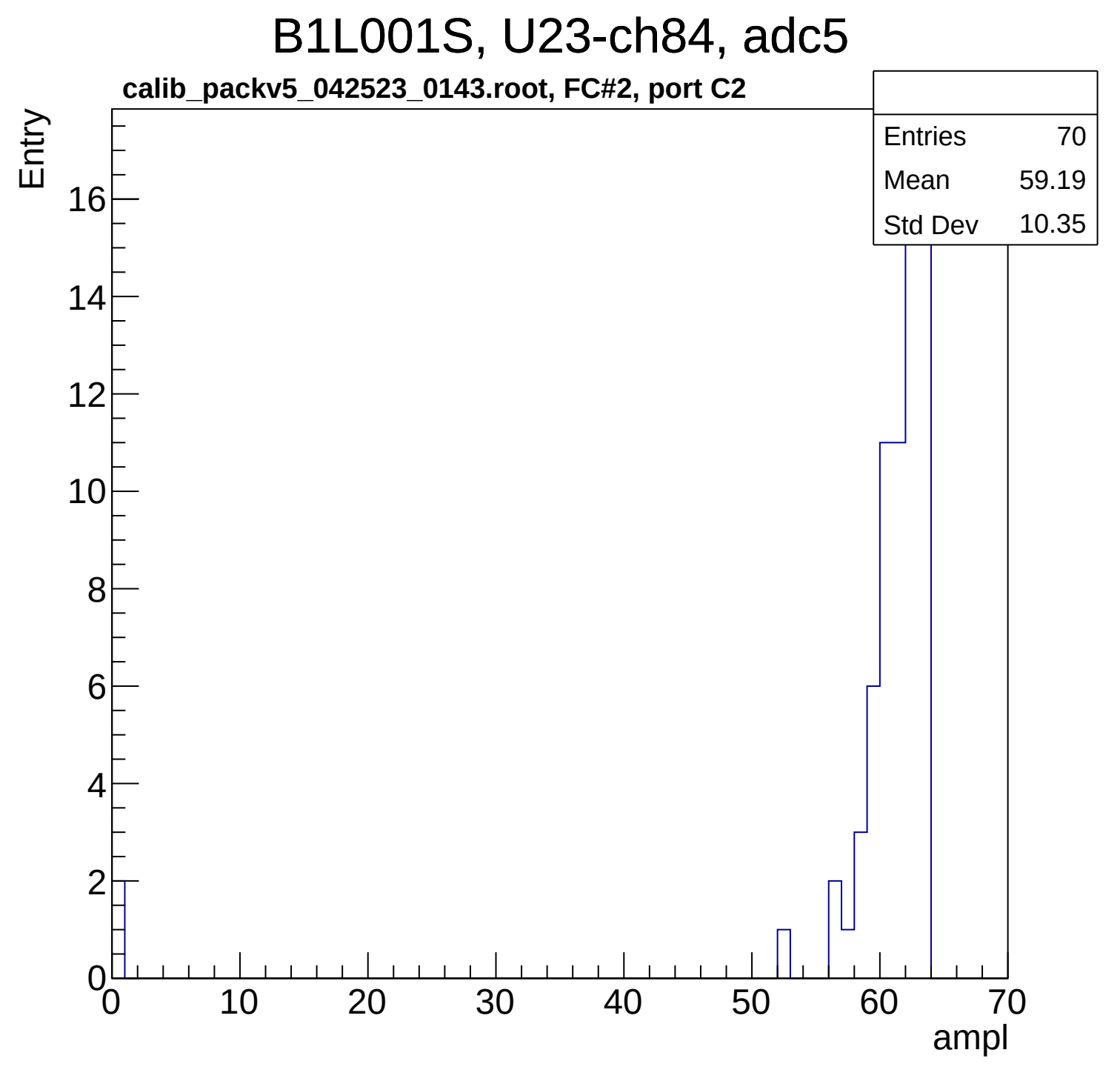
70
ampl


B1L001S, U23-ch84, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 70
Mean 59.19
Std Dev 10.35
Entry
0
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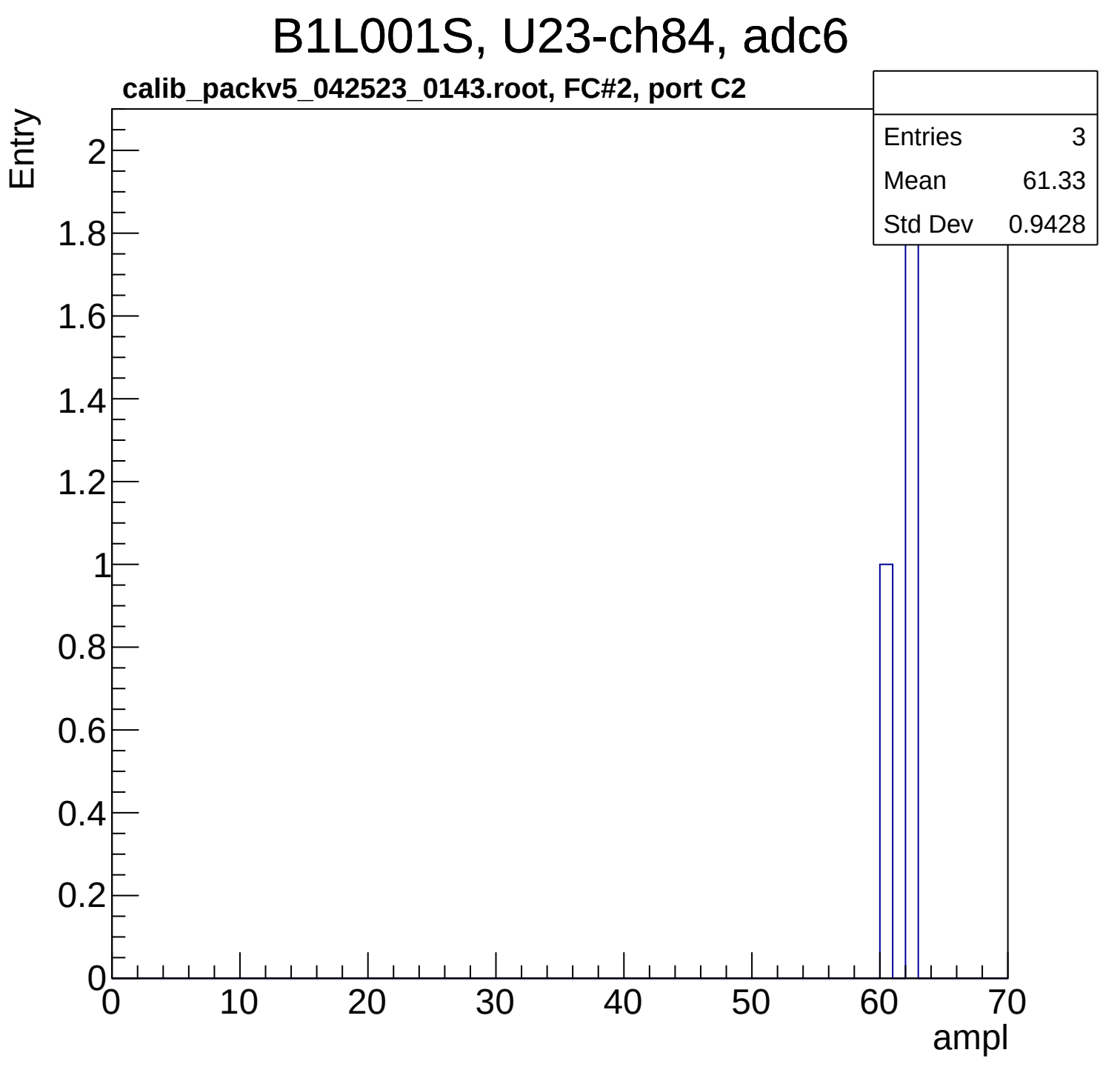
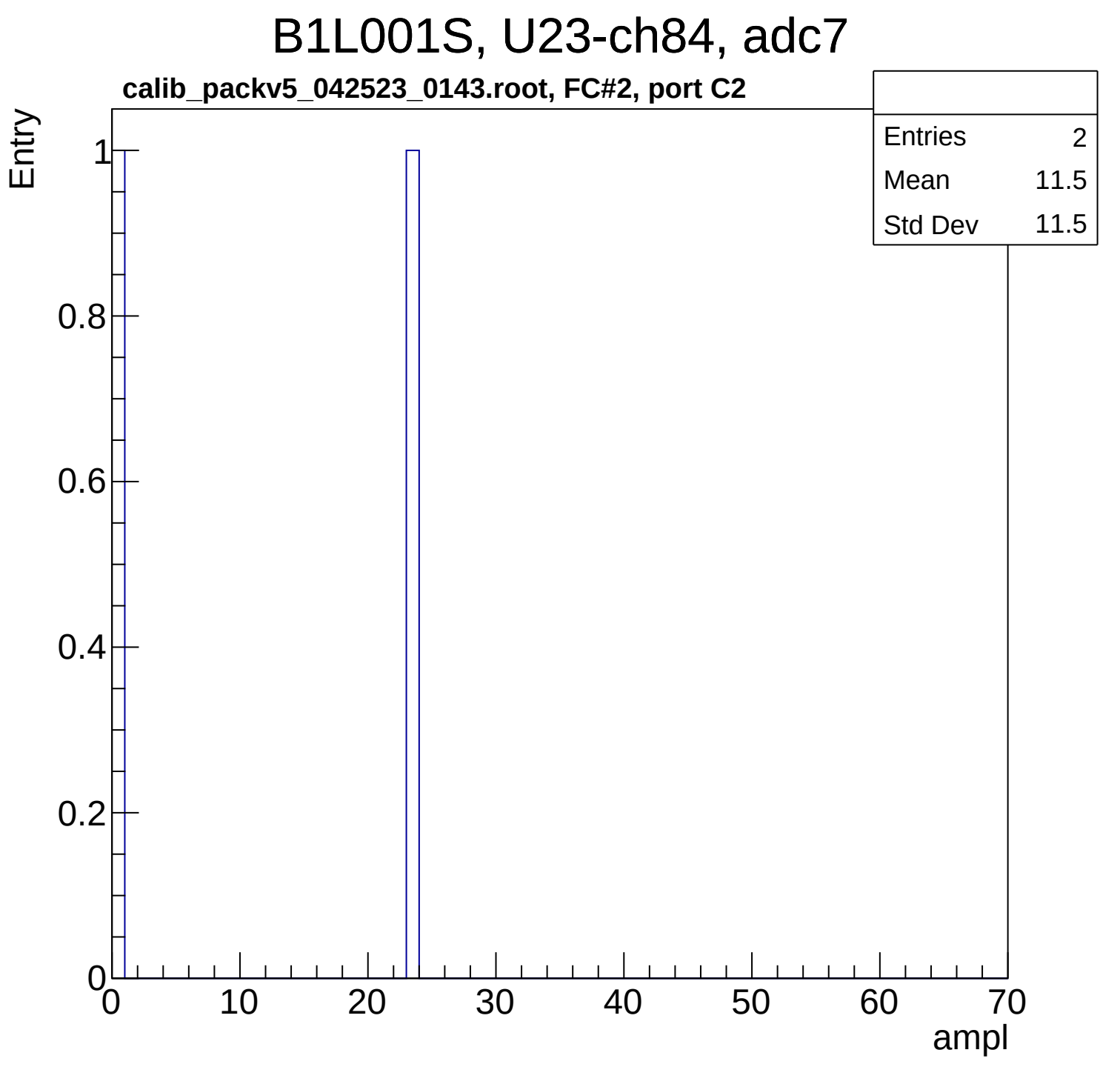
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch84, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 61.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch84, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
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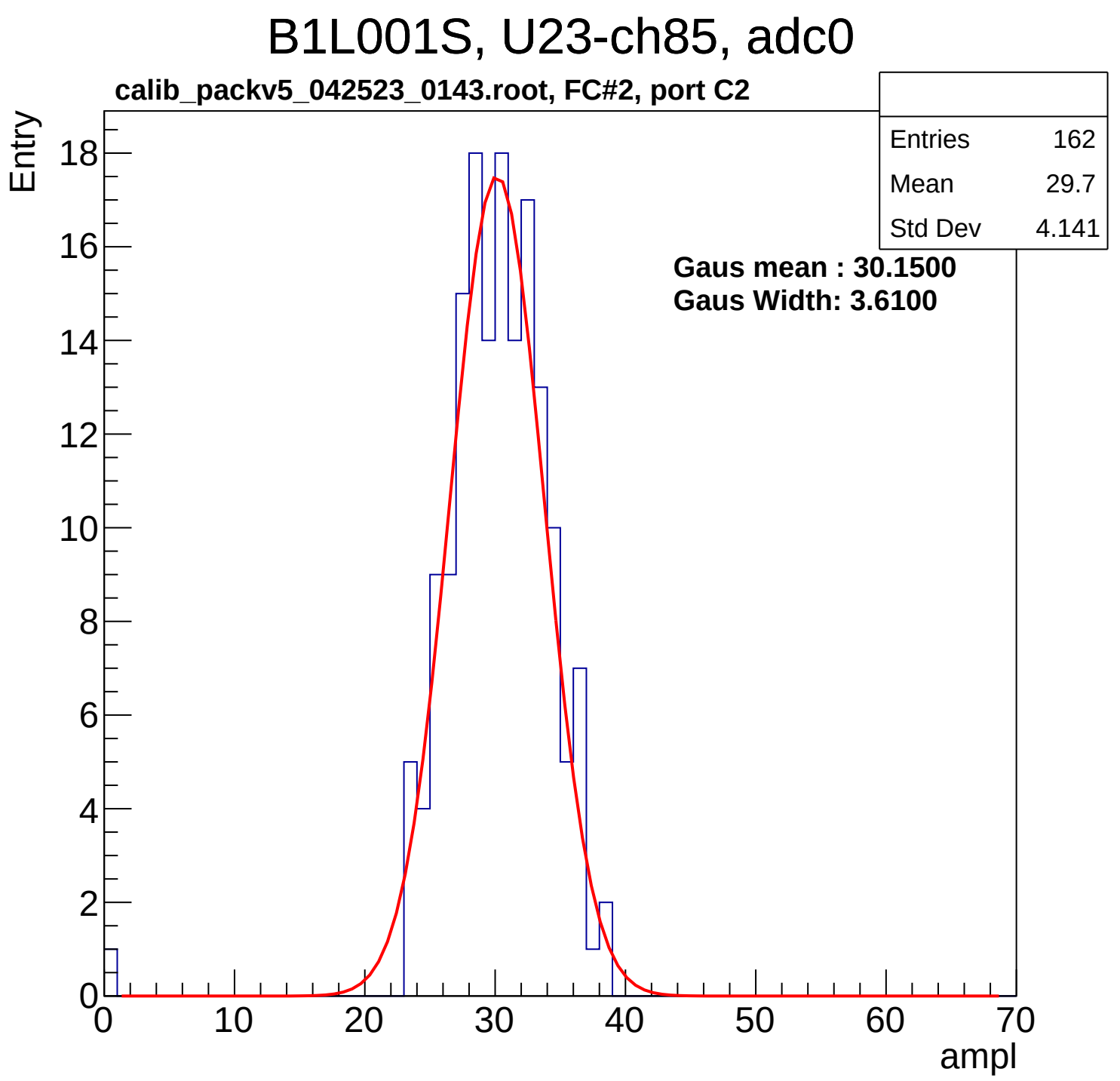
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch85, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 162
Mean 29.7
Std Dev 4.141
Gaus mean : 30.1500
Gaus Width: 3.6100
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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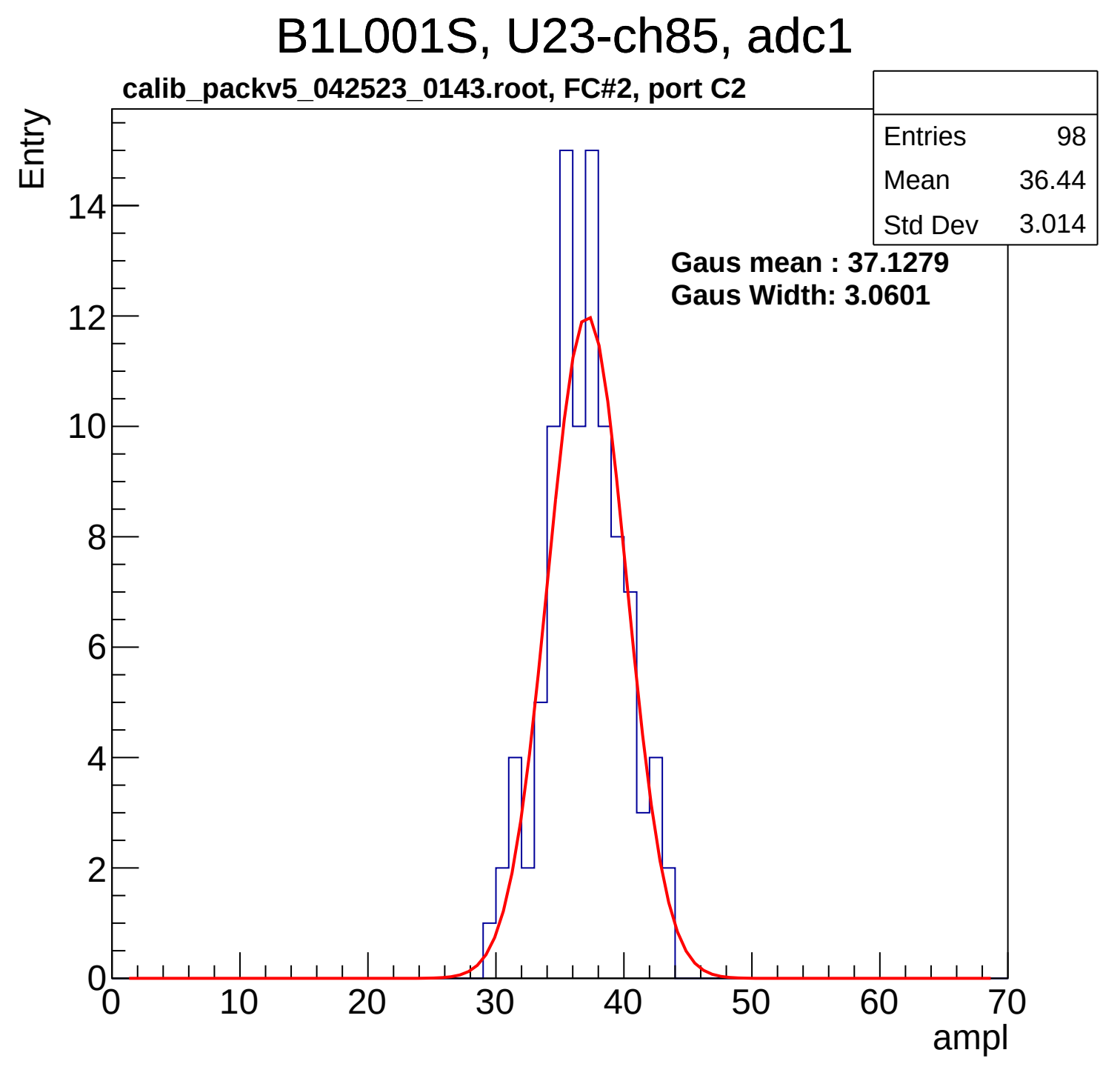
B1L001S, U23-ch85, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 36.44
Std Dev 3.014
Gaus mean : 37.1279
Gaus Width: 3.0601
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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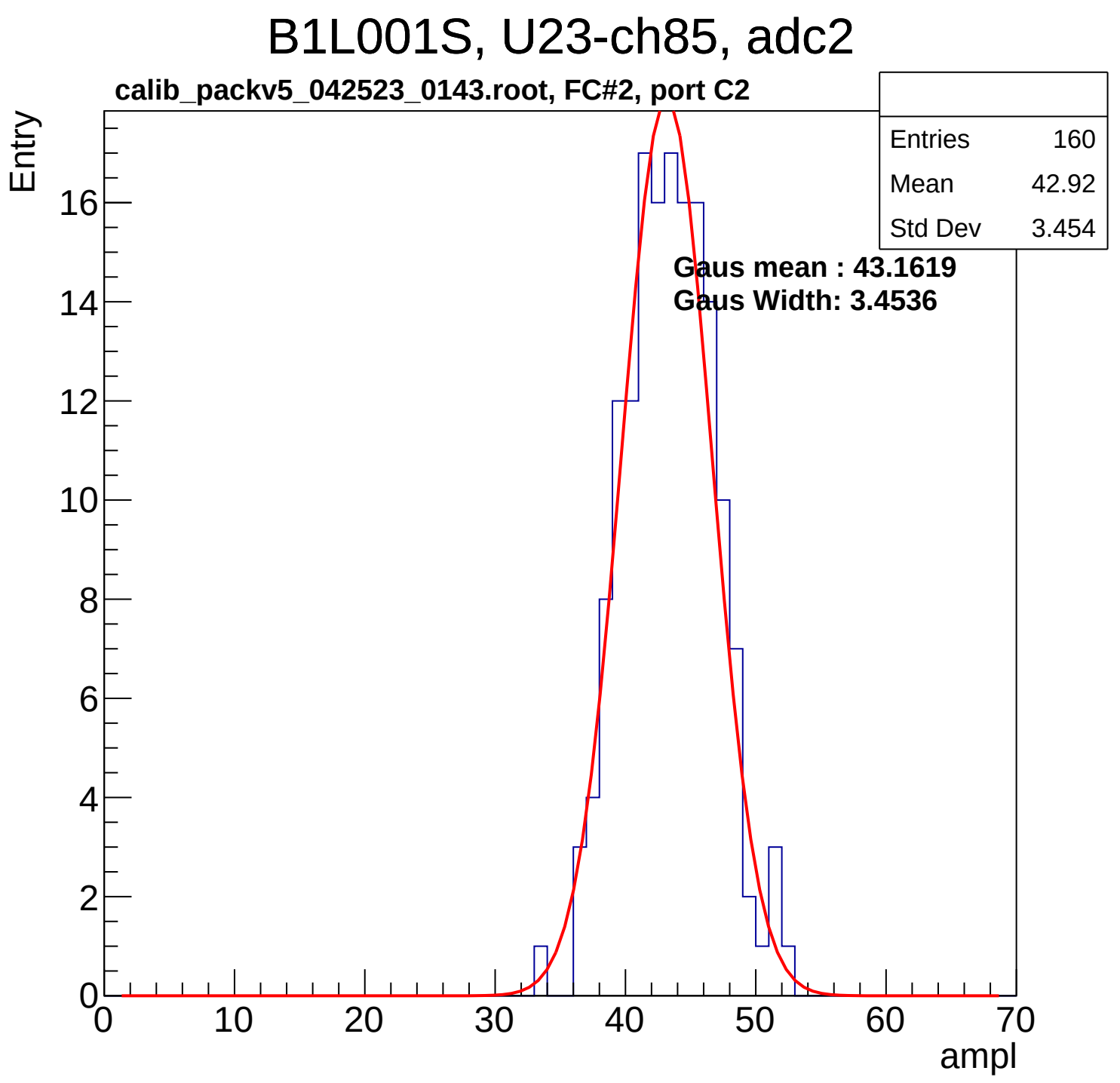
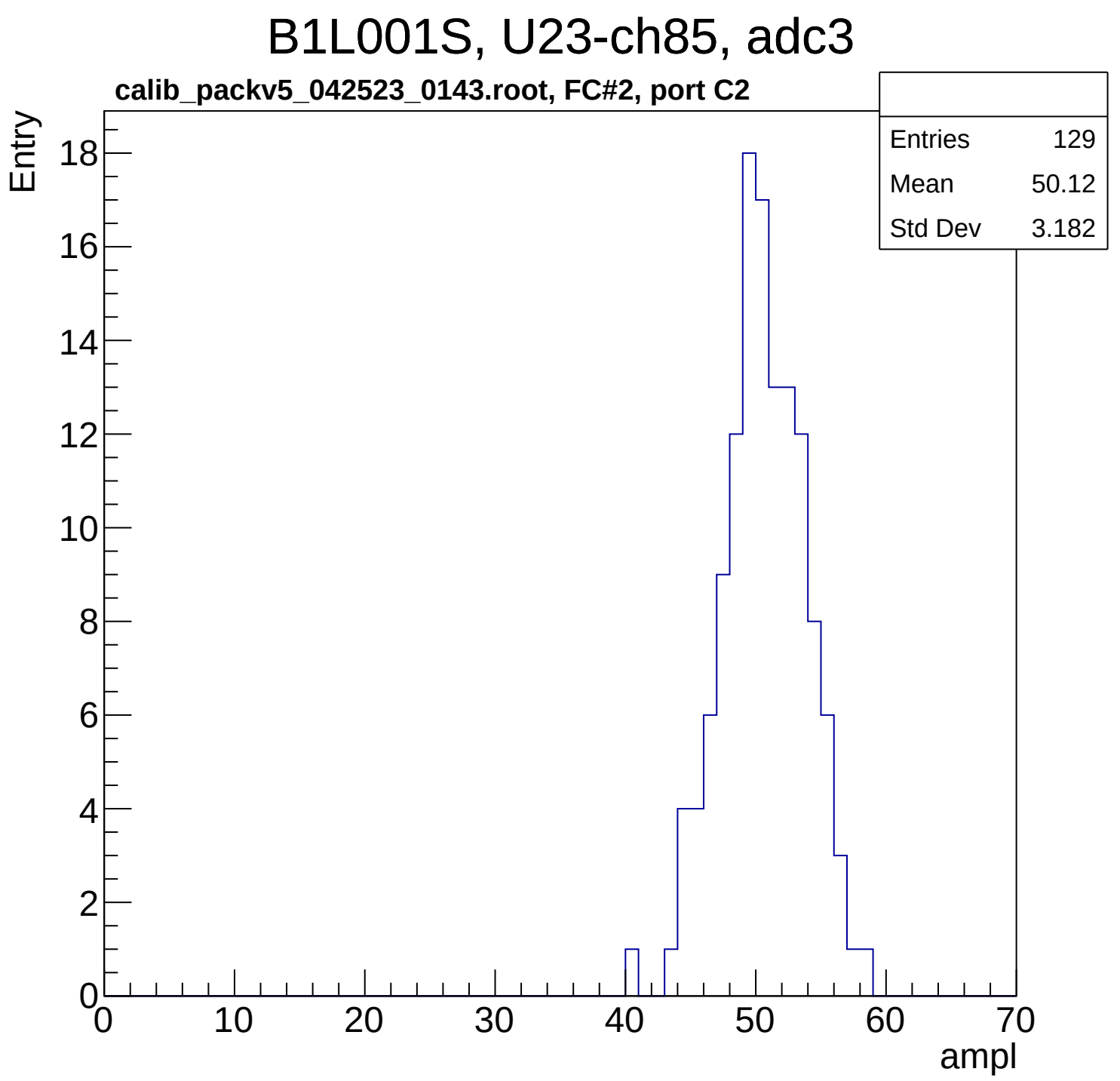
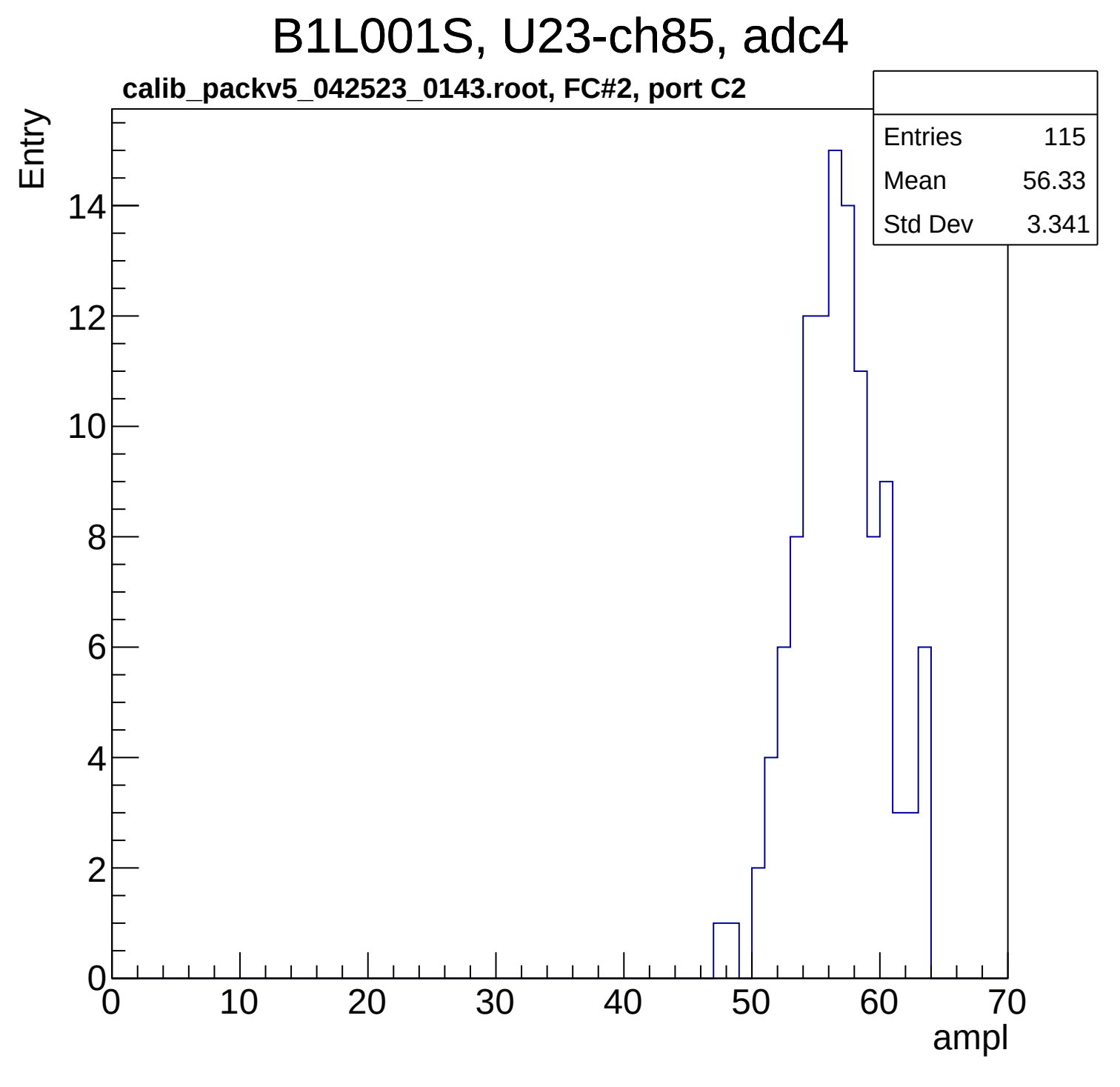
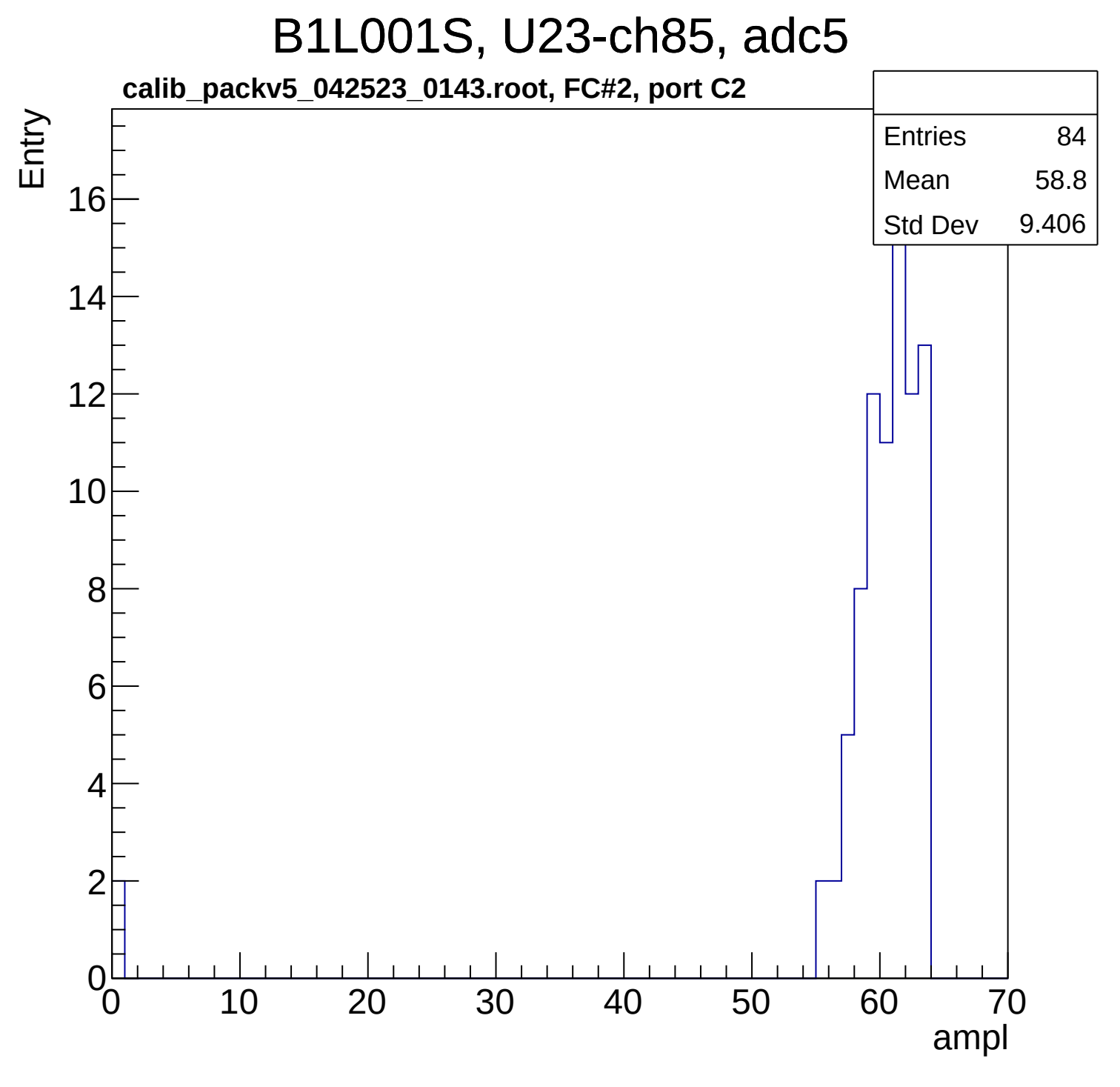

B1L001S, U23-ch85, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 160
Mean 42.92
Std Dev 3.454
Gaus mean : 43.1619
Gaus Width: 3.4536
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch85, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 50.12
Std Dev 3.182
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch85, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 56.33
Std Dev 3.341
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch85, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 84
Mean 58.8
Std Dev 9.406
Entry
ampl
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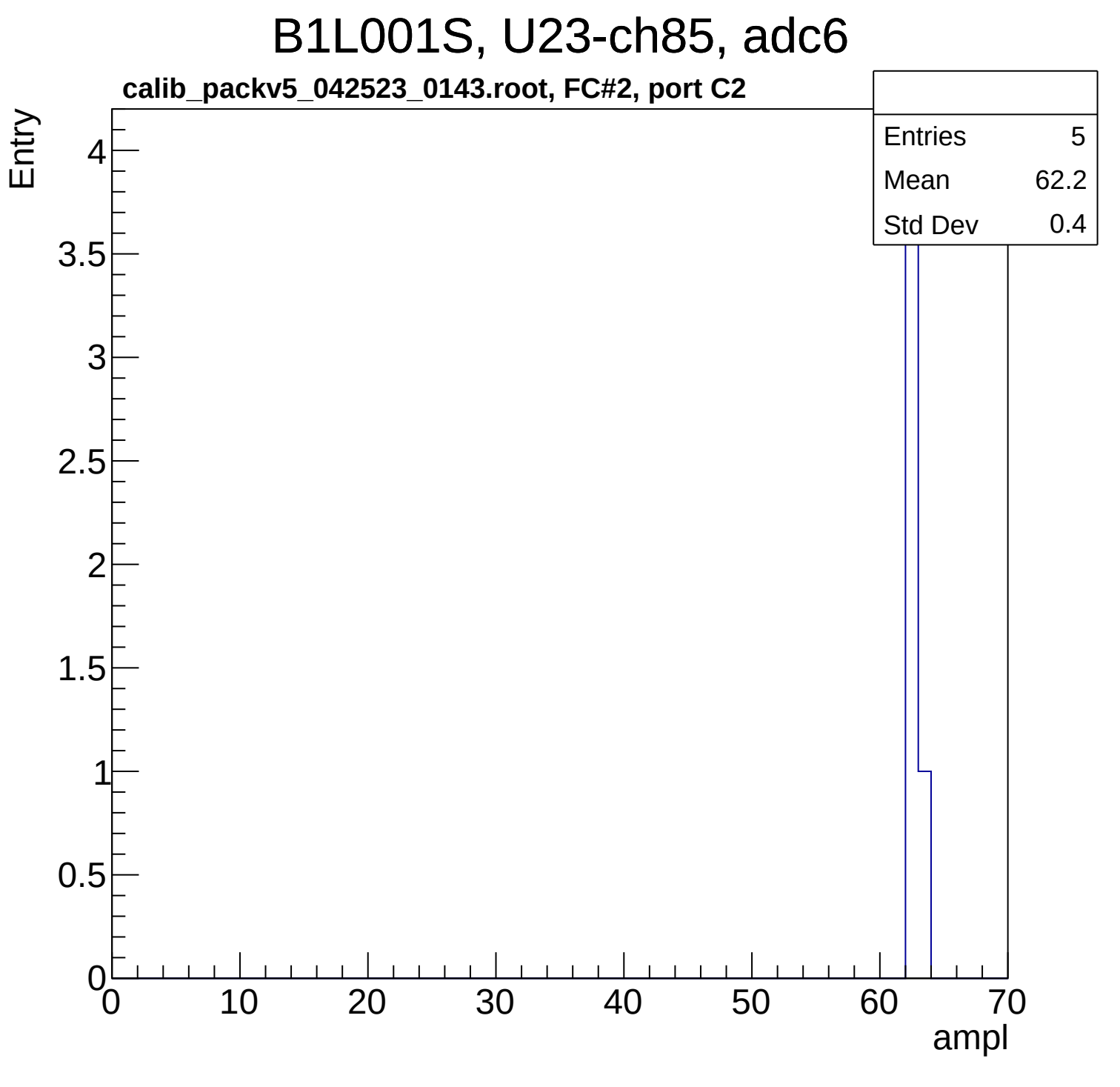
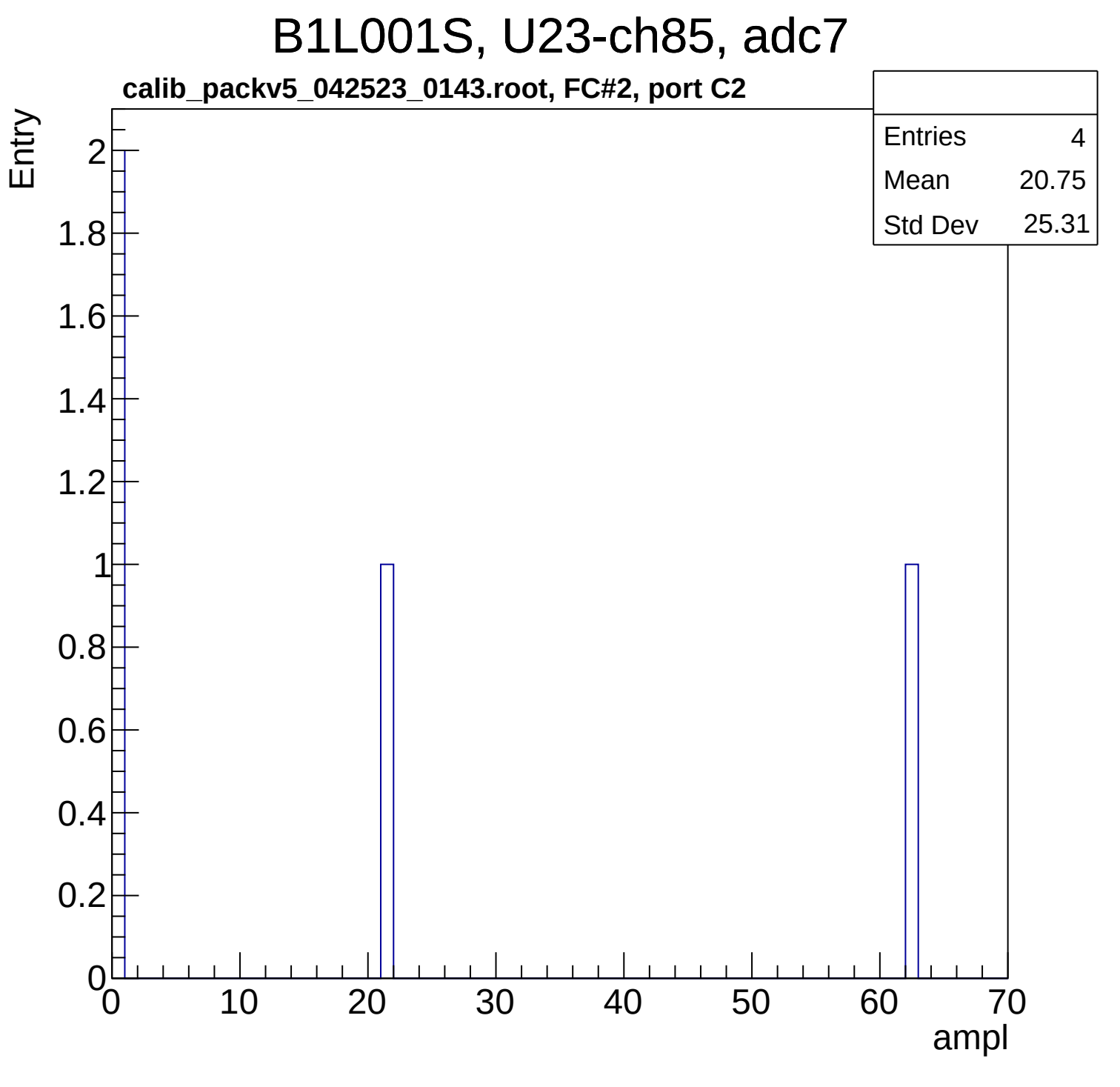
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch85, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 5
Mean 62.2
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch85, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 20.75
Std Dev 25.31
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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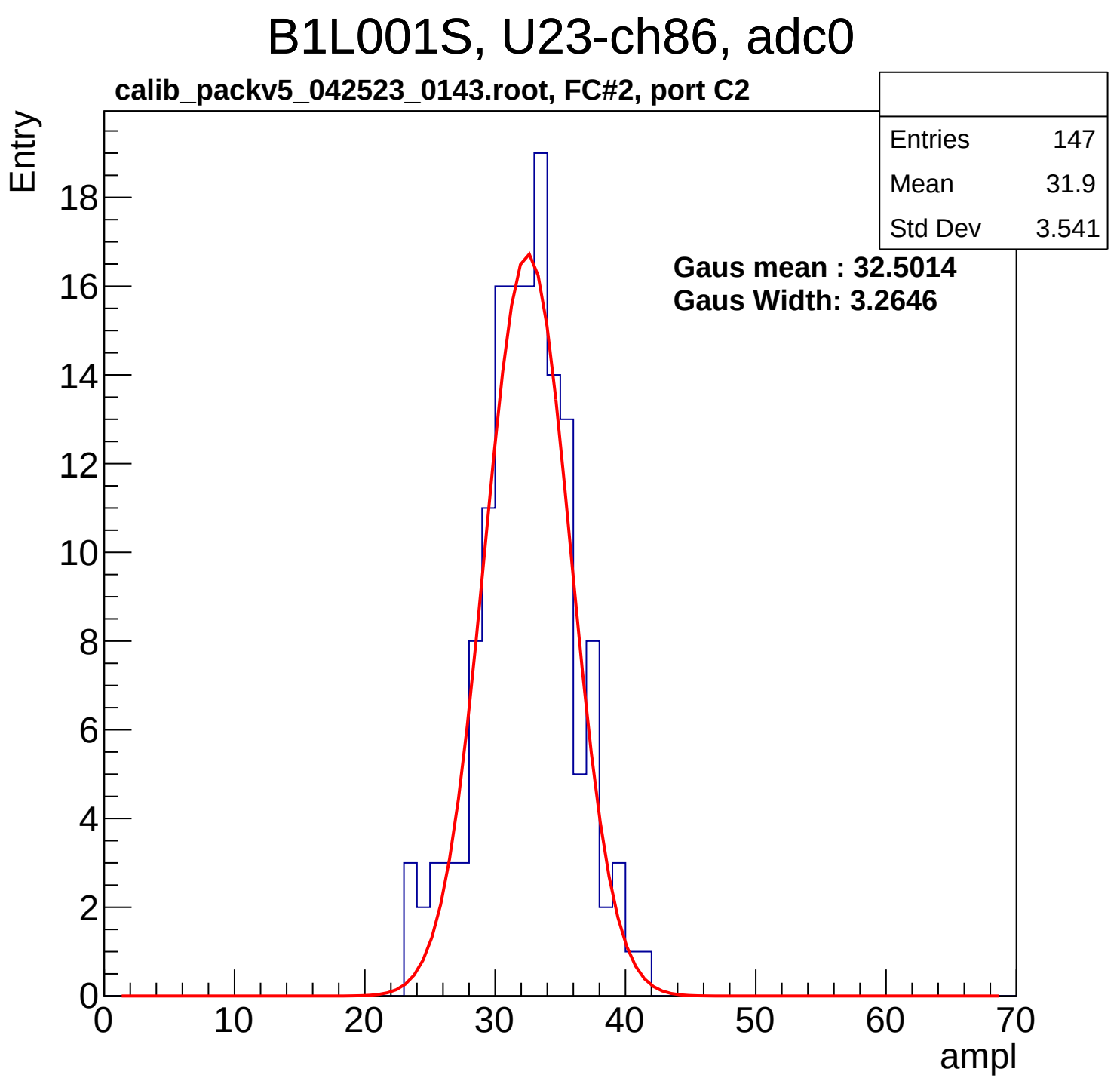
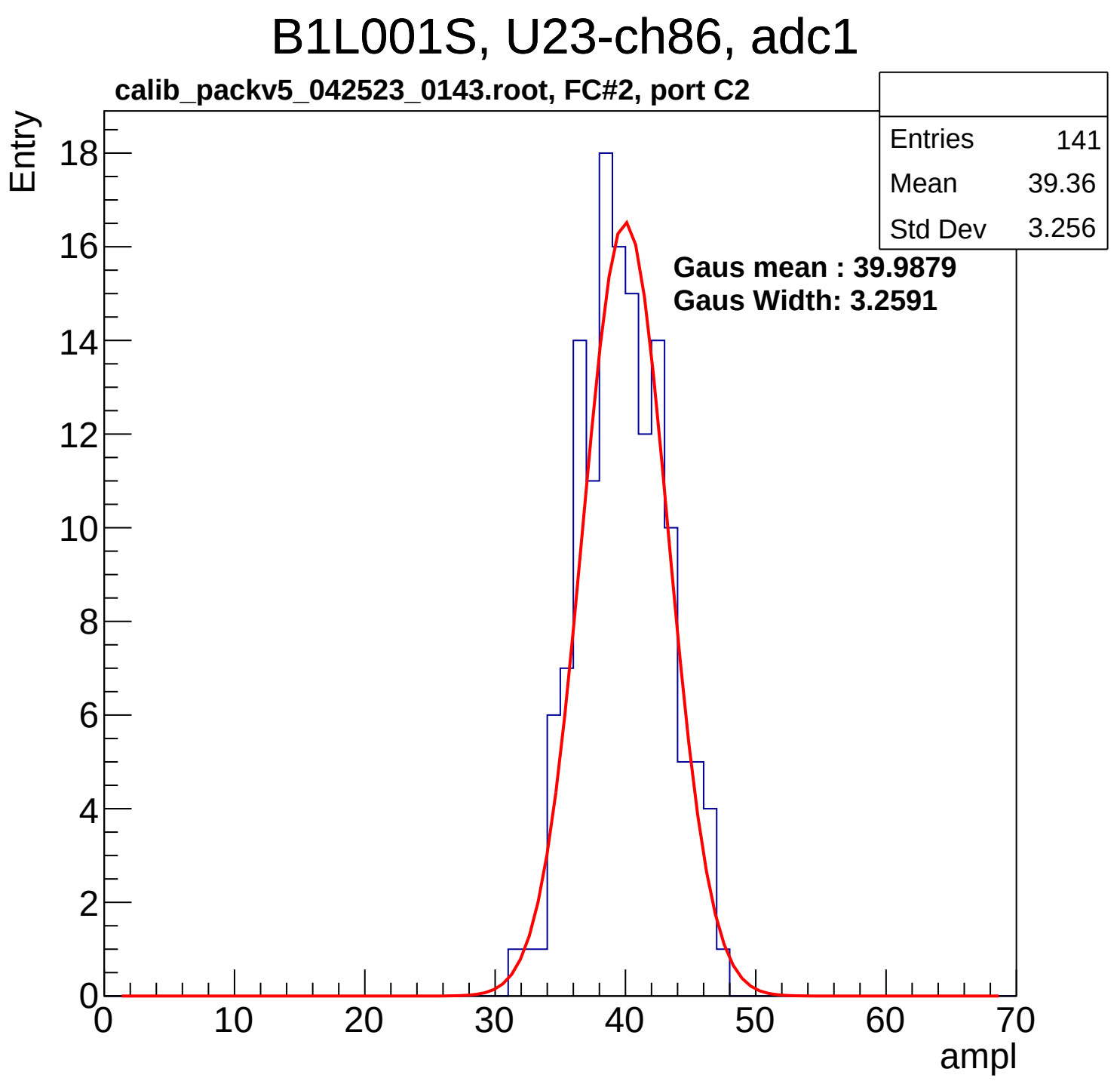
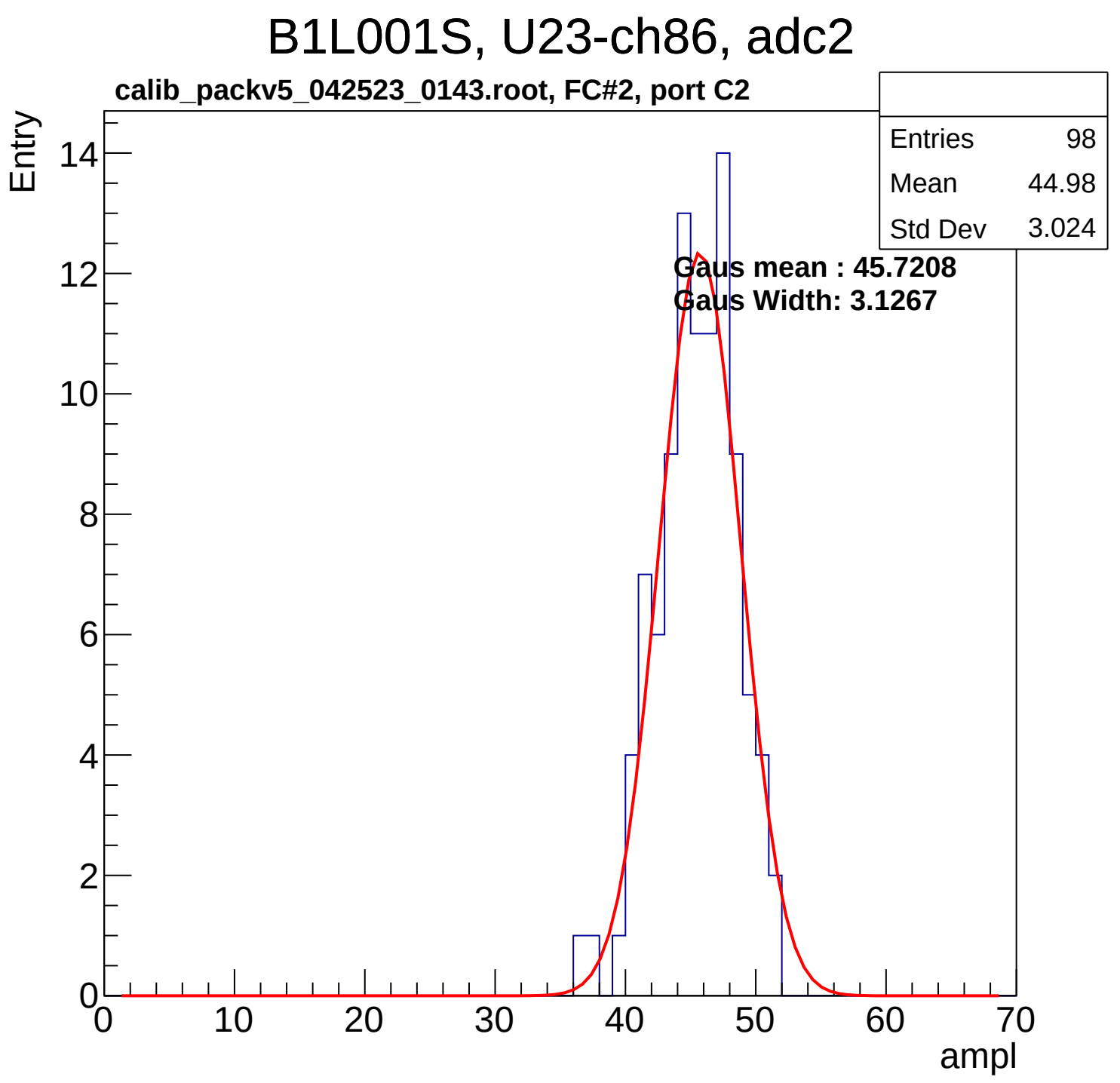
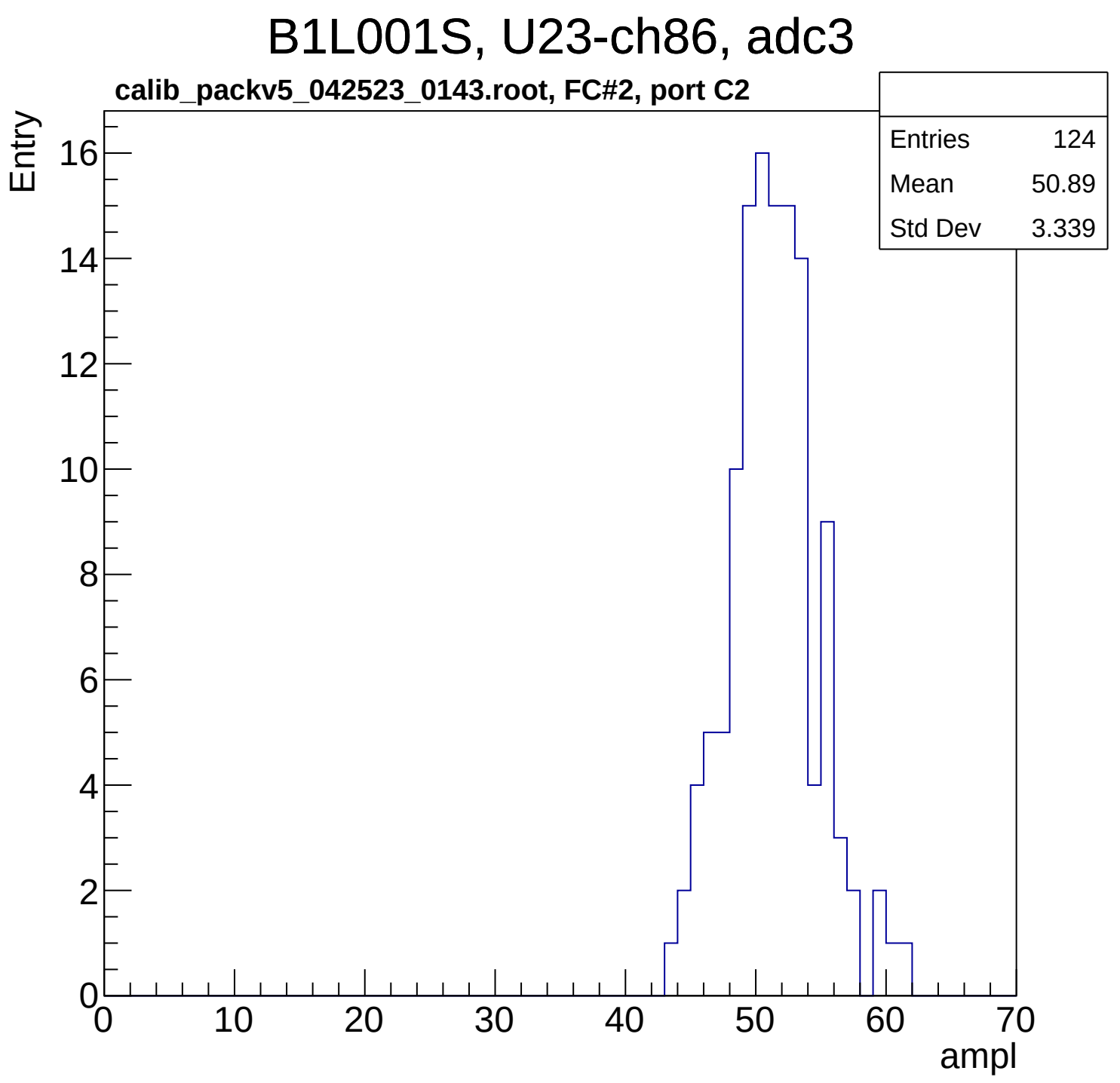

B1L001S, U23-ch86, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 31.9
Std Dev 3.541
Gaus mean : 32.5014
Gaus Width: 3.2646
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch86, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 39.36
Std Dev 3.256
Gaus mean : 39.9879
Gaus Width: 3.2591
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch86, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 44.98
Std Dev 3.024
Gaus mean : 45.7208
Gaus Width: 3.1267
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch86, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 50.89
Std Dev 3.339
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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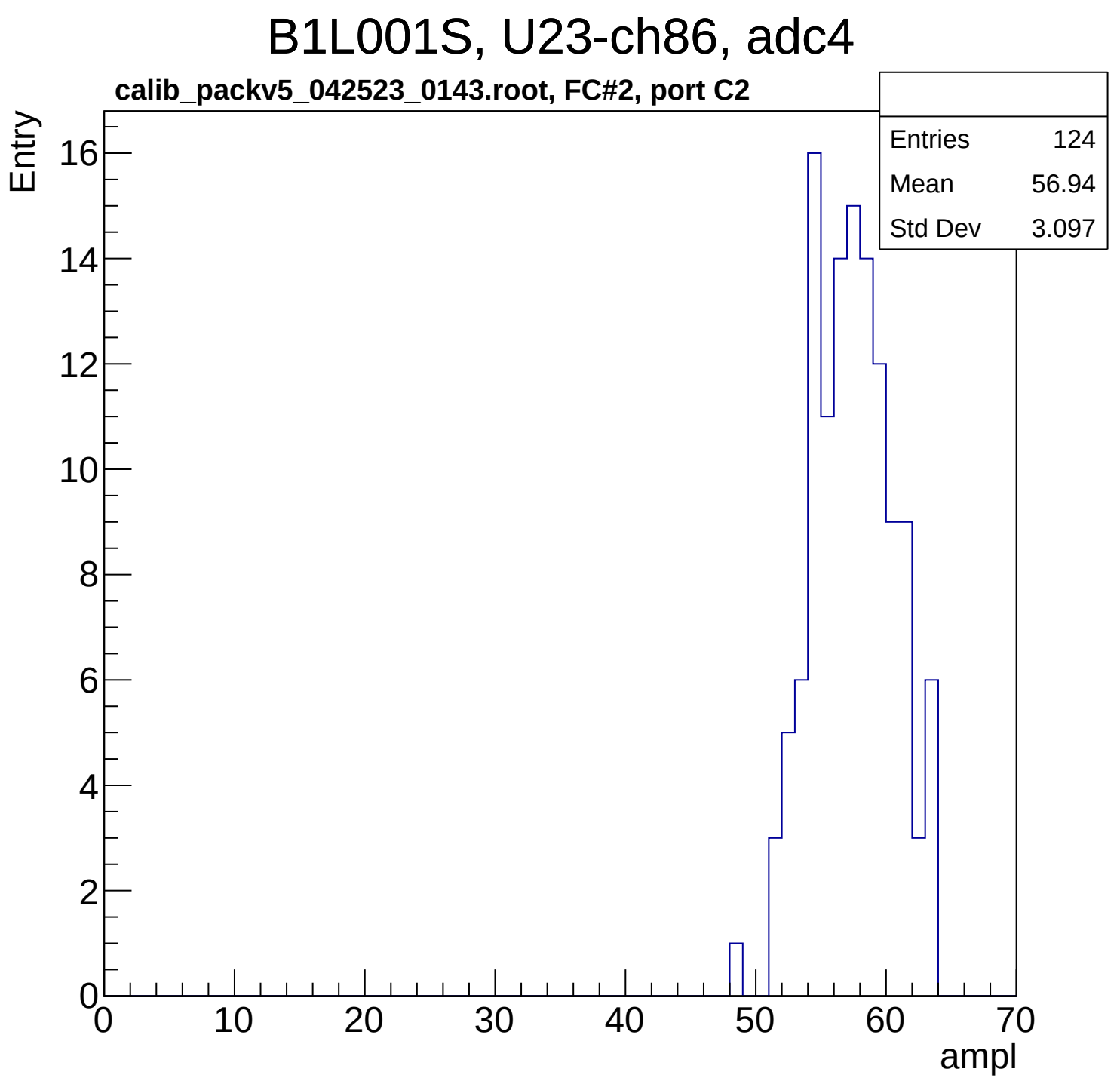
40
50
60
70


B1L001S, U23-ch86, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 56.94
Std Dev 3.097
Entry
ampl
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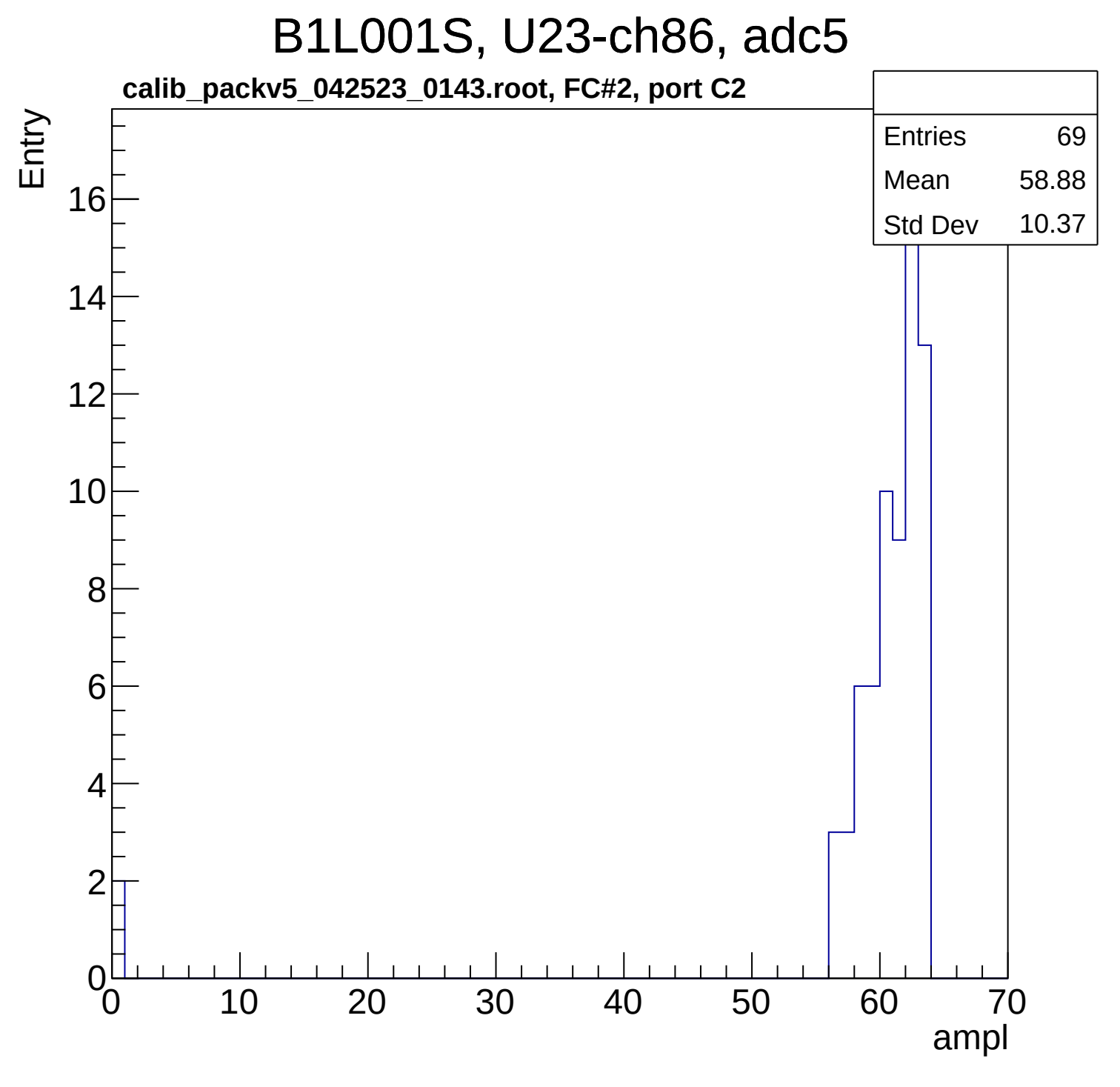
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch86, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 69
Mean 58.88
Std Dev 10.37
Entry
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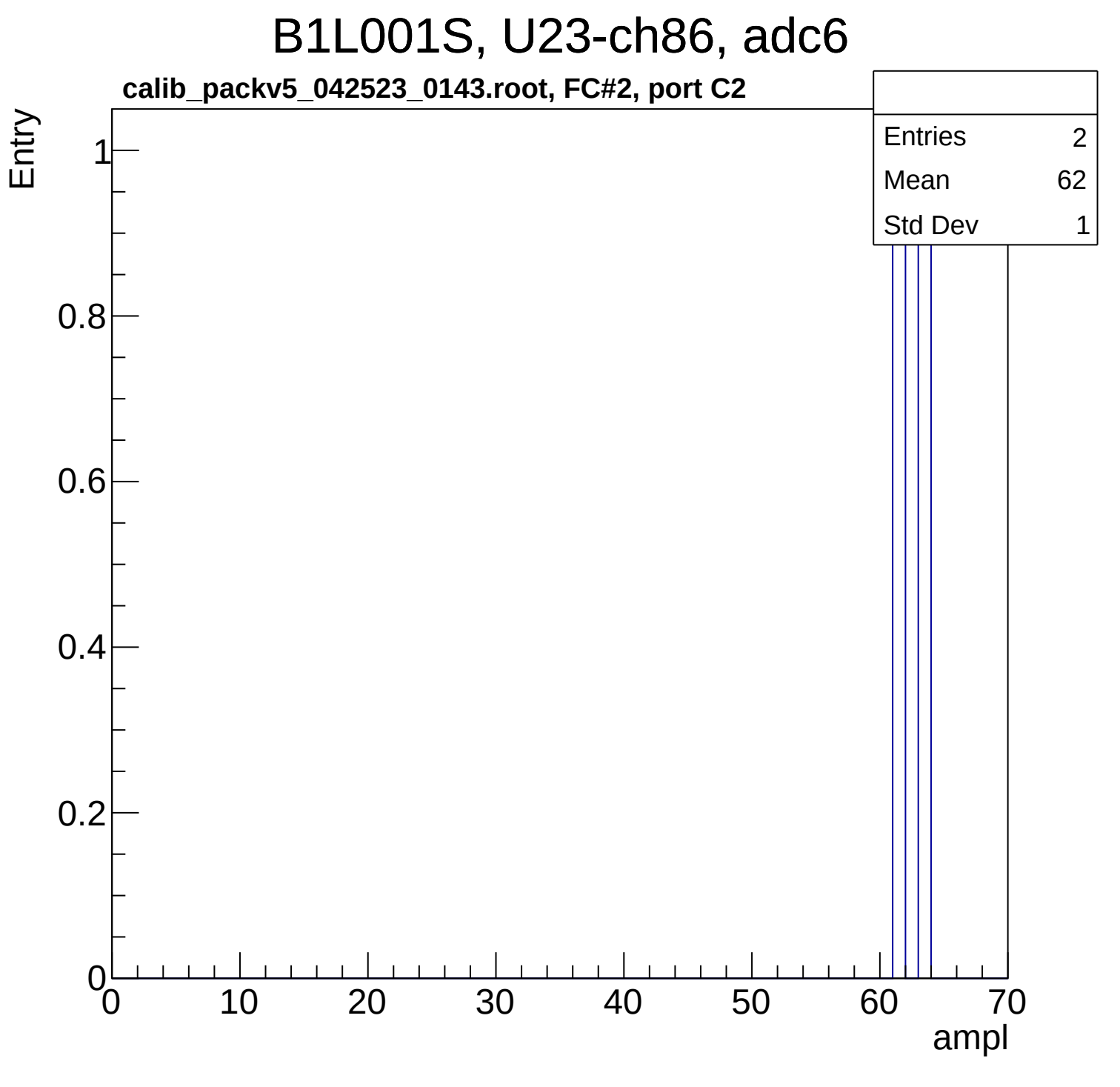
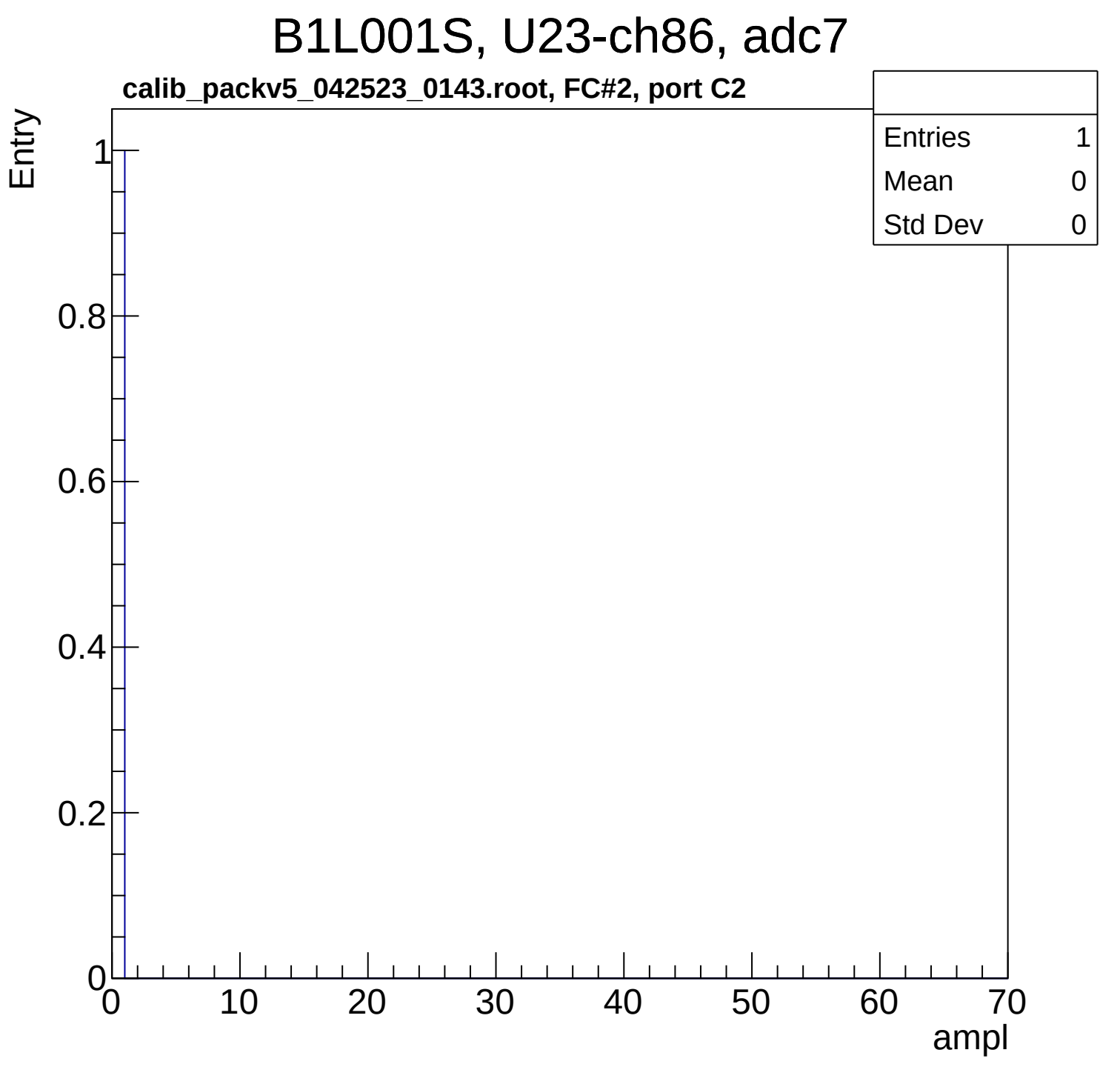
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch86, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch86, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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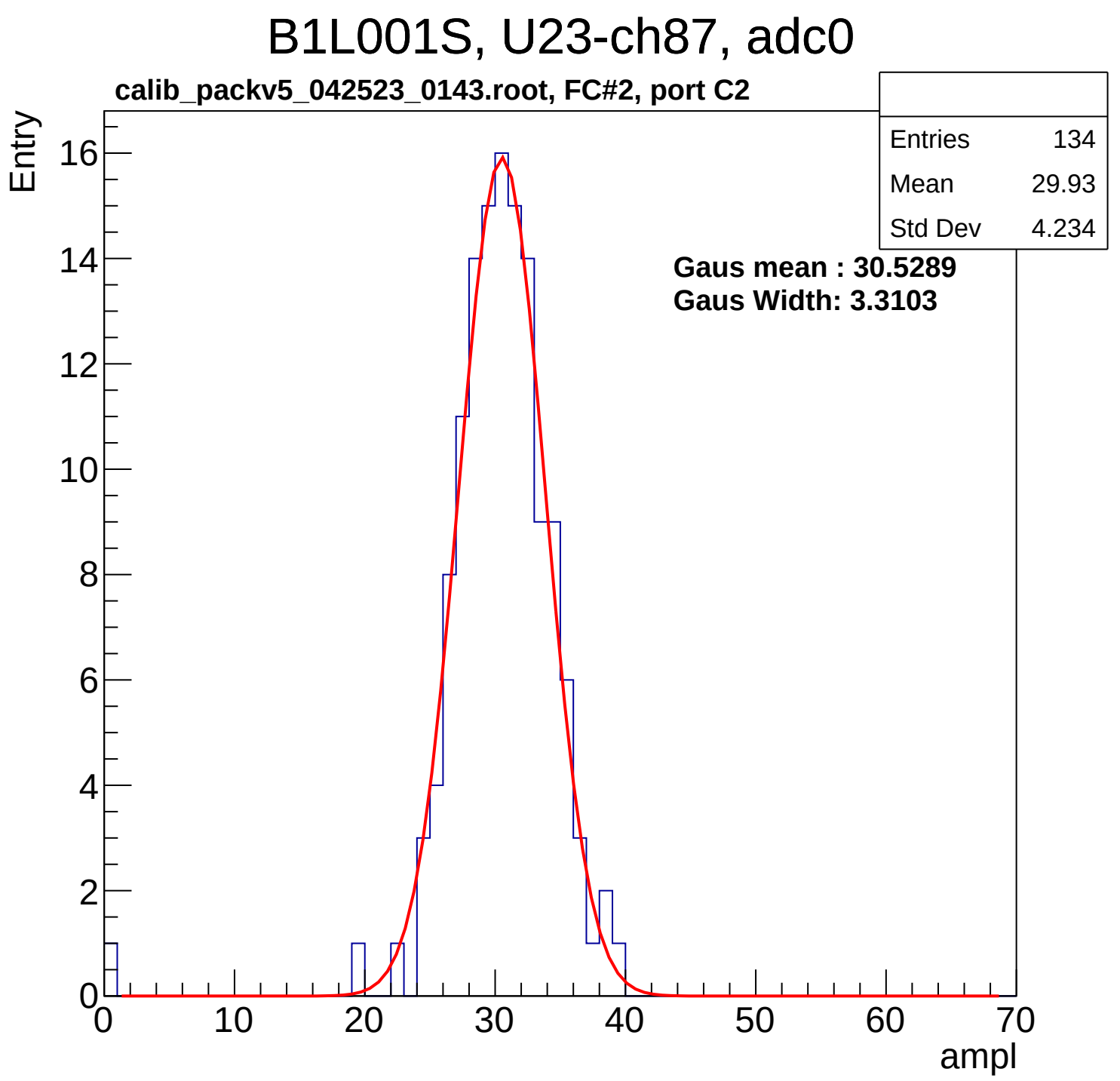
50
60
70
ampl


B1L001S, U23-ch87, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 29.93
Std Dev 4.234
Gaus mean : 30.5289
Gaus Width: 3.3103
Entry
ampl
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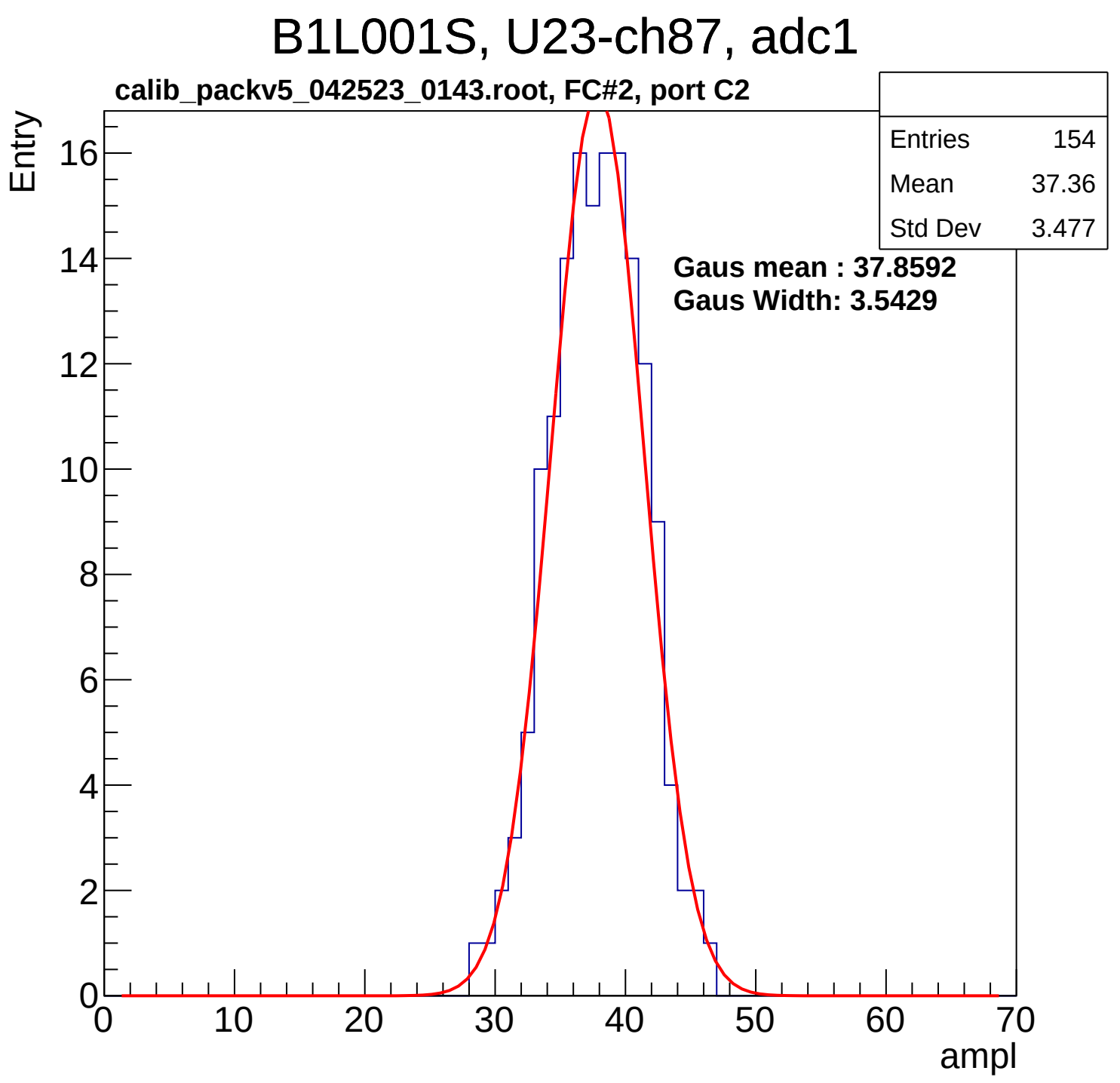
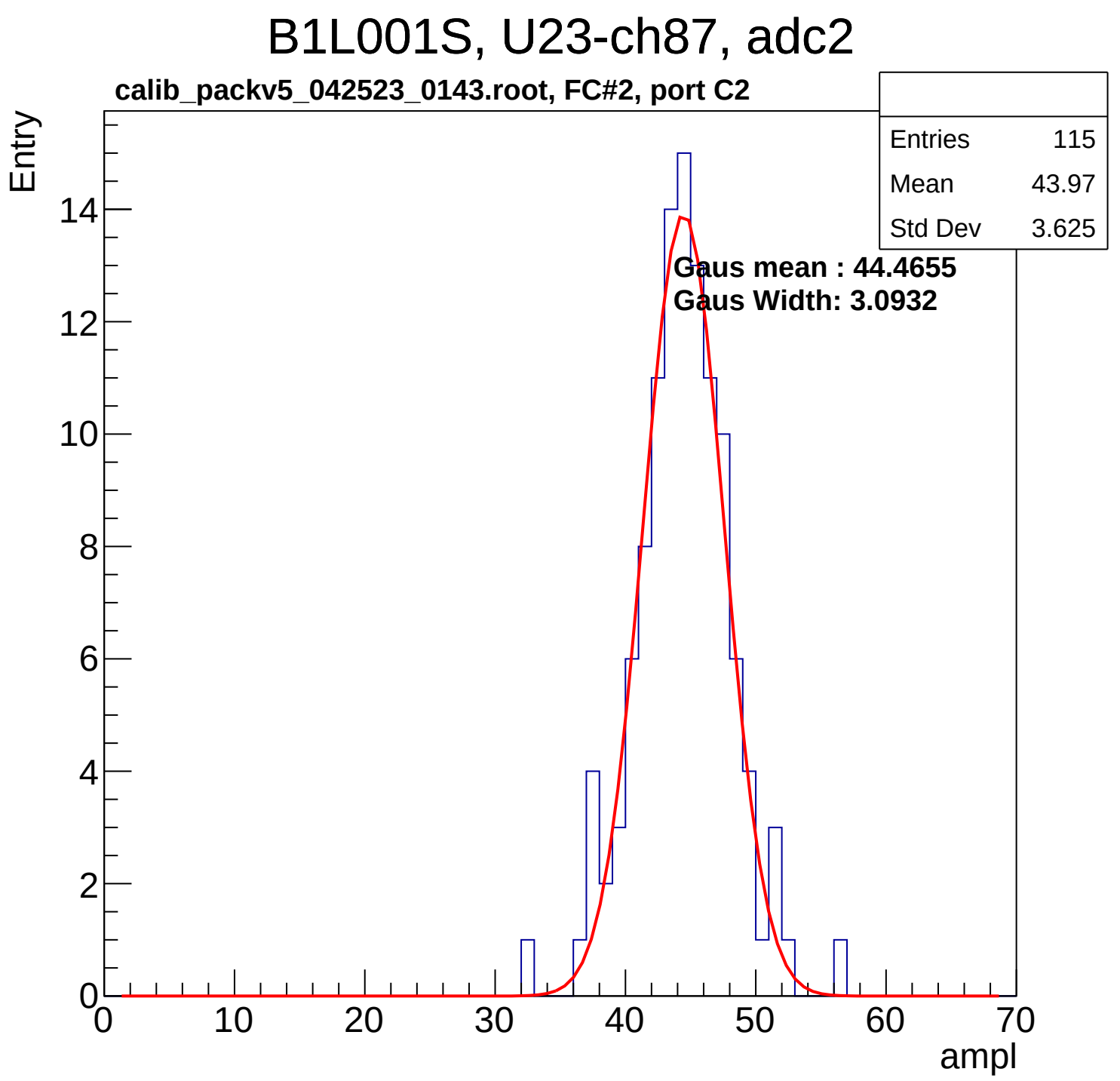
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch87, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 37.36
Std Dev 3.477
Gaus mean : 37.8592
Gaus Width: 3.5429
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch87, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 43.97
Std Dev 3.625
Gaus mean : 44.4655
Gaus Width: 3.0932
Entry
ampl
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2
4
6
8
10
12
14
0
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20
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60
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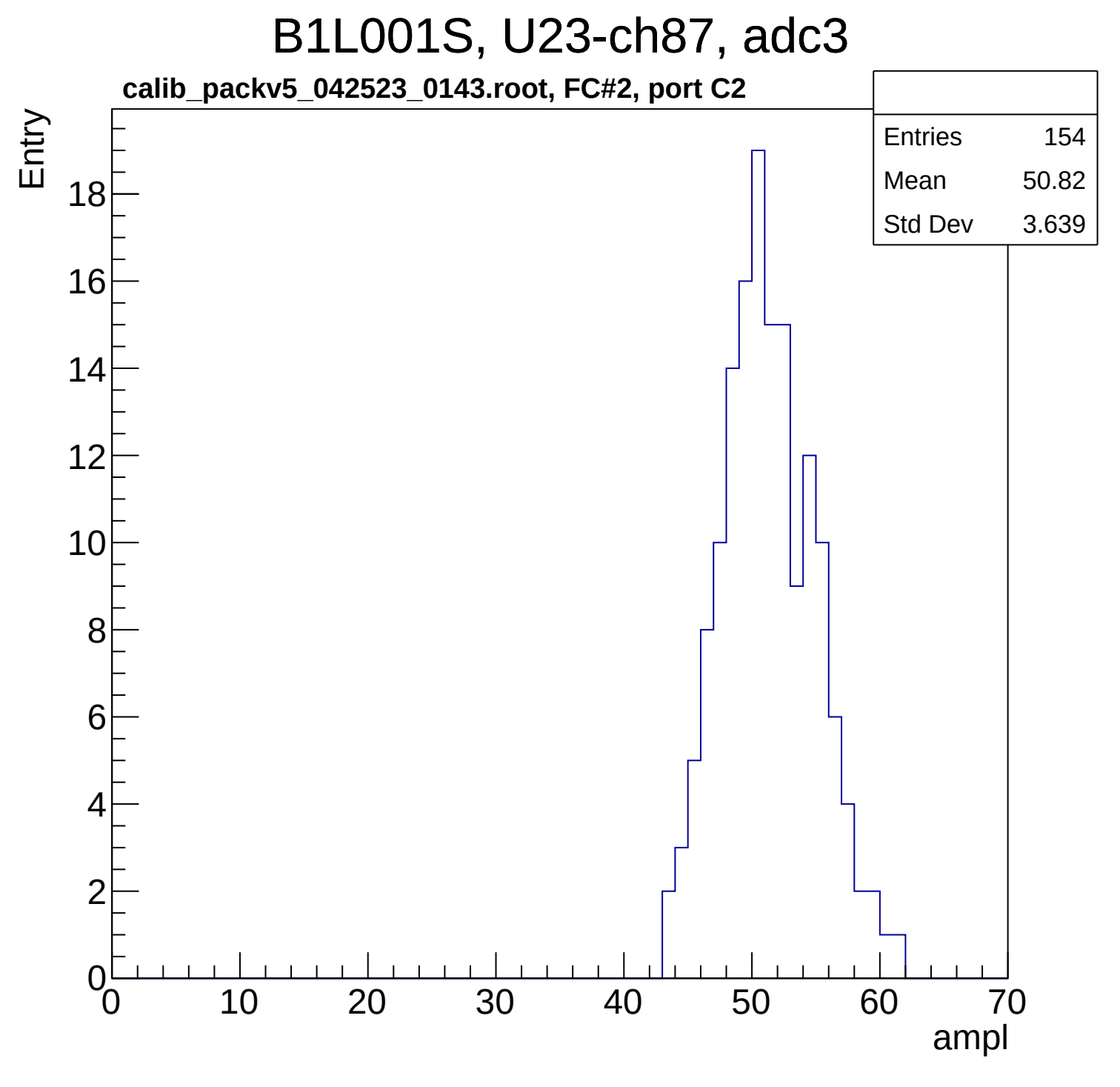
B1L001S, U23-ch87, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 50.82
Std Dev 3.639
Entry
ampl
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4
6
8
10
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14
16
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40
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60
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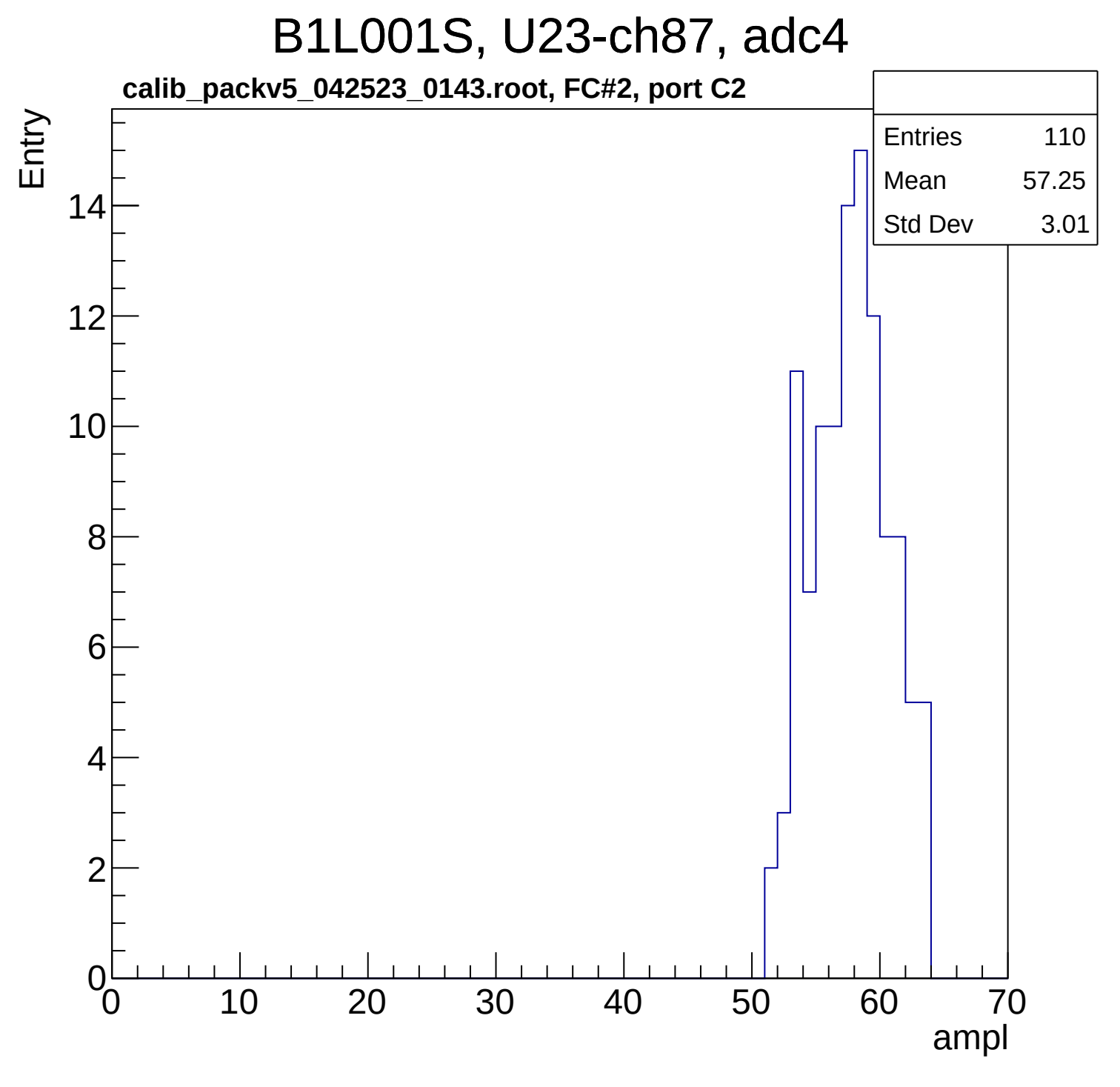
B1L001S, U23-ch87, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 57.25
Std Dev 3.01
Entry
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2
4
6
8
10
12
14
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10
20
30
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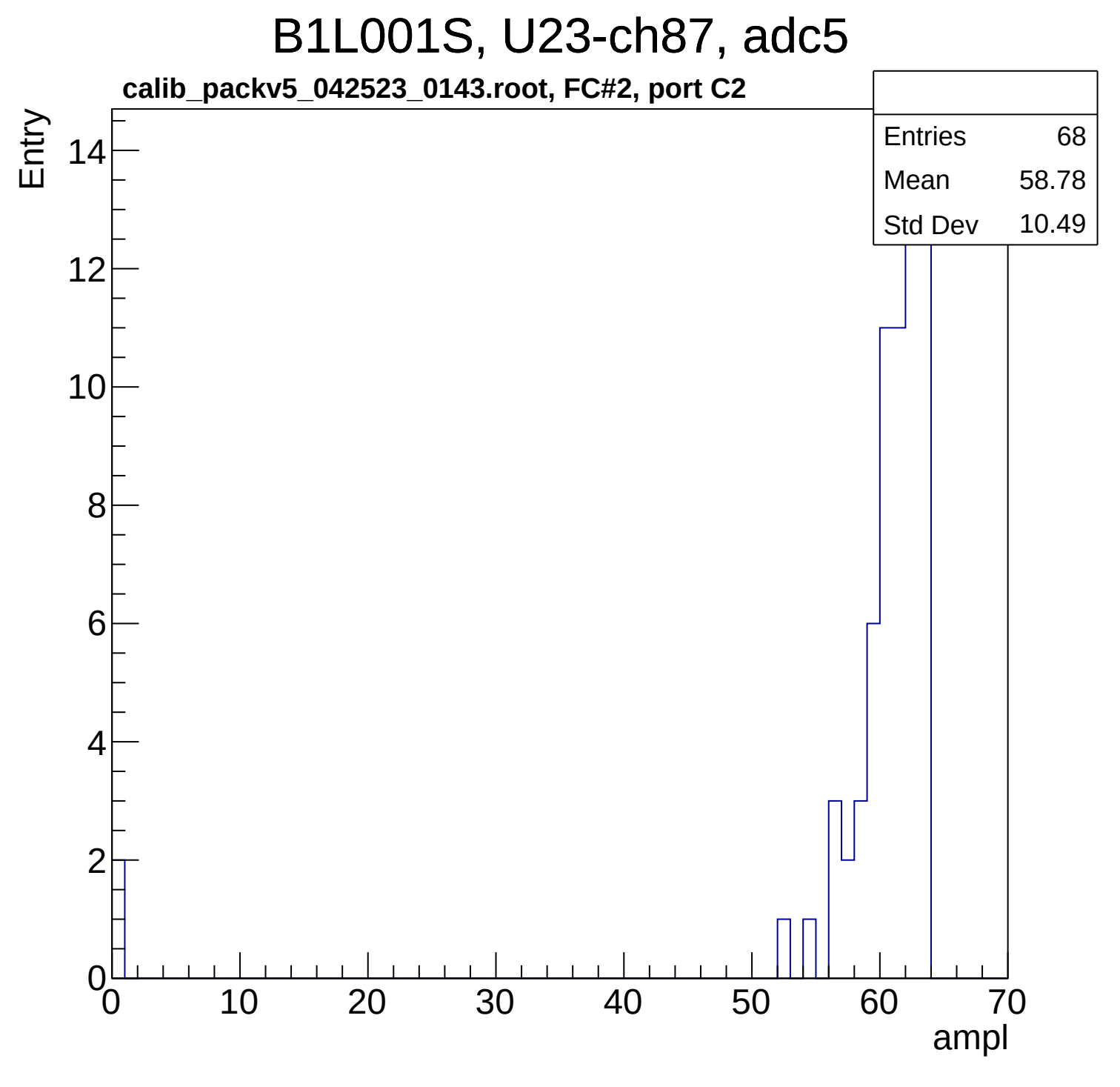
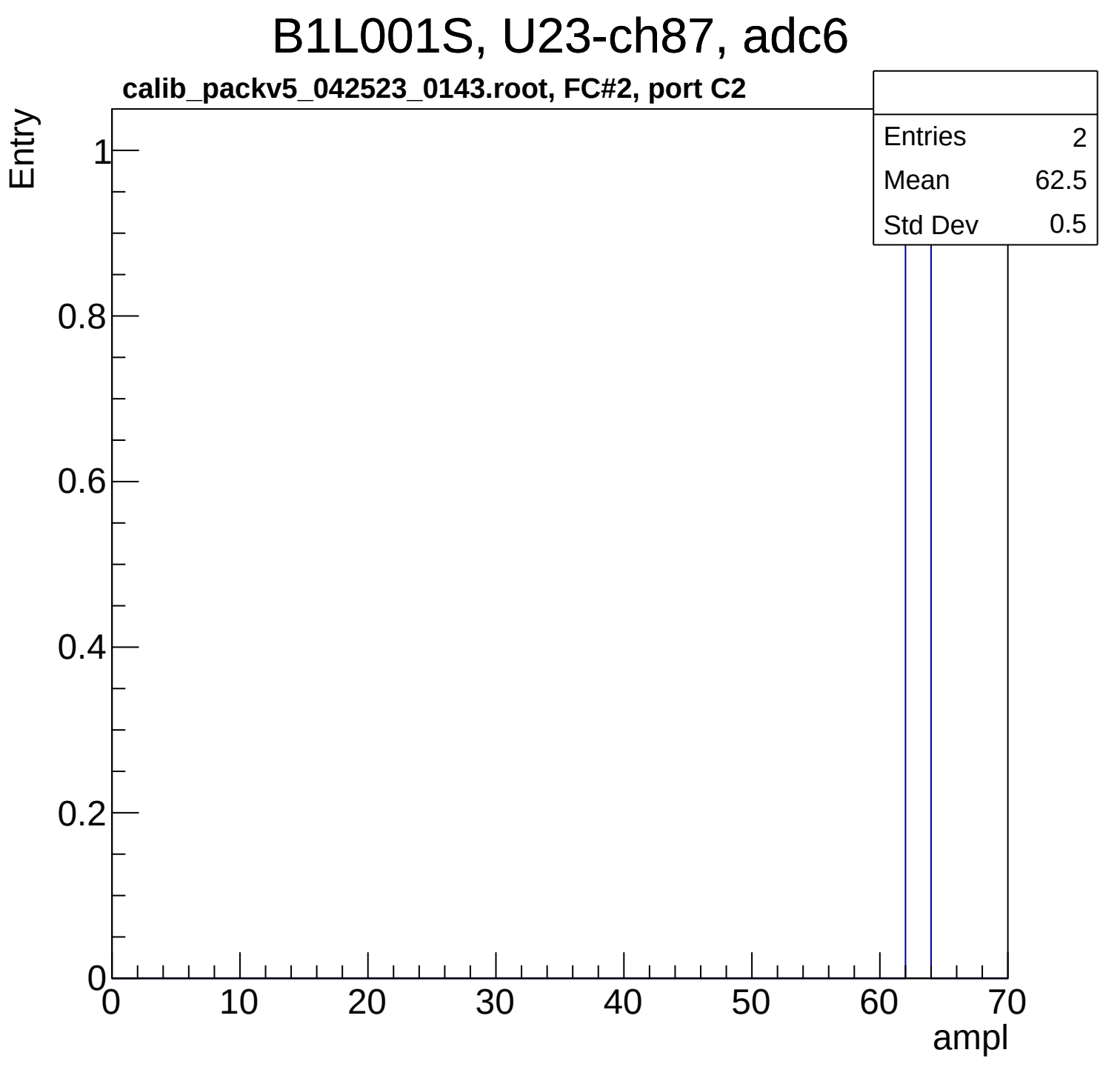
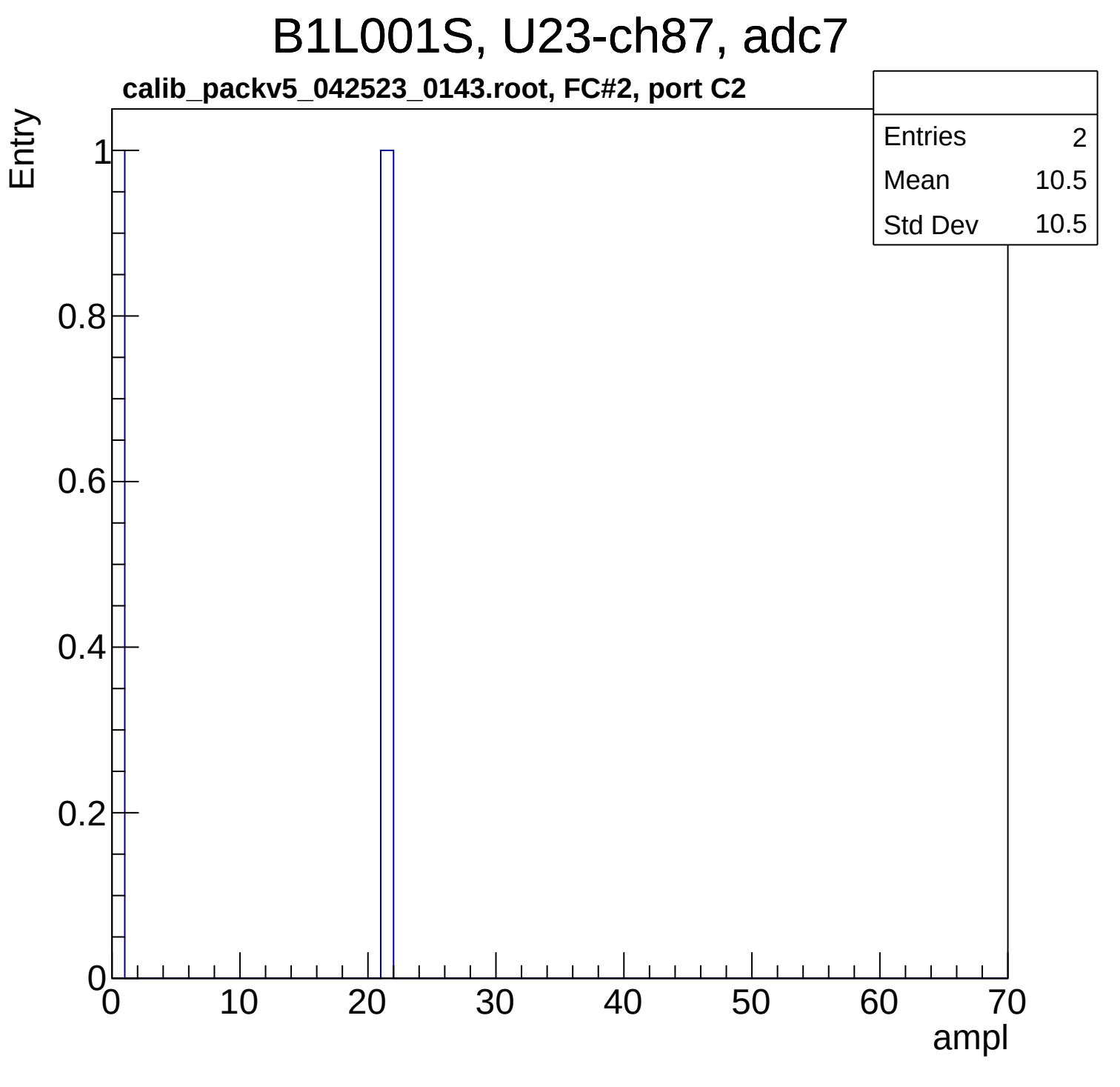
50
60
70
ampl


B1L001S, U23-ch87, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 68
Mean 58.78
Std Dev 10.49
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch87, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch87, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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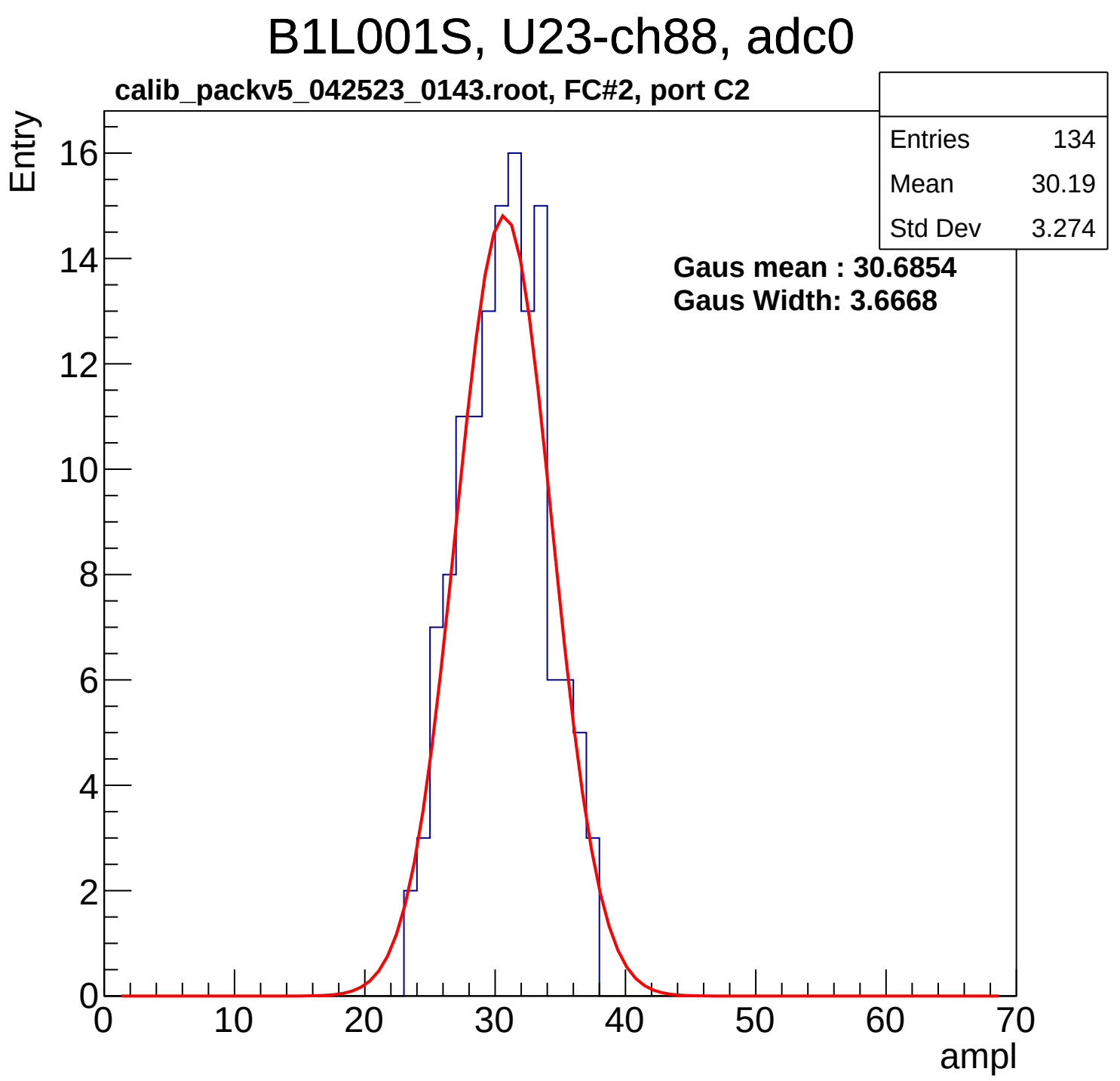
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch88, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 30.19
Std Dev 3.274
Gaus mean : 30.6854
Gaus Width: 3.6668
Entry
ampl
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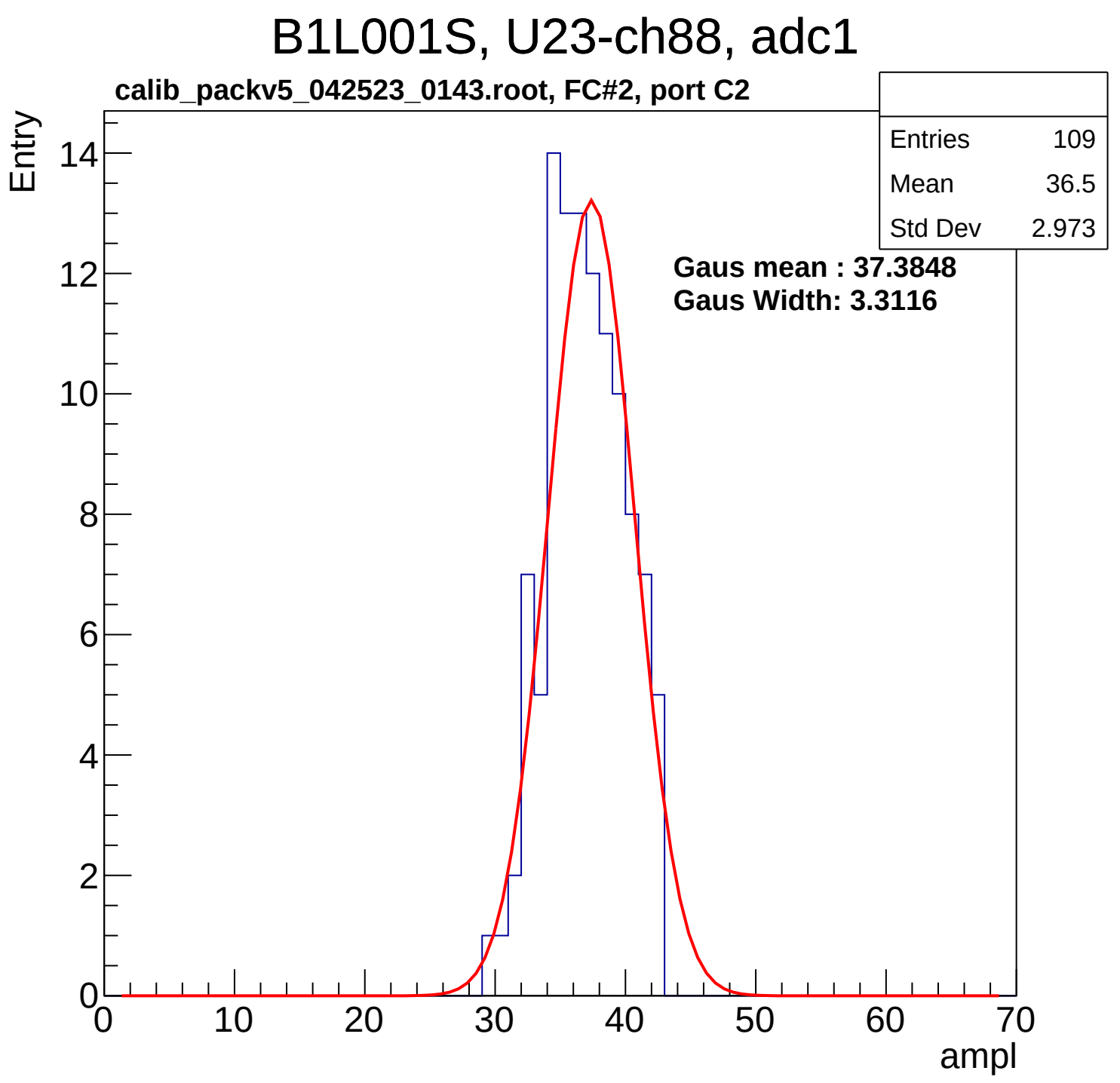
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch88, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 36.5
Std Dev 2.973
Gaus mean : 37.3848
Gaus Width: 3.3116
Entry
ampl
0
2
4
6
8
10
12
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0
10
20
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40
50
60
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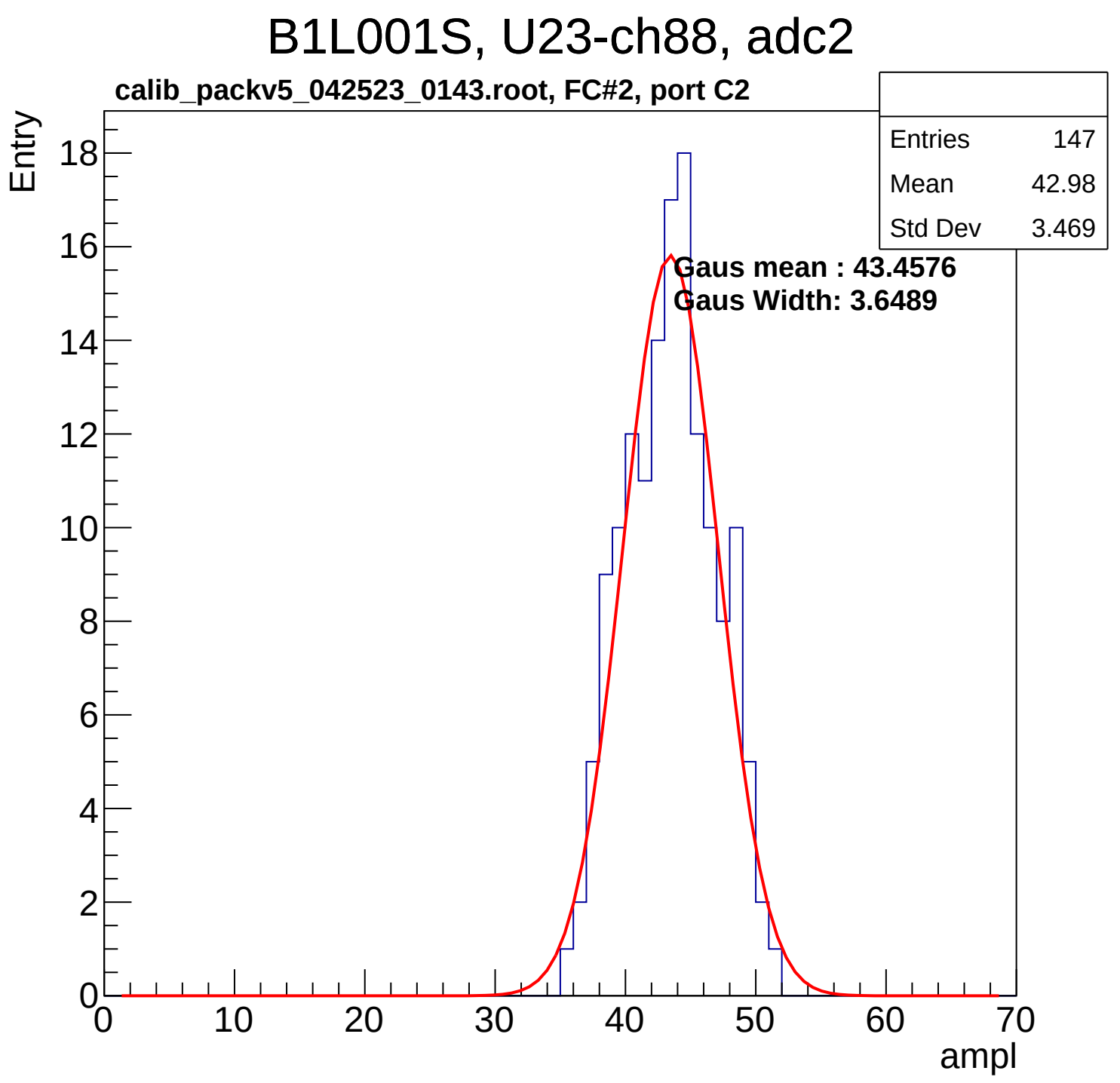
B1L001S, U23-ch88, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 42.98
Std Dev 3.469
Gaus mean : 43.4576
Gaus Width: 3.6489
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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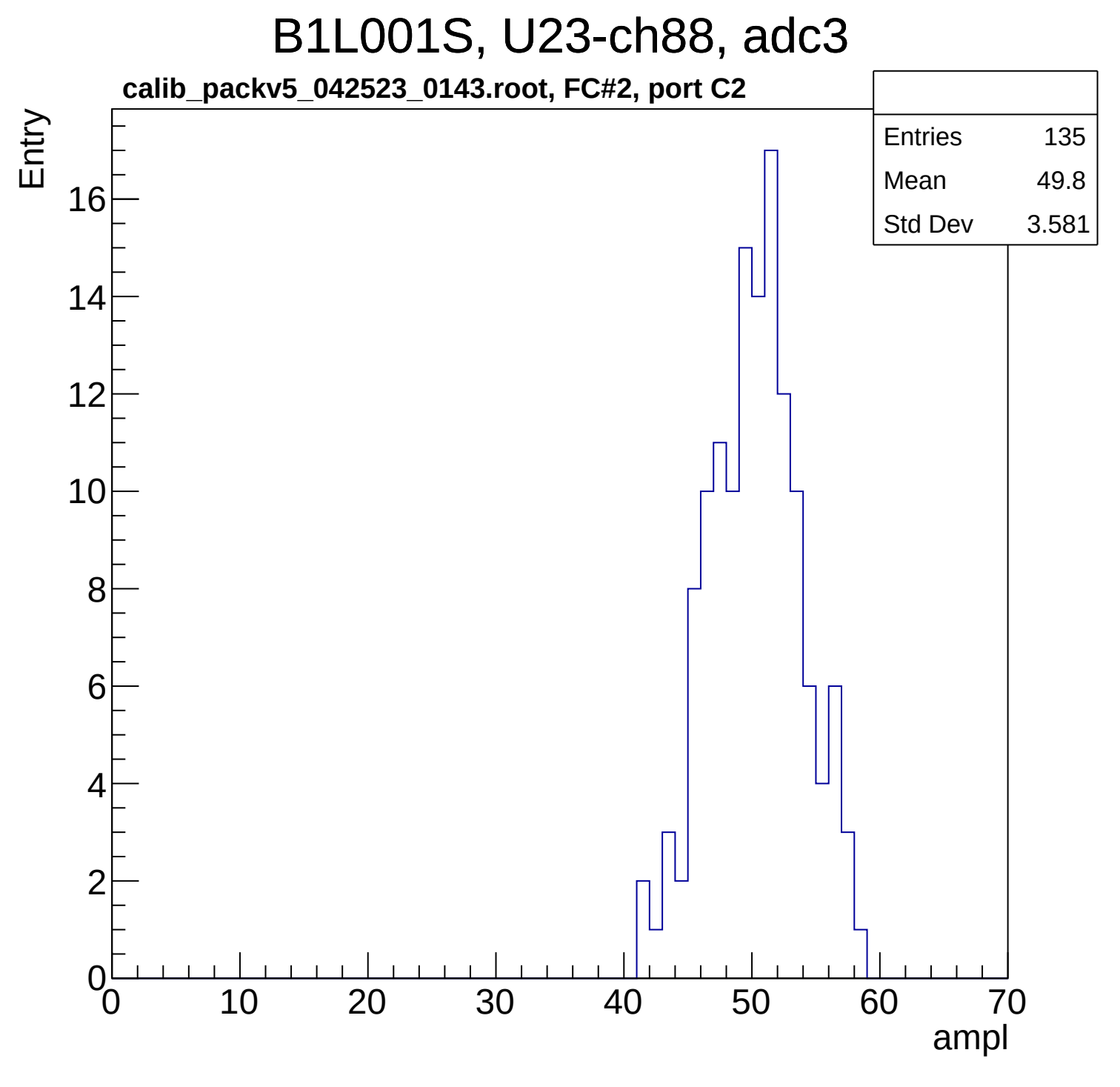
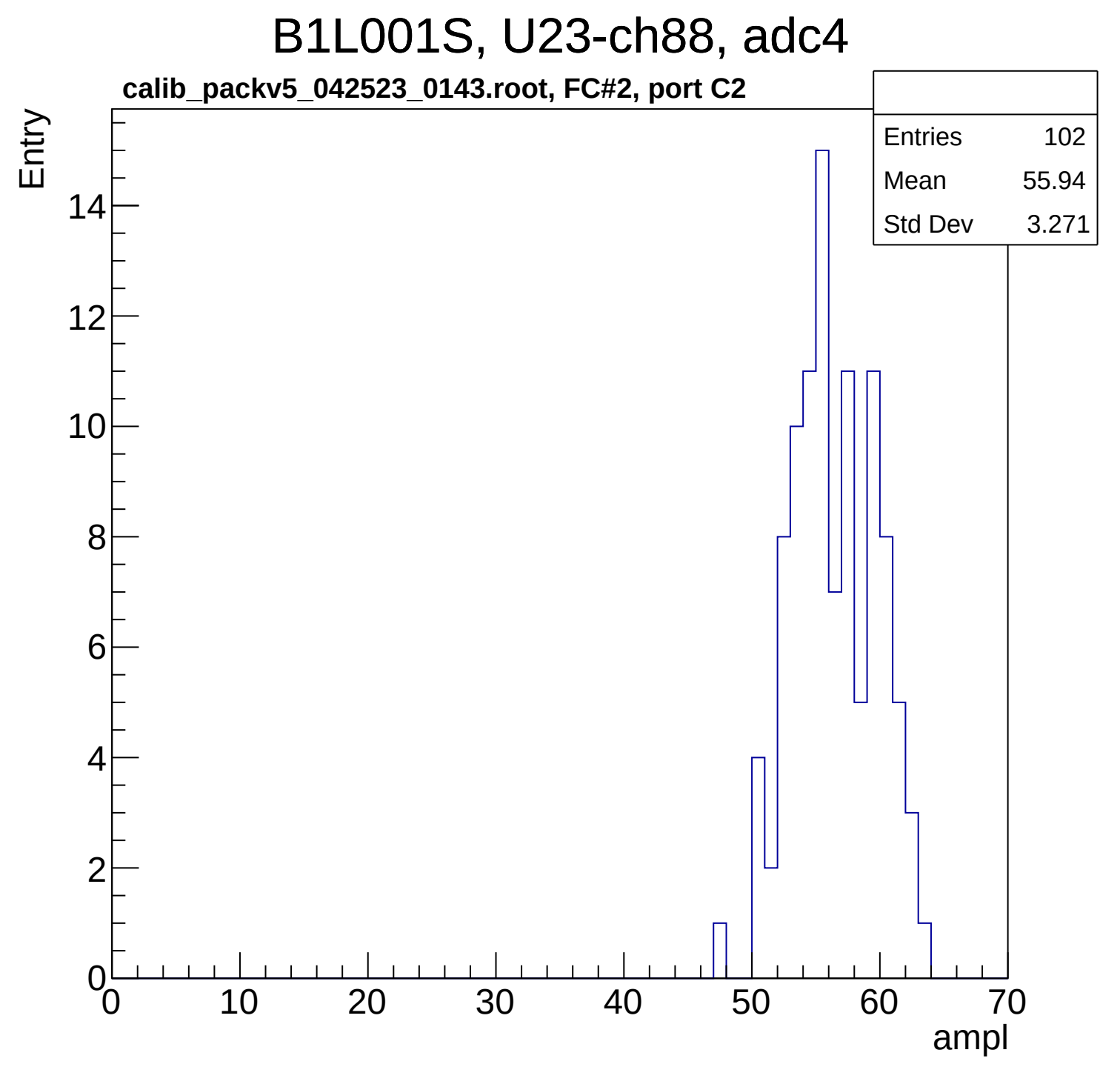

B1L001S, U23-ch88, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 49.8
Std Dev 3.581
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch88, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 102
Mean 55.94
Std Dev 3.271
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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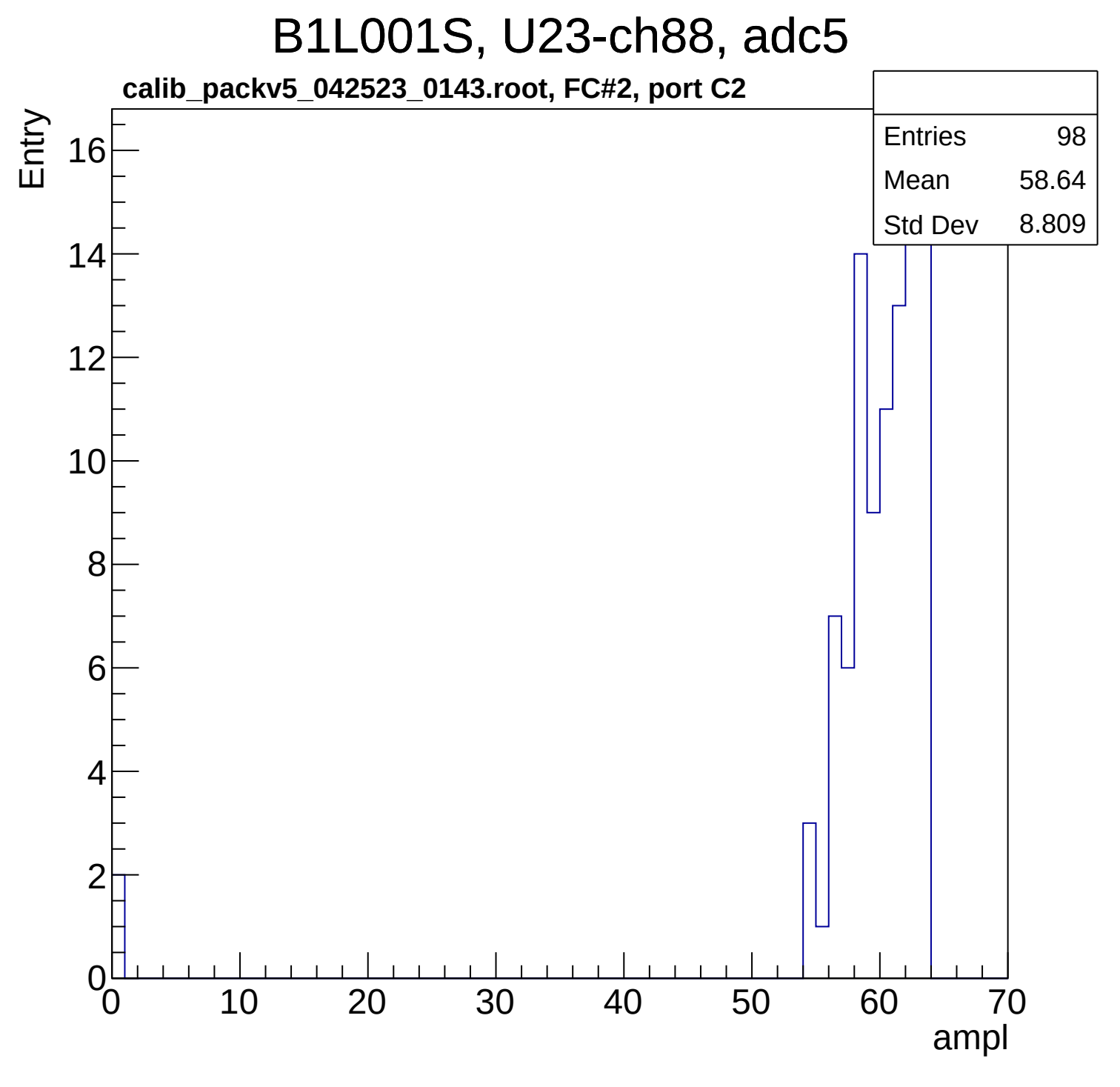
B1L001S, U23-ch88, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 98
Mean 58.64
Std Dev 8.809
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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40
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60
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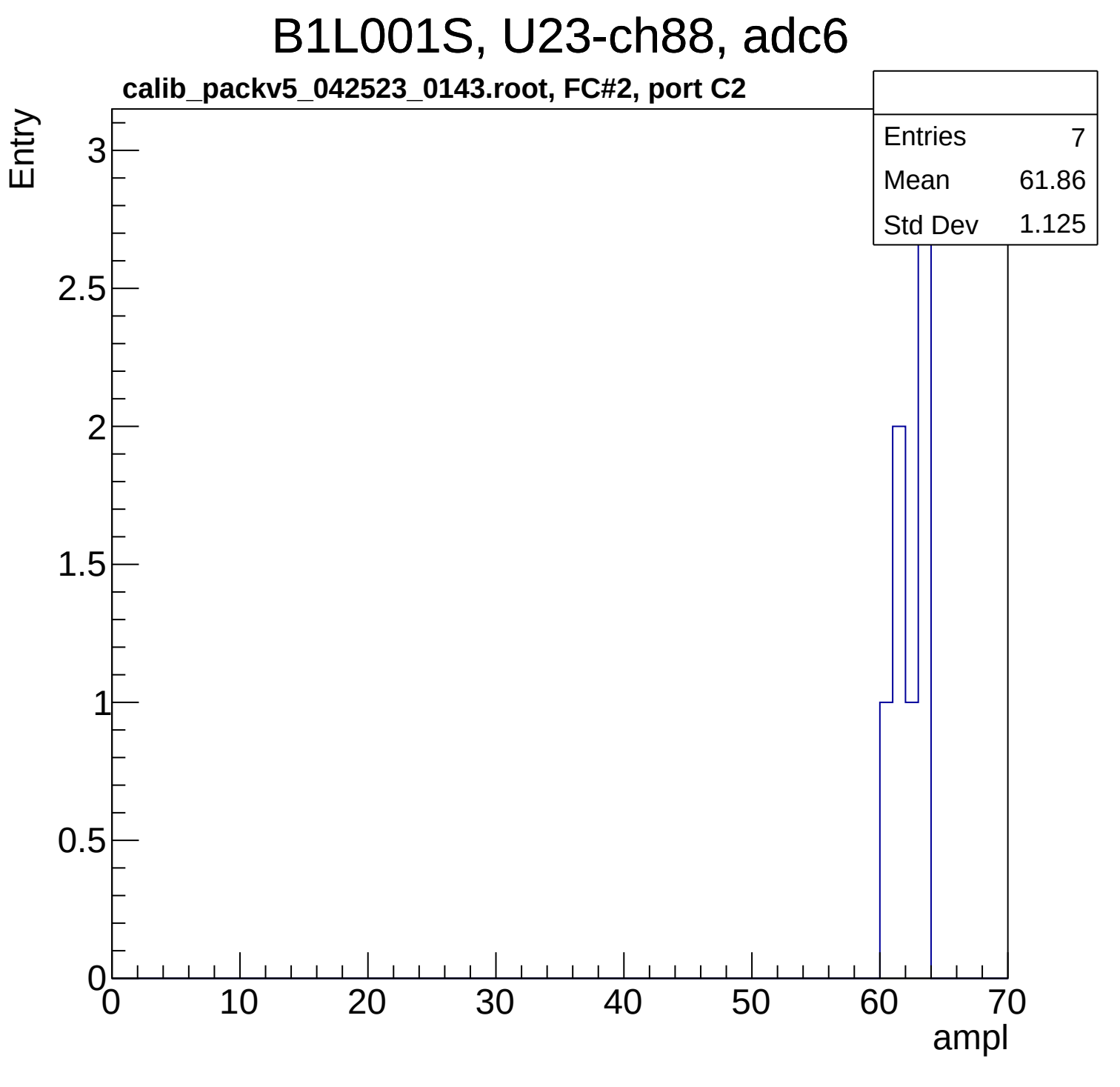
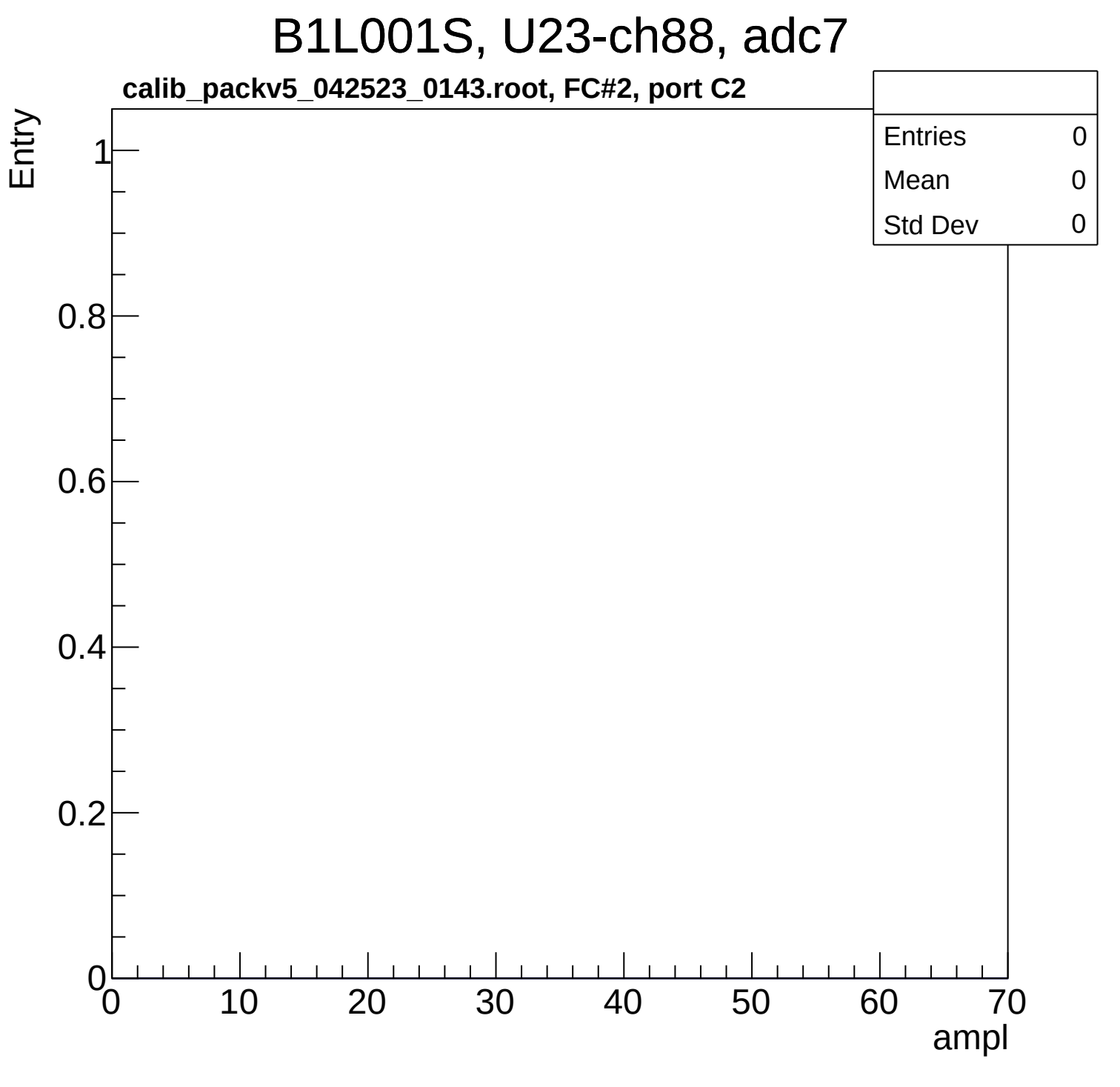
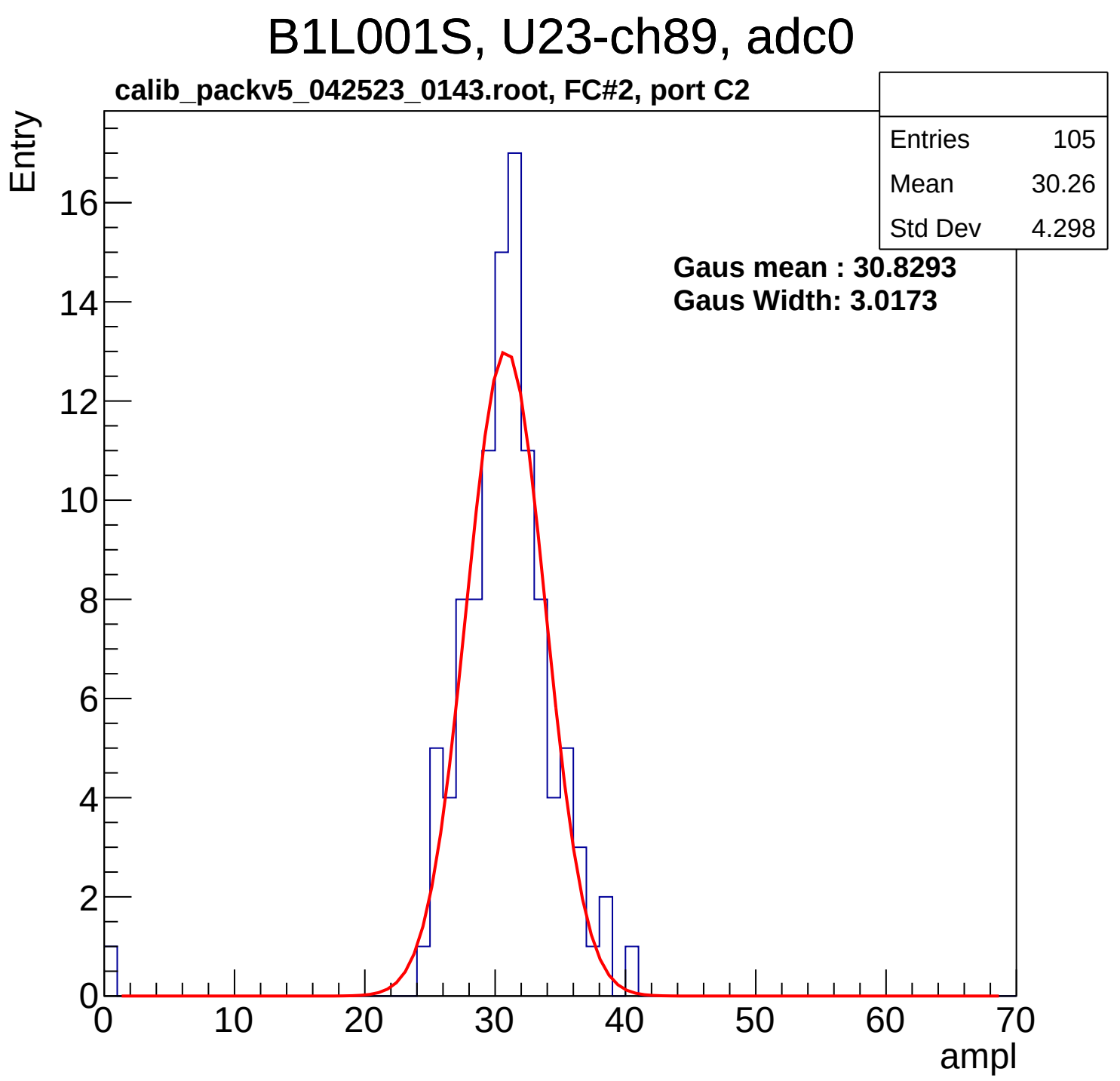
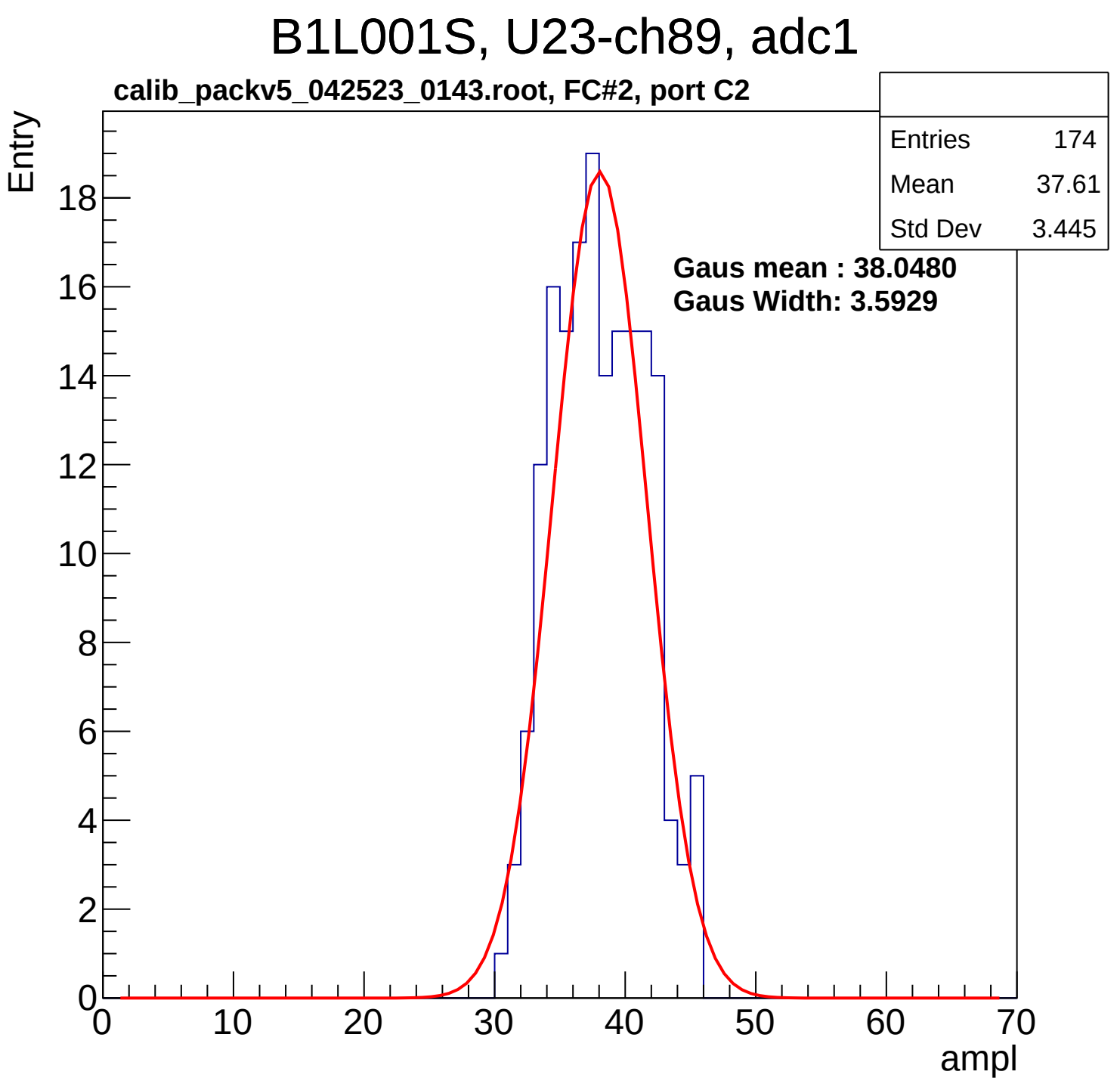

B1L001S, U23-ch88, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 61.86
Std Dev 1.125
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch88, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch89, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 105
Mean 30.26
Std Dev 4.298
Gaus mean : 30.8293
Gaus Width: 3.0173
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch89, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 174
Mean 37.61
Std Dev 3.445
Gaus mean : 38.0480
Gaus Width: 3.5929
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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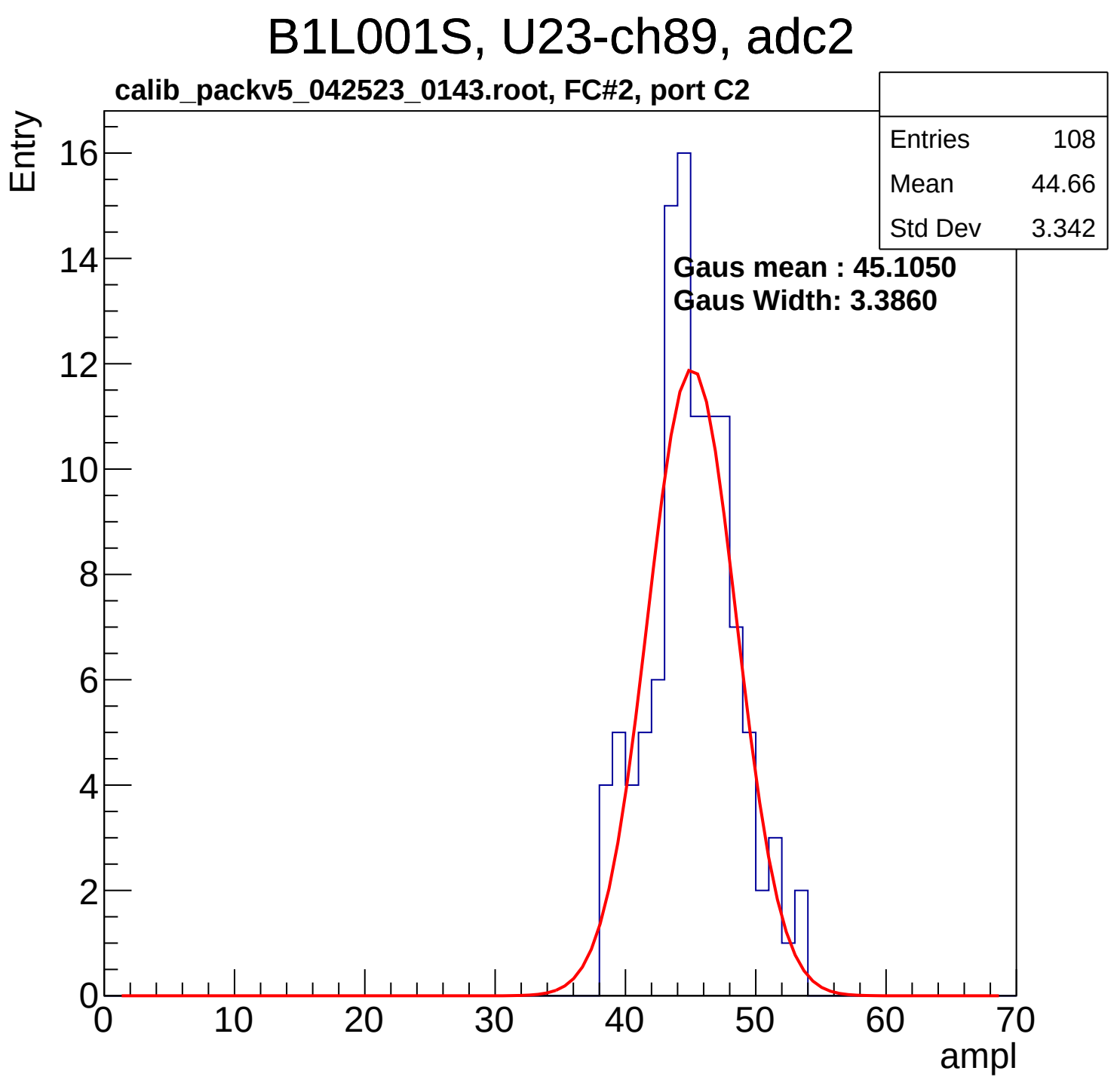
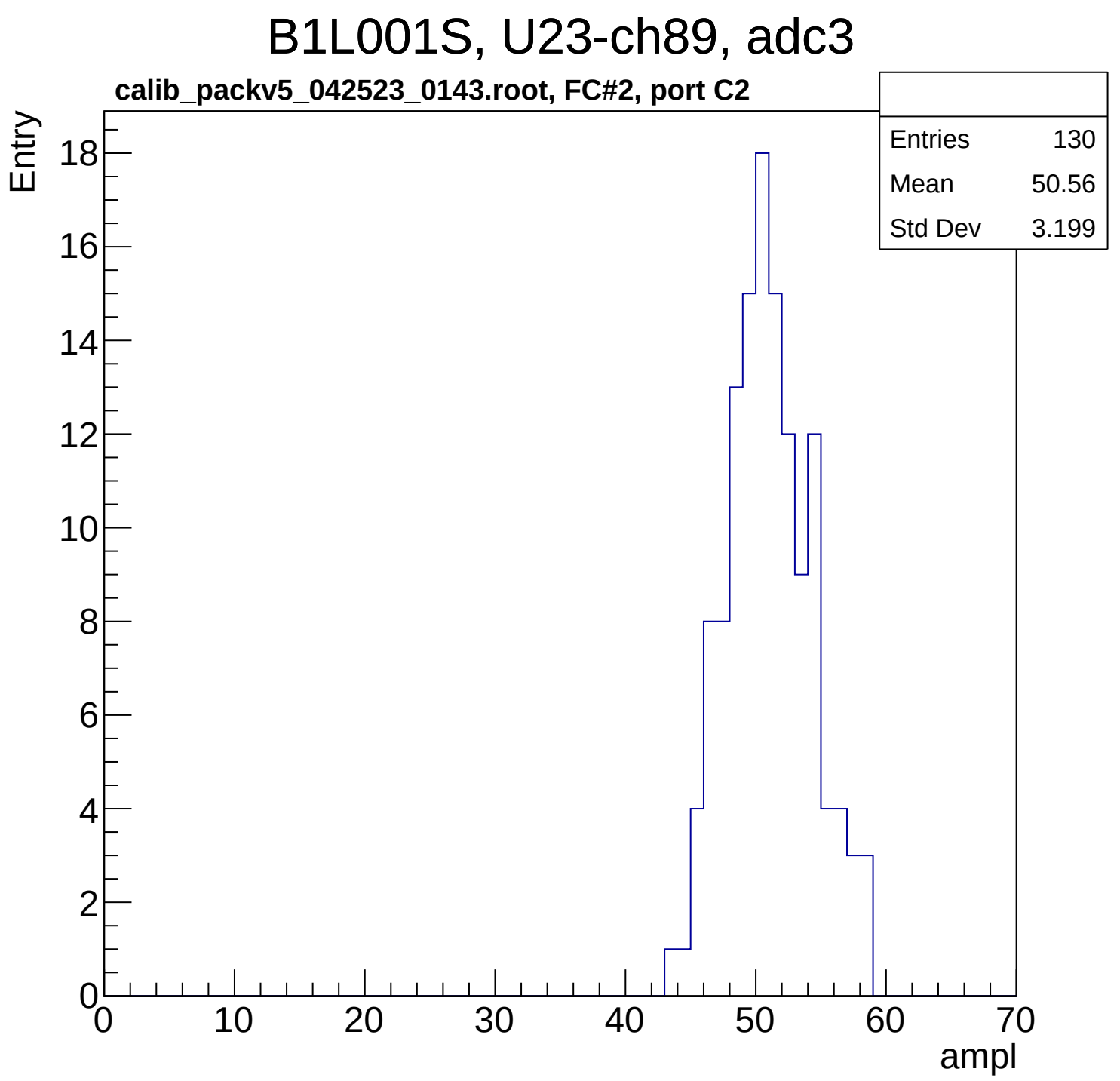
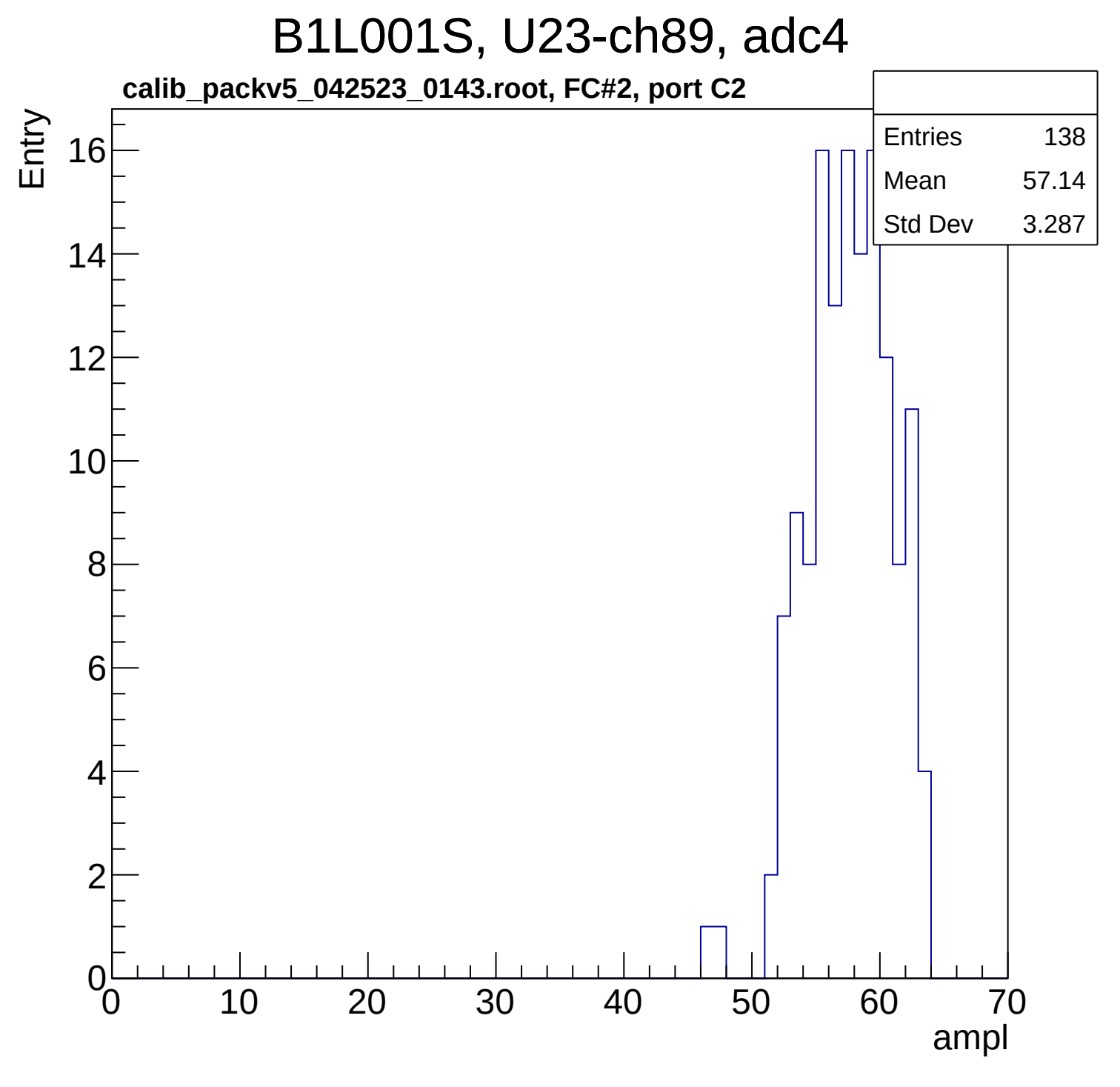

B1L001S, U23-ch89, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 44.66
Std Dev 3.342
Gaus mean : 45.1050
Gaus Width: 3.3860
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch89, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 50.56
Std Dev 3.199
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch89, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 57.14
Std Dev 3.287
Entry
ampl
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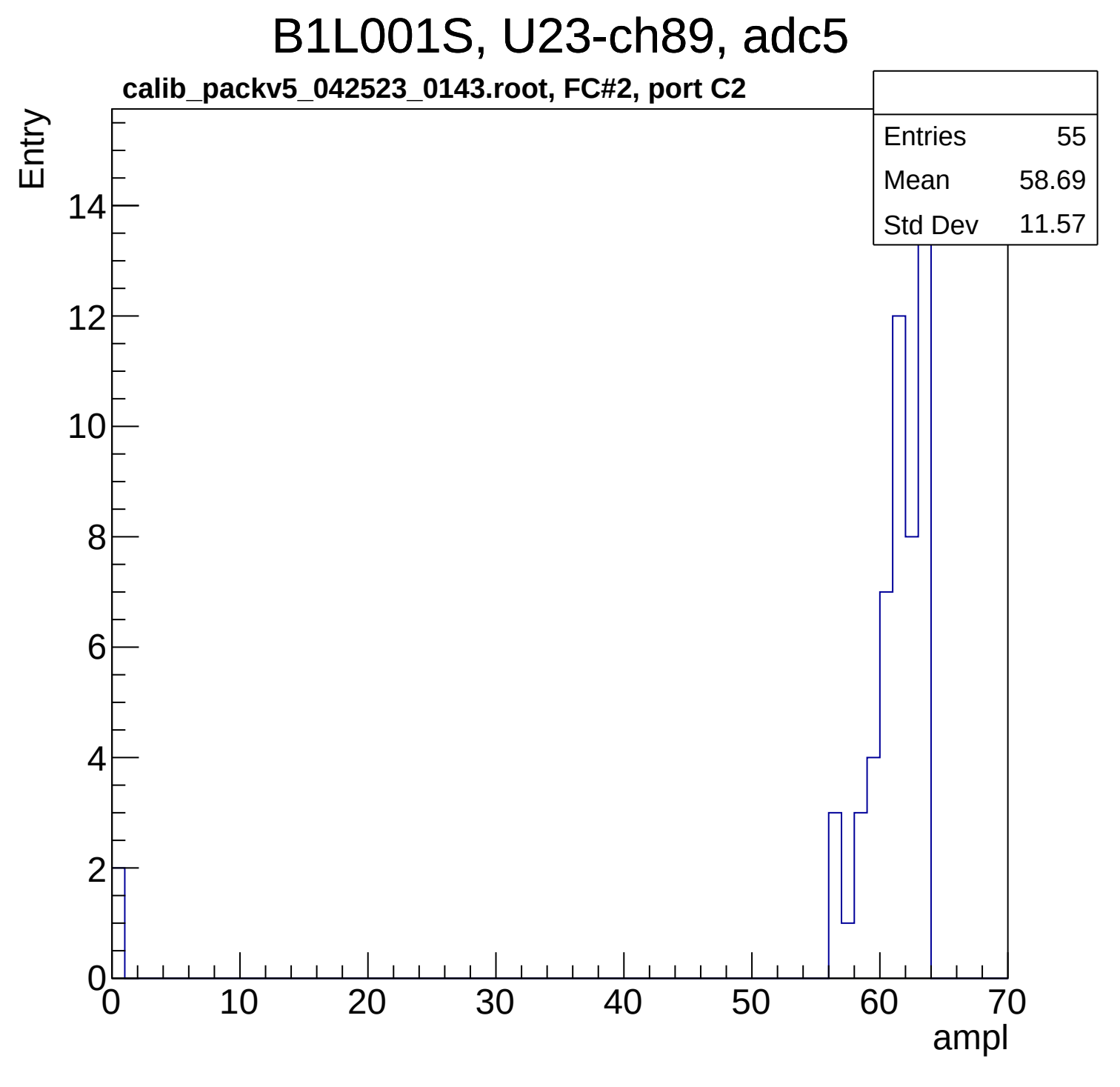
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch89, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 55
Mean 58.69
Std Dev 11.57
Entry
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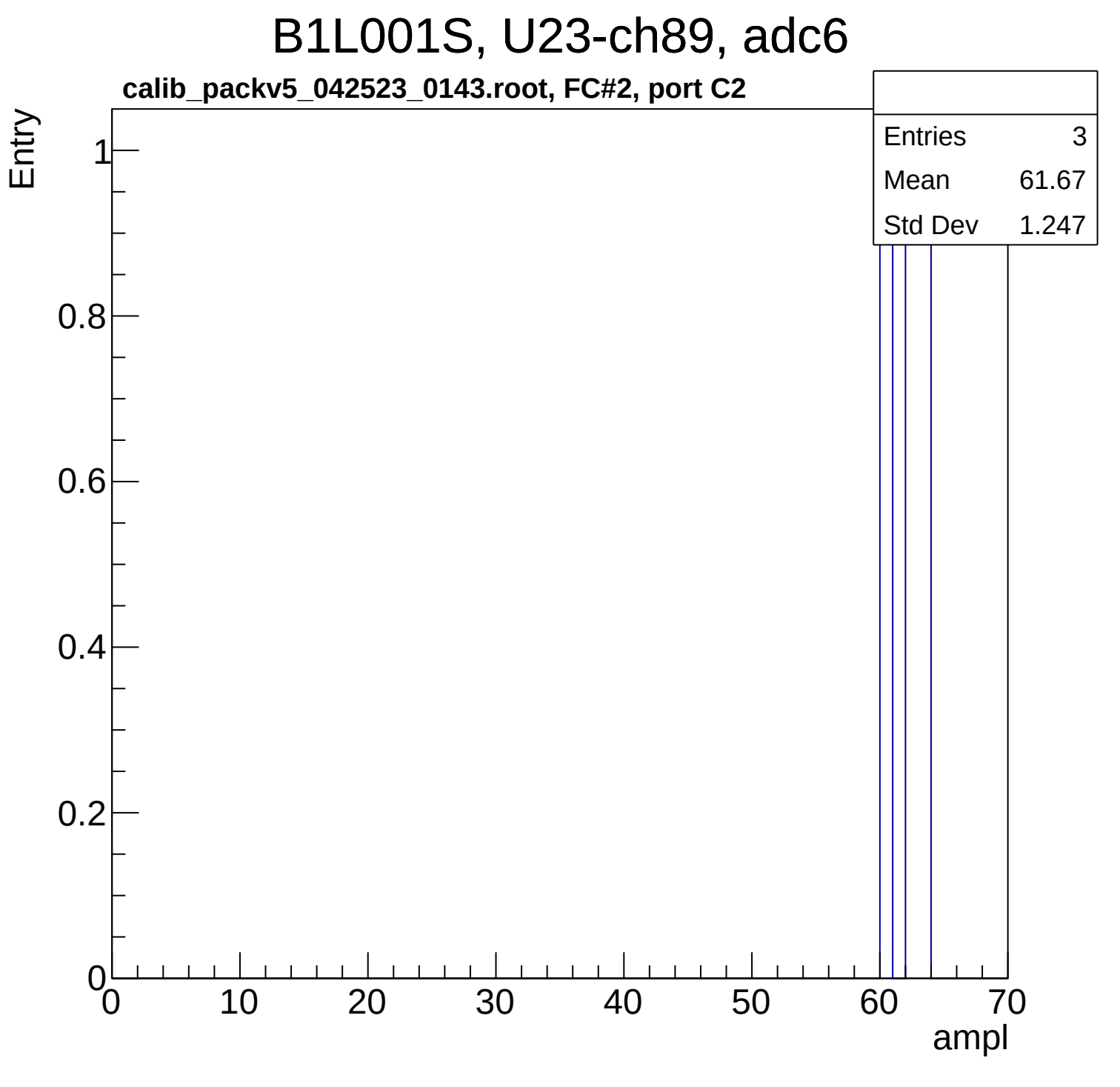
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch89, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
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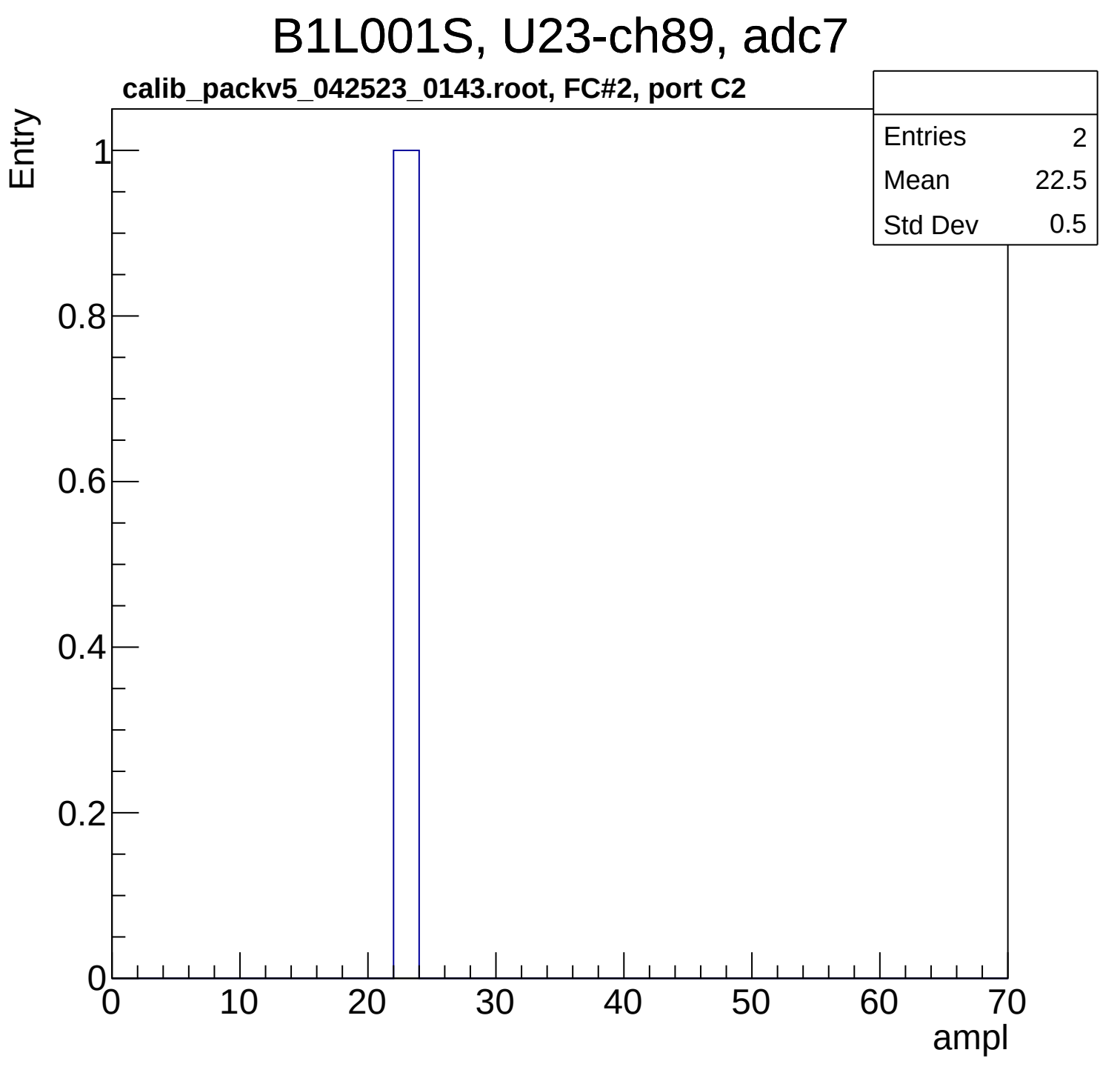
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch89, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 22.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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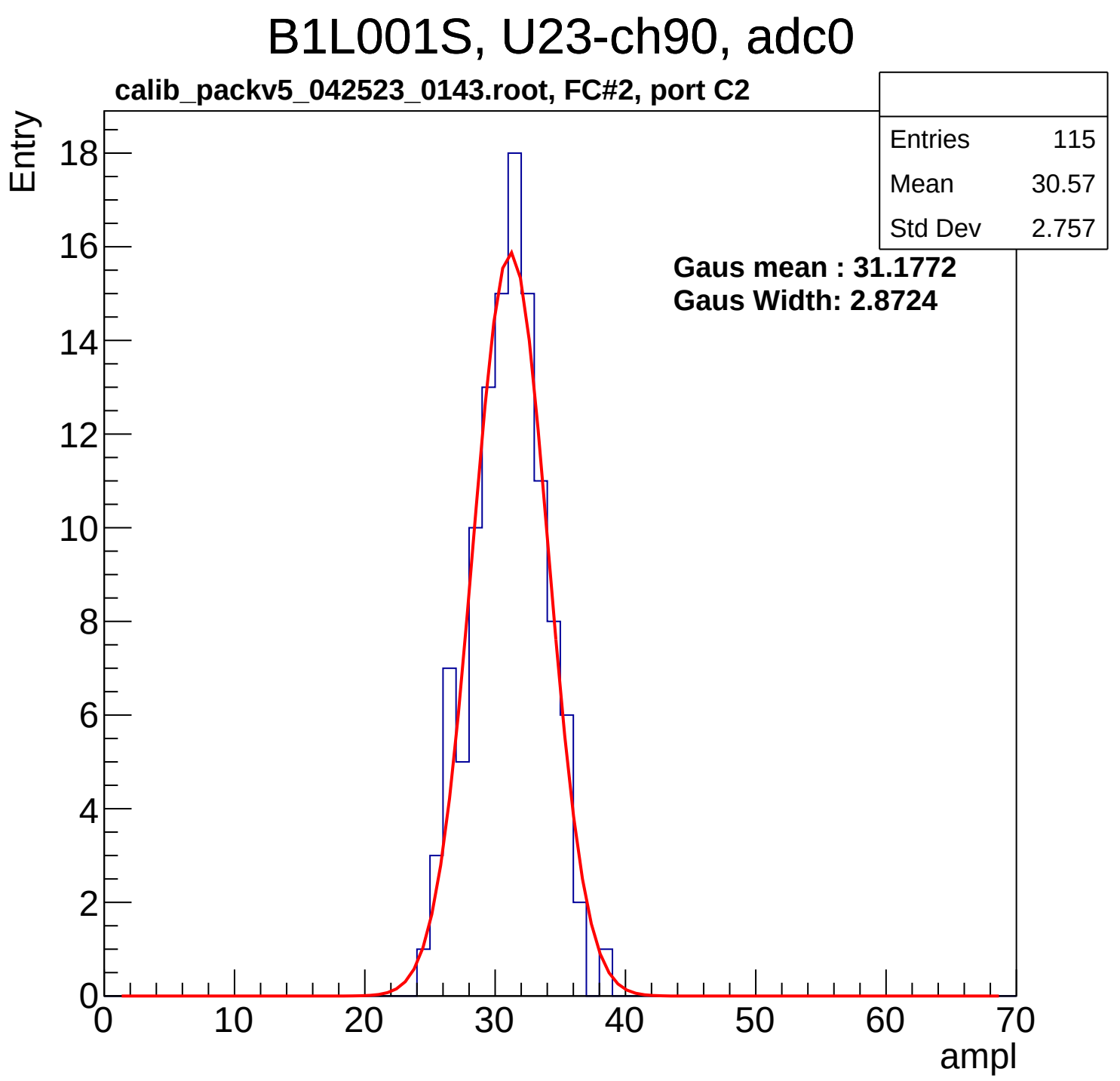
B1L001S, U23-ch90, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 30.57
Std Dev 2.757
Gaus mean : 31.1772
Gaus Width: 2.8724
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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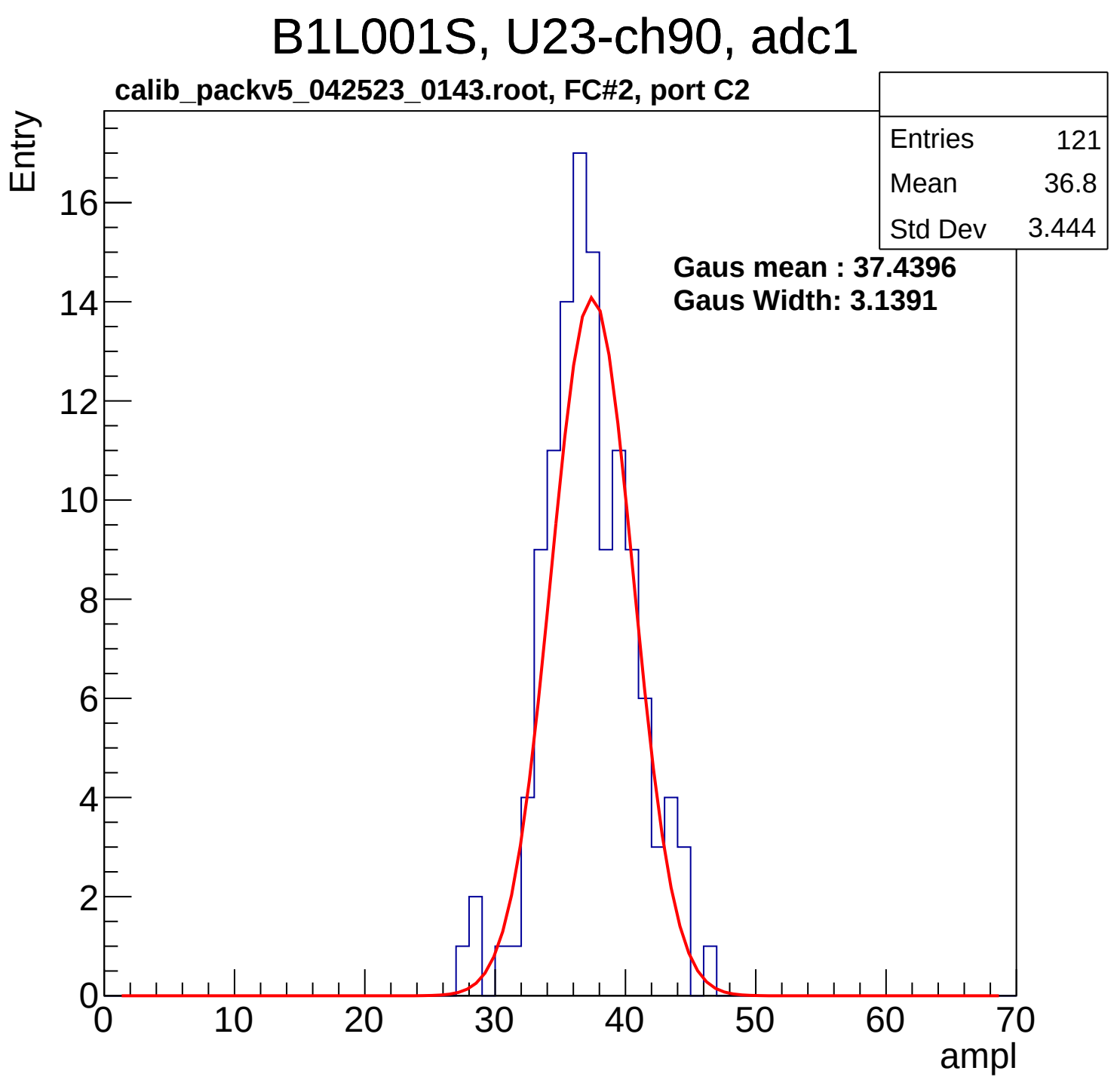
B1L001S, U23-ch90, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 36.8
Std Dev 3.444
Gaus mean : 37.4396
Gaus Width: 3.1391
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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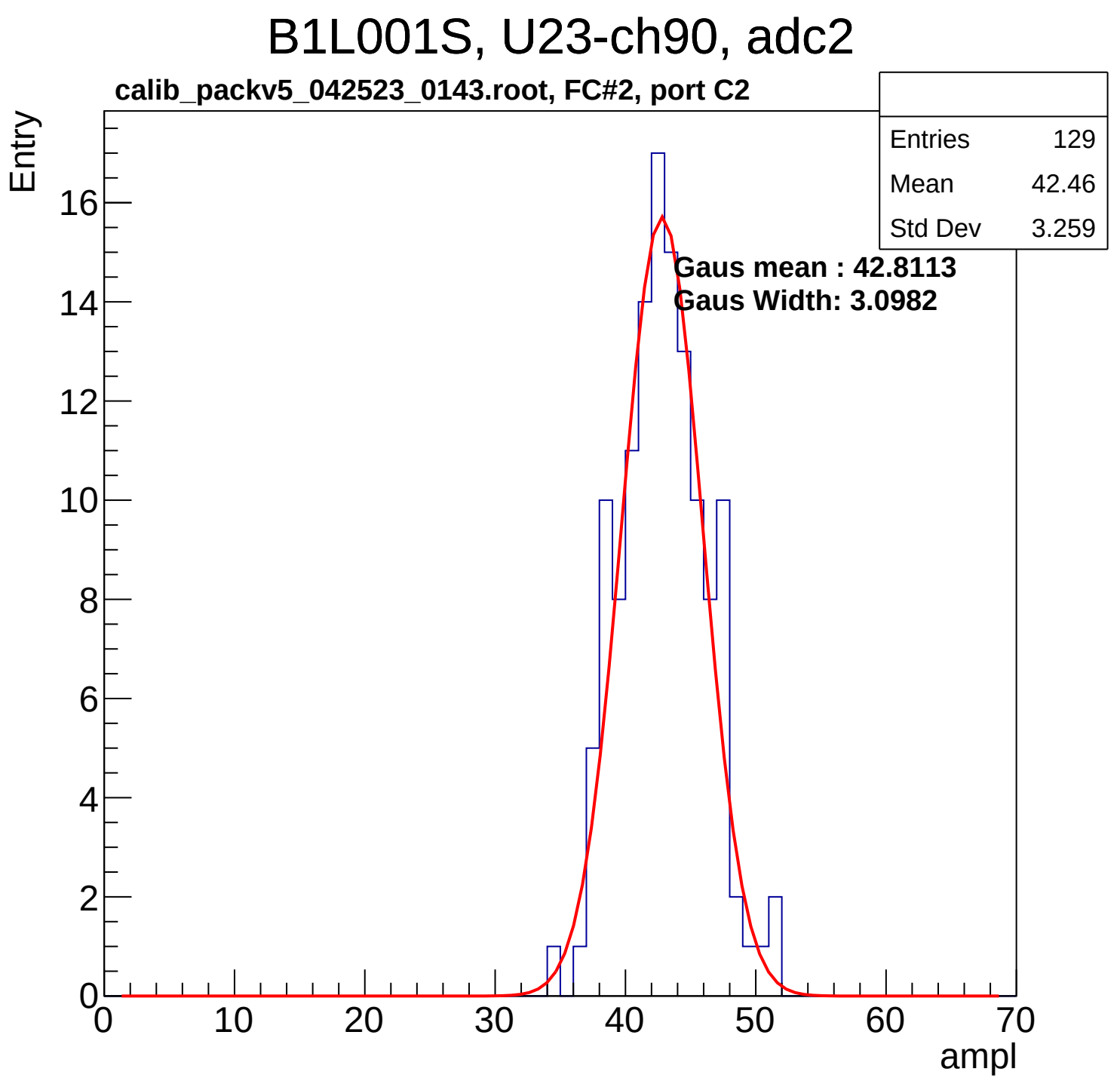
B1L001S, U23-ch90, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 42.46
Std Dev 3.259
Gaus mean : 42.8113
Gaus Width: 3.0982
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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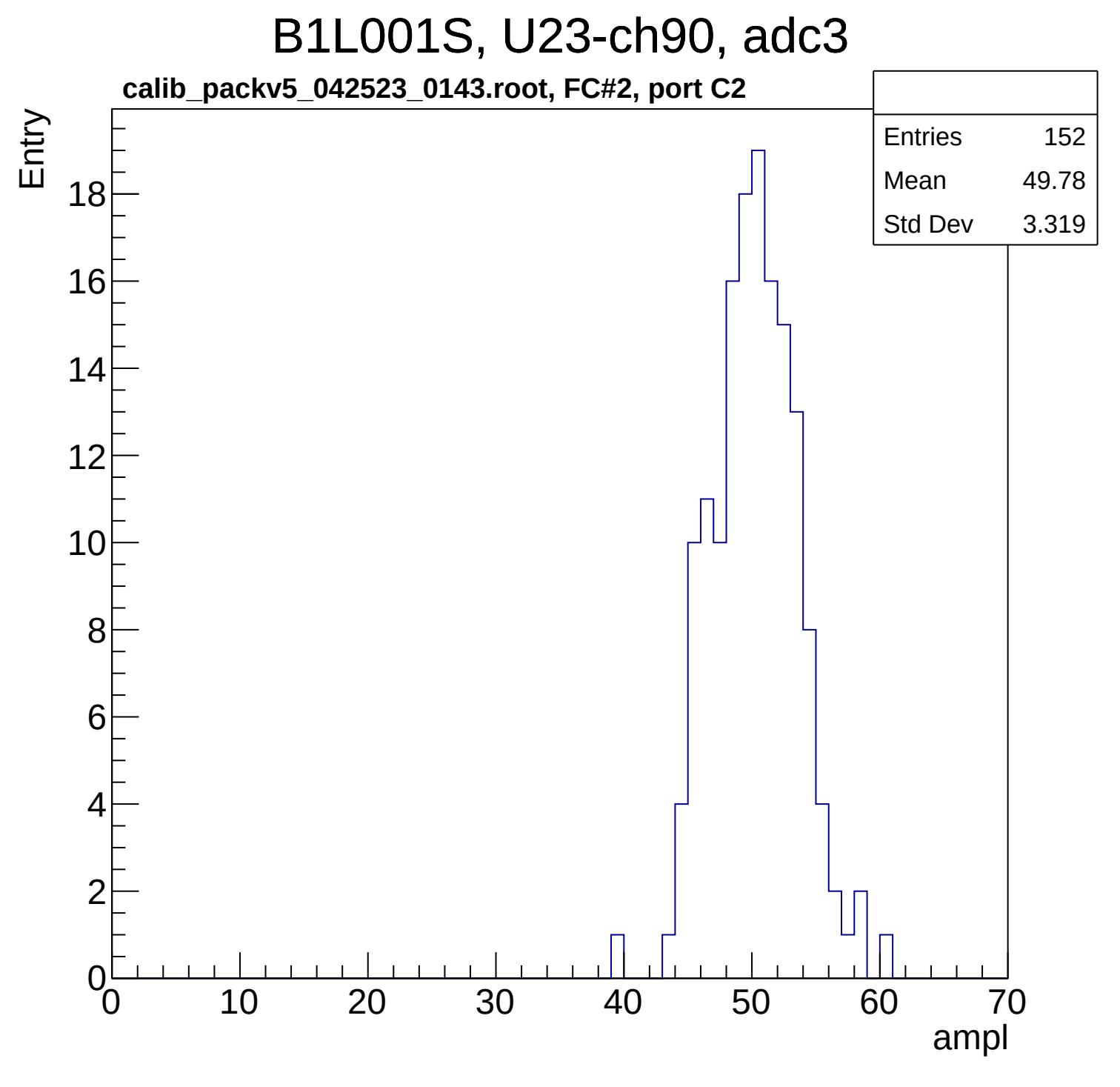
B1L001S, U23-ch90, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 49.78
Std Dev 3.319
Entry
ampl
0
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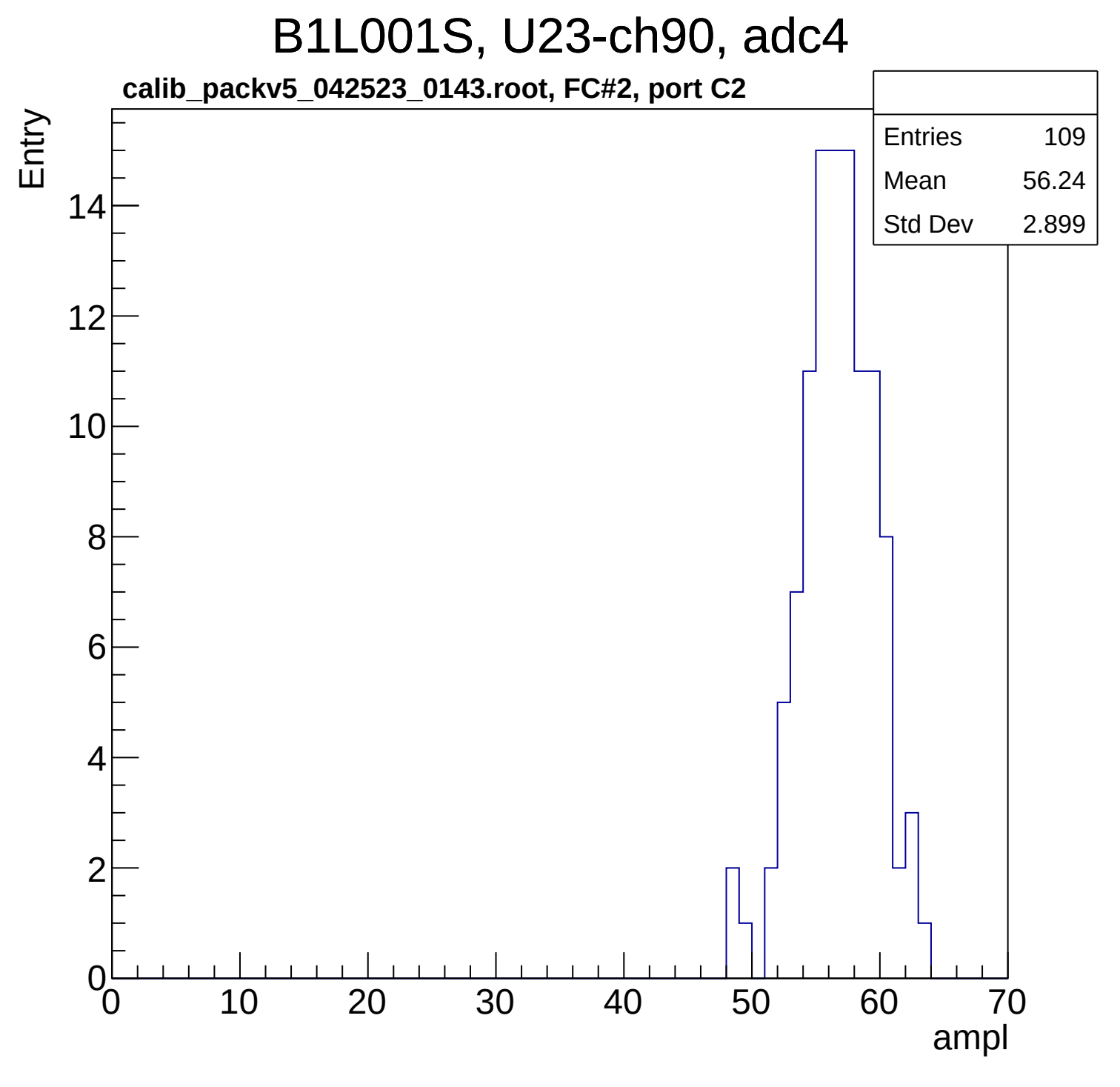
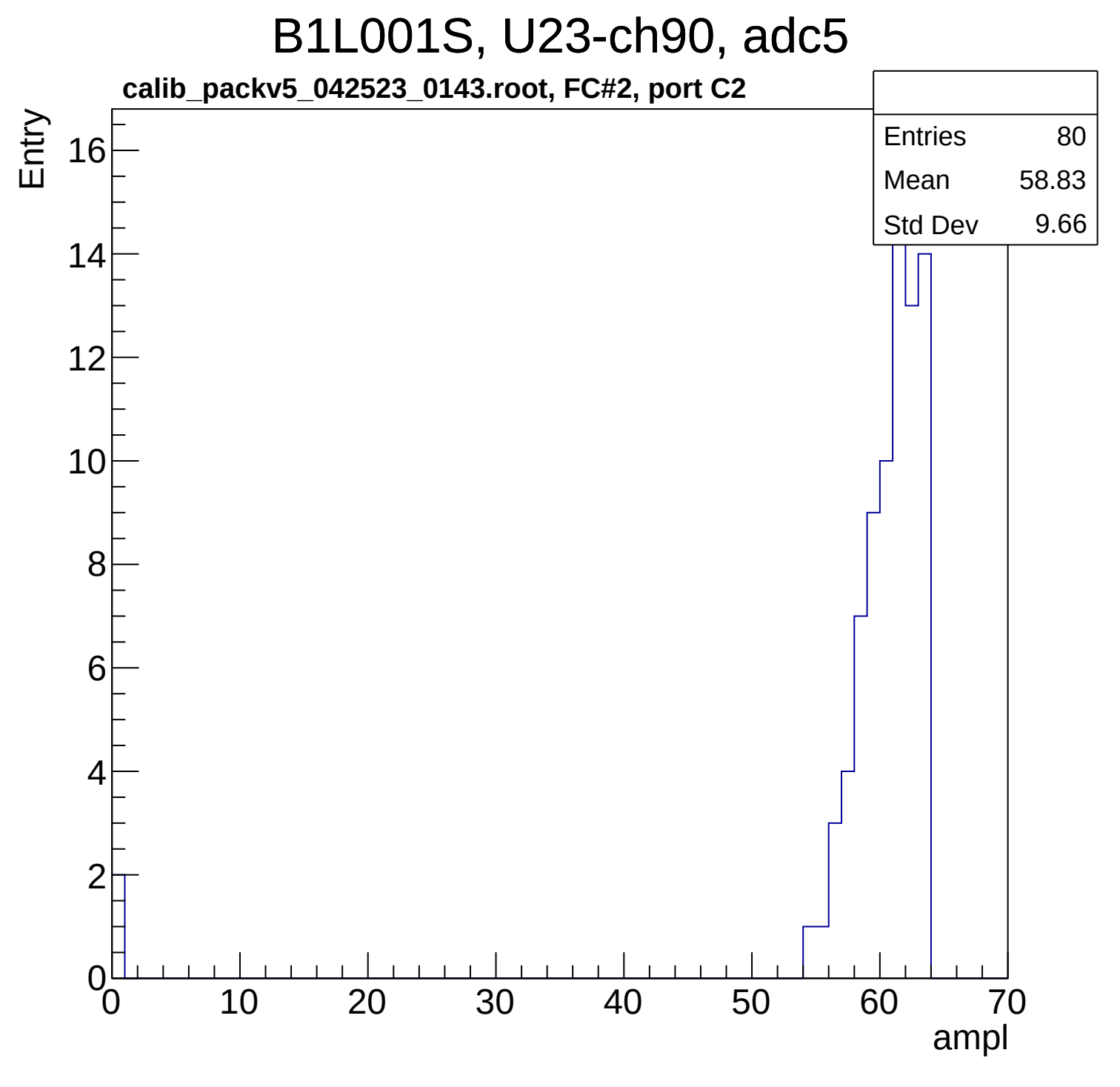
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch90, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 56.24
Std Dev 2.899
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch90, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 58.83
Std Dev 9.66
Entry
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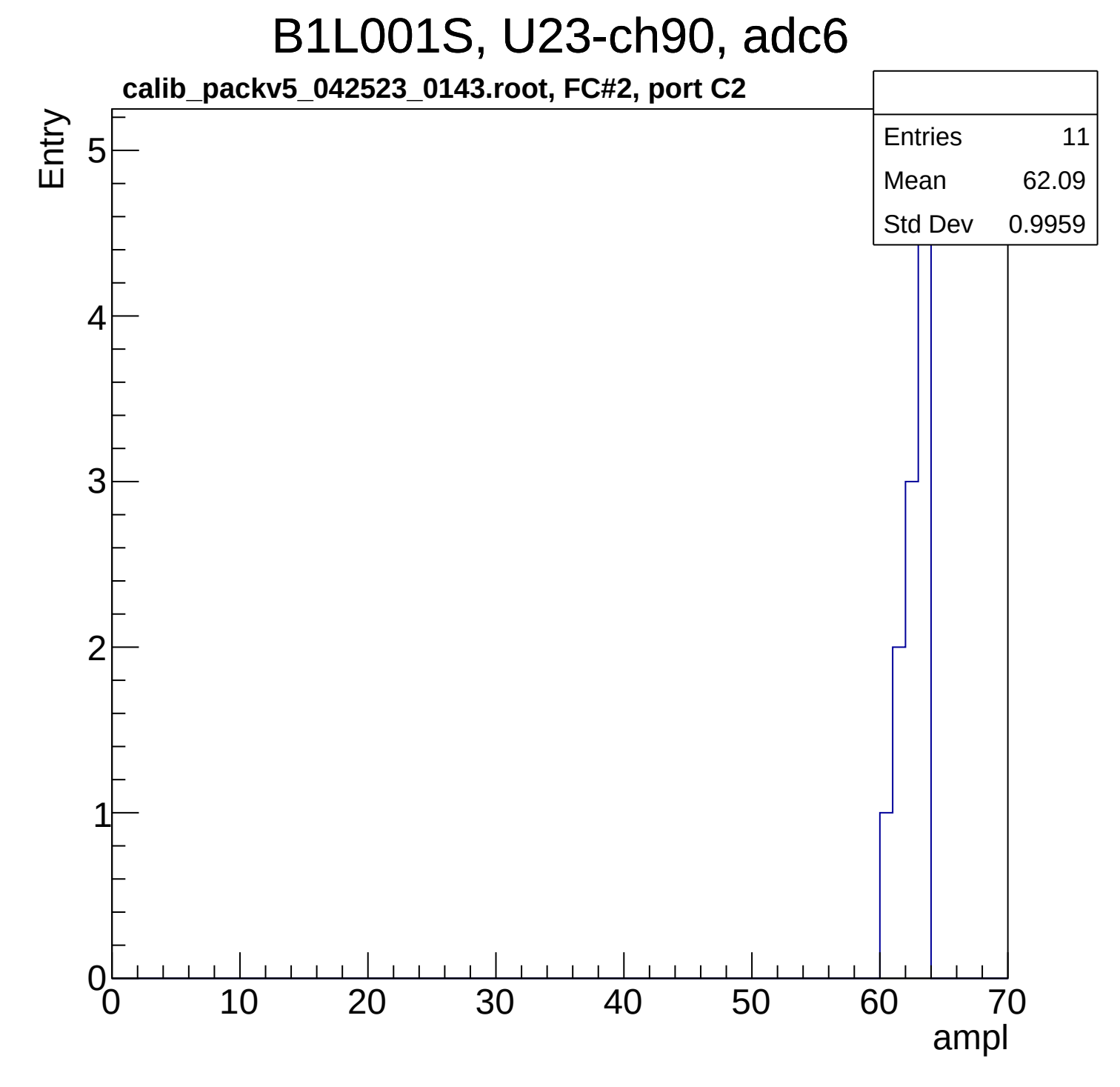
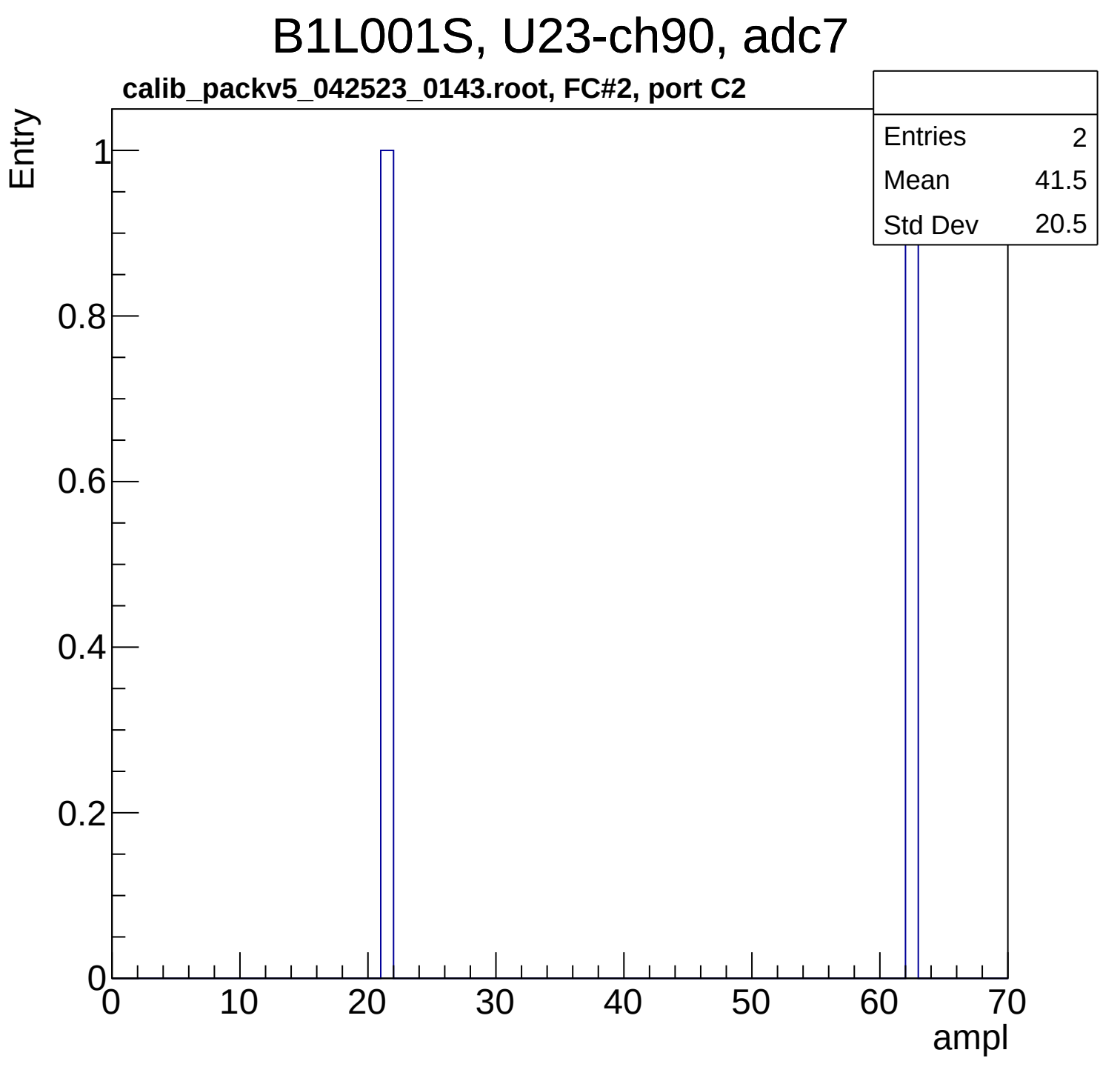
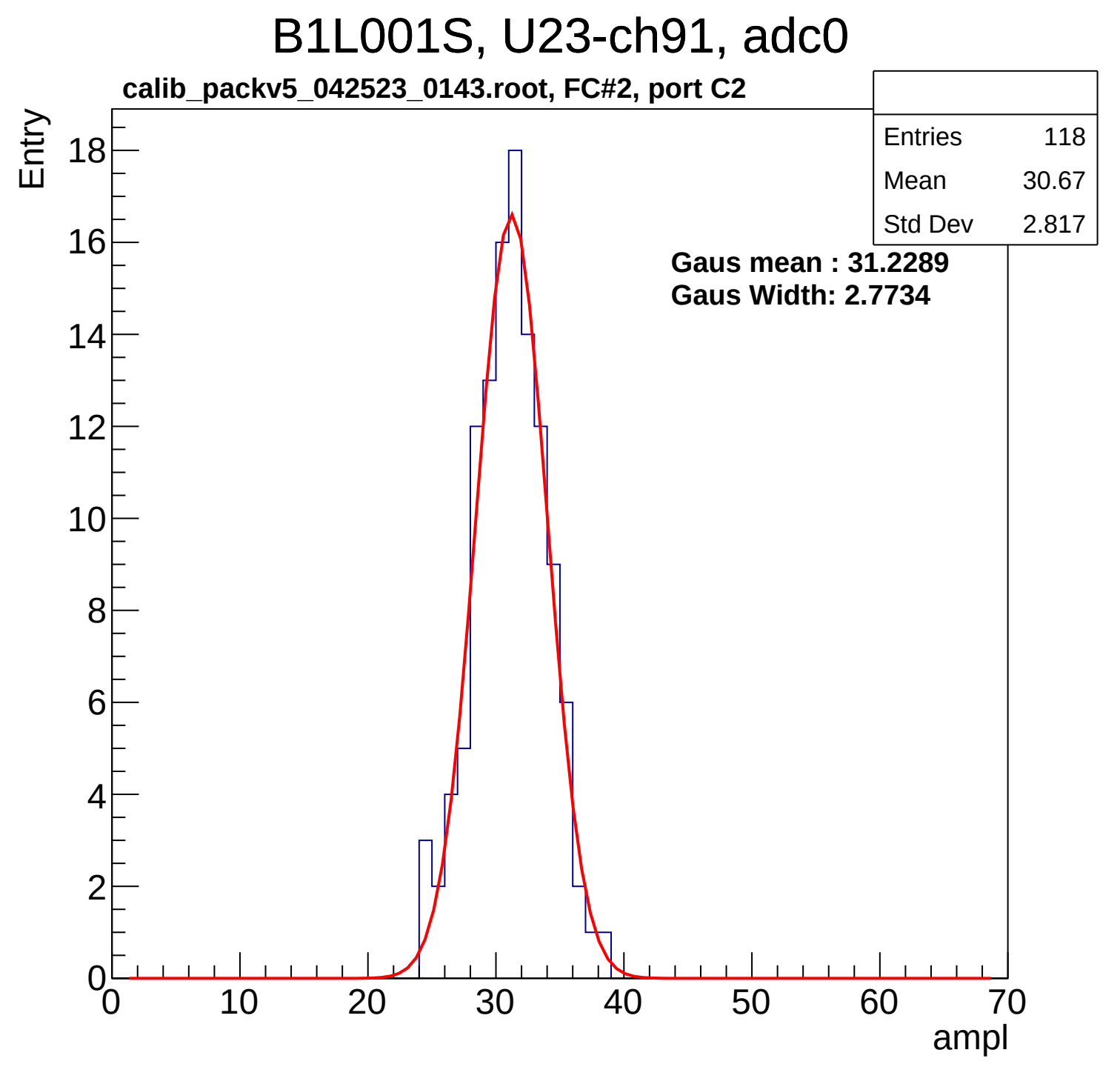
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch90, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 62.09
Std Dev 0.9959
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch90, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 41.5
Std Dev 20.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch91, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 30.67
Std Dev 2.817
Gaus mean : 31.2289
Gaus Width: 2.7734
Entry
ampl
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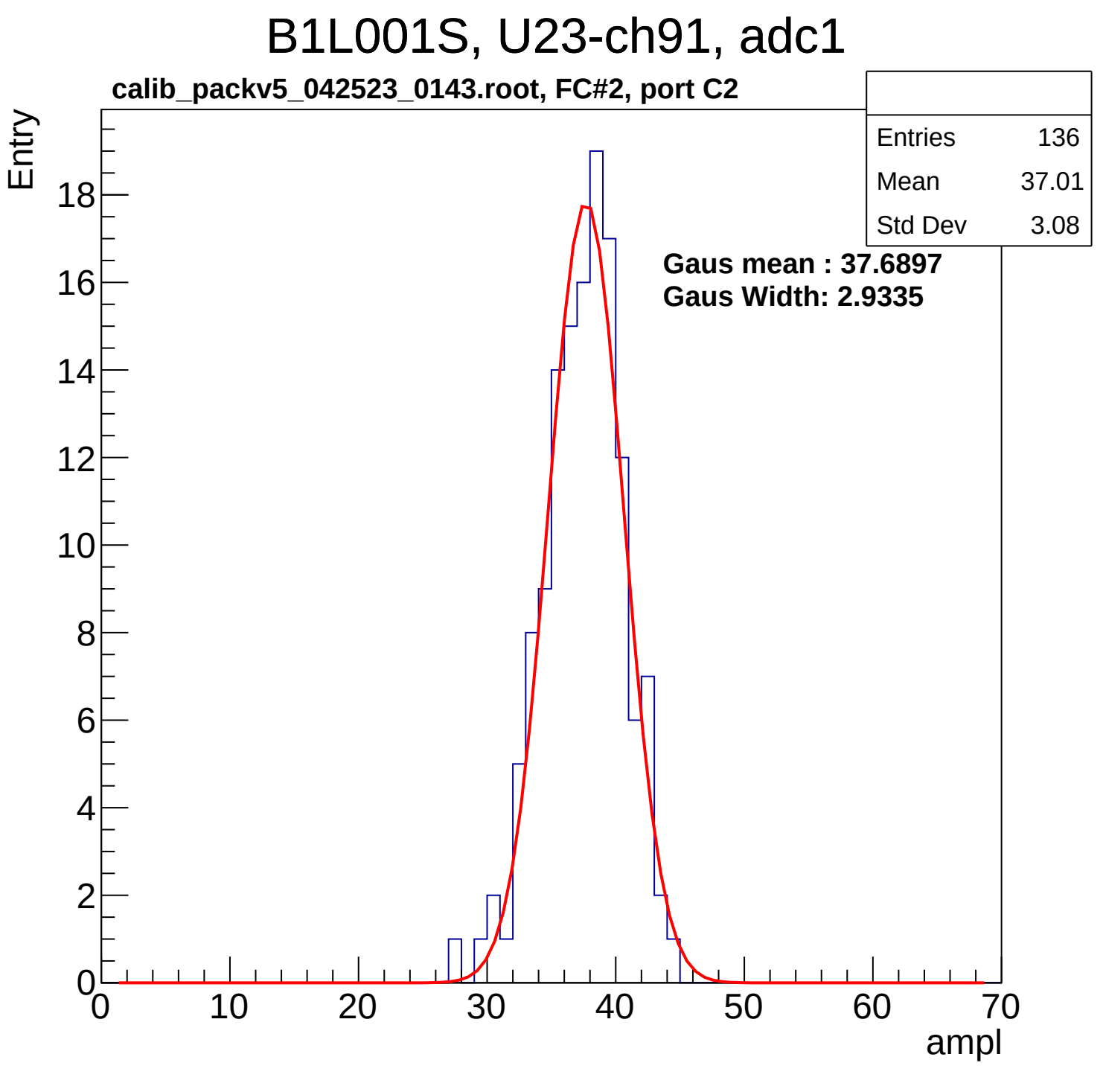
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch91, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 37.01
Std Dev 3.08
Gaus mean : 37.6897
Gaus Width: 2.9335
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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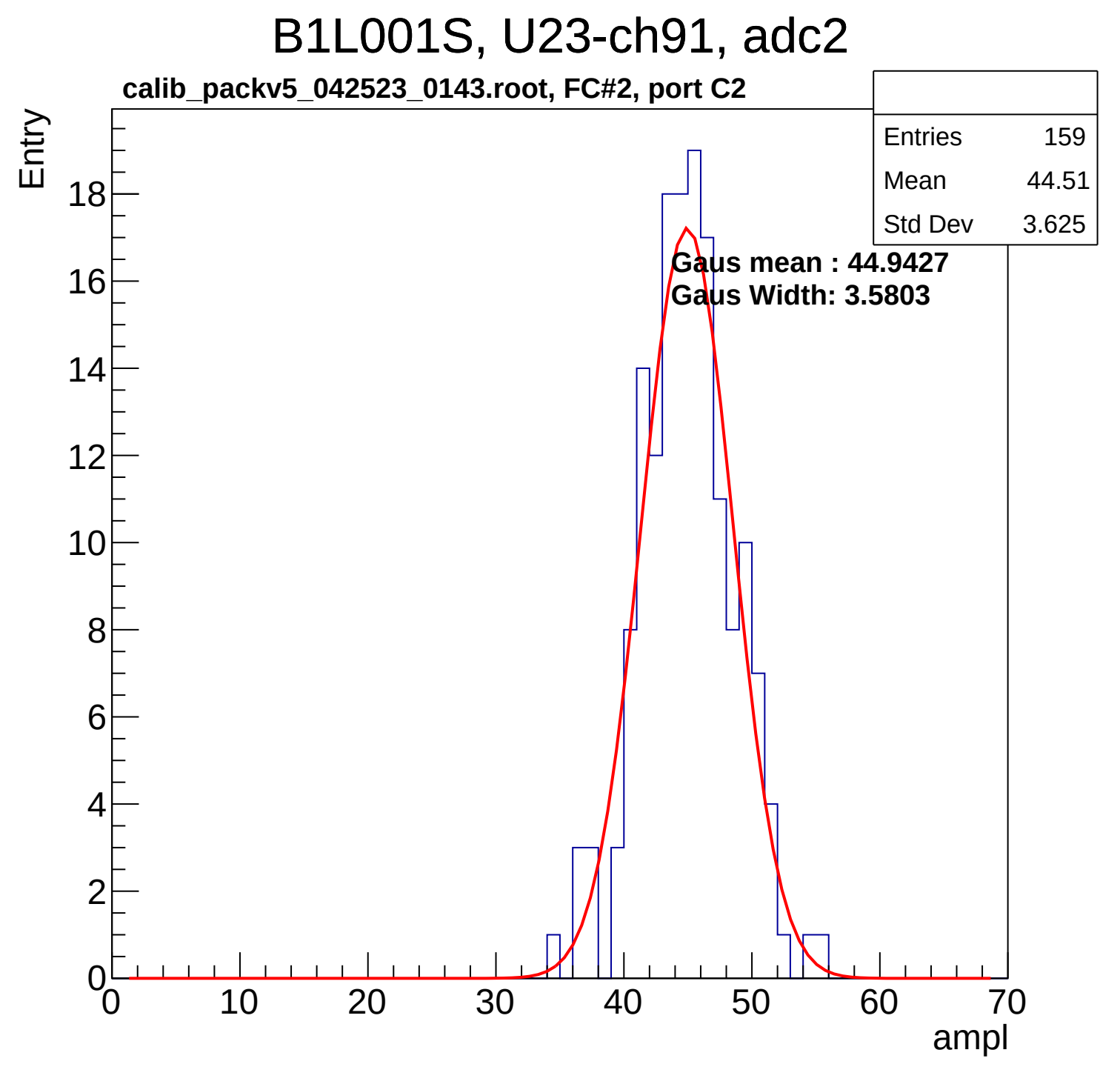
B1L001S, U23-ch91, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 44.51
Std Dev 3.625
Gaus mean : 44.9427
Gaus Width: 3.5803
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
30
40
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60
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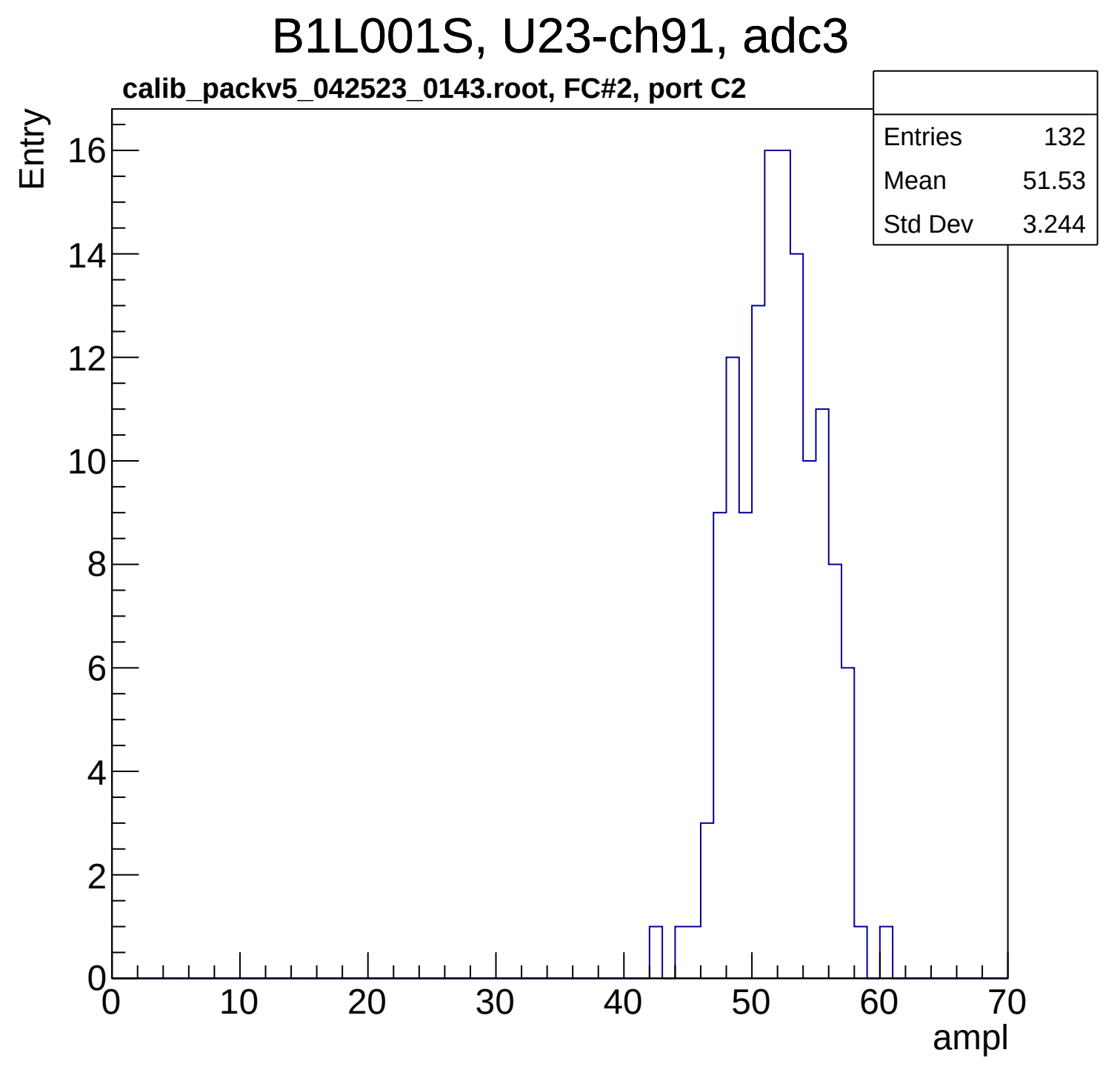
B1L001S, U23-ch91, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 51.53
Std Dev 3.244
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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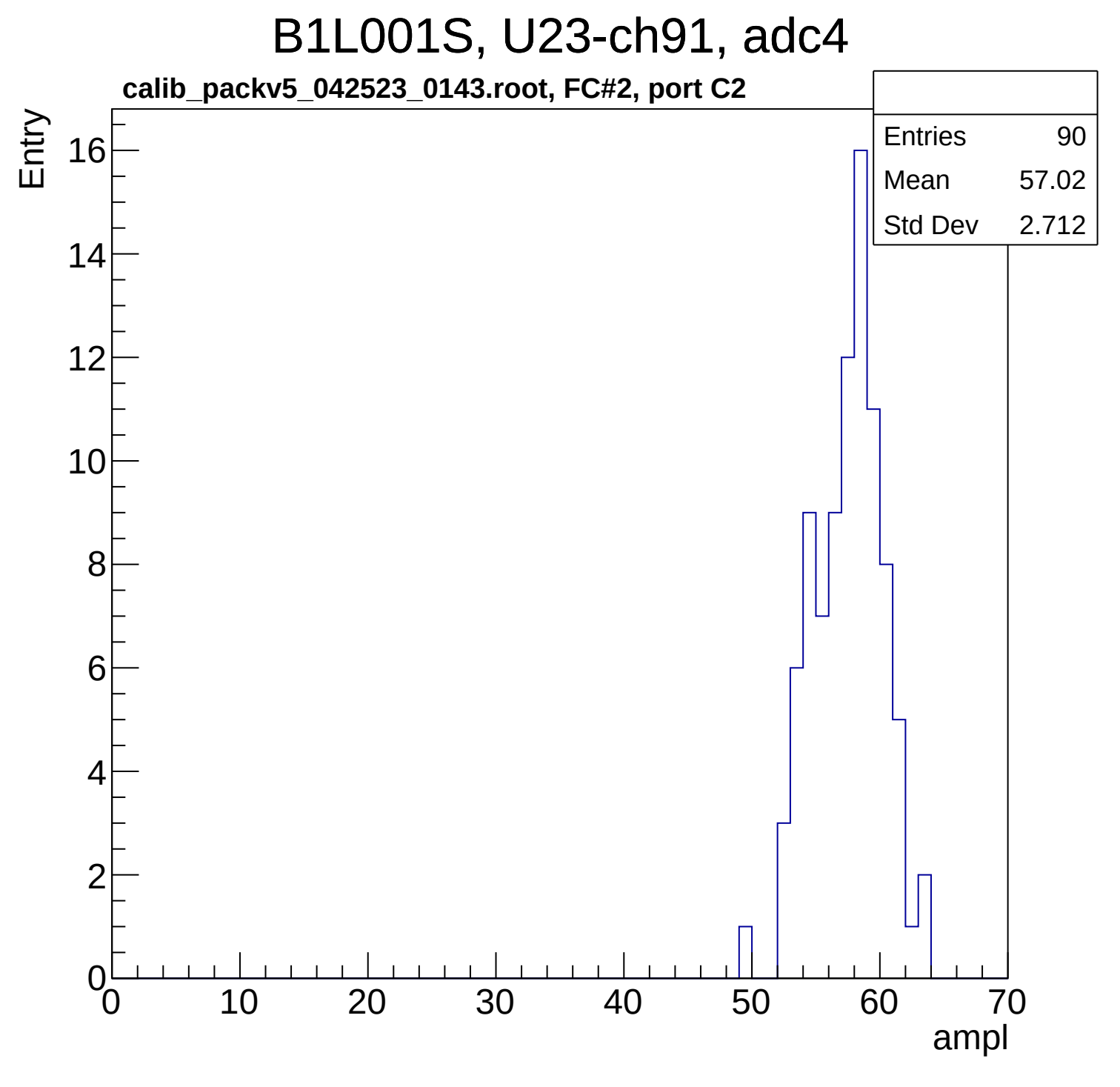
B1L001S, U23-ch91, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 57.02
Std Dev 2.712
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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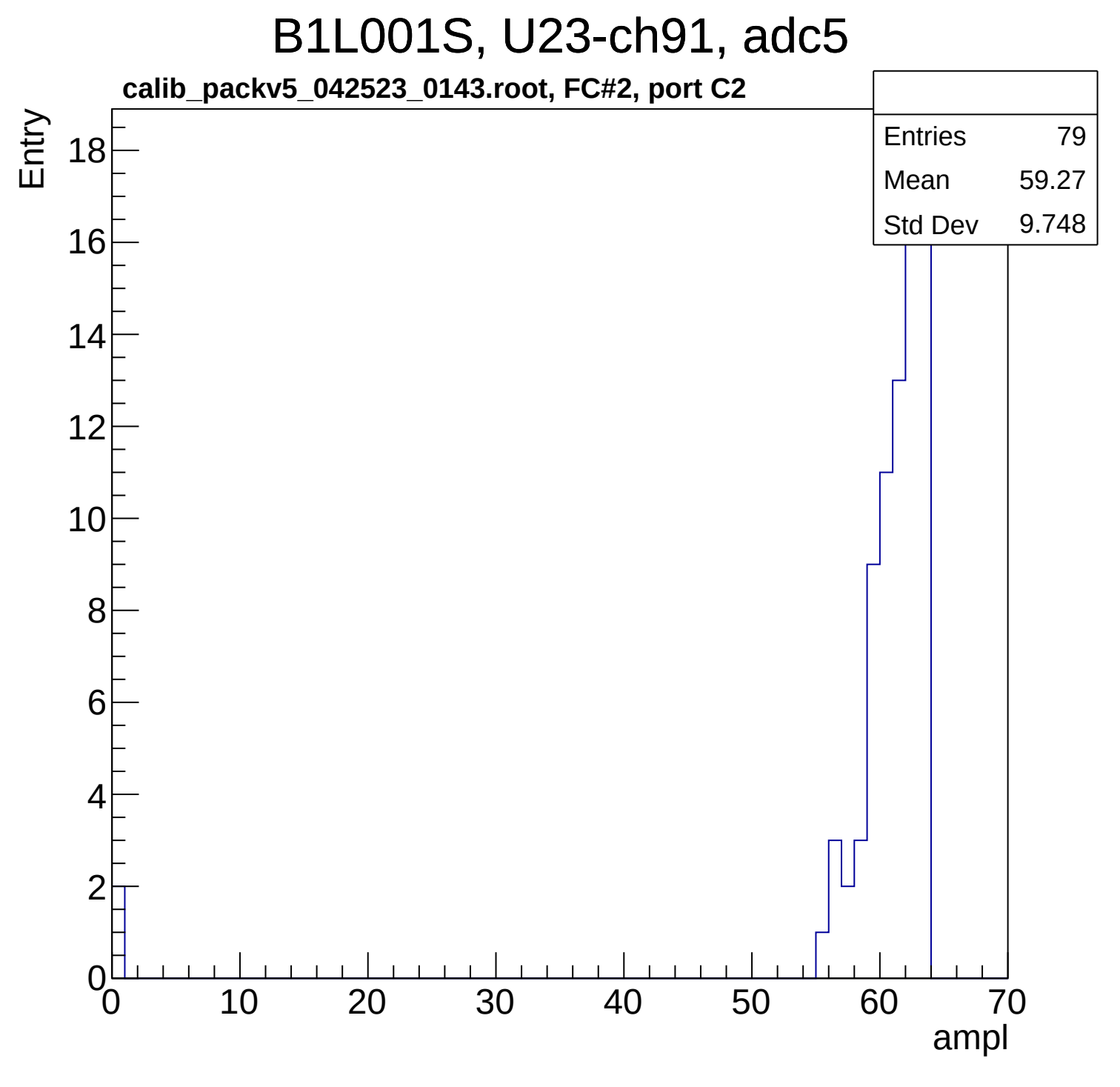
60
70


B1L001S, U23-ch91, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 79
Mean 59.27
Std Dev 9.748
Entry
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4
6
8
10
12
14
16
18
0
10
20
30
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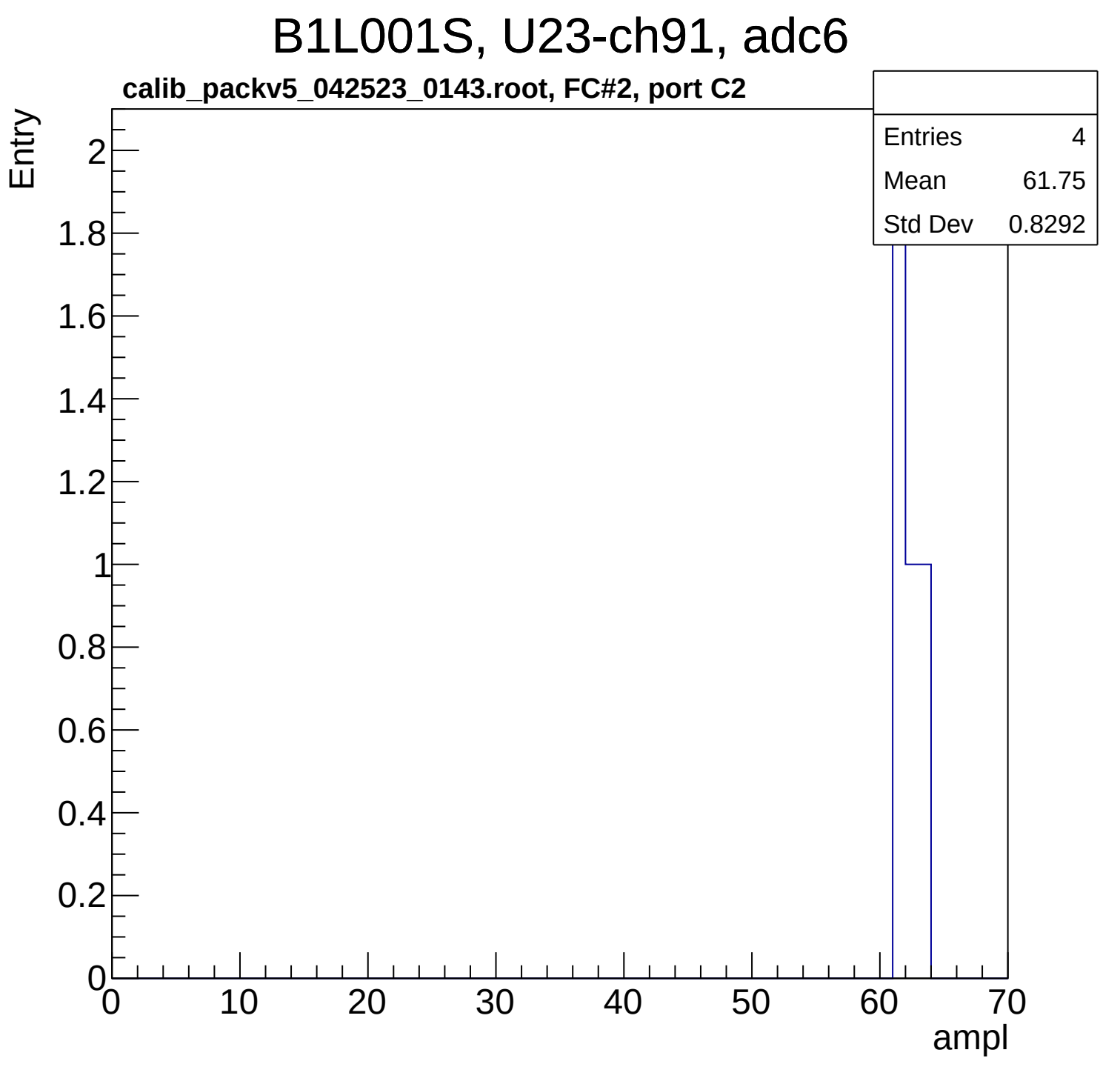
50
60
70
ampl


B1L001S, U23-ch91, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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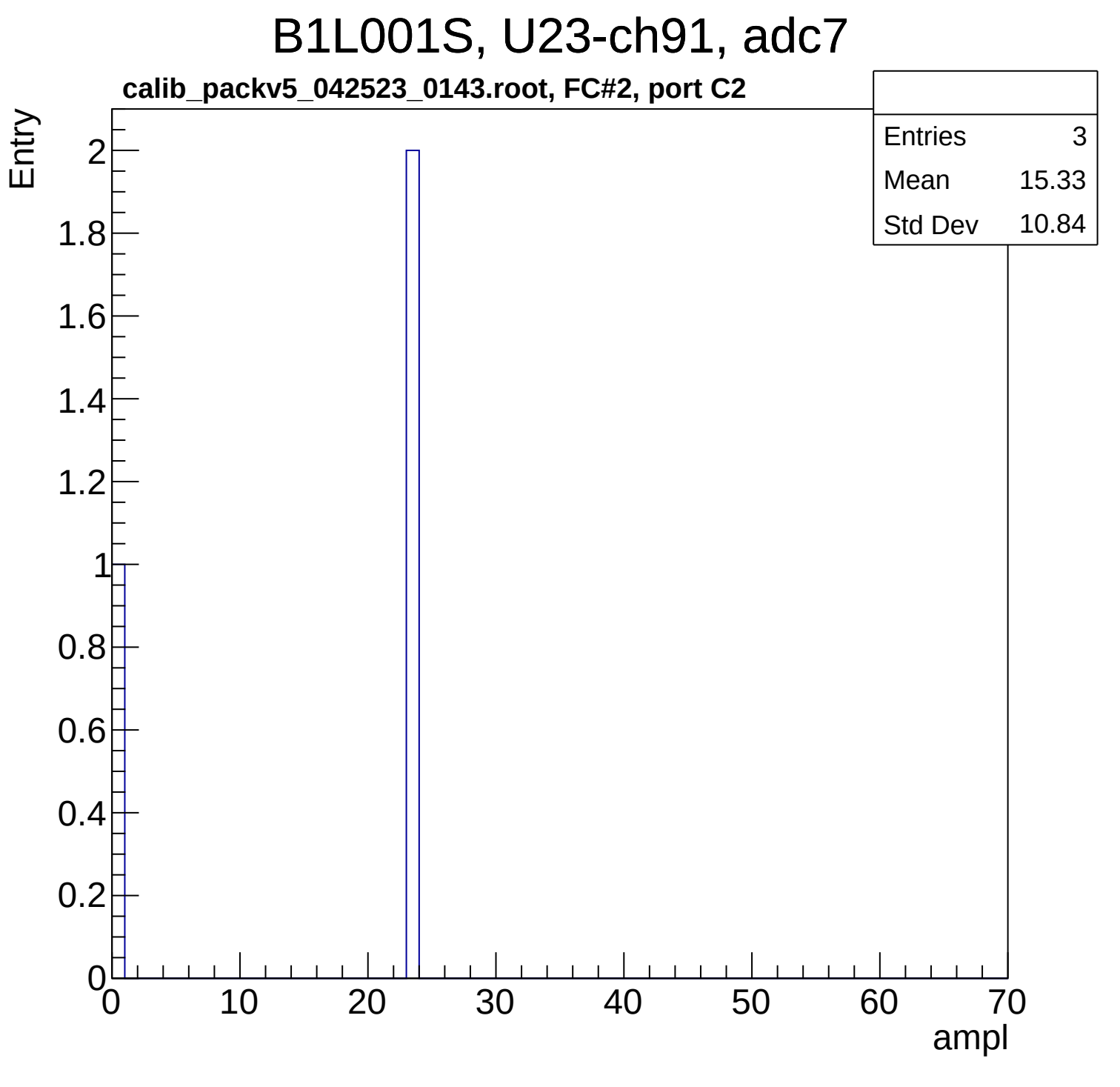
B1L001S, U23-ch91, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 15.33
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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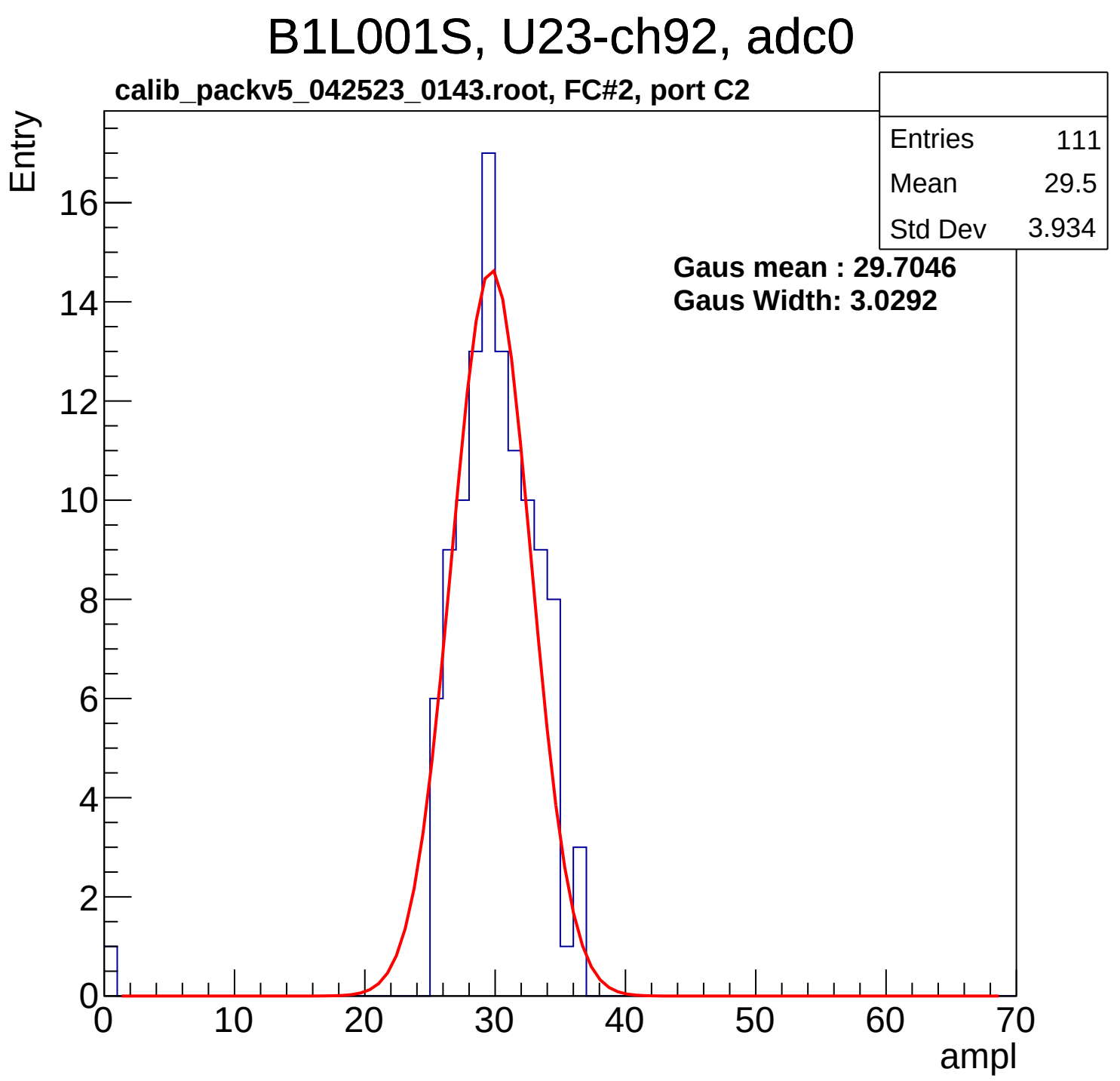
B1L001S, U23-ch92, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 29.5
Std Dev 3.934
Gaus mean : 29.7046
Gaus Width: 3.0292
Entry
ampl
0
2
4
6
8
10
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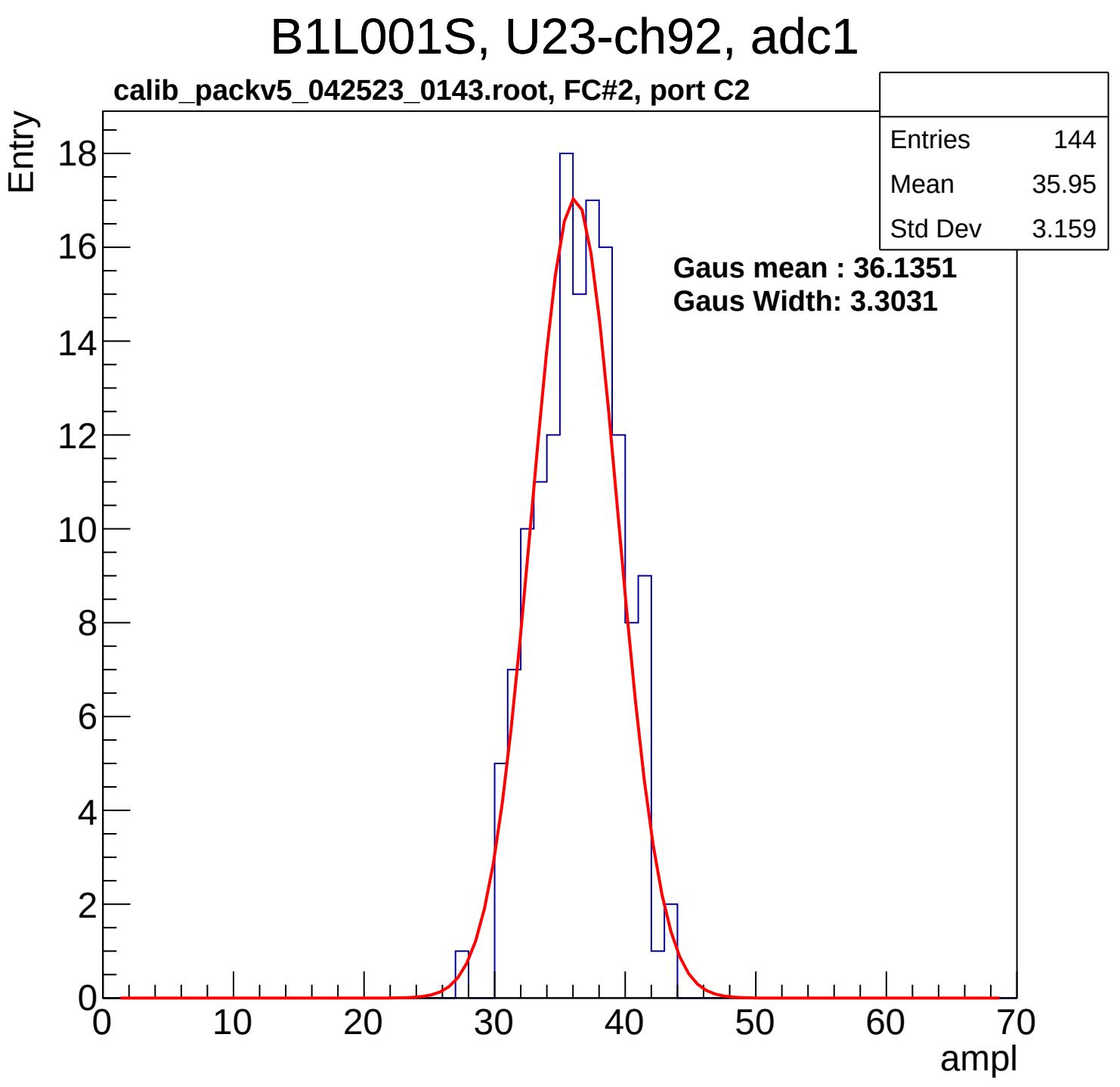
B1L001S, U23-ch92, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 35.95
Std Dev 3.159
Gaus mean : 36.1351
Gaus Width: 3.3031
Entry
ampl
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4
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0
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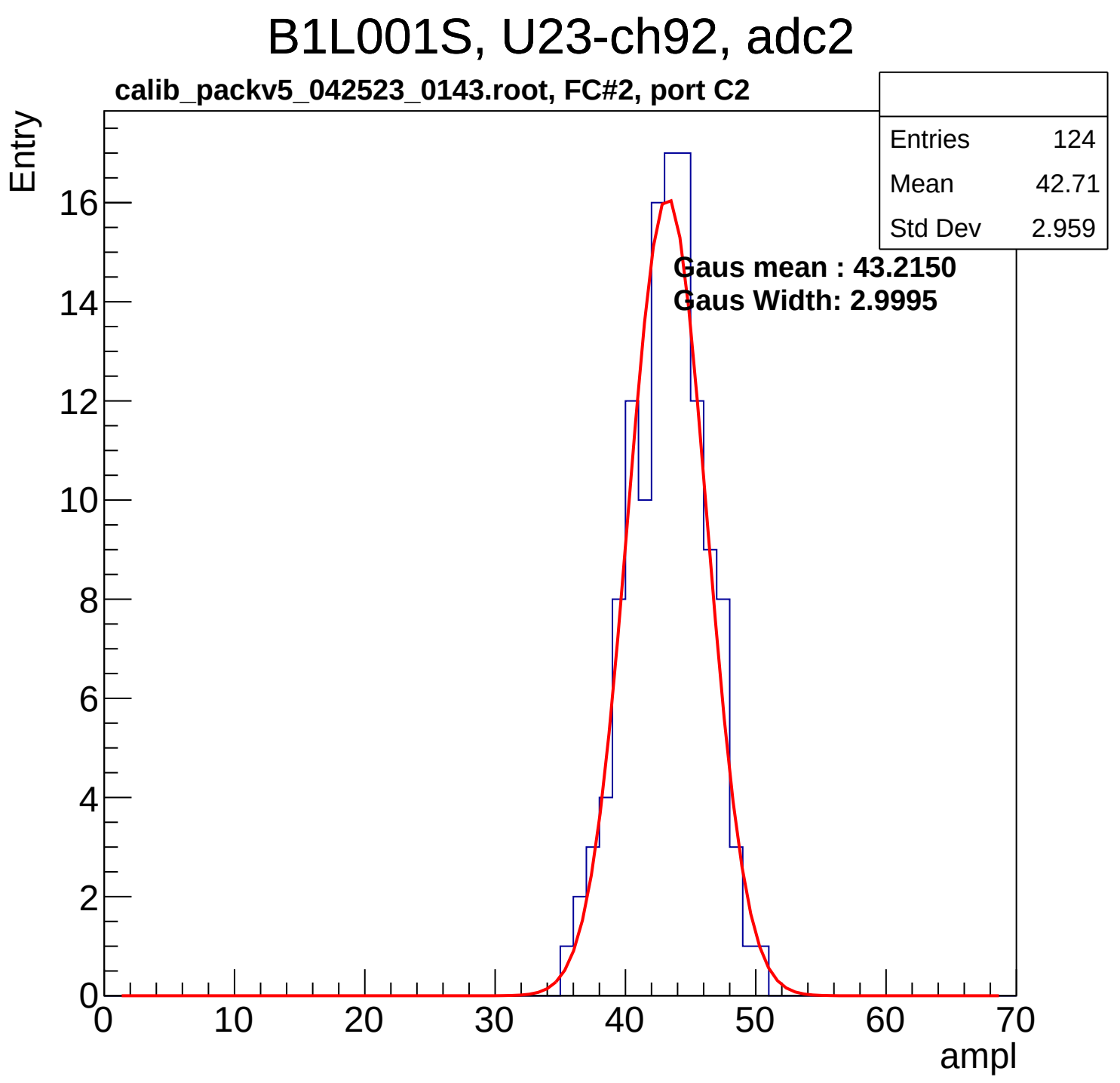
B1L001S, U23-ch92, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 42.71
Std Dev 2.959
Gaus mean : 43.2150
Gaus Width: 2.9995
Entry
ampl
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2
4
6
8
10
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14
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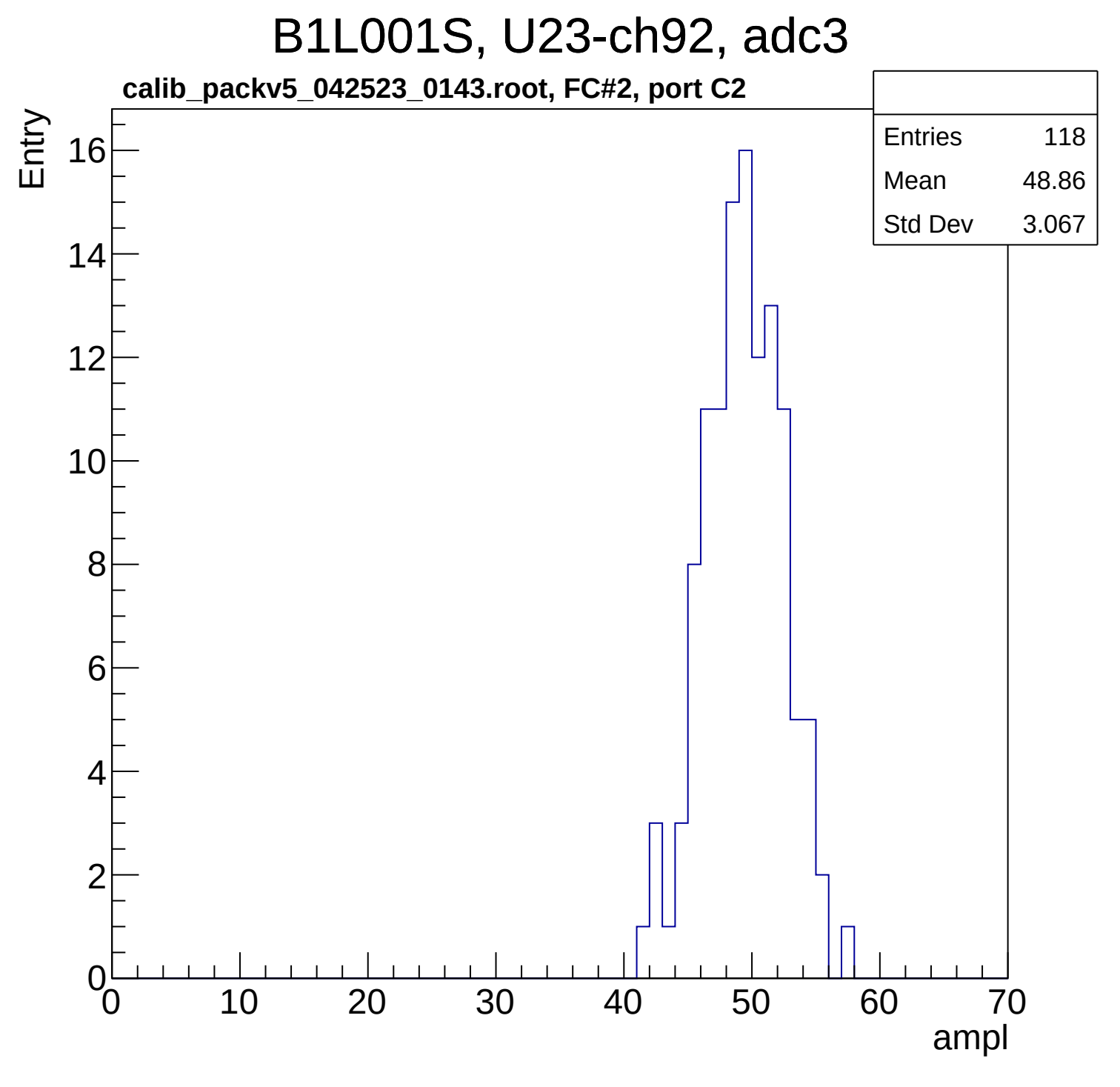
B1L001S, U23-ch92, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 48.86
Std Dev 3.067
Entry
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2
4
6
8
10
12
14
16
0
10
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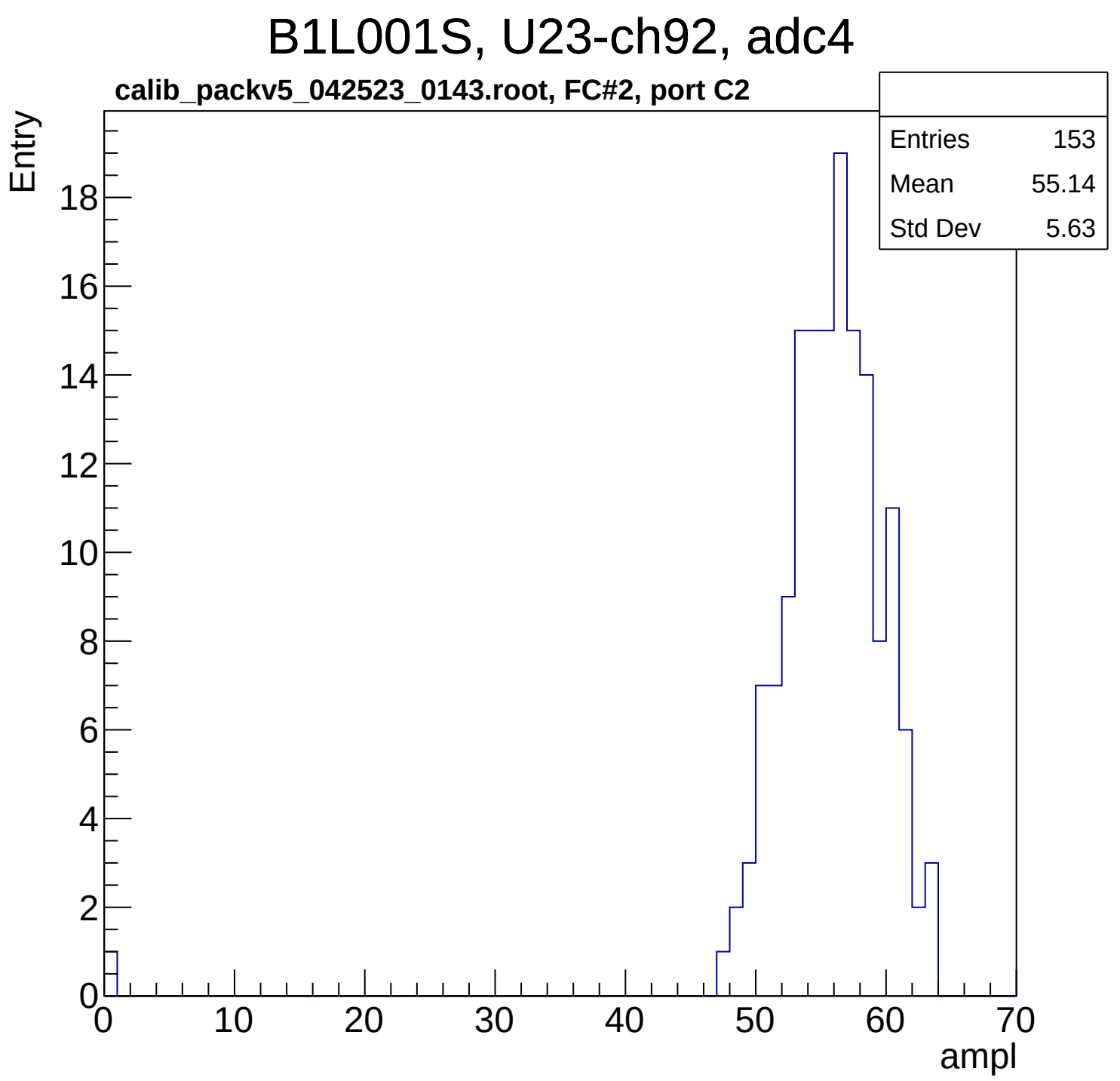
70
ampl


B1L001S, U23-ch92, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 55.14
Std Dev 5.63
Entry
ampl
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4
6
8
10
12
14
16
18
0
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60
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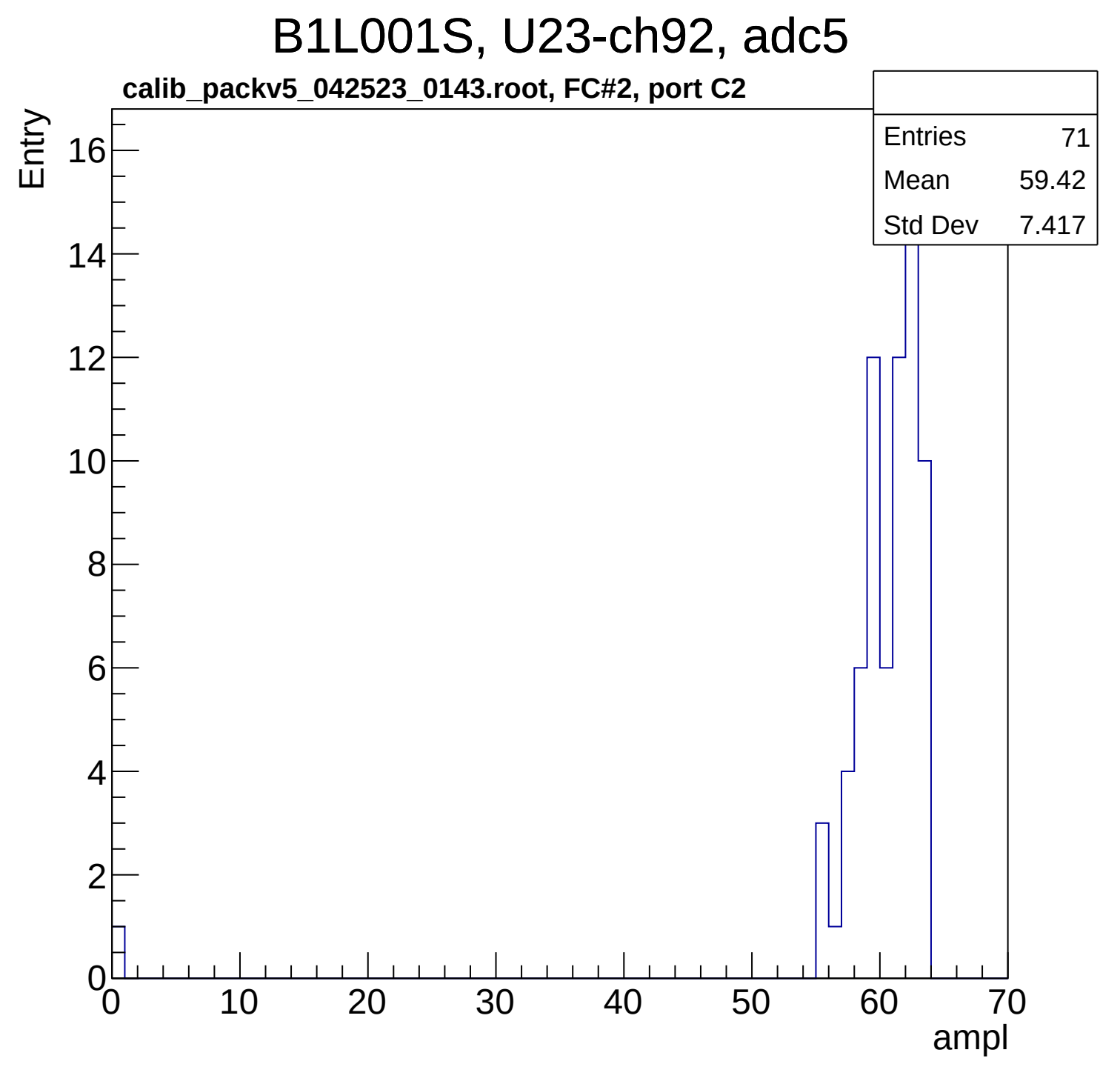
B1L001S, U23-ch92, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 71
Mean 59.42
Std Dev 7.417
Entry
ampl
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2
4
6
8
10
12
14
16
0
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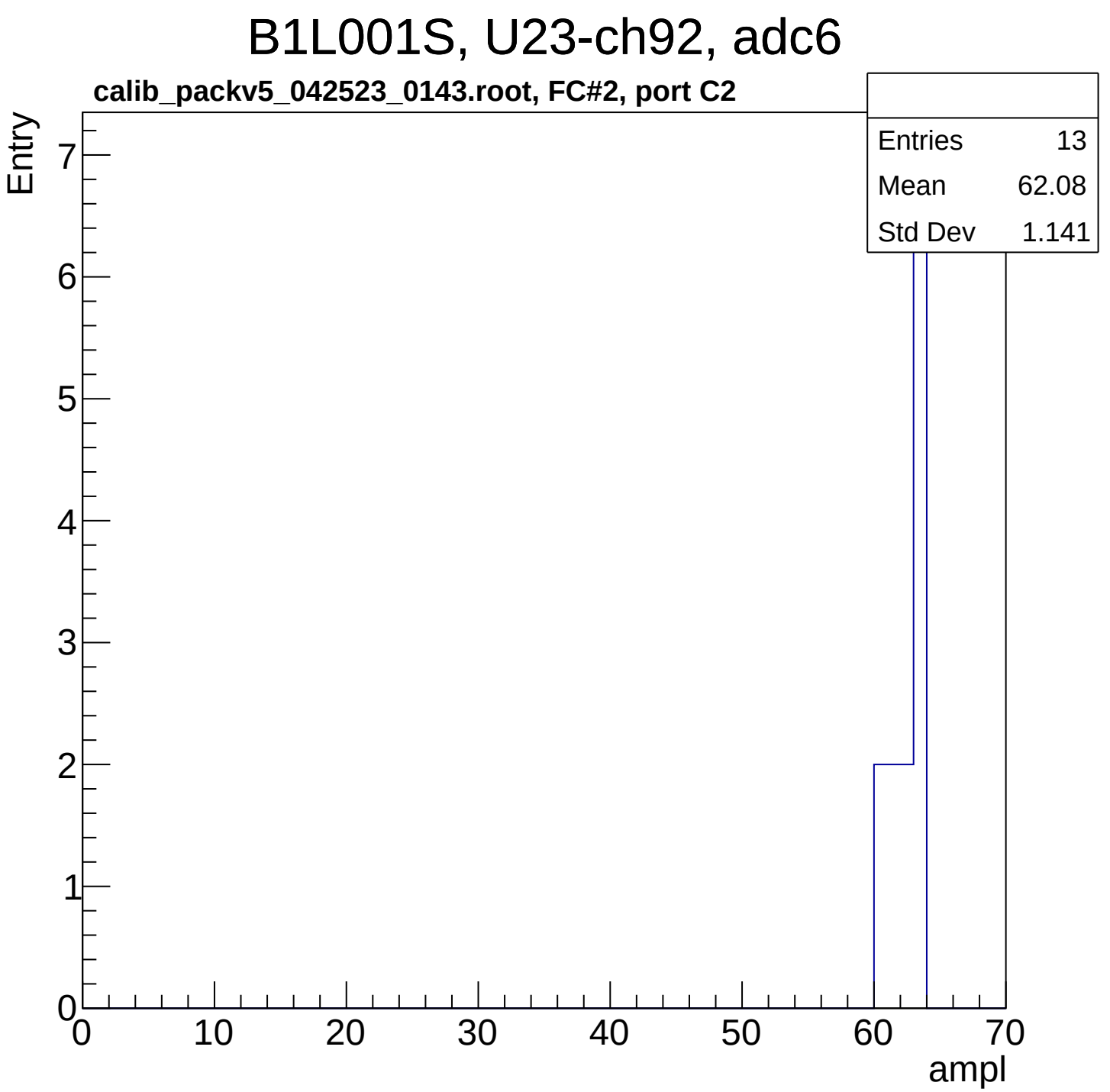
B1L001S, U23-ch92, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62.08
Std Dev 1.141
Entry
ampl
0
1
2
3
4
5
6
7
0
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60
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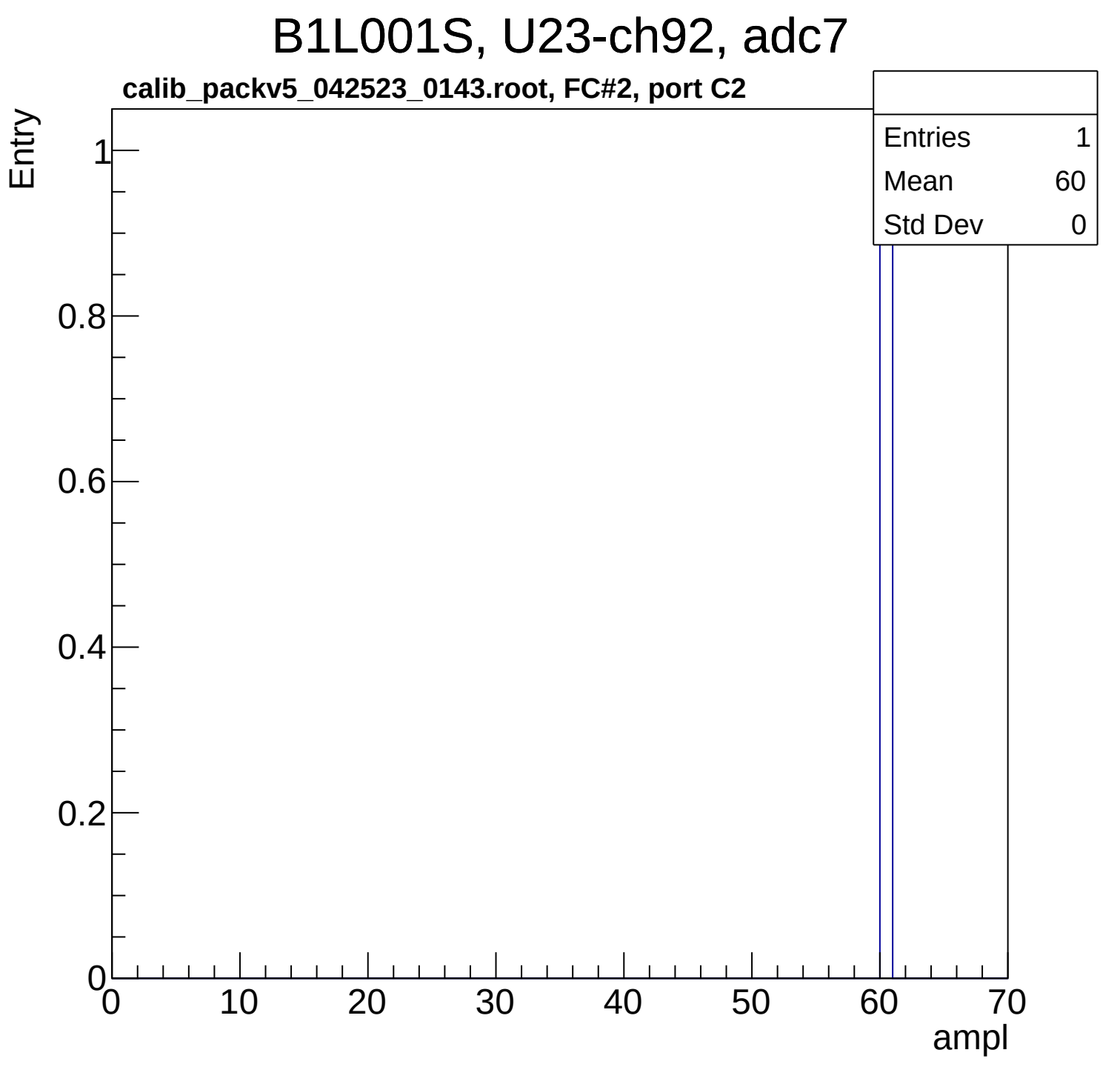
B1L001S, U23-ch92, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 60
Std Dev 0
Entry
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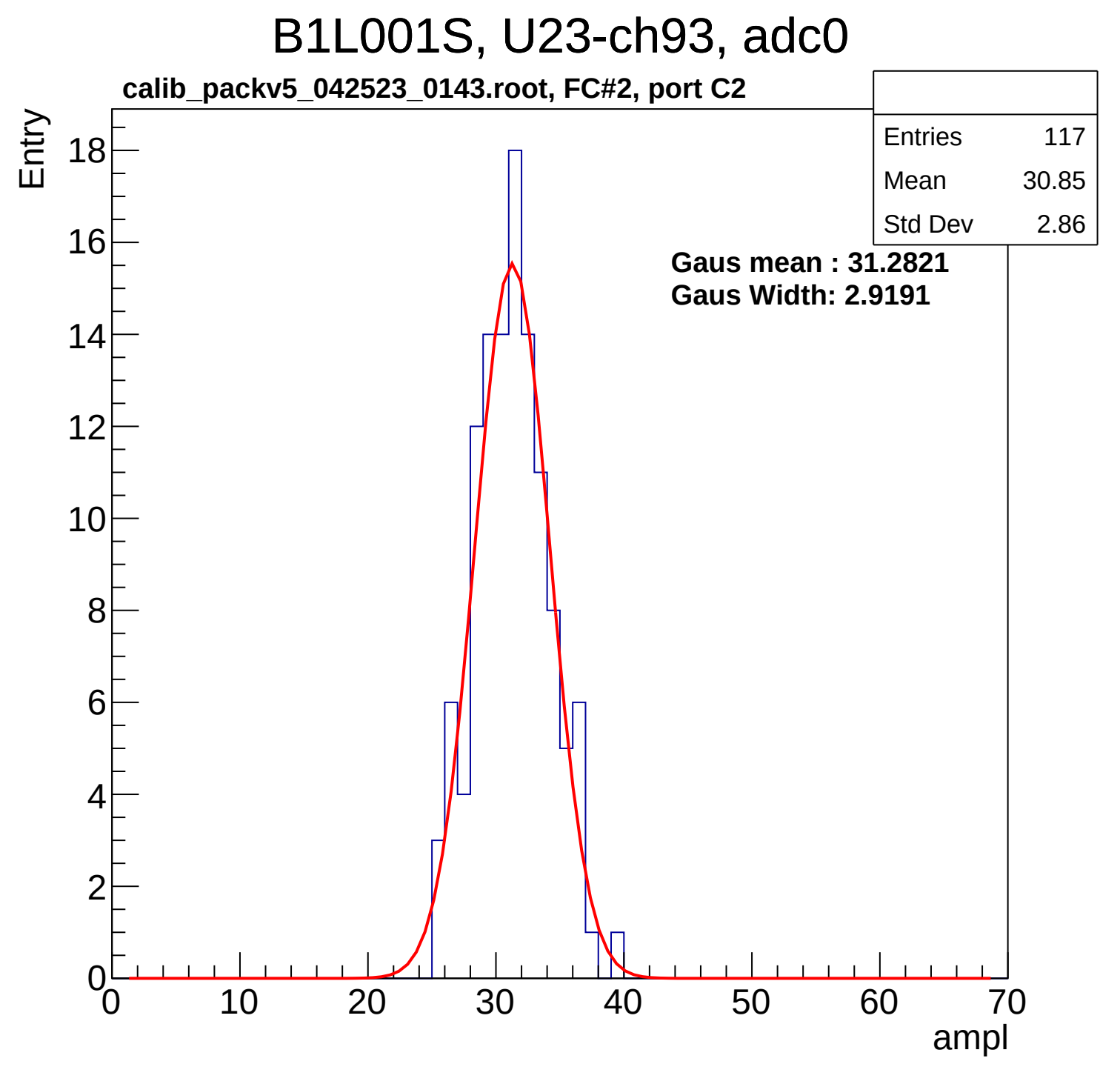
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch93, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 30.85
Std Dev 2.86
Gaus mean : 31.2821
Gaus Width: 2.9191
Entry
ampl
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4
6
8
10
12
14
16
18
0
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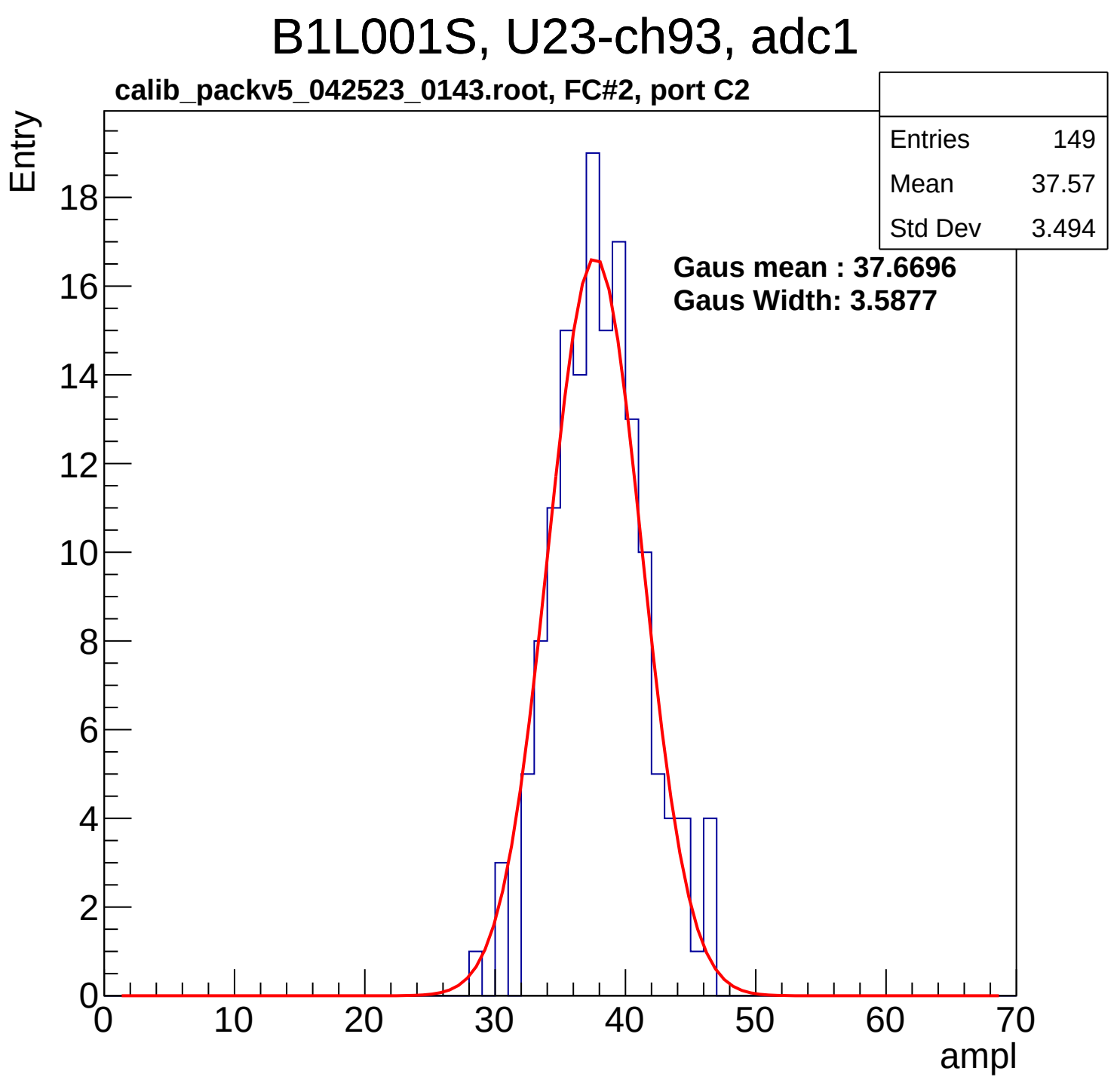
B1L001S, U23-ch93, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 37.57
Std Dev 3.494
Gaus mean : 37.6696
Gaus Width: 3.5877
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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60
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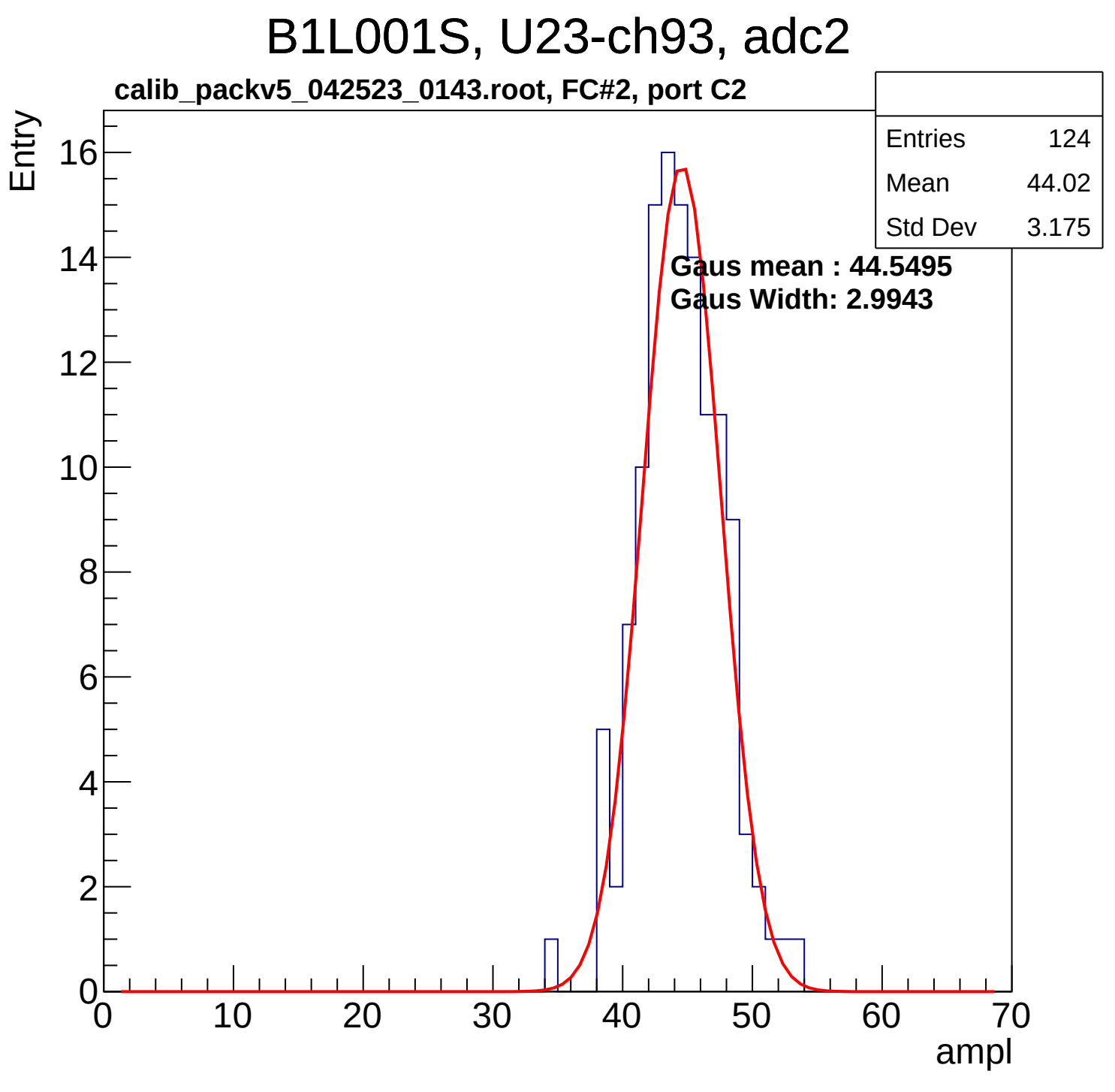
B1L001S, U23-ch93, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 44.02
Std Dev 3.175
Gaus mean : 44.5495
Gaus Width: 2.9943
Entry
ampl
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4
6
8
10
12
14
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0
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60
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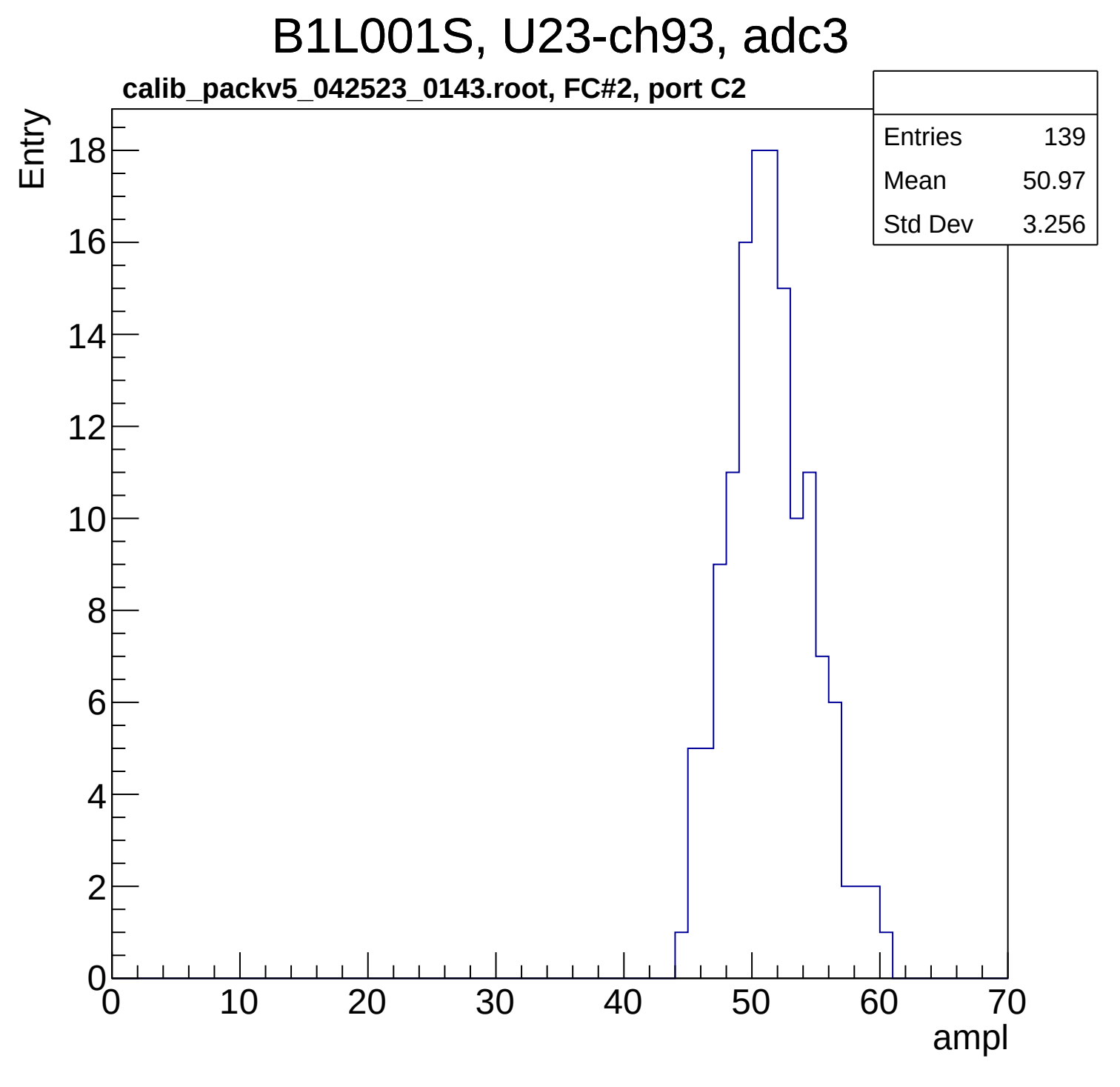
B1L001S, U23-ch93, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 50.97
Std Dev 3.256
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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60
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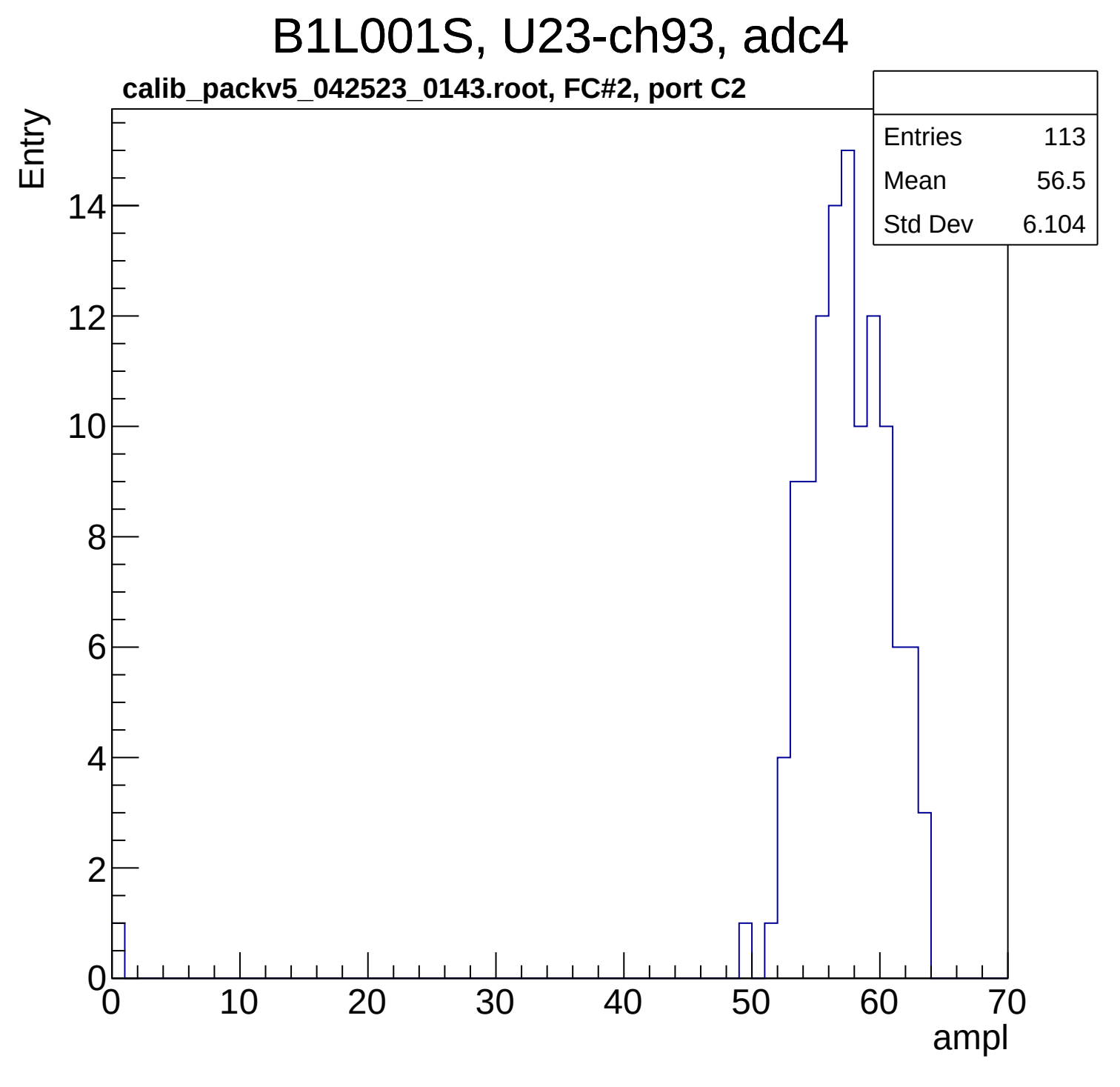
B1L001S, U23-ch93, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 56.5
Std Dev 6.104
Entry
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2
4
6
8
10
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14
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10
20
30
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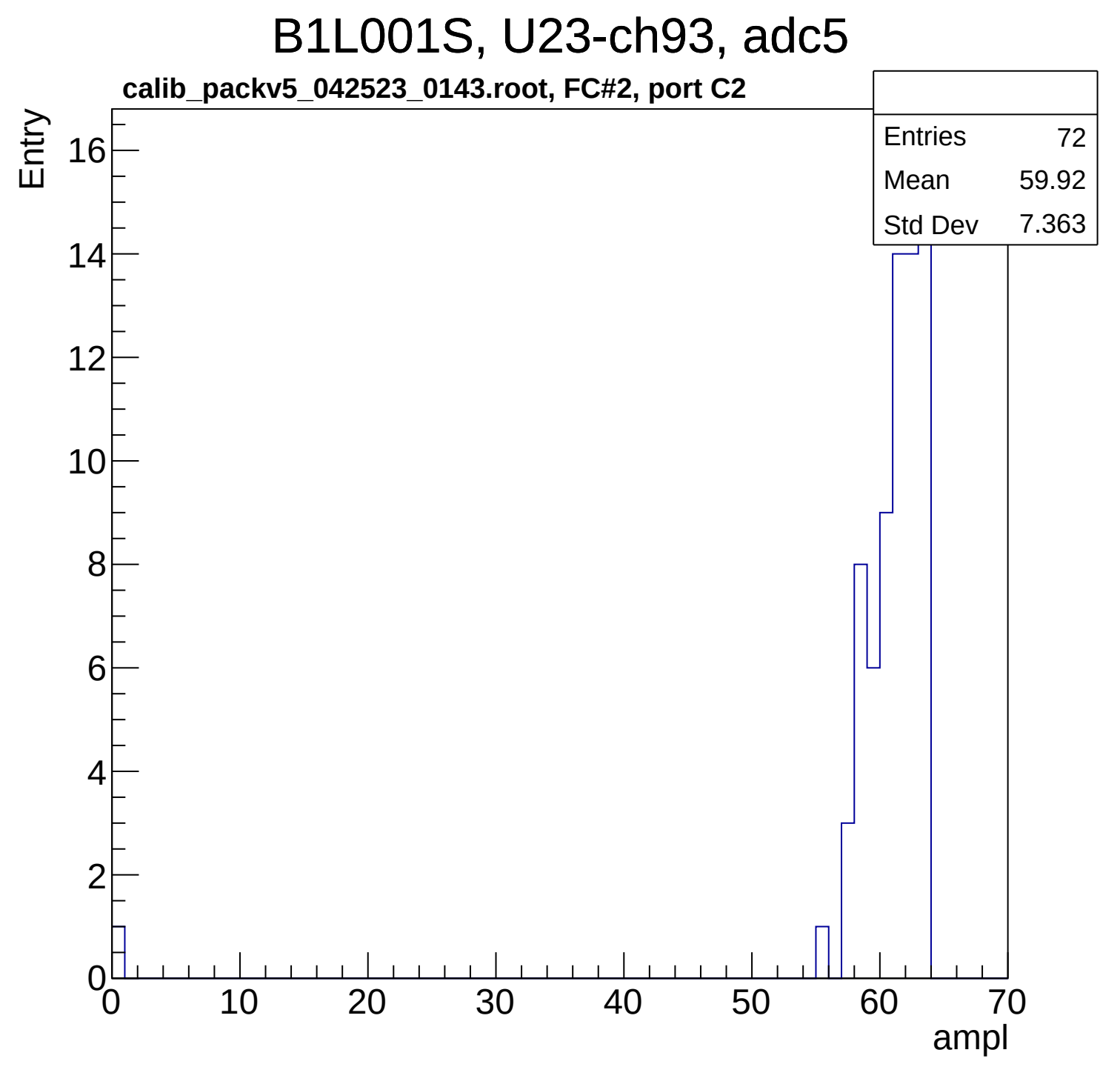
50
60
70
ampl


B1L001S, U23-ch93, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 59.92
Std Dev 7.363
Entry
ampl
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4
6
8
10
12
14
16
0
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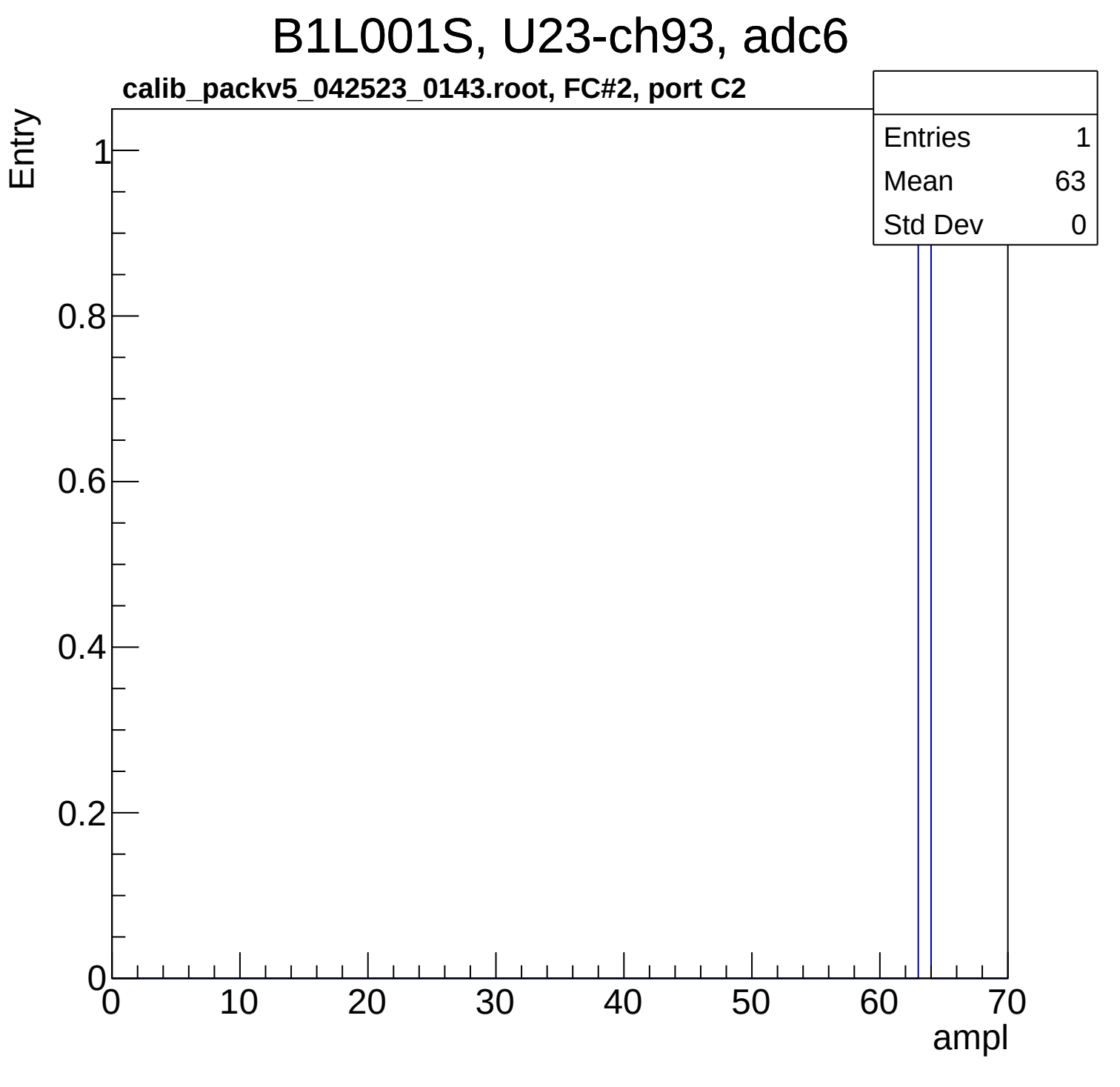
B1L001S, U23-ch93, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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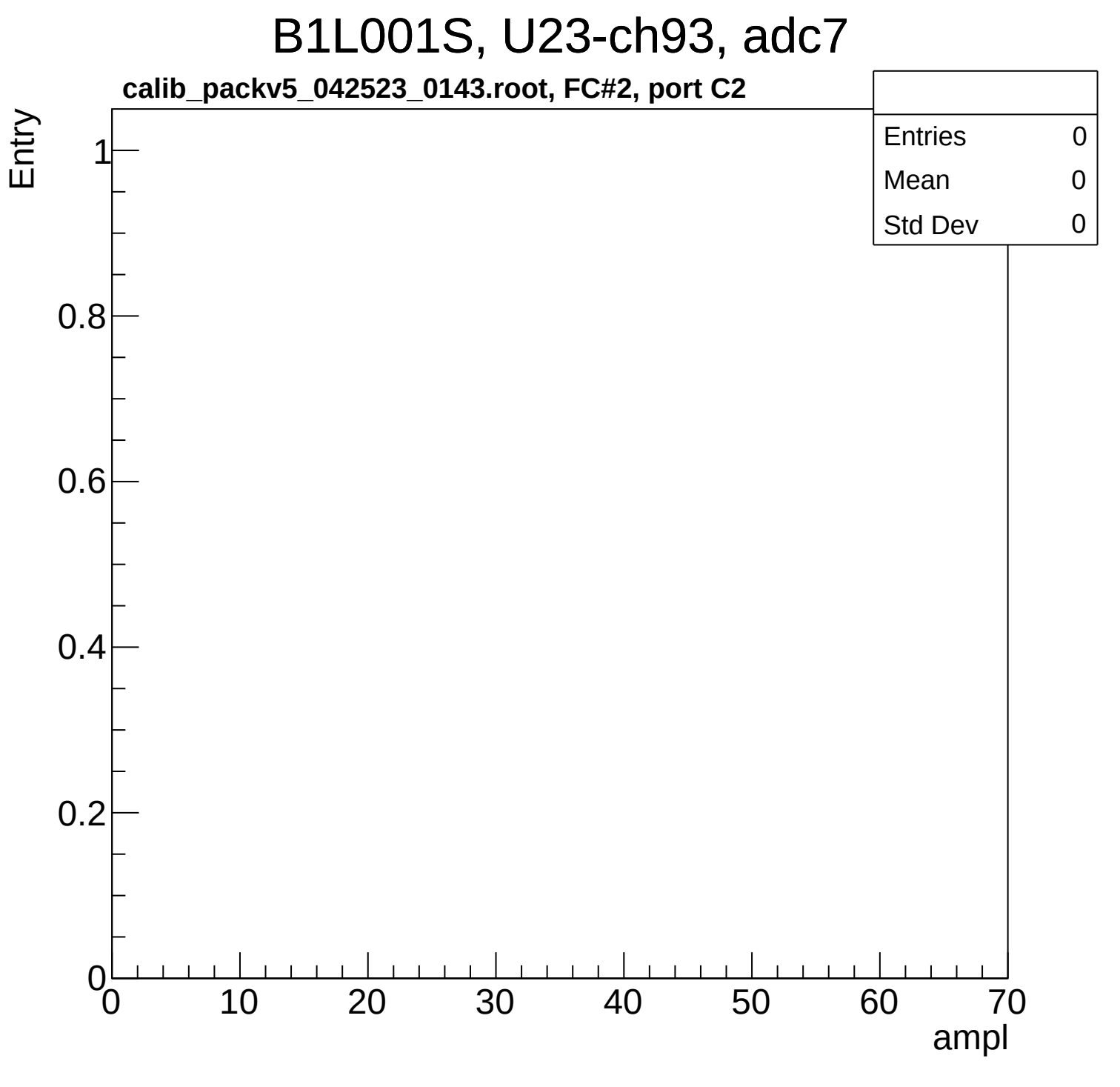
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch93, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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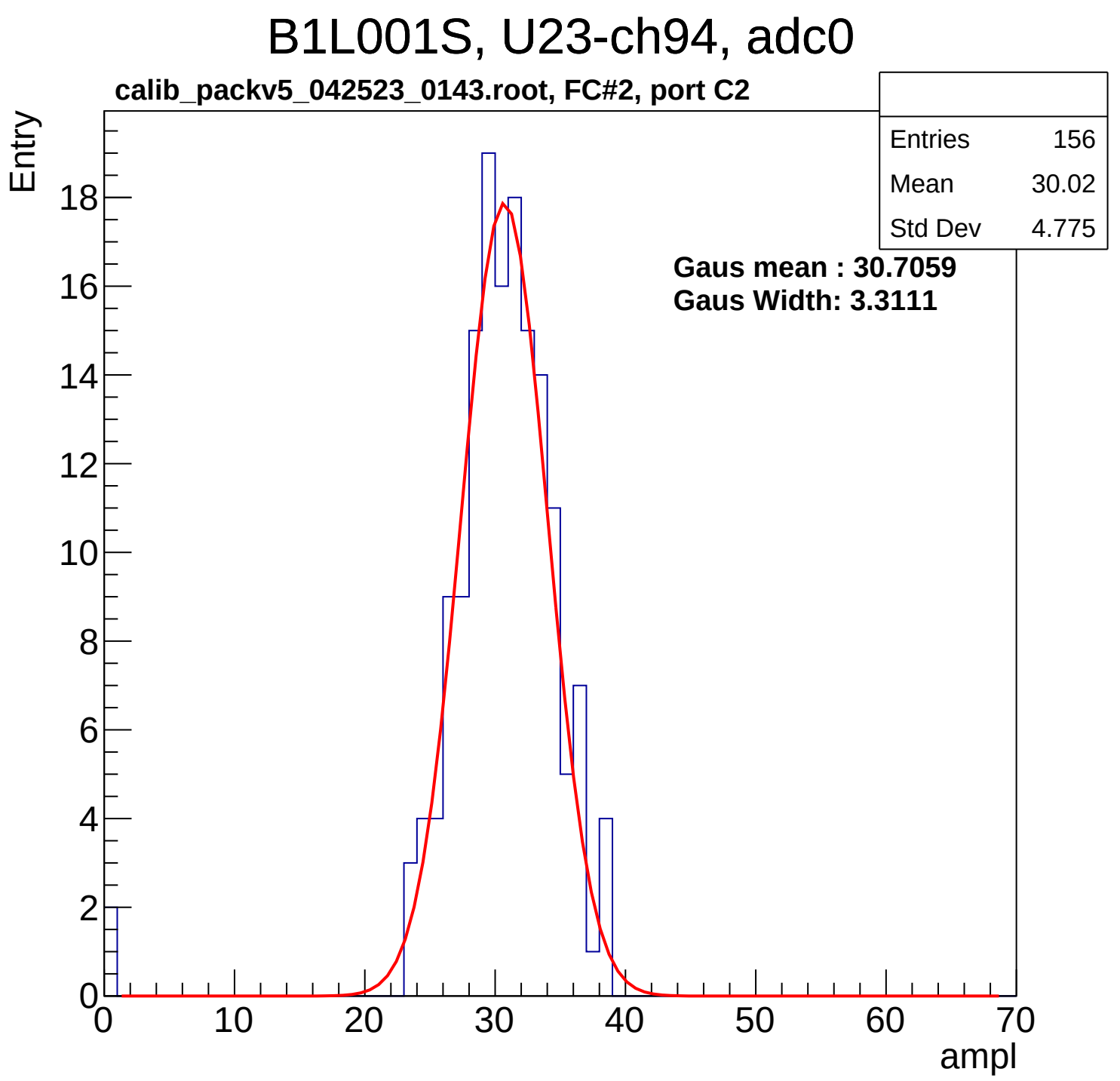
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch94, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 30.02
Std Dev 4.775
Gaus mean : 30.7059
Gaus Width: 3.3111
Entry
ampl
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4
6
8
10
12
14
16
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0
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40
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60
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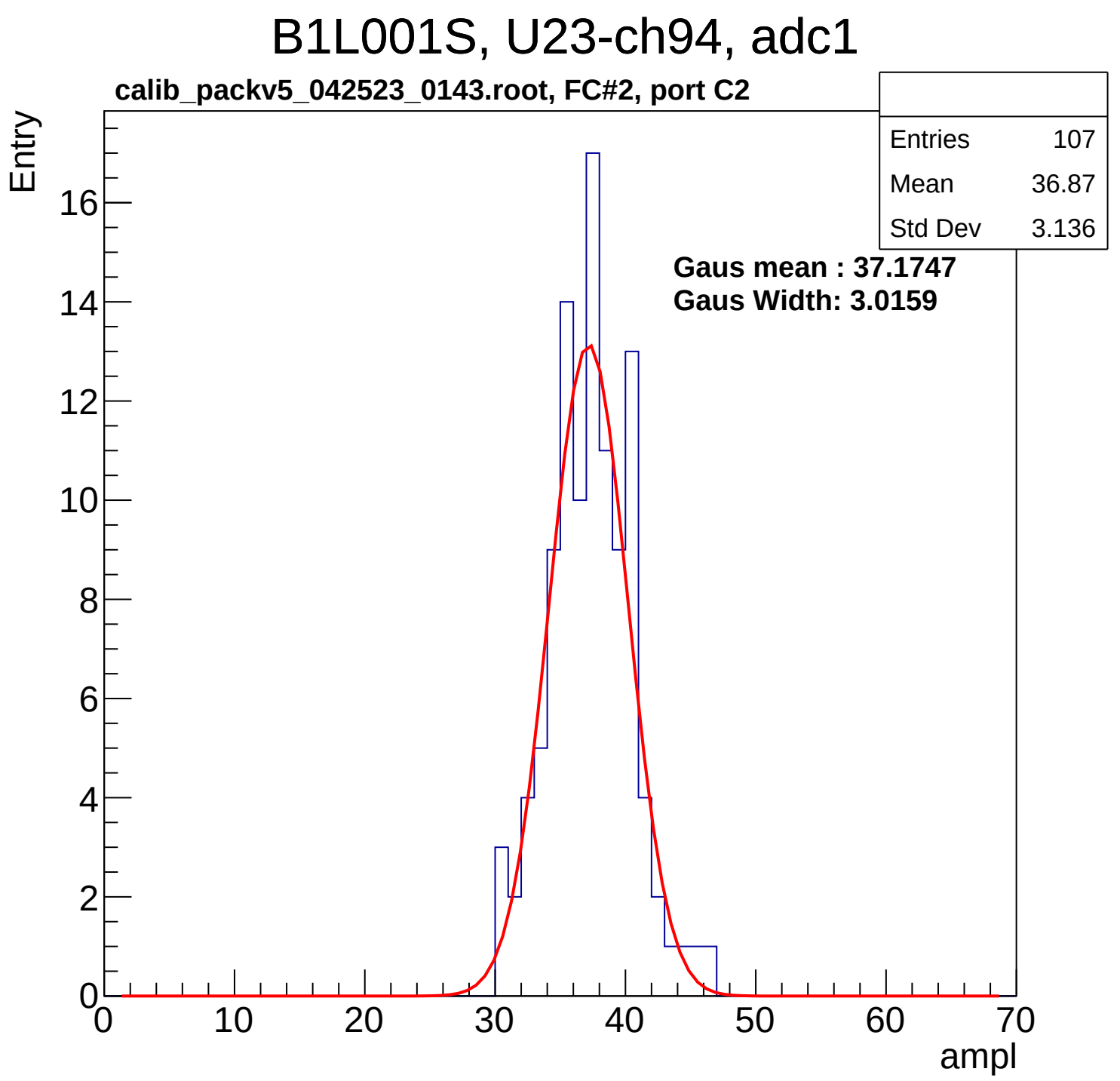
B1L001S, U23-ch94, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 36.87
Std Dev 3.136
Gaus mean : 37.1747
Gaus Width: 3.0159
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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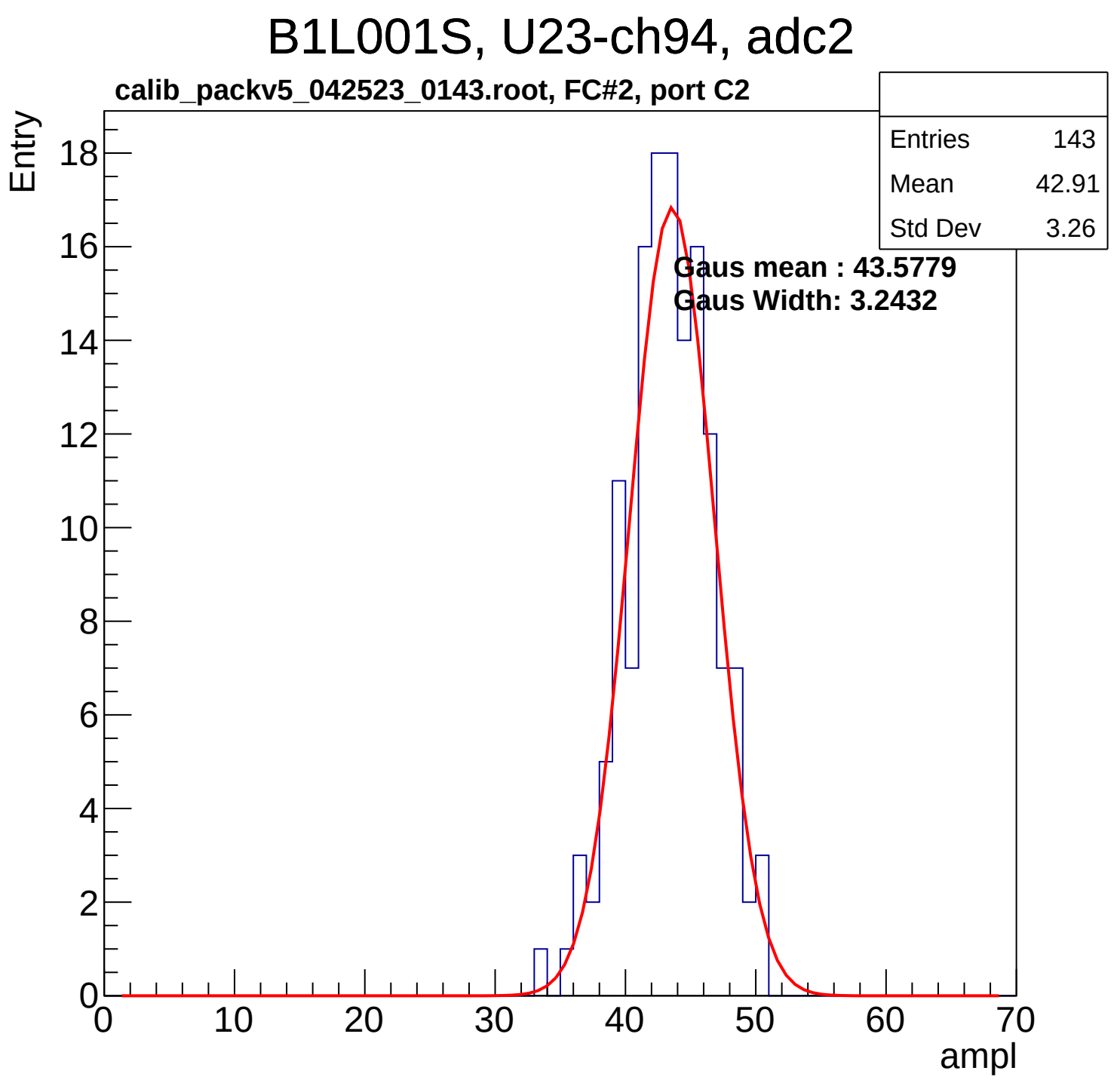
B1L001S, U23-ch94, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 42.91
Std Dev 3.26
Gaus mean : 43.5779
Gaus Width: 3.2432
Entry
ampl
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6
8
10
12
14
16
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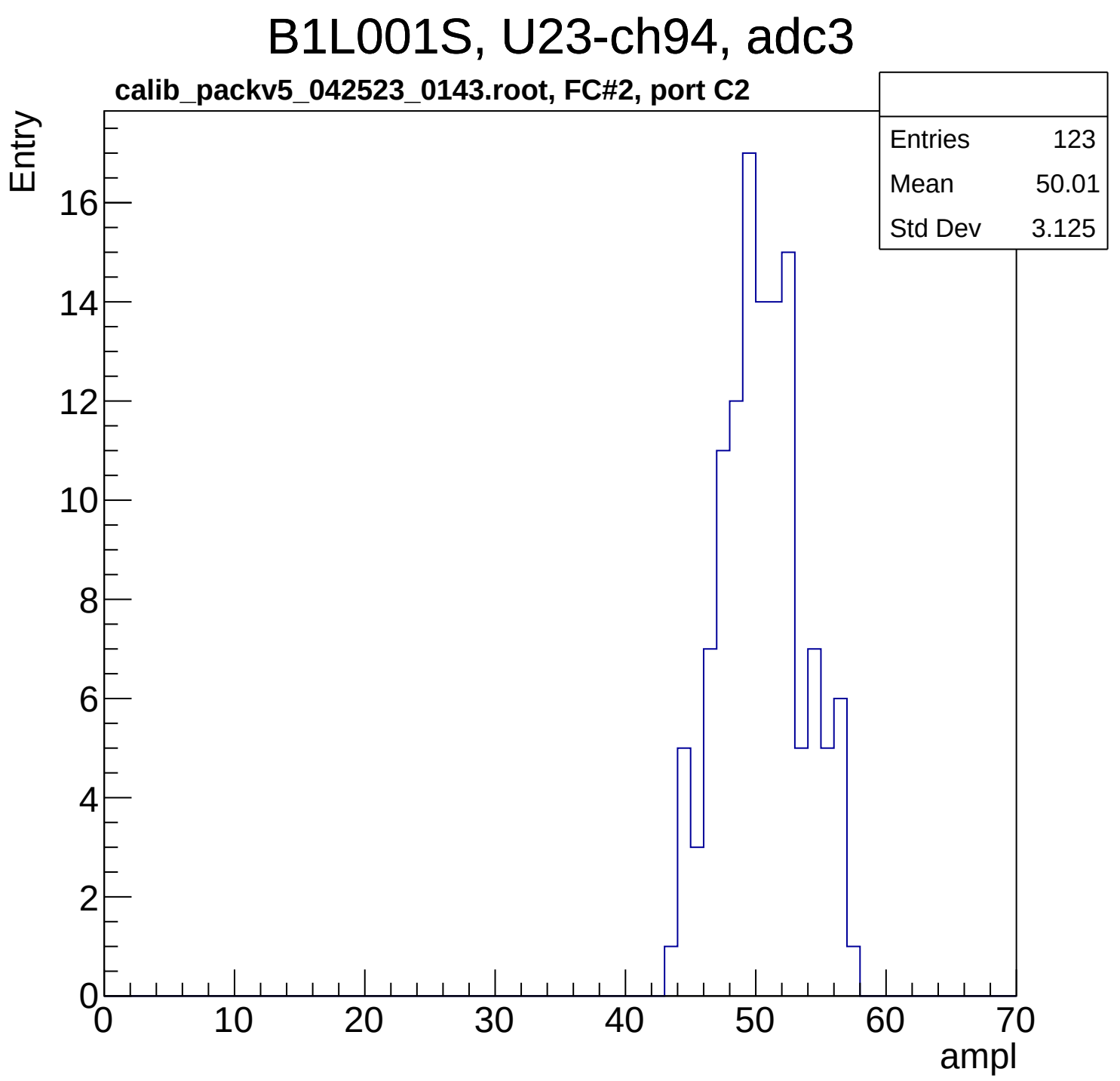
B1L001S, U23-ch94, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 50.01
Std Dev 3.125
Entry
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4
6
8
10
12
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16
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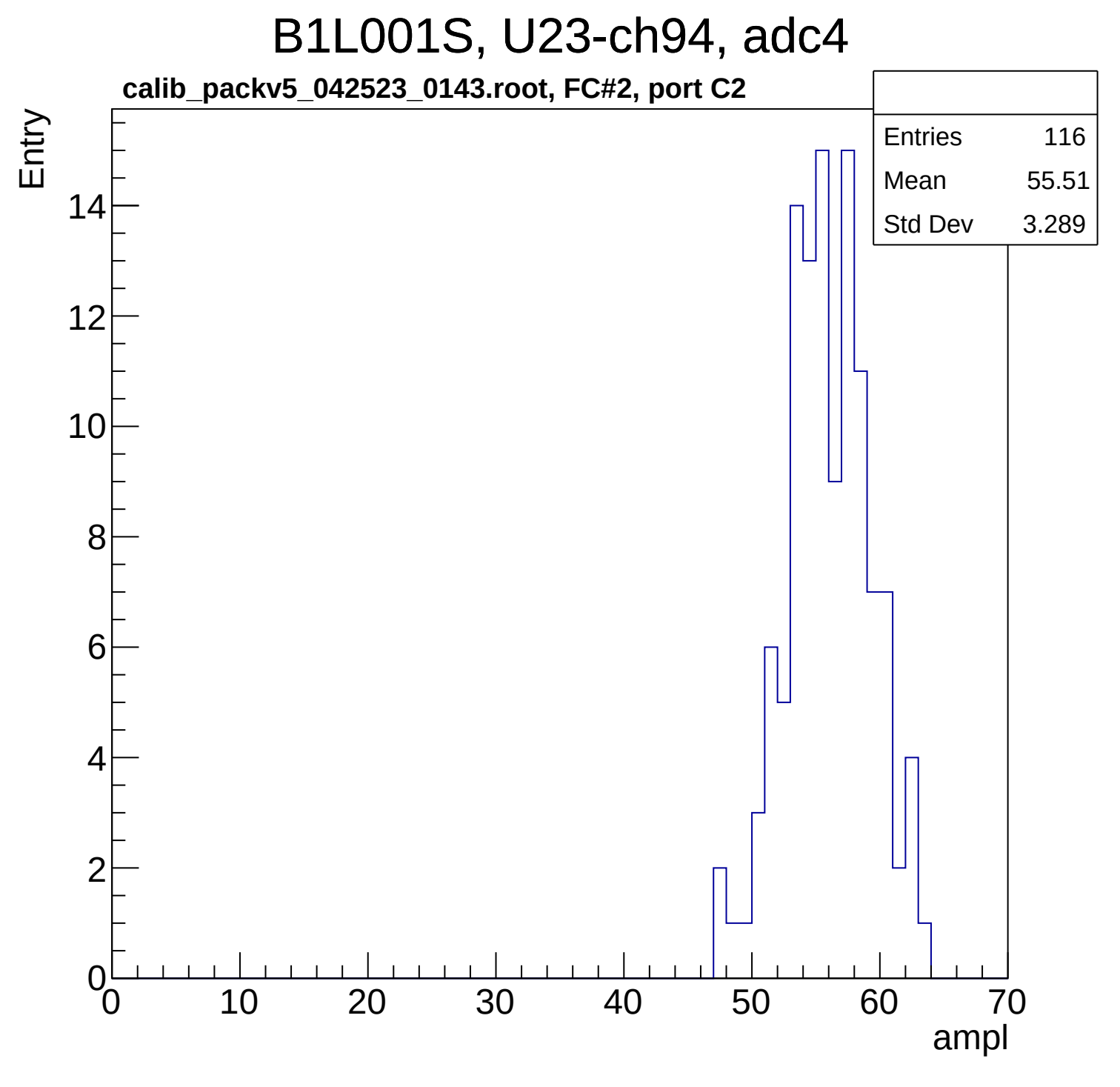
50
60
70
ampl


B1L001S, U23-ch94, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 55.51
Std Dev 3.289
Entry
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50
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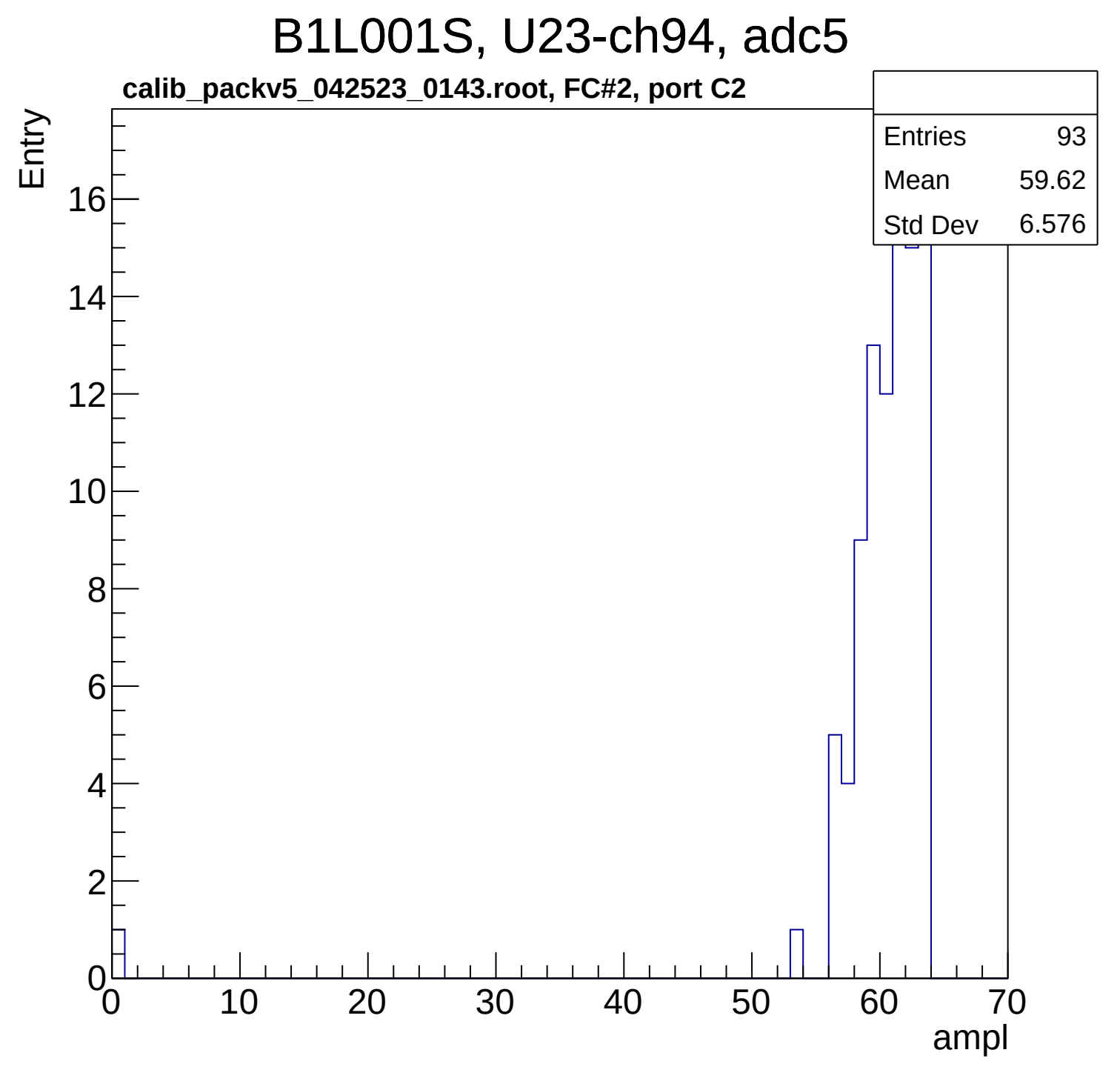
70
ampl


B1L001S, U23-ch94, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 59.62
Std Dev 6.576
Entry
ampl
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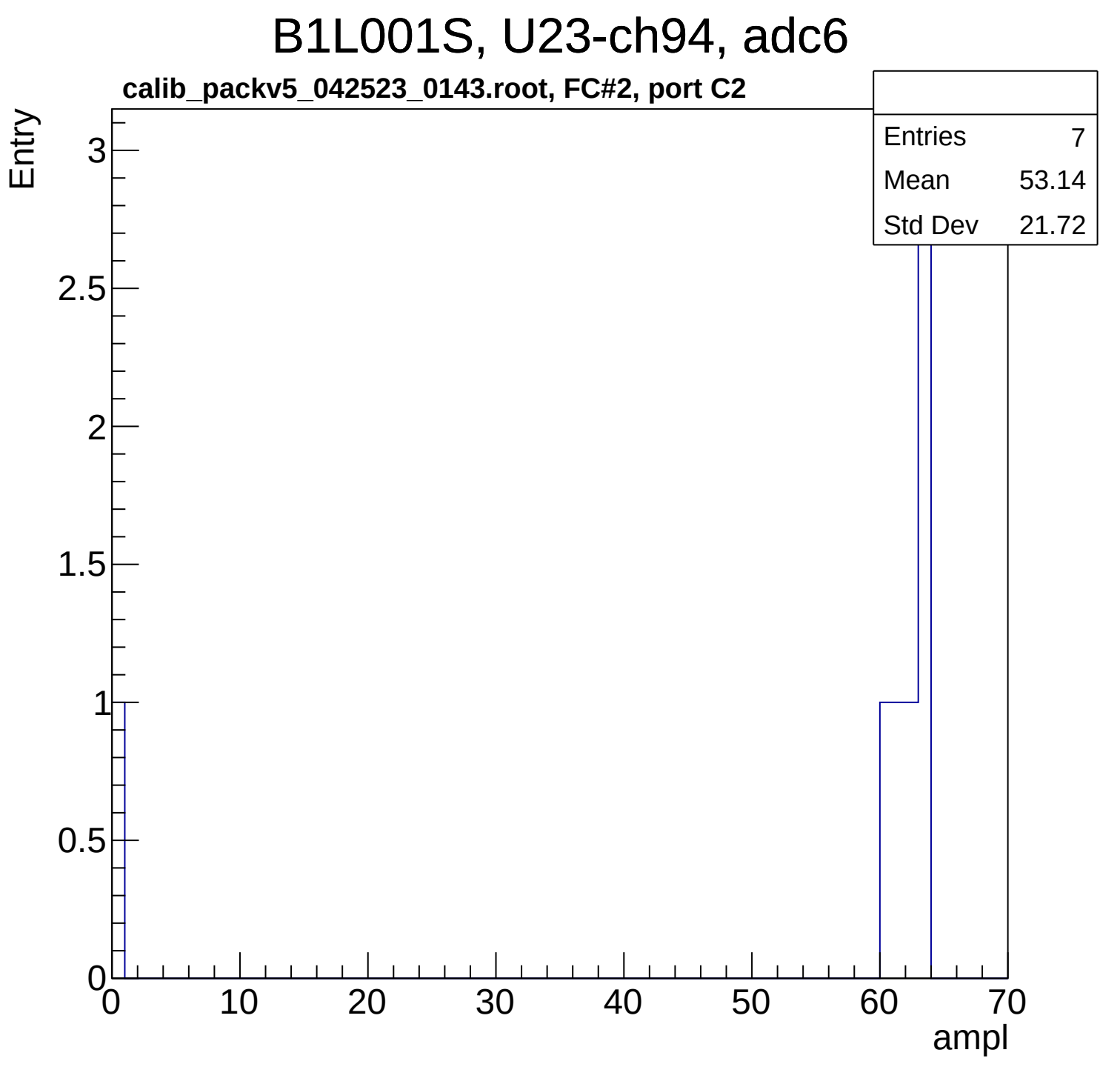
B1L001S, U23-ch94, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53.14
Std Dev 21.72
Entry
ampl
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1
1.5
2
2.5
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0
10
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60
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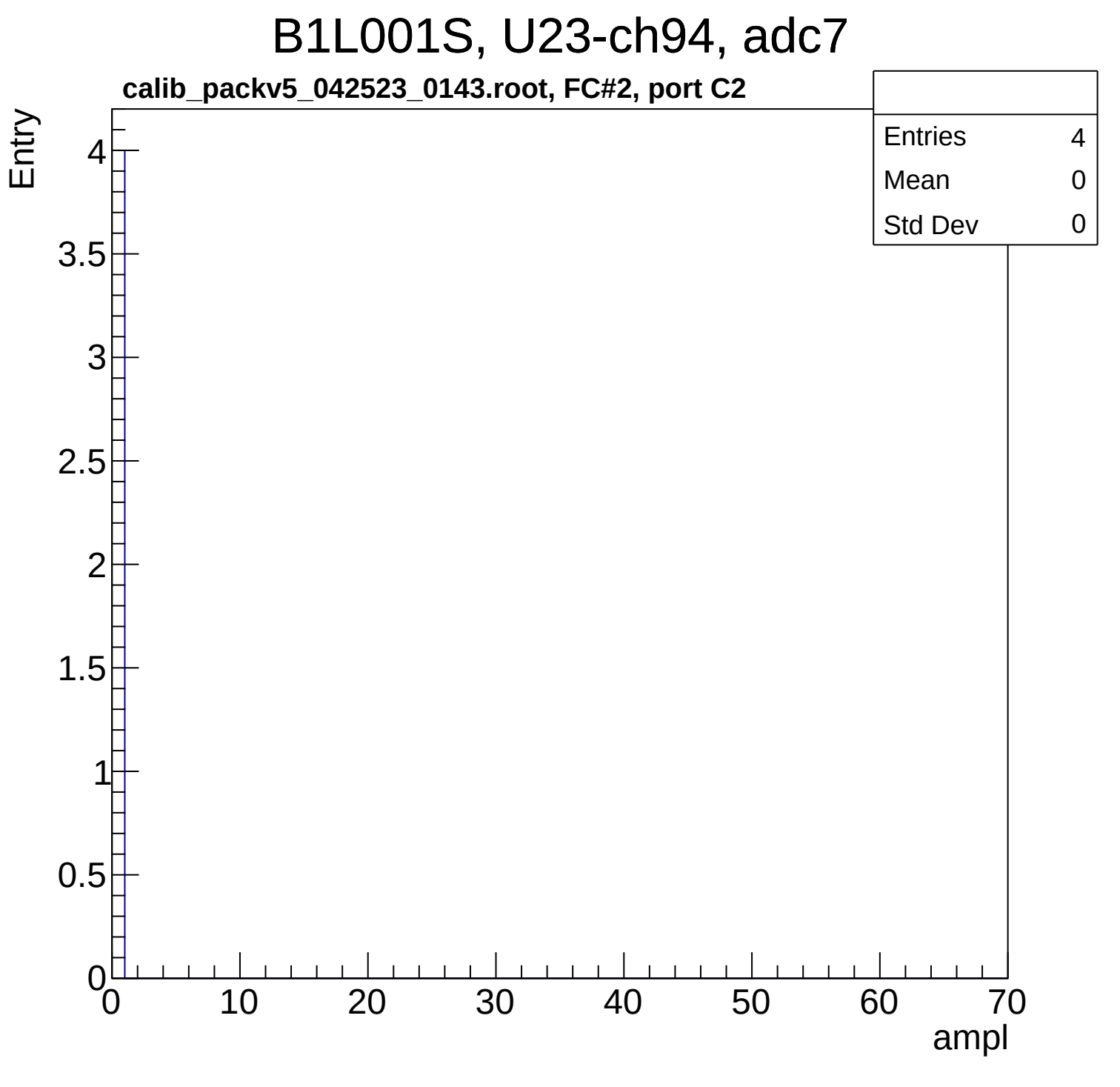
B1L001S, U23-ch94, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 0
Std Dev 0
Entry
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3.5
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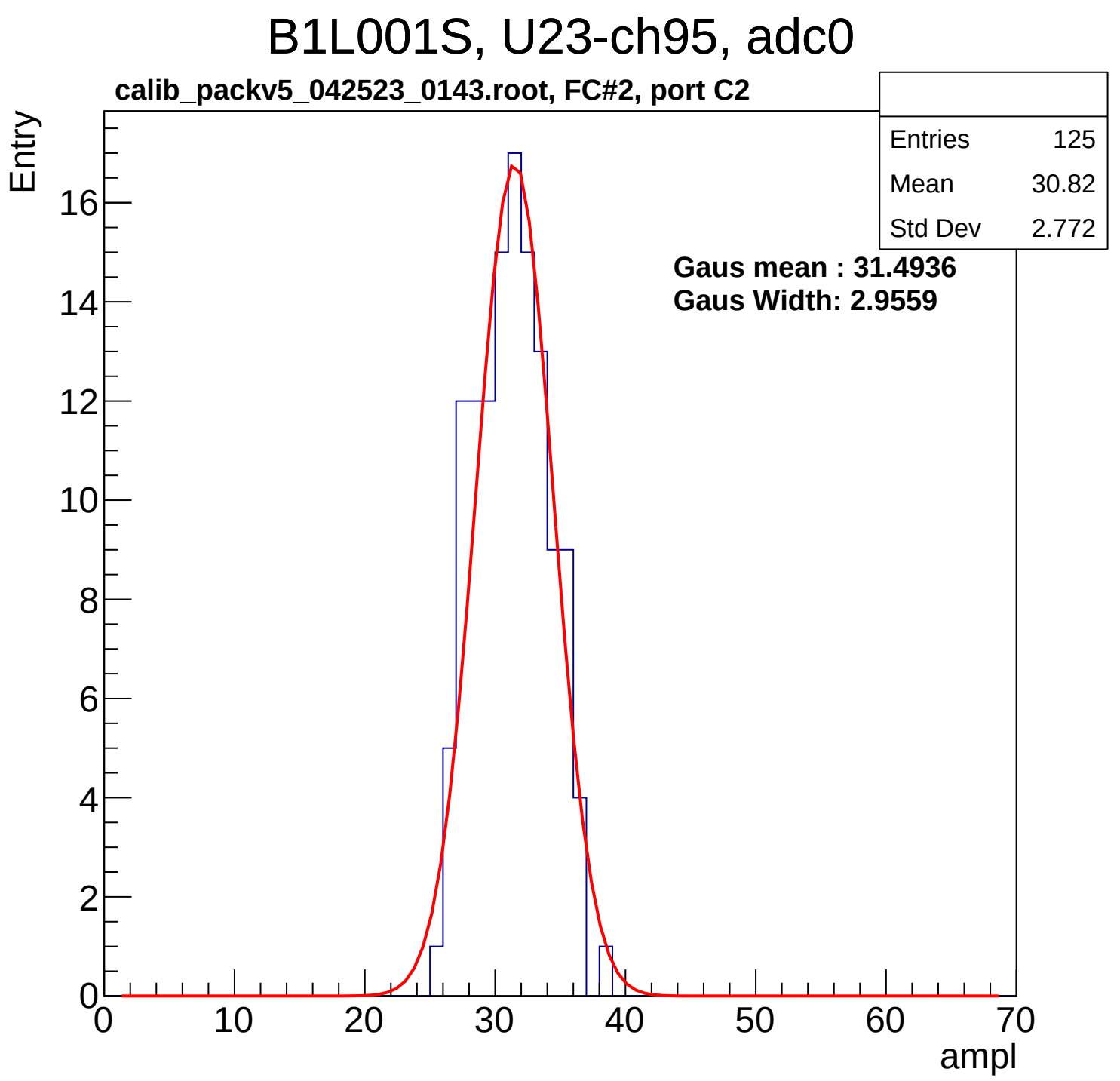
50
60
70
ampl


B1L001S, U23-ch95, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 30.82
Std Dev 2.772
Gaus mean : 31.4936
Gaus Width: 2.9559
Entry
ampl
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4
6
8
10
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0
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60
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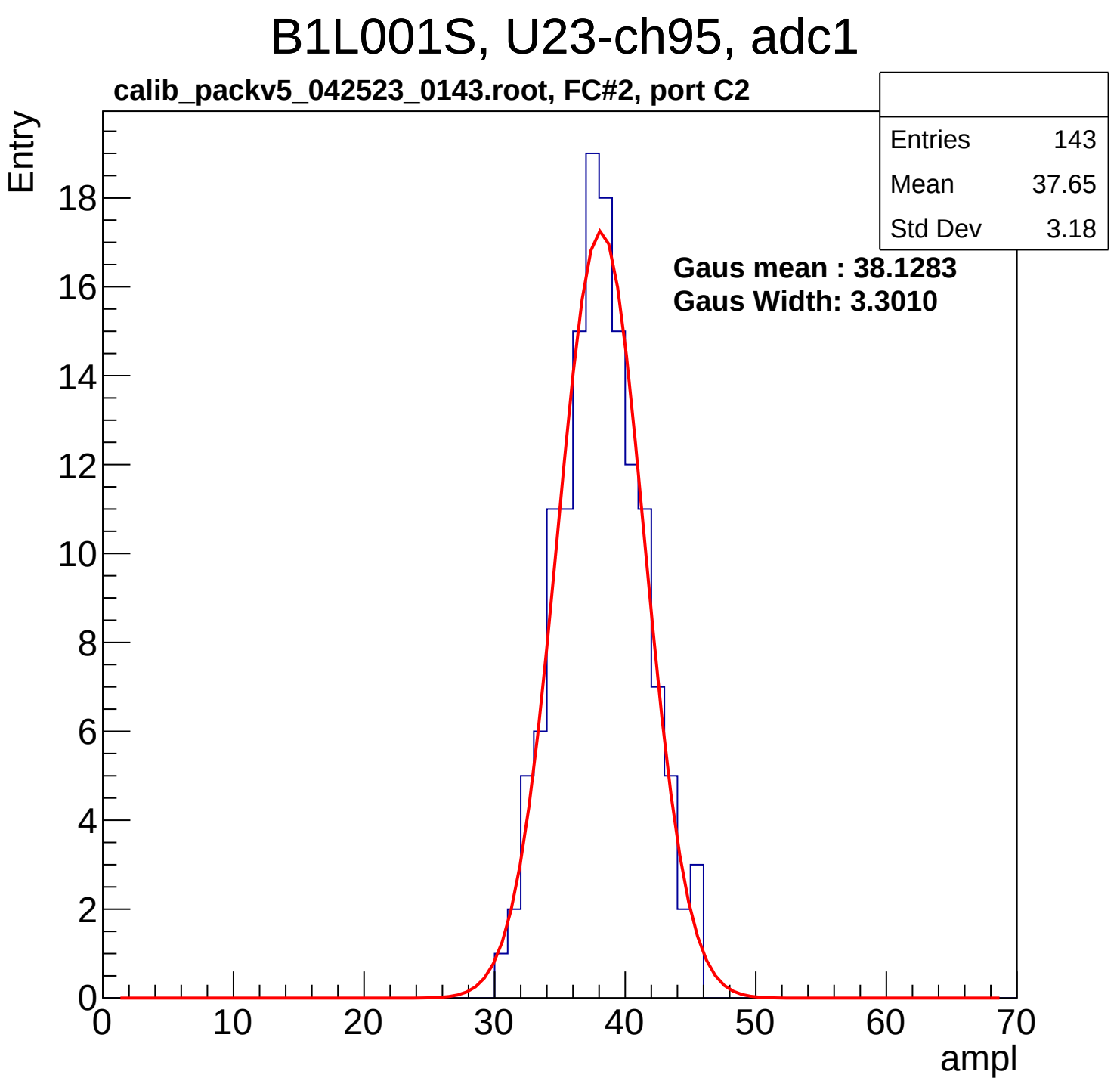
B1L001S, U23-ch95, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 37.65
Std Dev 3.18
Gaus mean : 38.1283
Gaus Width: 3.3010
Entry
ampl
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4
6
8
10
12
14
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18
0
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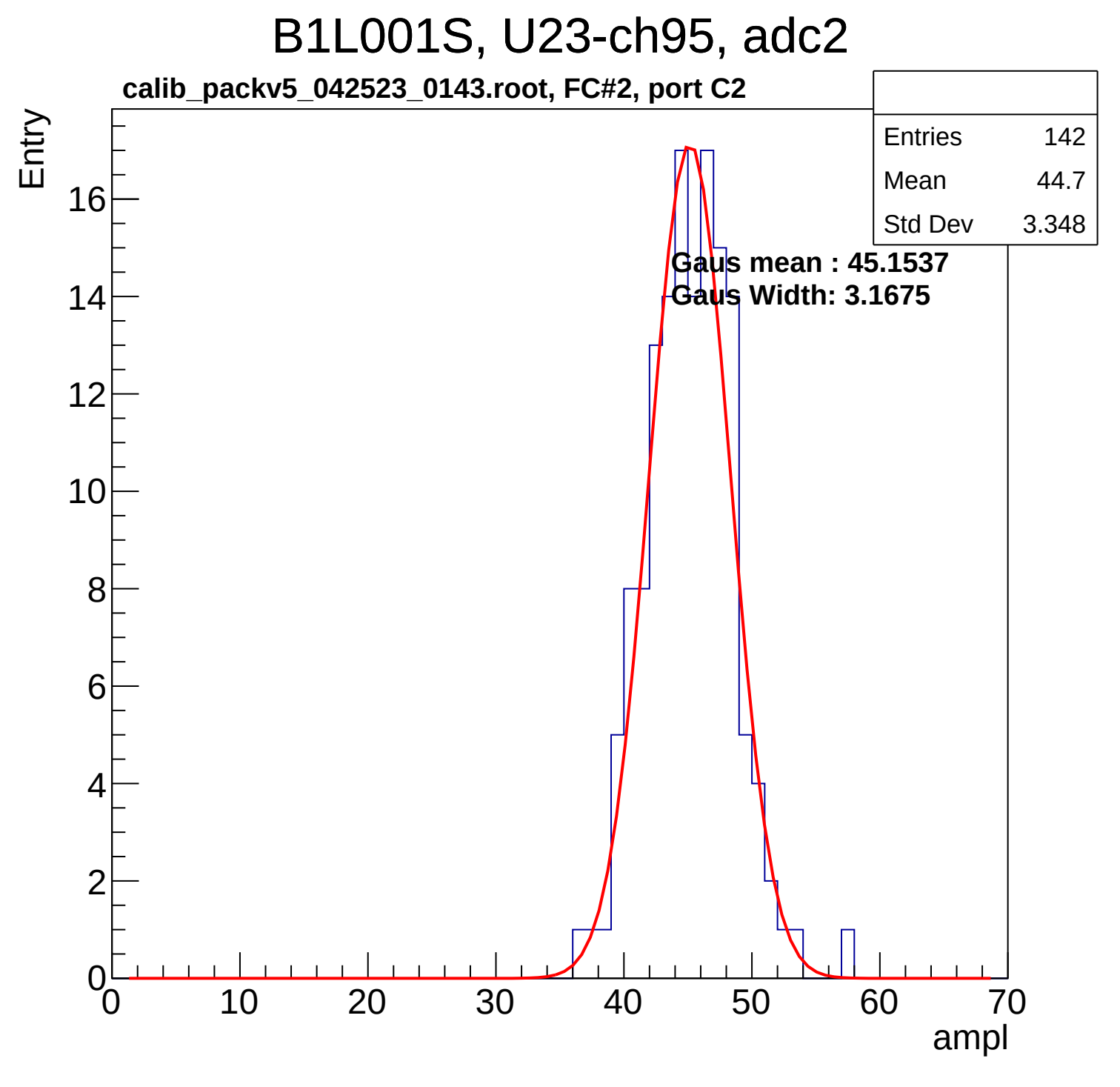
B1L001S, U23-ch95, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 44.7
Std Dev 3.348
Gaus mean : 45.1537
Gaus Width: 3.1675
Entry
ampl
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2
4
6
8
10
12
14
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0
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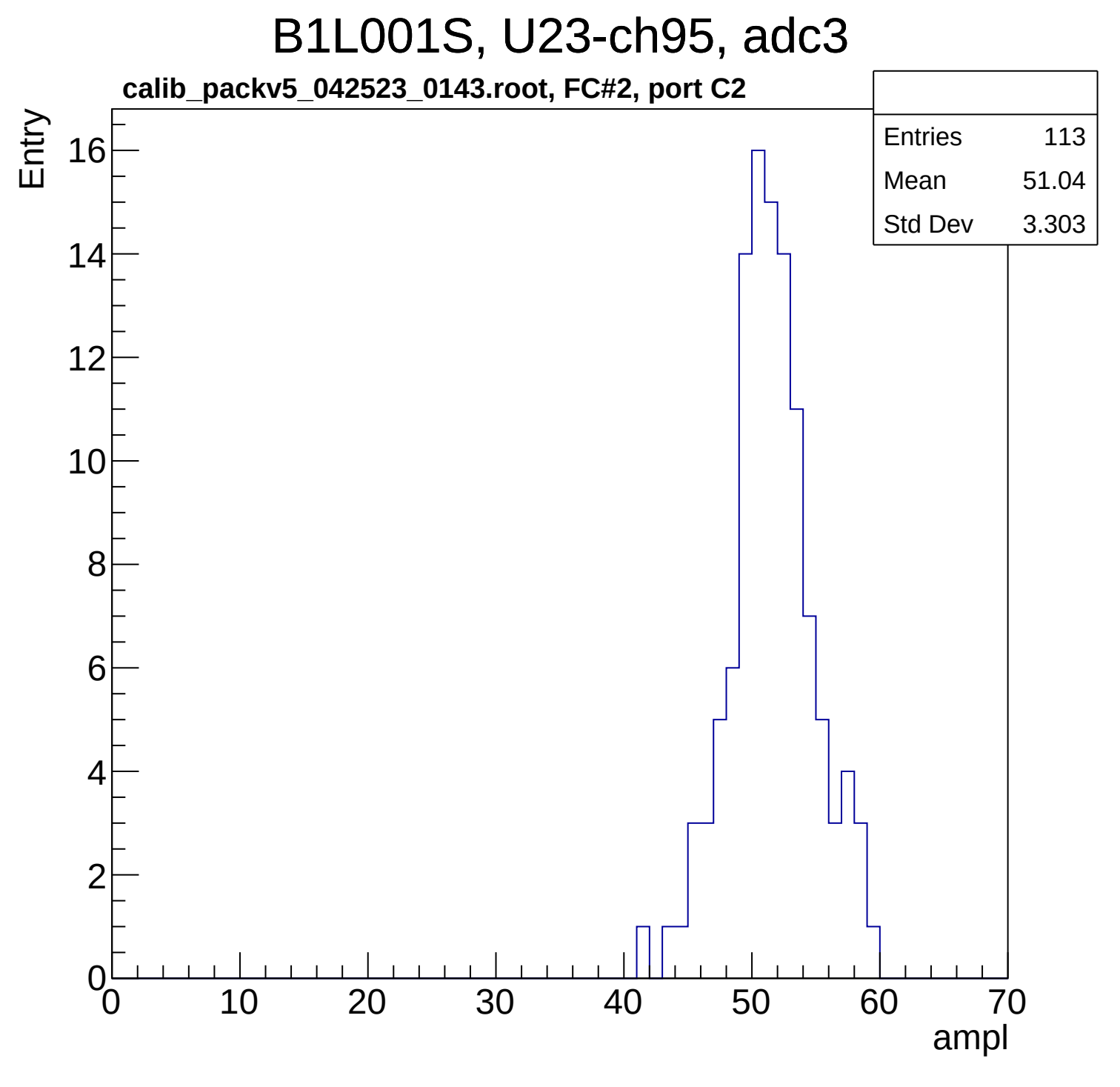
B1L001S, U23-ch95, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 113
Mean 51.04
Std Dev 3.303
Entry
ampl
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10
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0
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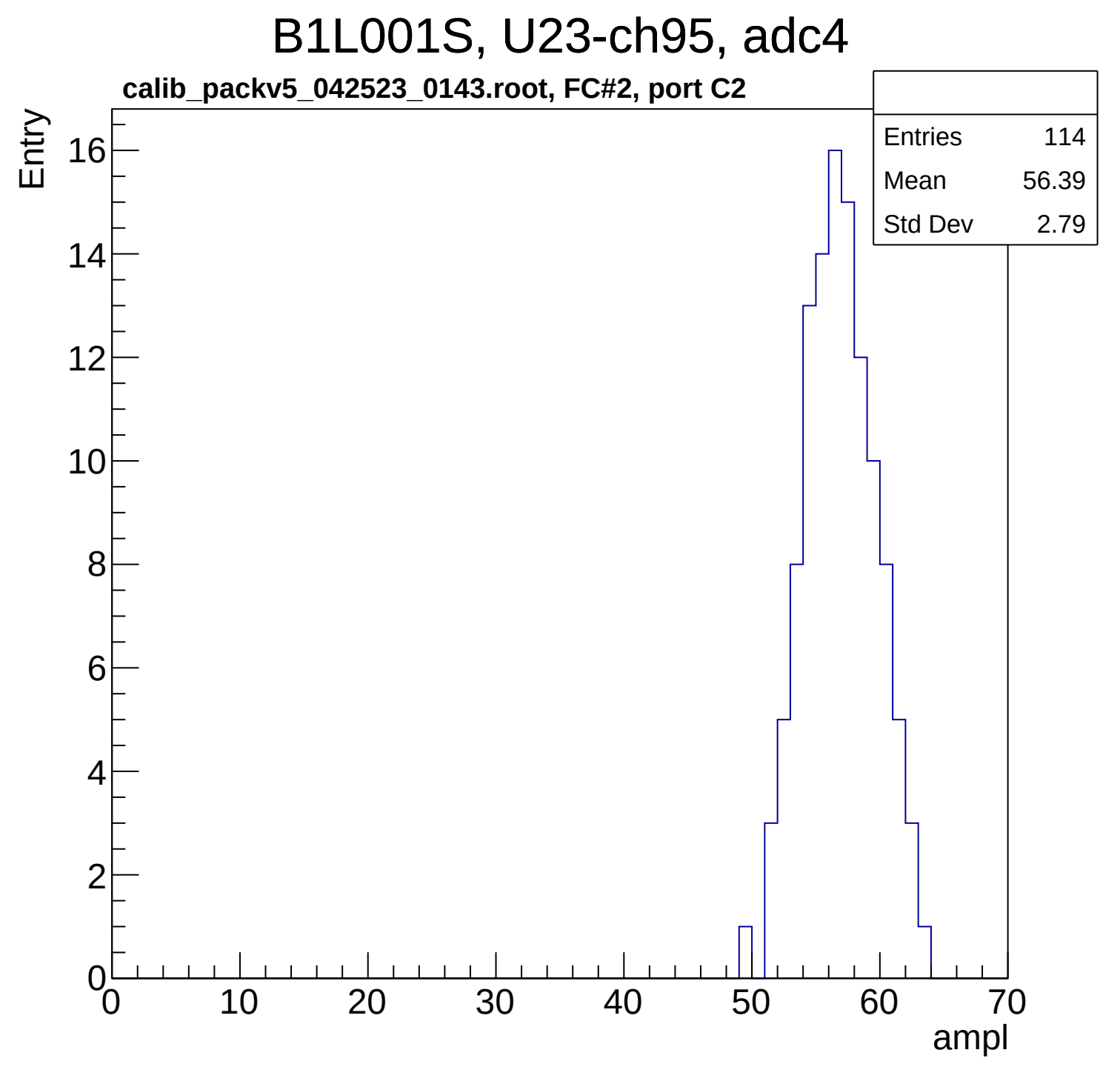
B1L001S, U23-ch95, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 56.39
Std Dev 2.79
Entry
ampl
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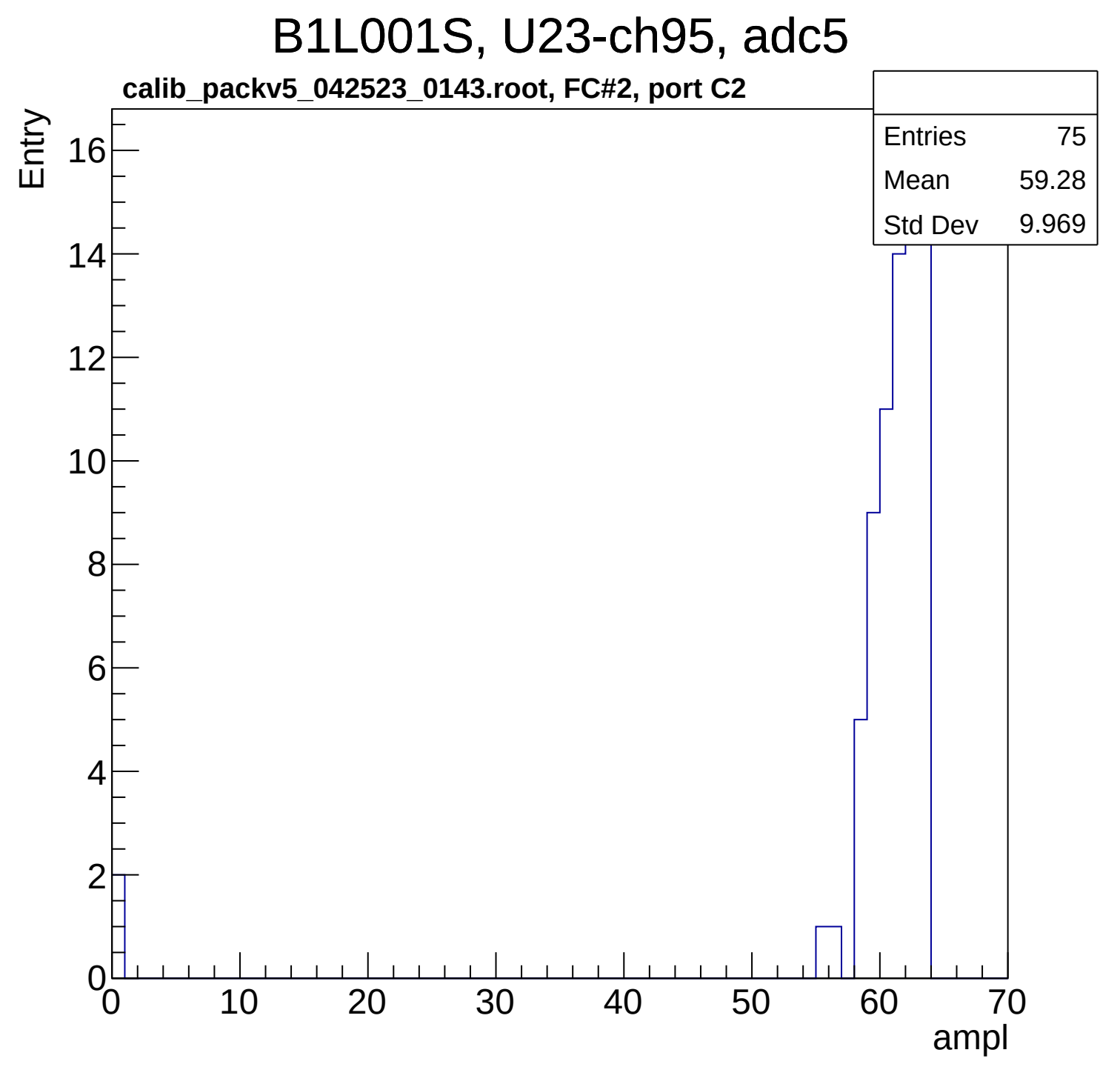
B1L001S, U23-ch95, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 75
Mean 59.28
Std Dev 9.969
Entry
ampl
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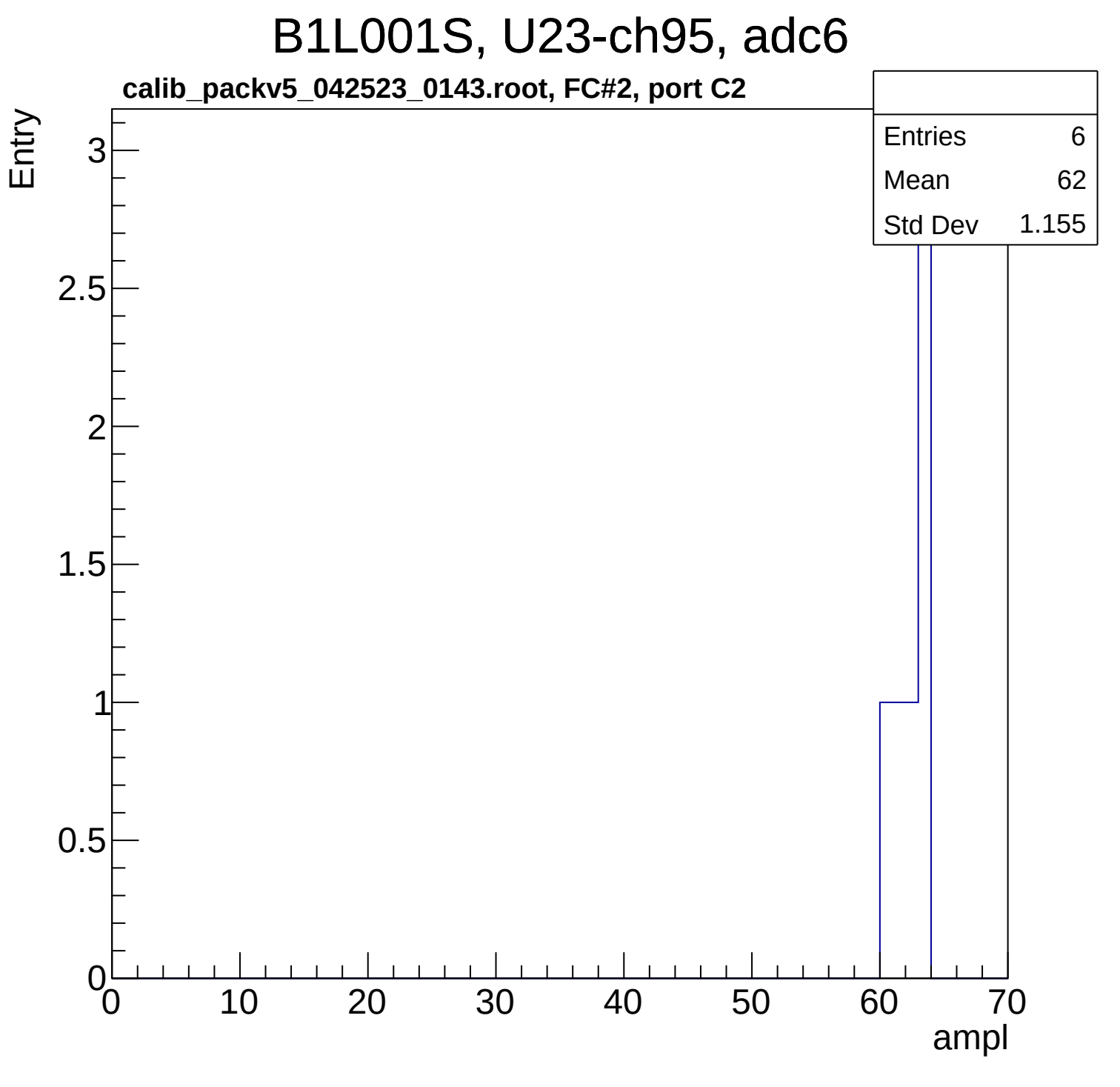
B1L001S, U23-ch95, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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1
1.5
2
2.5
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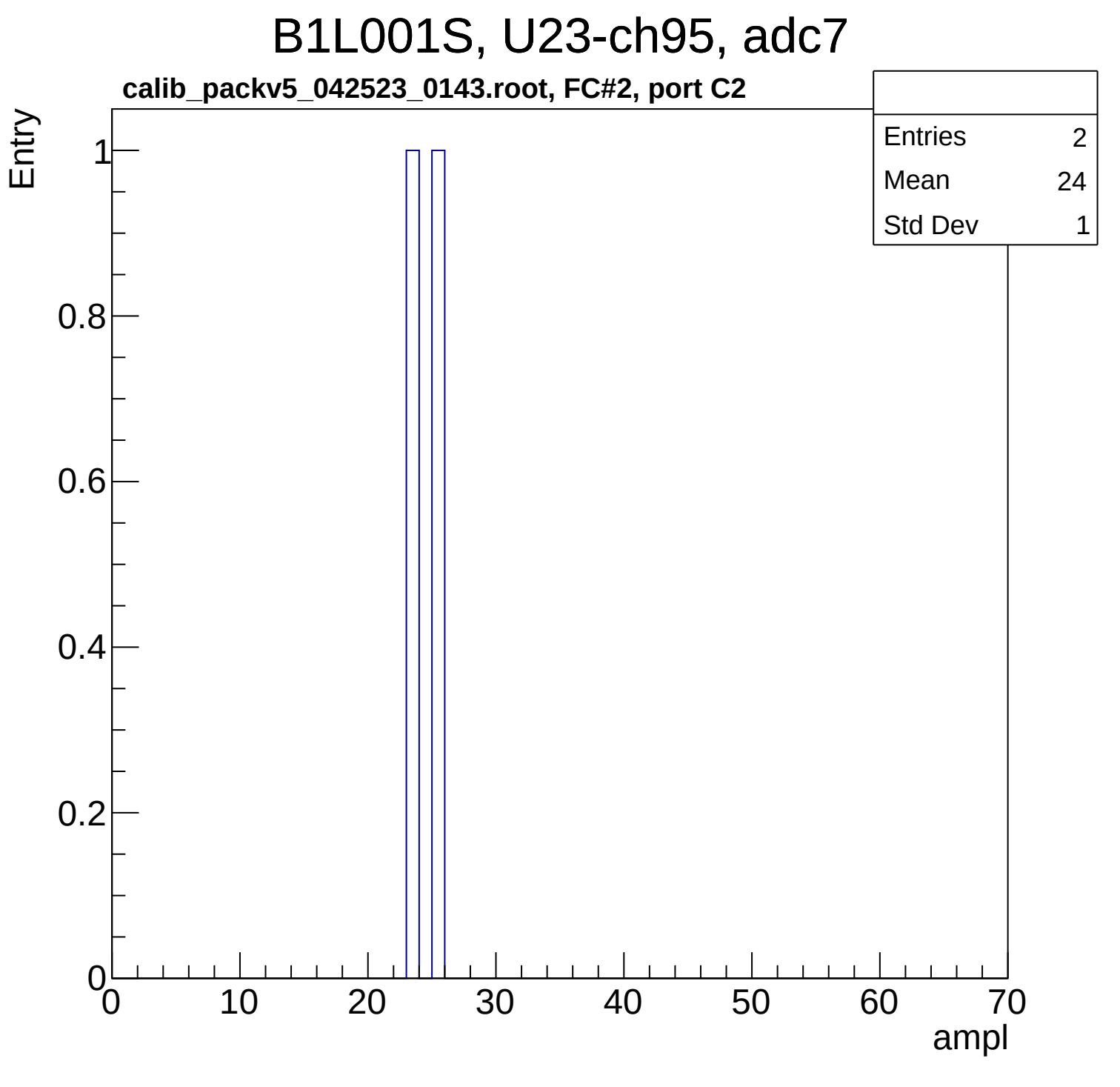
B1L001S, U23-ch95, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 24
Std Dev 1
Entry
ampl
0
0.2
0.4
0.6
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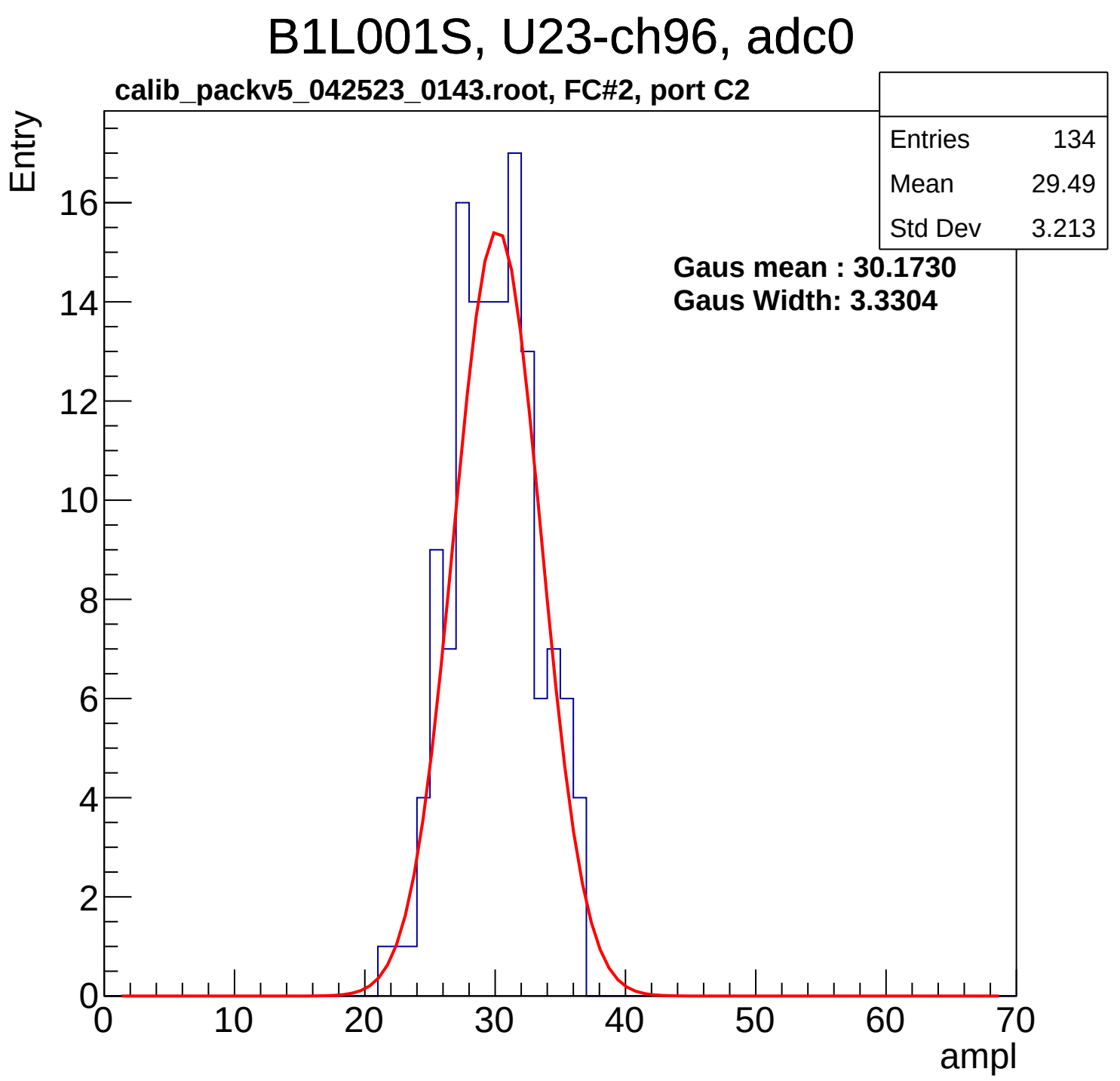
B1L001S, U23-ch96, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 29.49
Std Dev 3.213
Gaus mean : 30.1730
Gaus Width: 3.3304
Entry
ampl
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4
6
8
10
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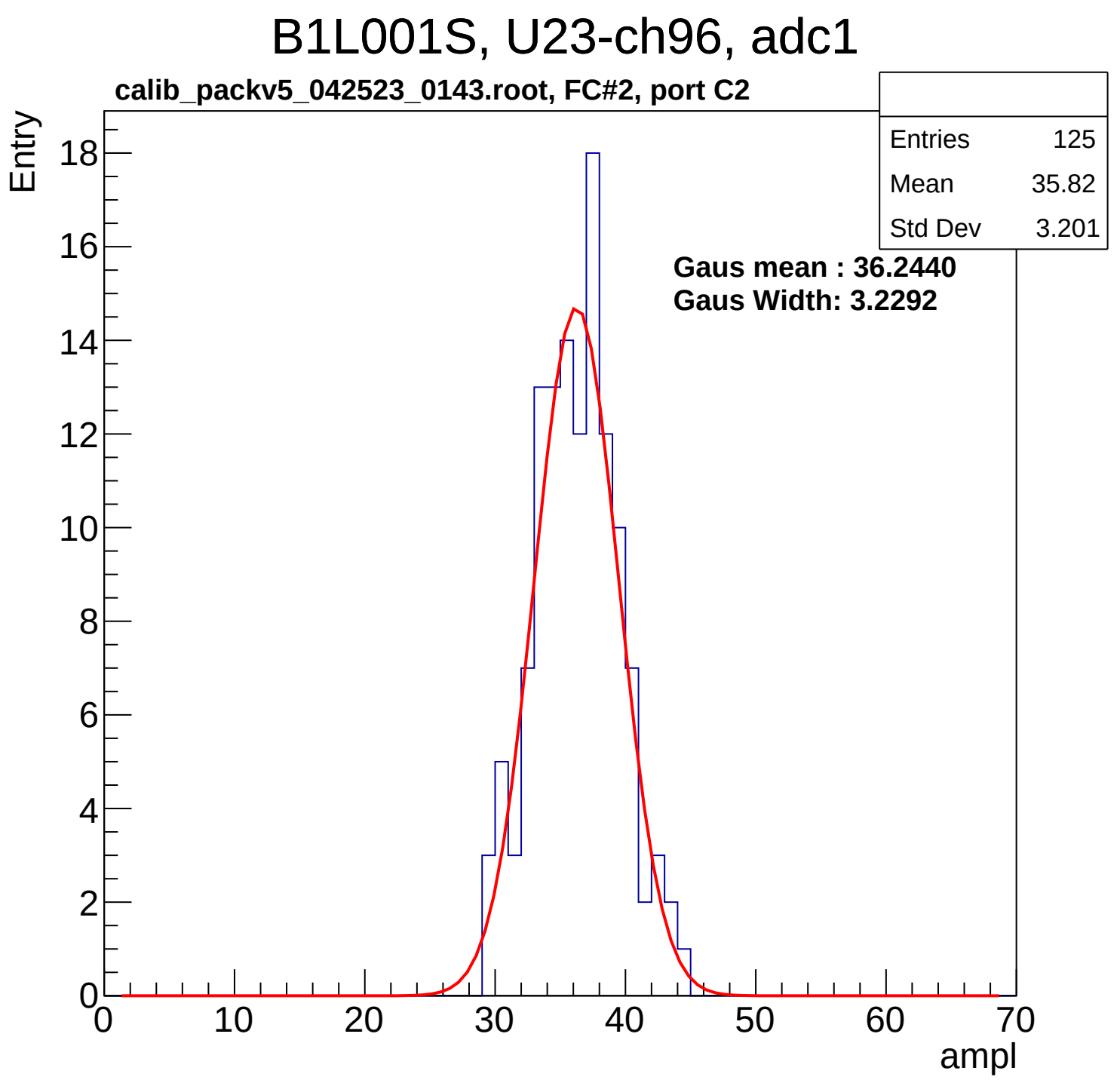
B1L001S, U23-ch96, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 35.82
Std Dev 3.201
Gaus mean : 36.2440
Gaus Width: 3.2292
Entry
ampl
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4
6
8
10
12
14
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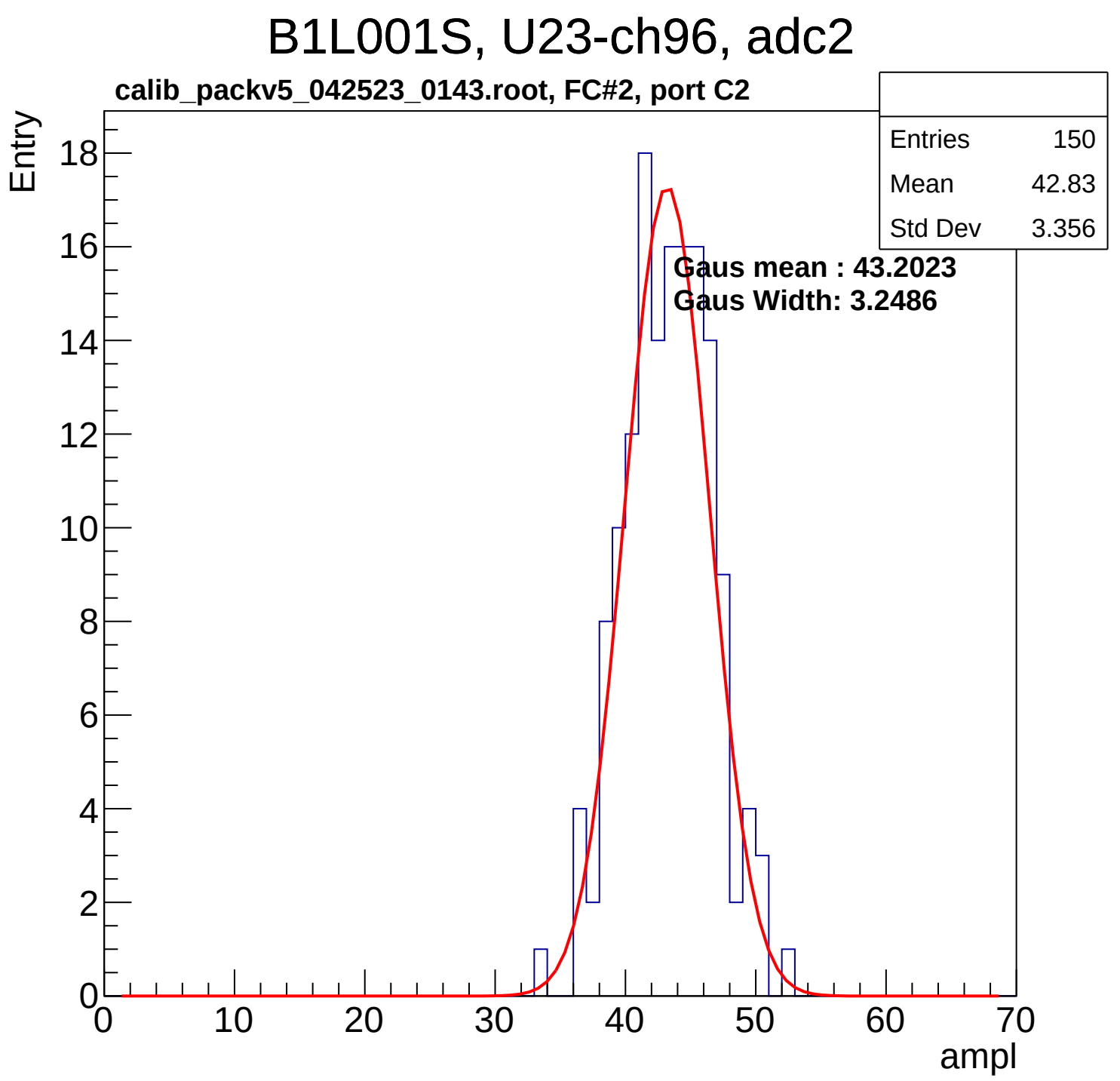
B1L001S, U23-ch96, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 42.83
Std Dev 3.356
Gaus mean : 43.2023
Gaus Width: 3.2486
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
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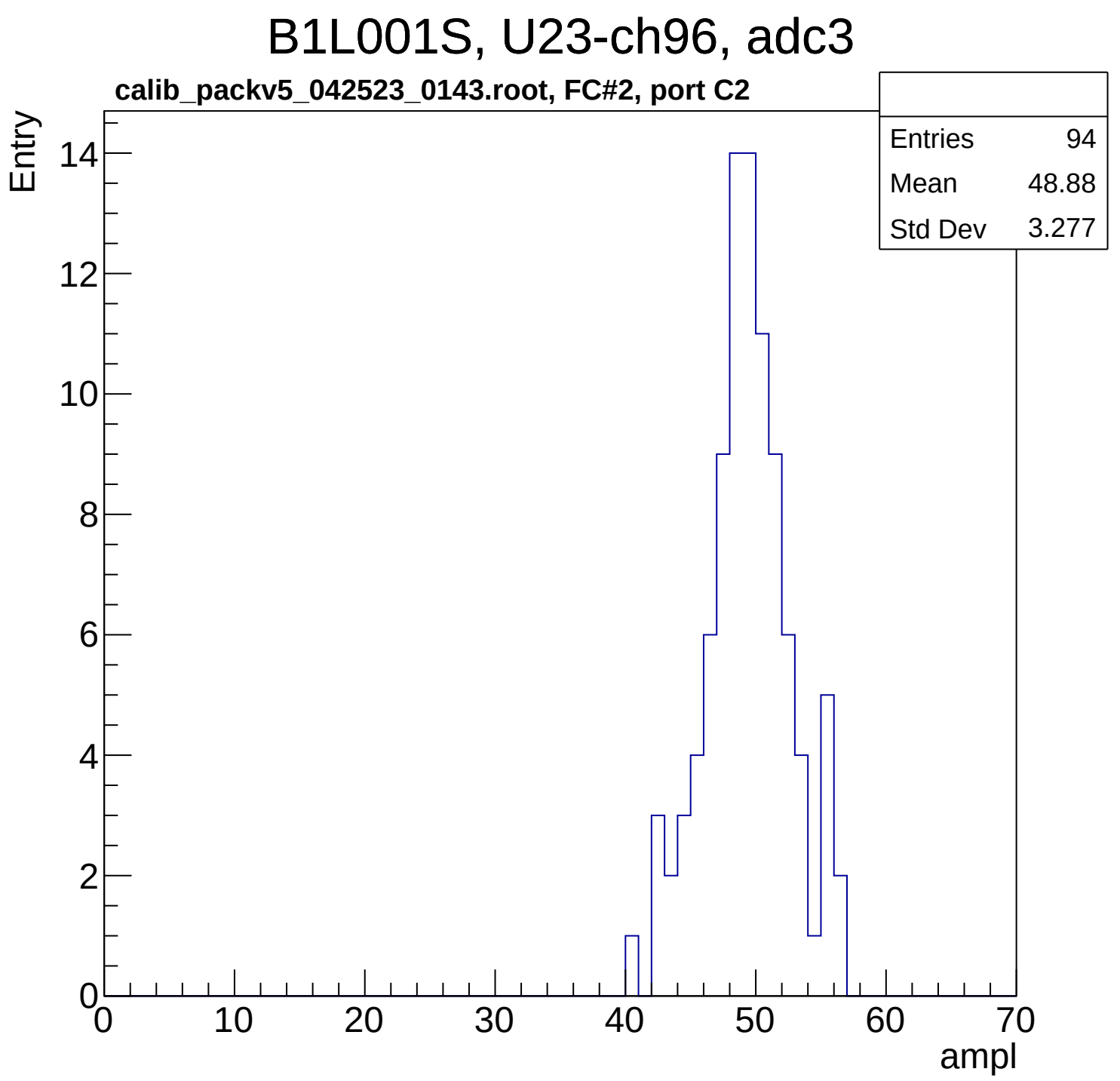
B1L001S, U23-ch96, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 48.88
Std Dev 3.277
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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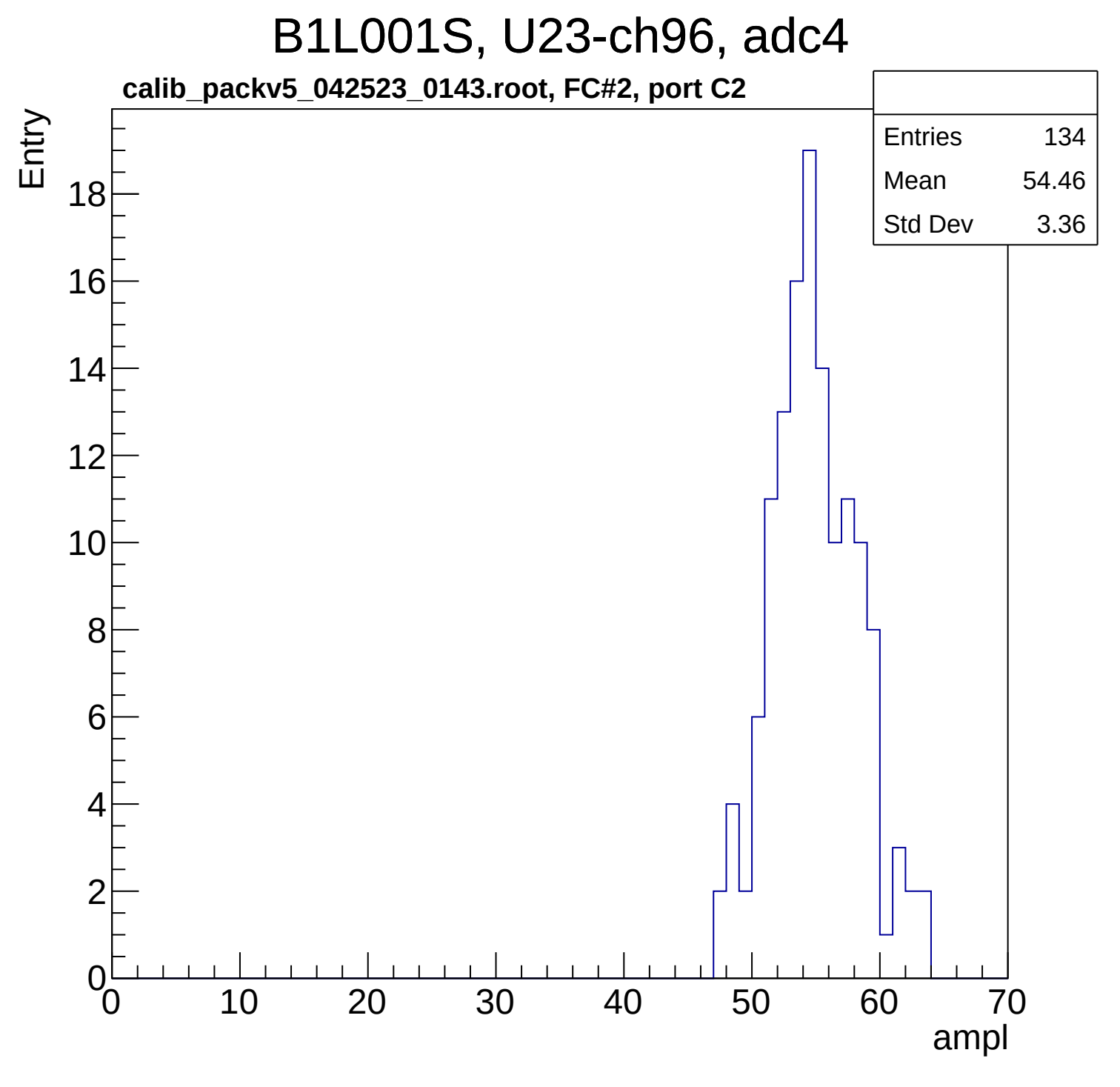
B1L001S, U23-ch96, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 54.46
Std Dev 3.36
Entry
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6
8
10
12
14
16
18
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10
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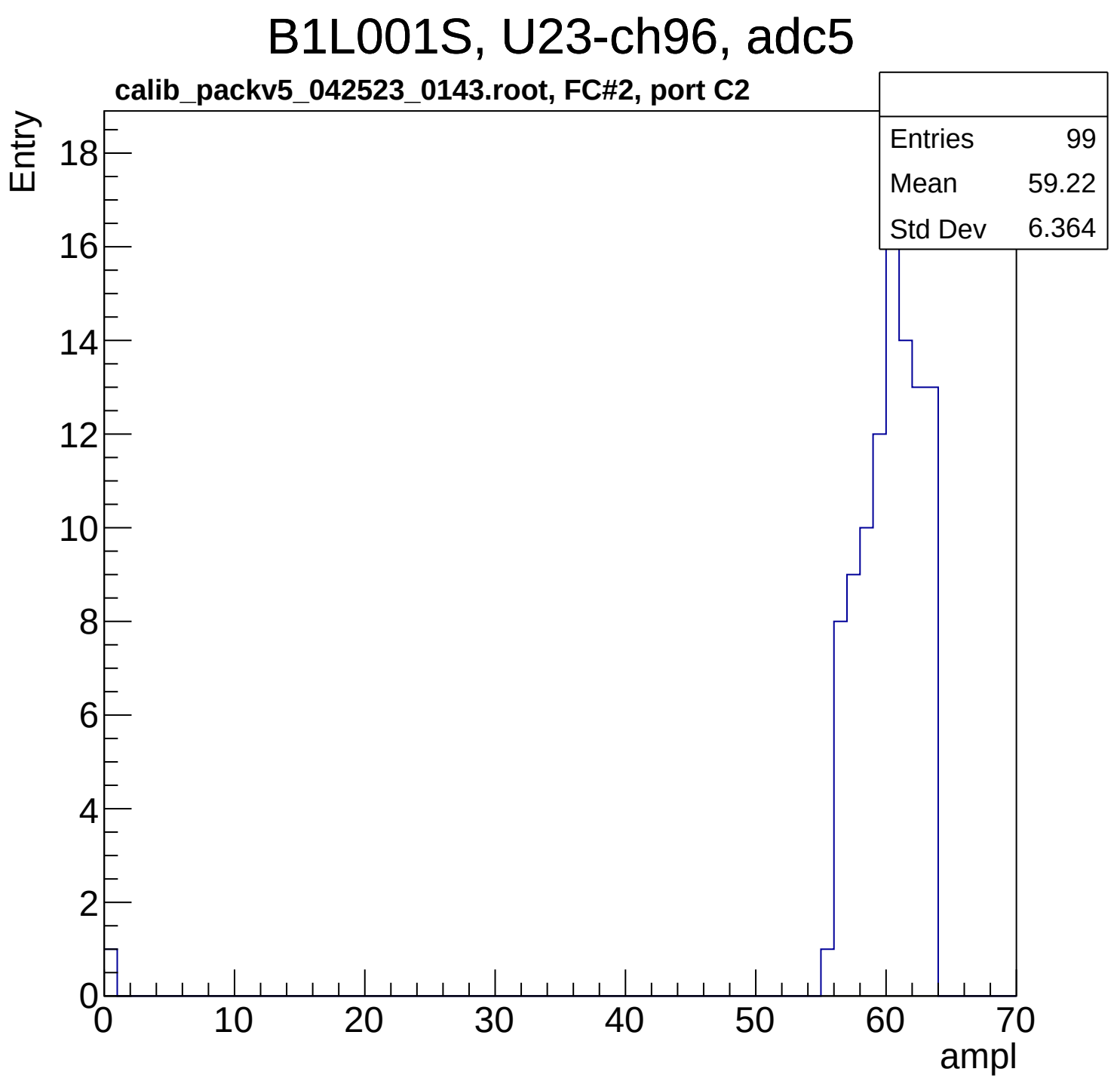
50
60
70
ampl


B1L001S, U23-ch96, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 59.22
Std Dev 6.364
Entry
ampl
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6
8
10
12
14
16
18
0
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60
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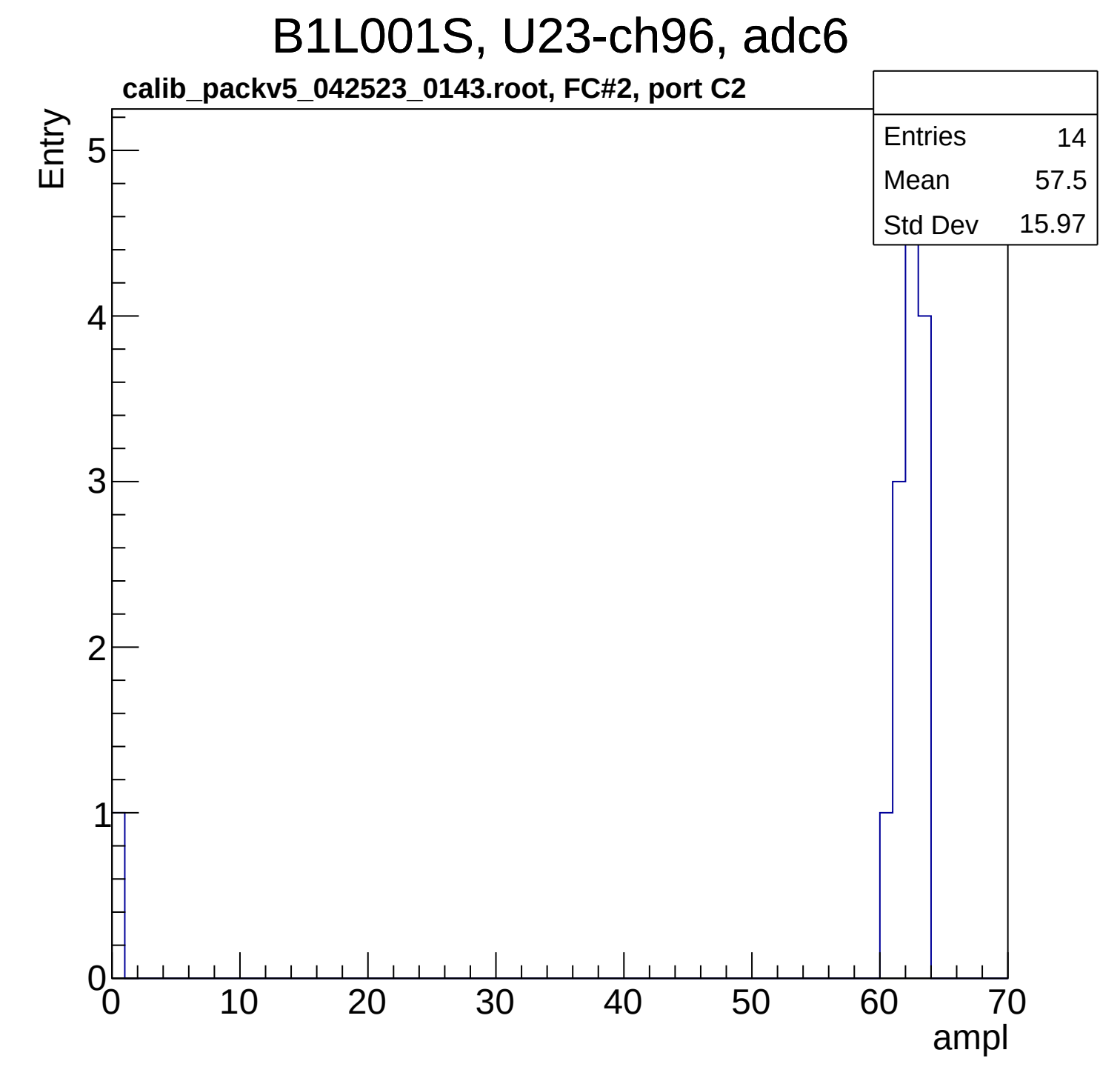
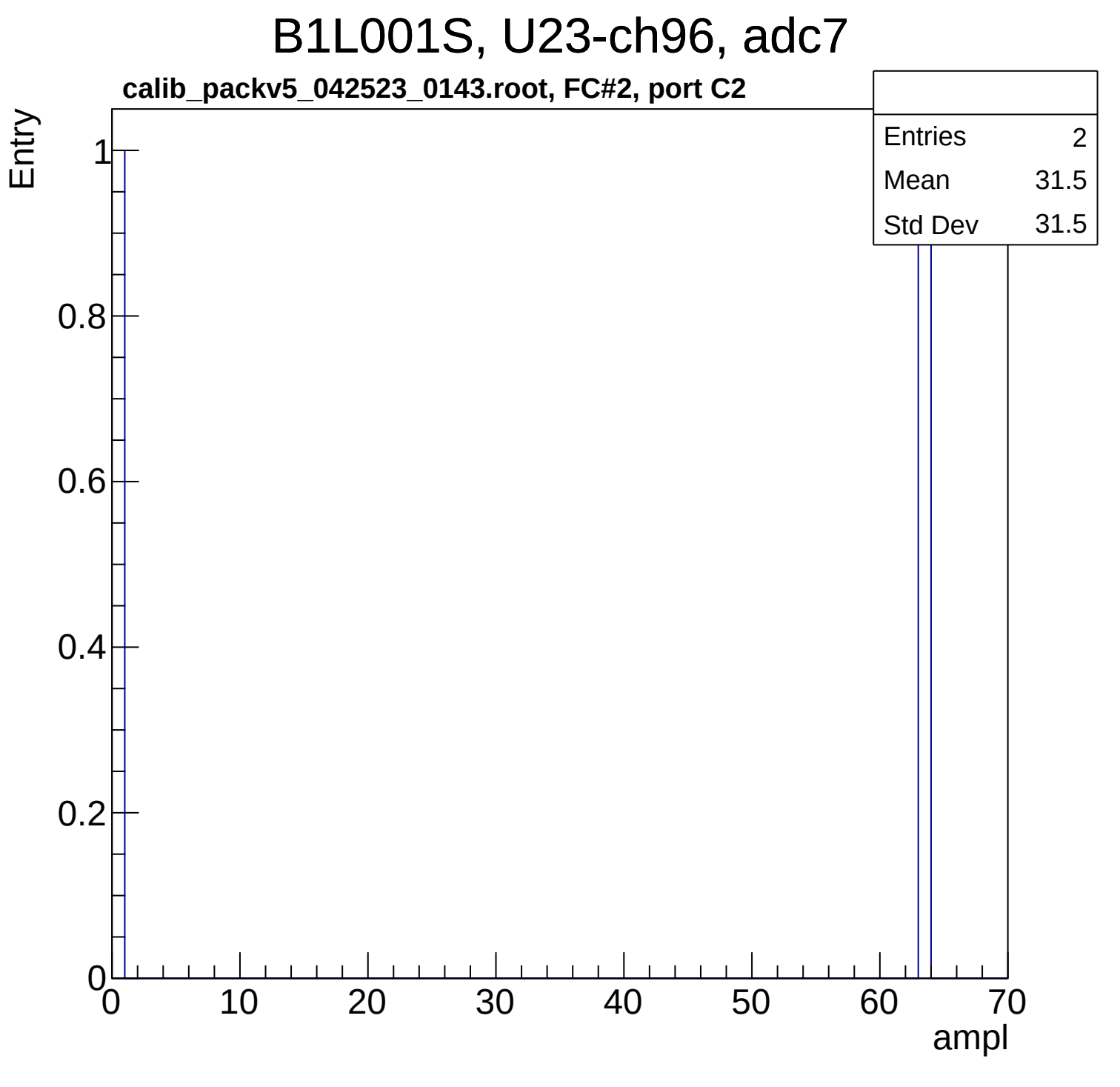

B1L001S, U23-ch96, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 57.5
Std Dev 15.97
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch96, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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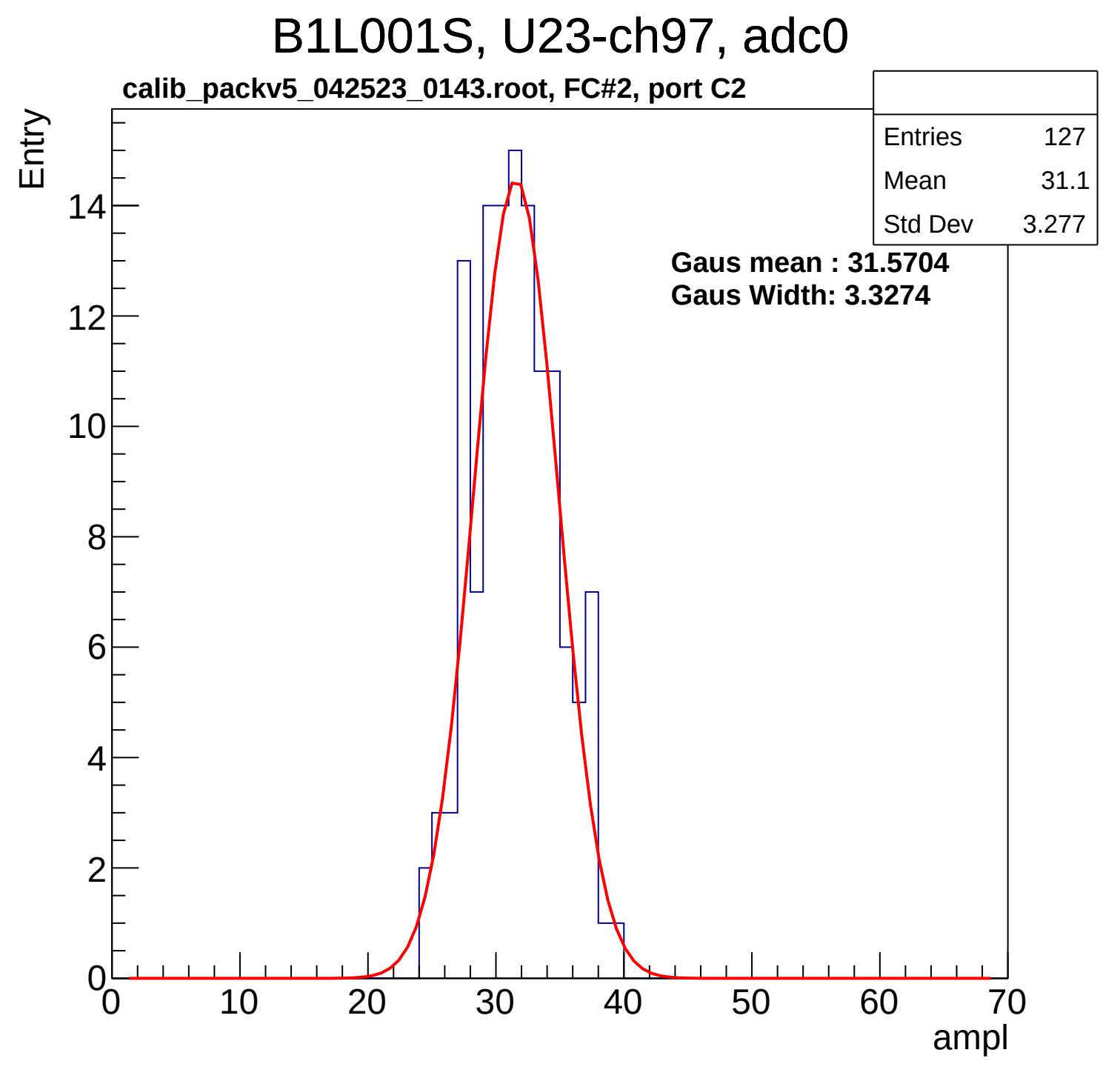
50
60
70
ampl


B1L001S, U23-ch97, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 31.1
Std Dev 3.277
Gaus mean : 31.5704
Gaus Width: 3.3274
Entry
ampl
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4
6
8
10
12
14
0
10
20
30
40
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60
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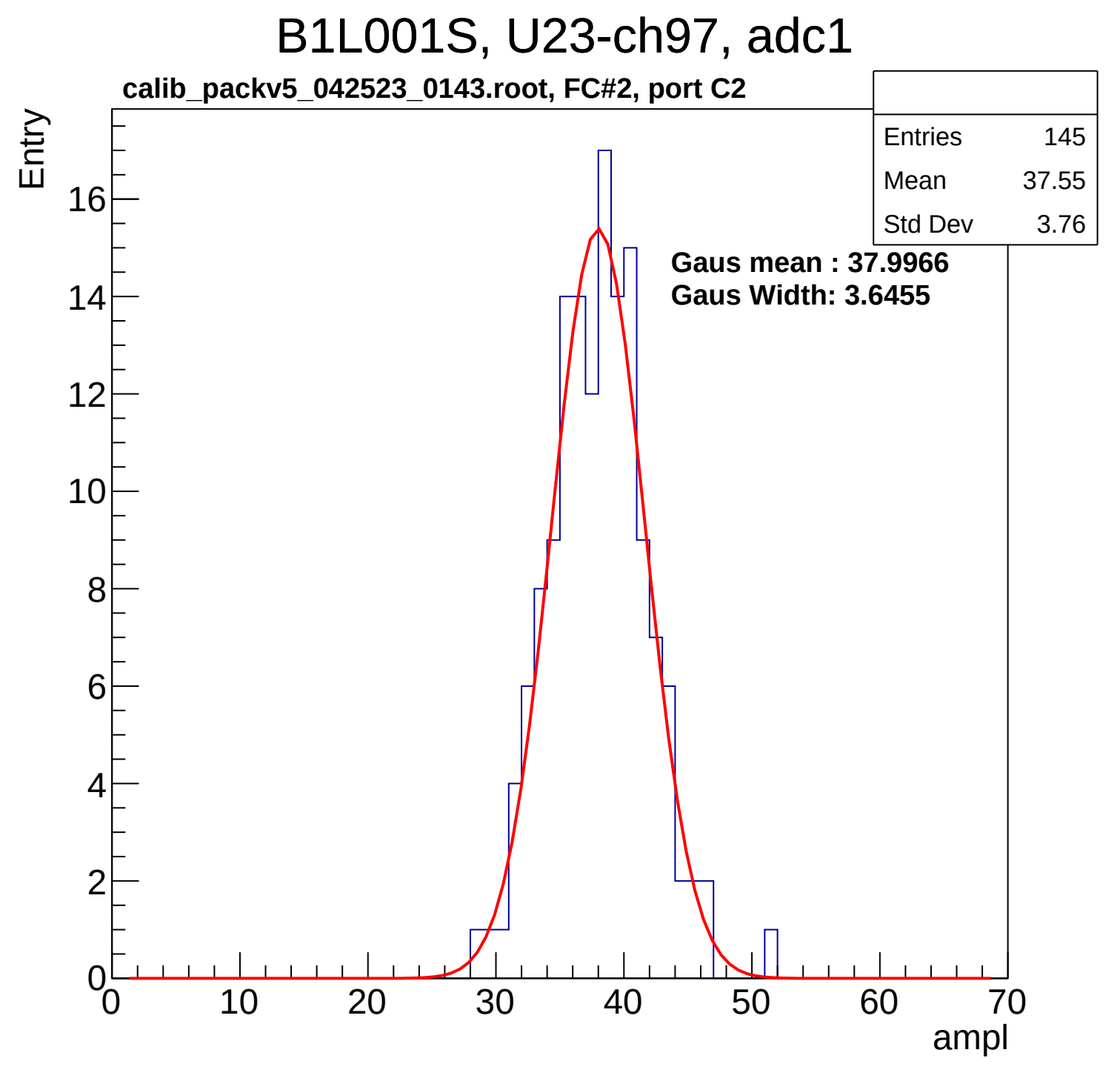
B1L001S, U23-ch97, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 37.55
Std Dev 3.76
Gaus mean : 37.9966
Gaus Width: 3.6455
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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40
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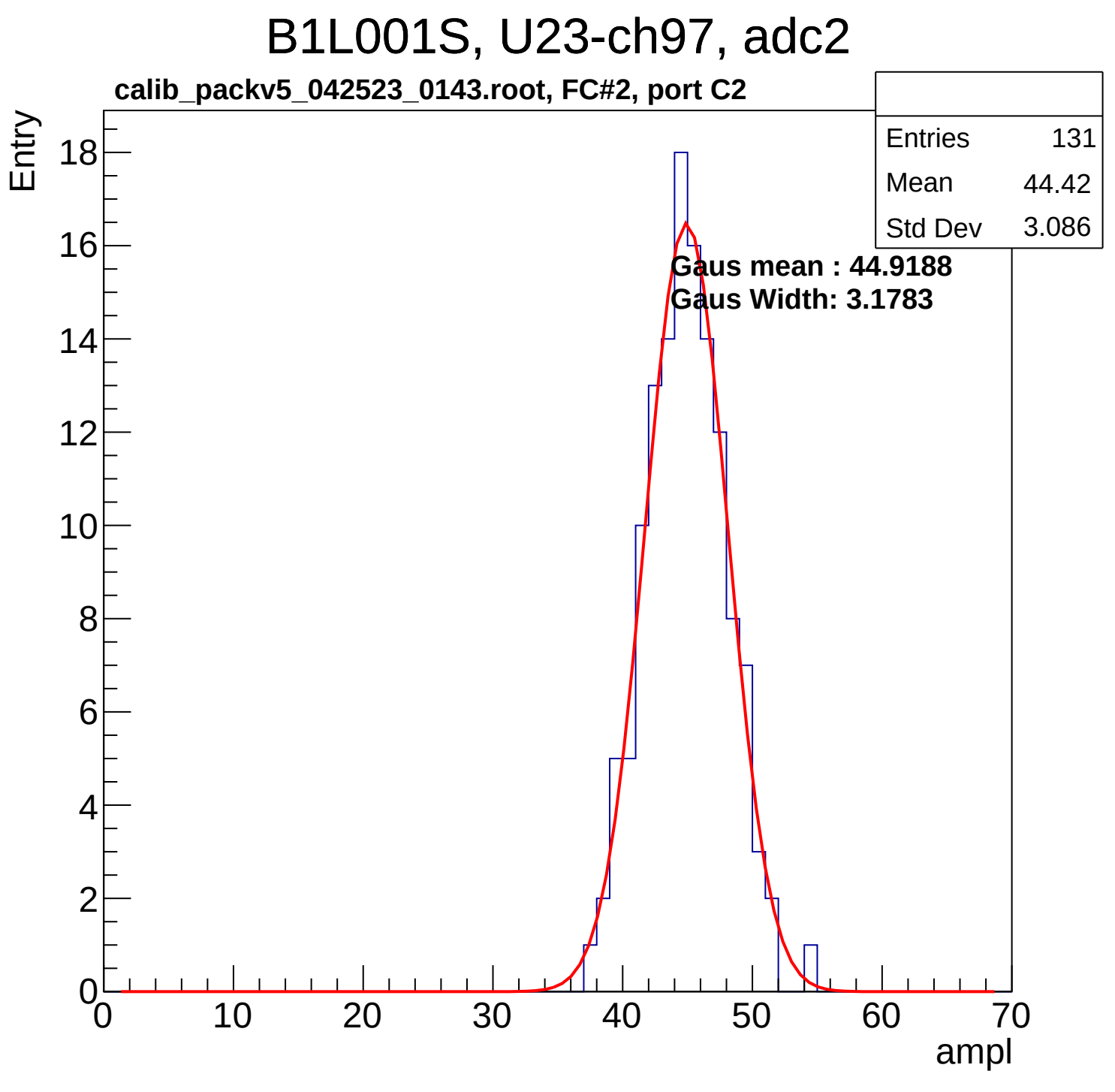
B1L001S, U23-ch97, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 44.42
Std Dev 3.086
Gaus mean : 44.9188
Gaus Width: 3.1783
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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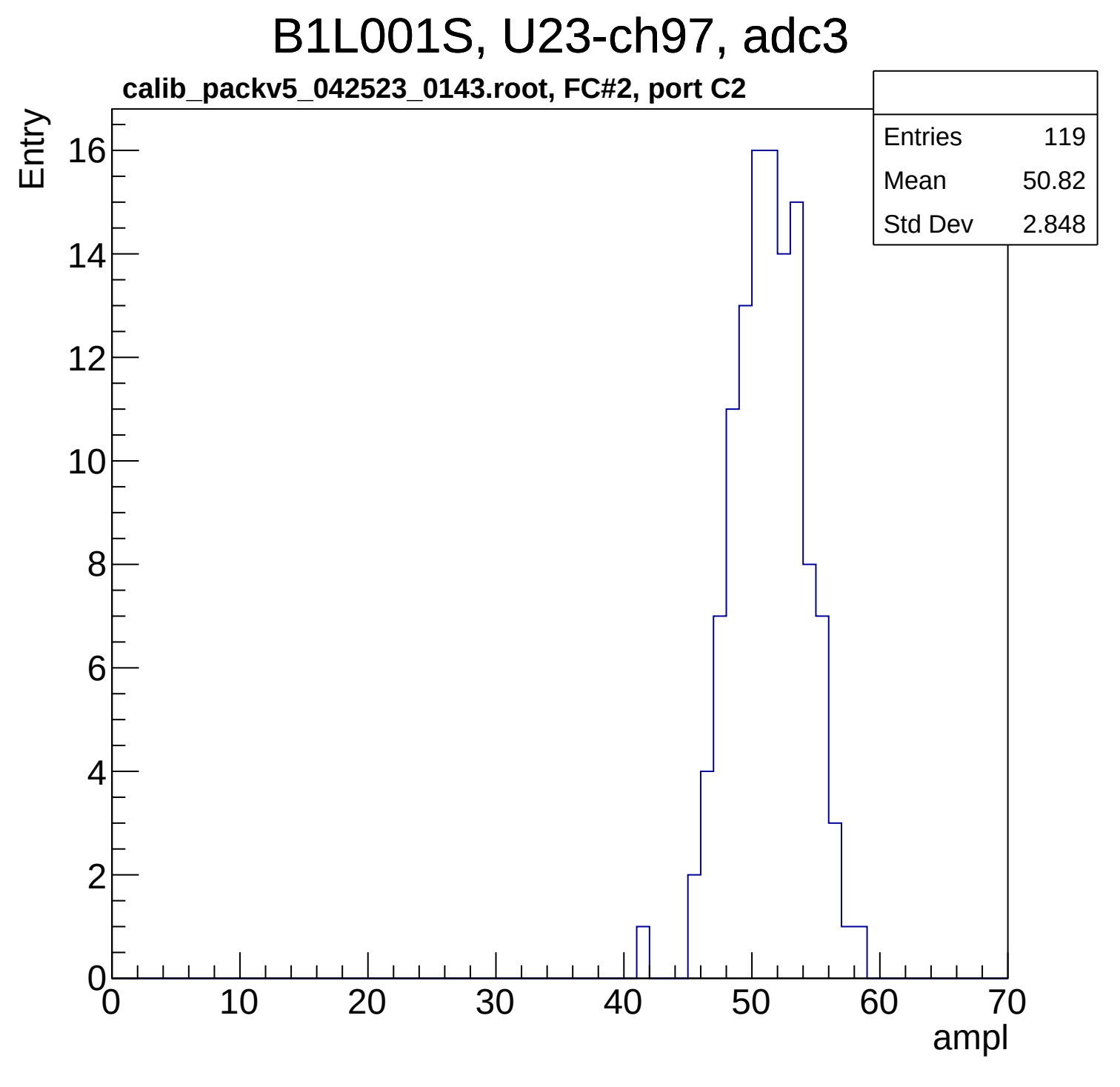
B1L001S, U23-ch97, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 50.82
Std Dev 2.848
Entry
ampl
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4
6
8
10
12
14
16
0
10
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60
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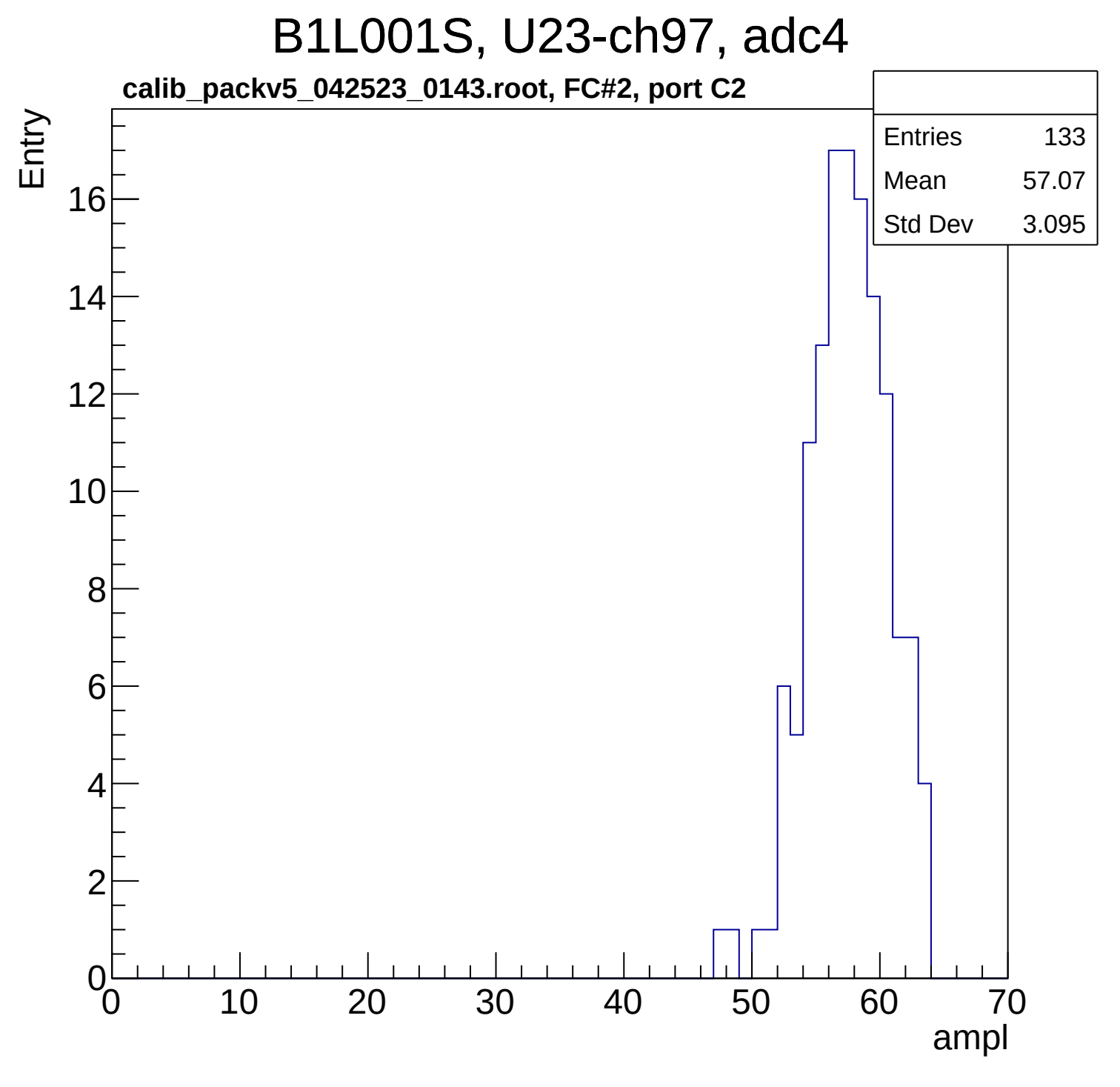
B1L001S, U23-ch97, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 57.07
Std Dev 3.095
Entry
ampl
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4
6
8
10
12
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0
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60
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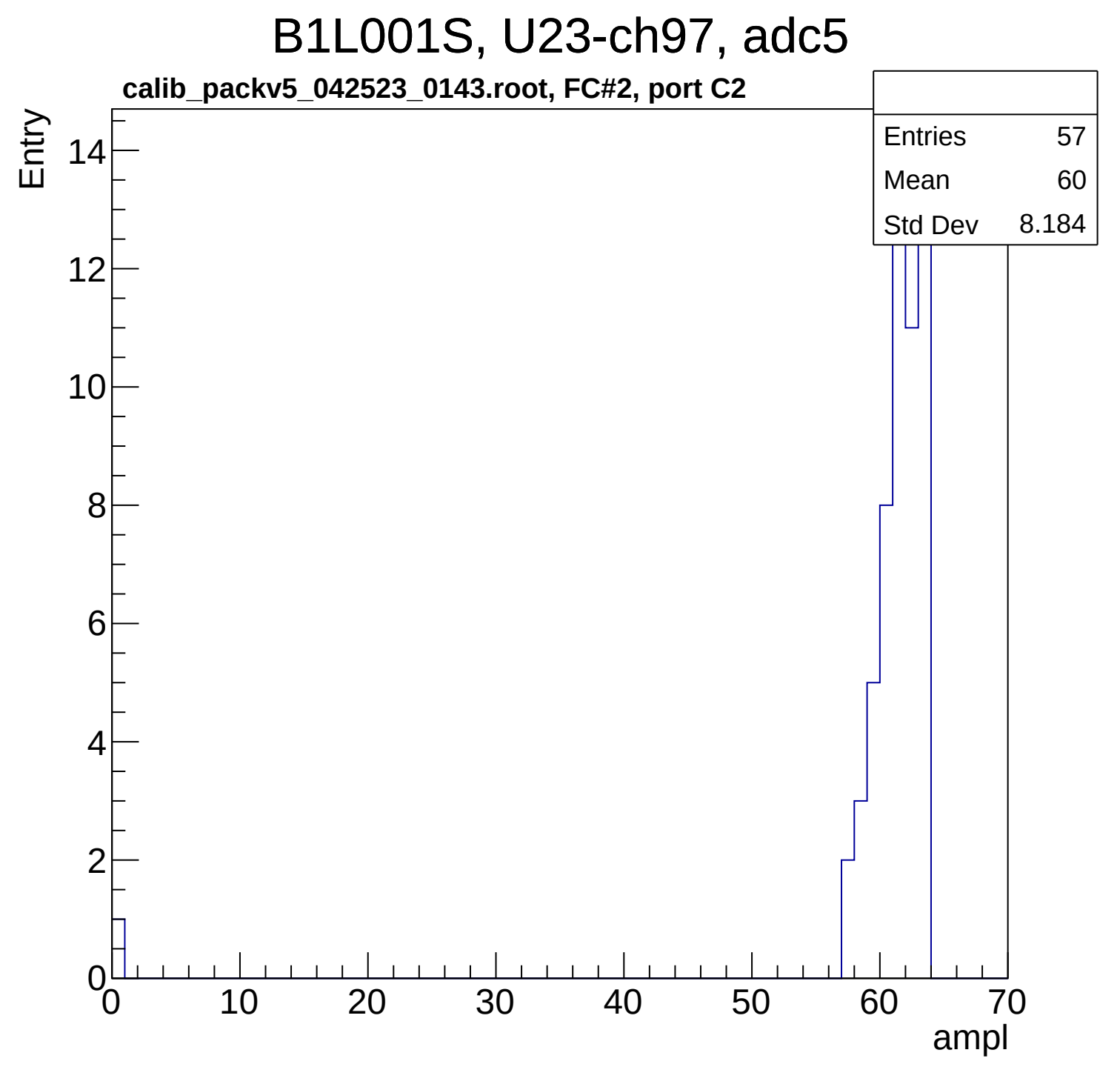
B1L001S, U23-ch97, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 57
Mean 60
Std Dev 8.184
Entry
ampl
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6
8
10
12
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0
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60
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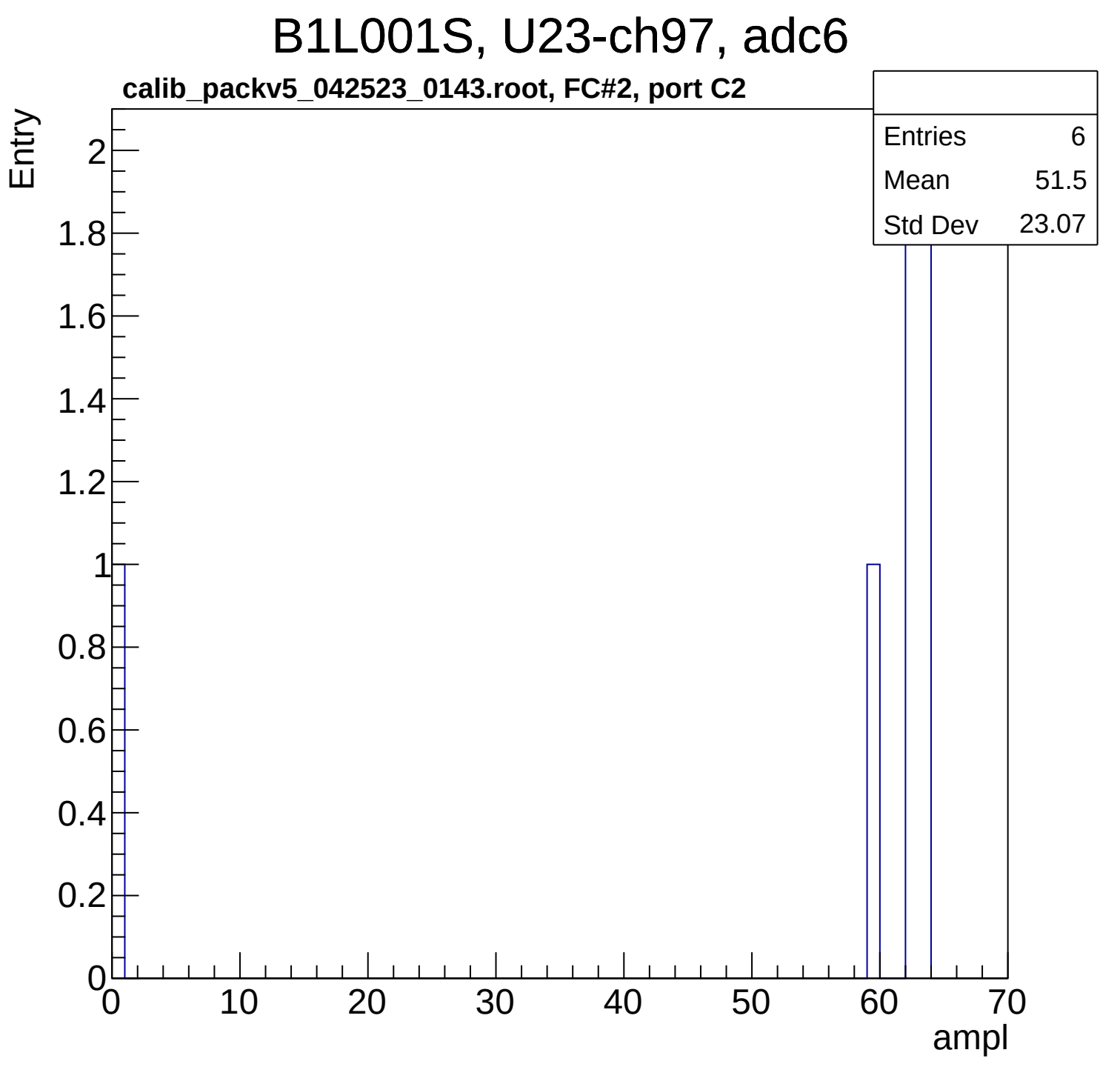
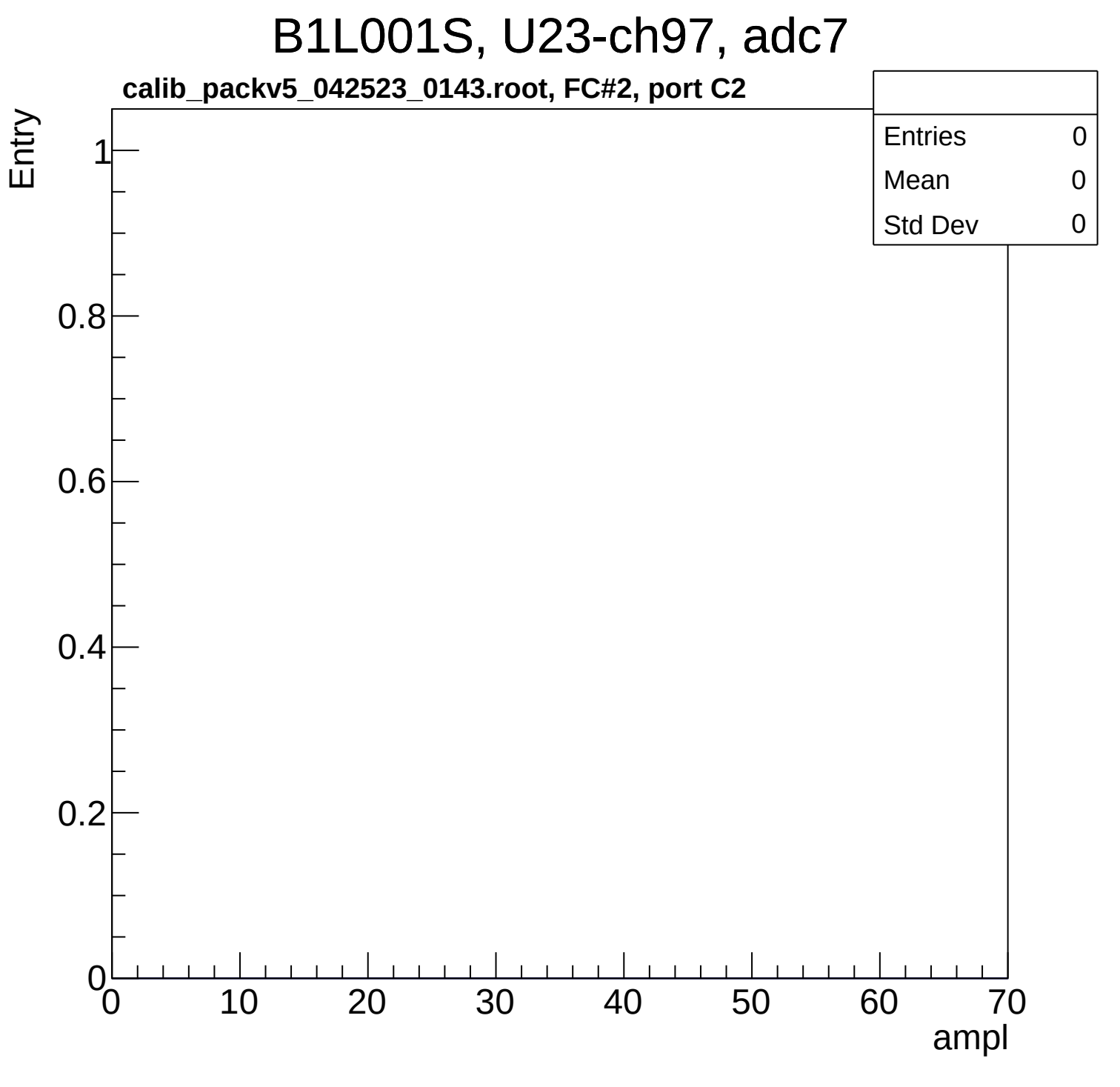

B1L001S, U23-ch97, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 51.5
Std Dev 23.07
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch97, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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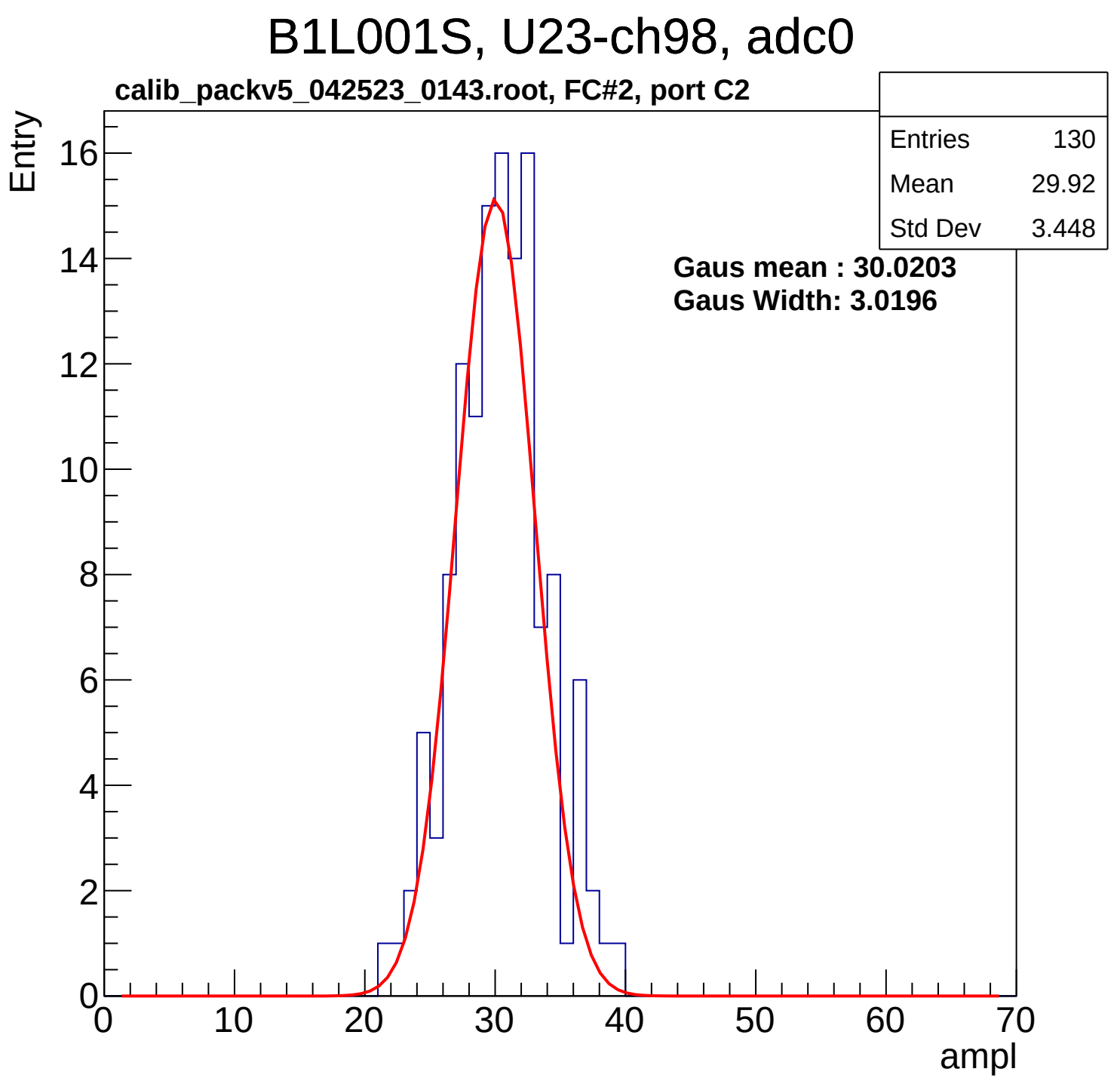
50
60
70
ampl


B1L001S, U23-ch98, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 29.92
Std Dev 3.448
Gaus mean : 30.0203
Gaus Width: 3.0196
Entry
ampl
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4
6
8
10
12
14
16
0
10
20
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40
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60
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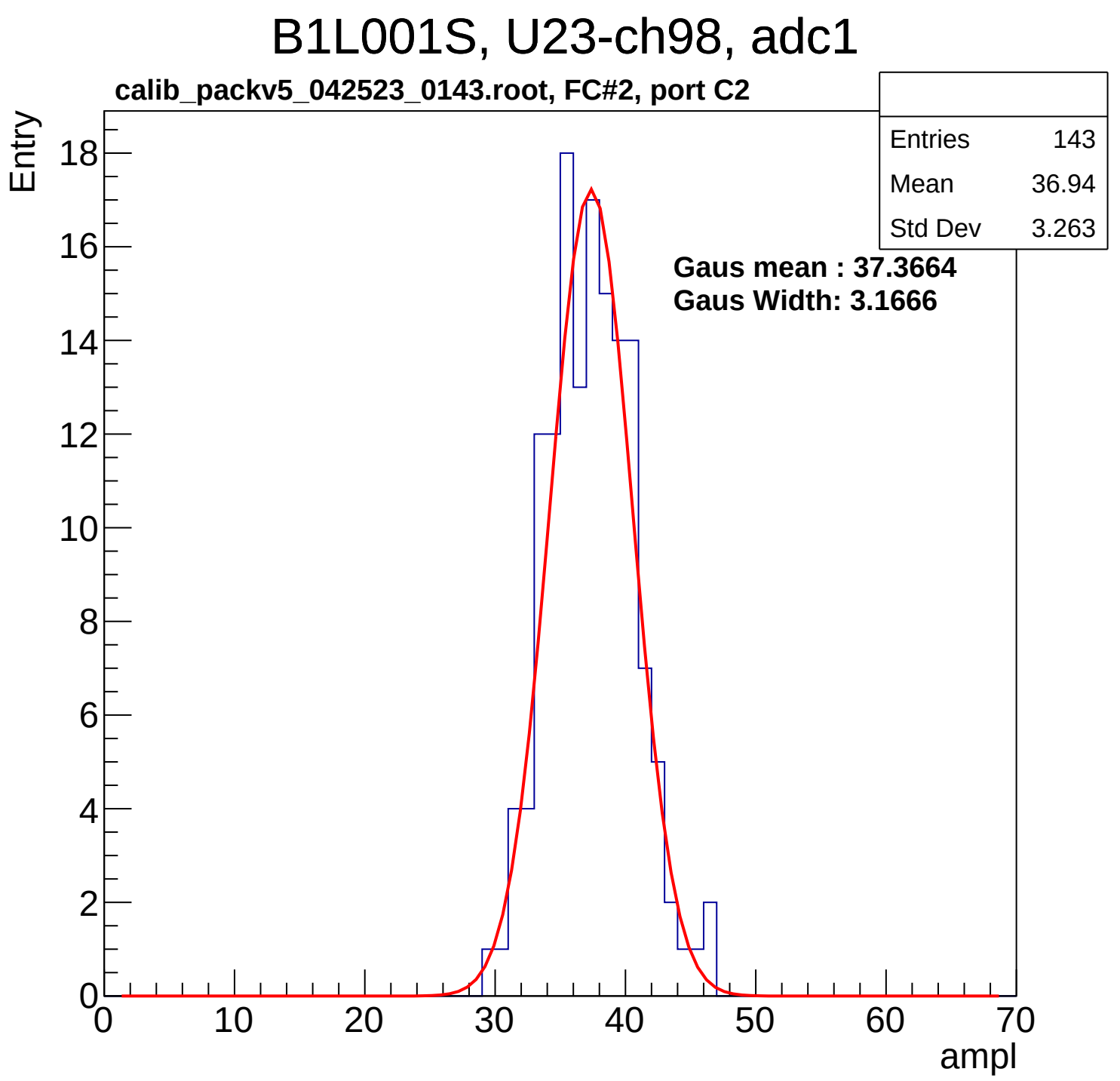
B1L001S, U23-ch98, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 36.94
Std Dev 3.263
Gaus mean : 37.3664
Gaus Width: 3.1666
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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60
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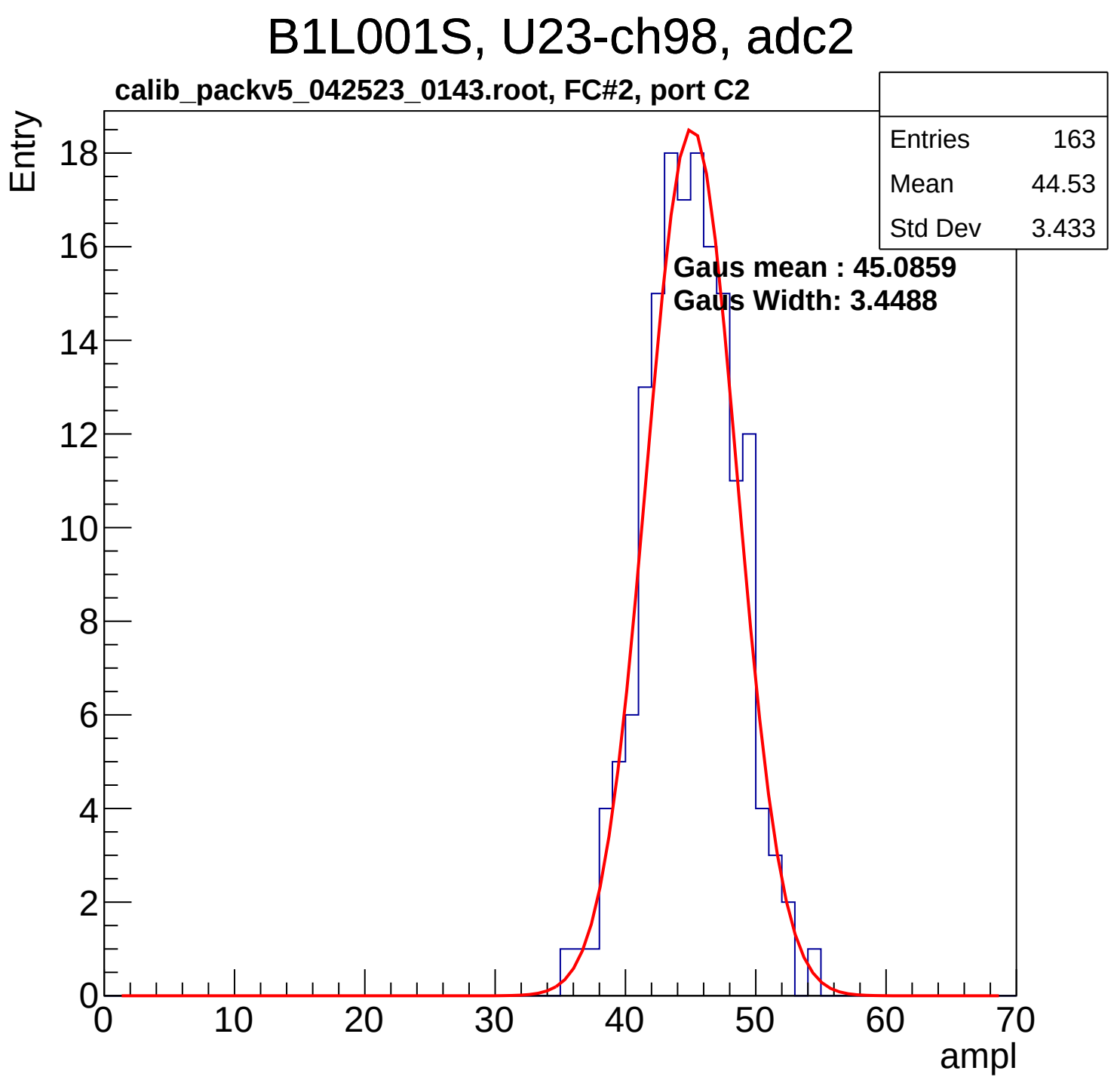
B1L001S, U23-ch98, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 163
Mean 44.53
Std Dev 3.433
Gaus mean : 45.0859
Gaus Width: 3.4488
Entry
ampl
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10
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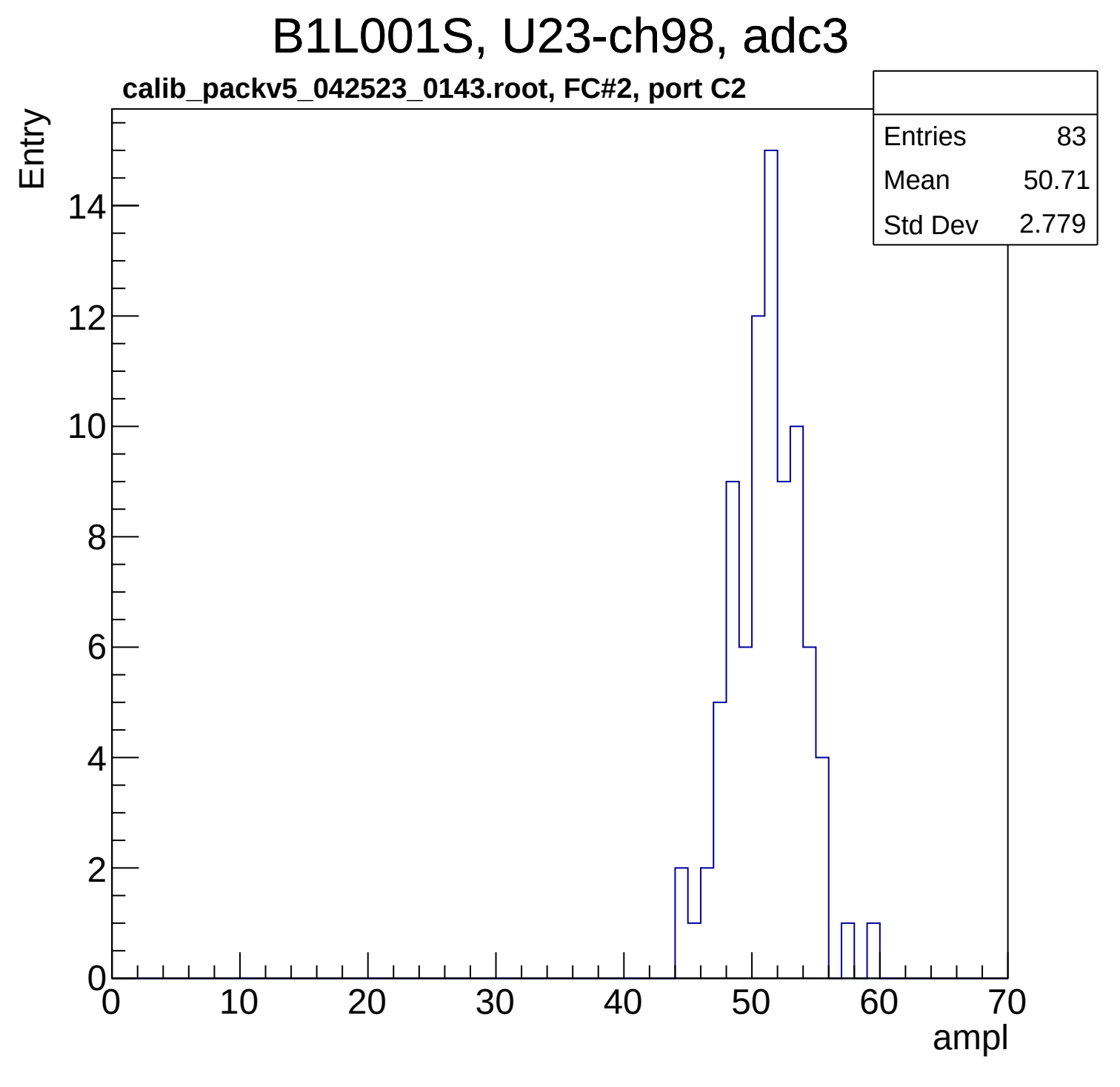
B1L001S, U23-ch98, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 83
Mean 50.71
Std Dev 2.779
Entry
ampl
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0
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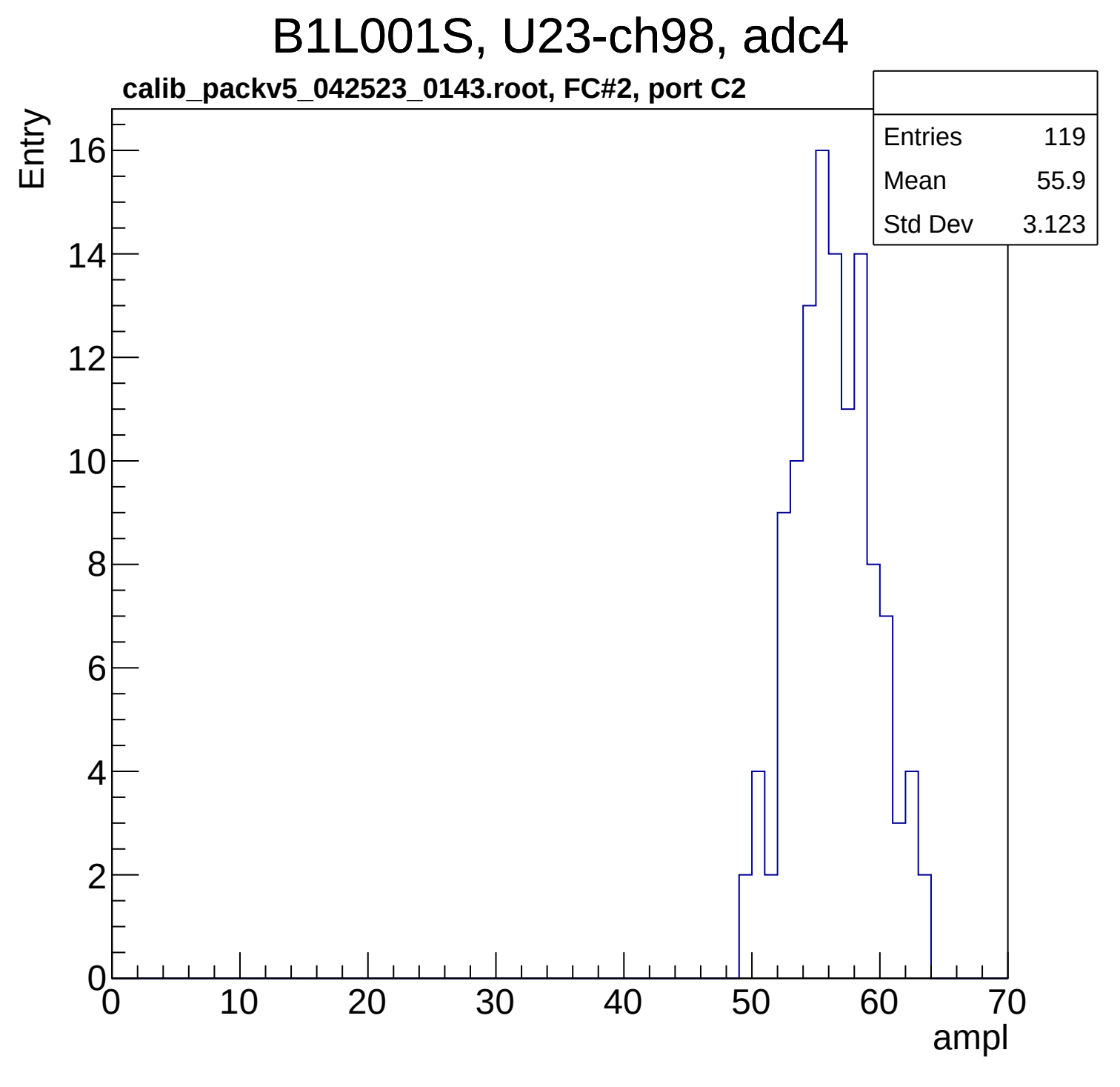
B1L001S, U23-ch98, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 55.9
Std Dev 3.123
Entry
ampl
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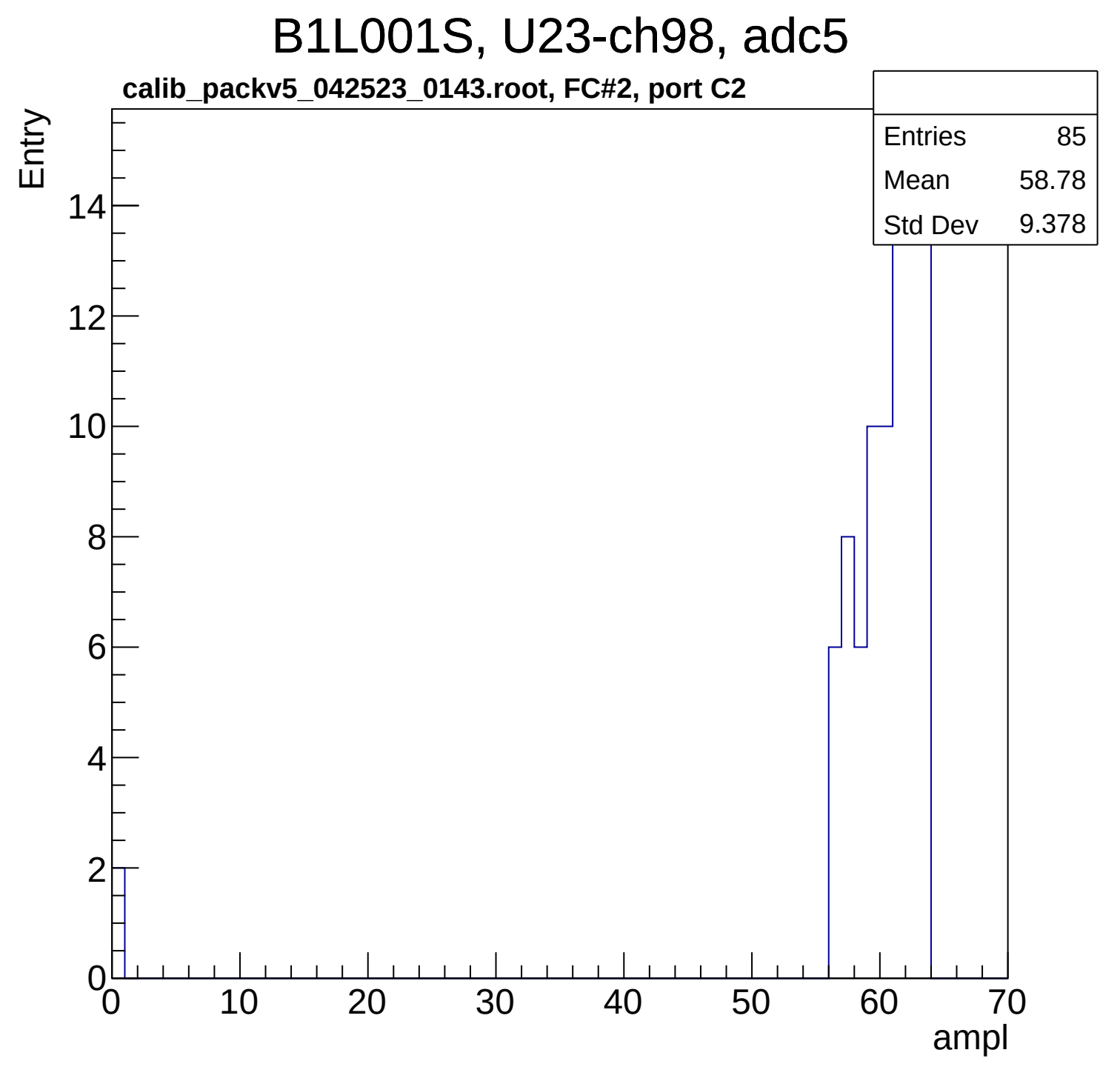
B1L001S, U23-ch98, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 85
Mean 58.78
Std Dev 9.378
Entry
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10
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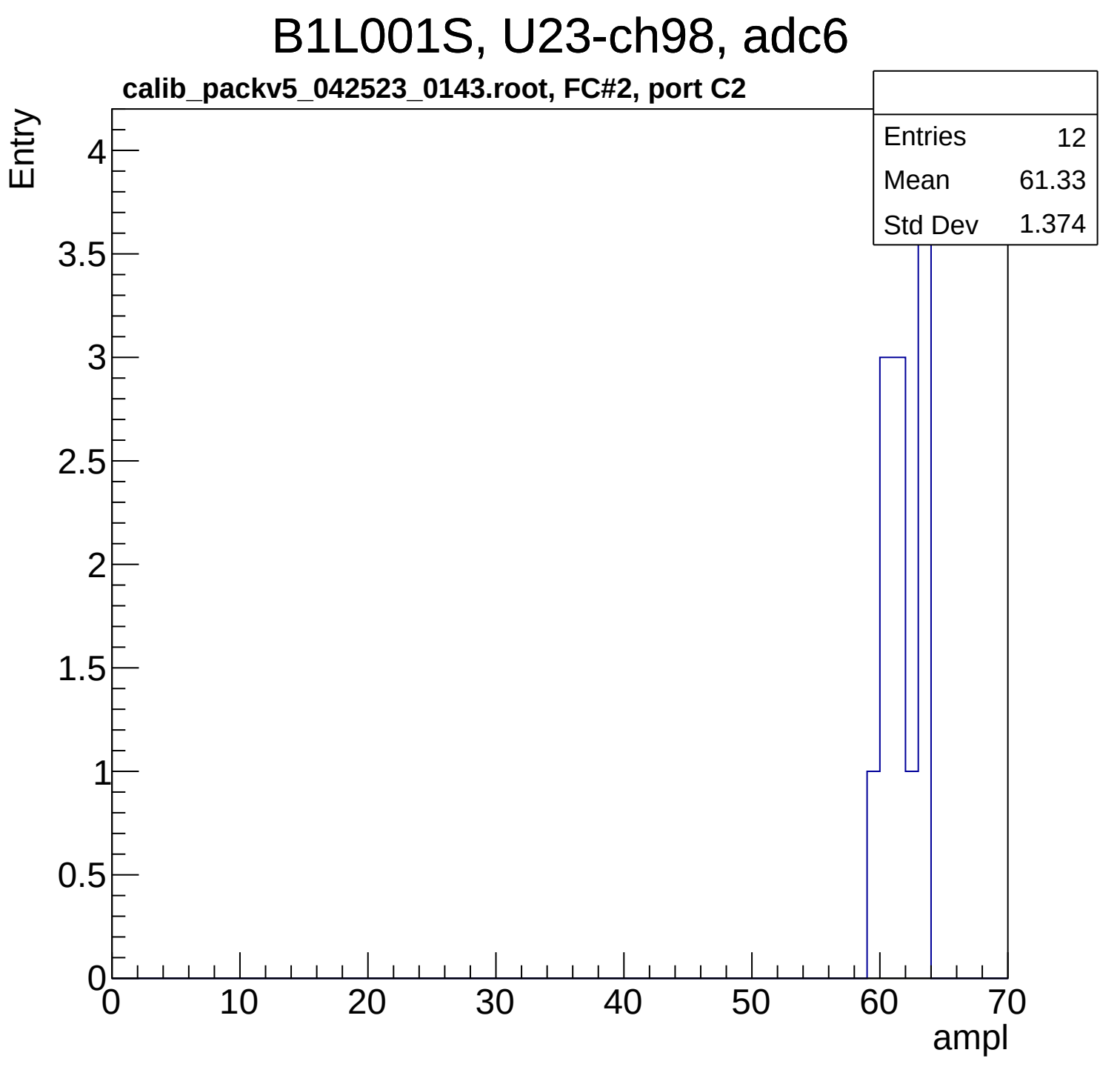
50
60
70
ampl


B1L001S, U23-ch98, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 61.33
Std Dev 1.374
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
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20
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40
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60
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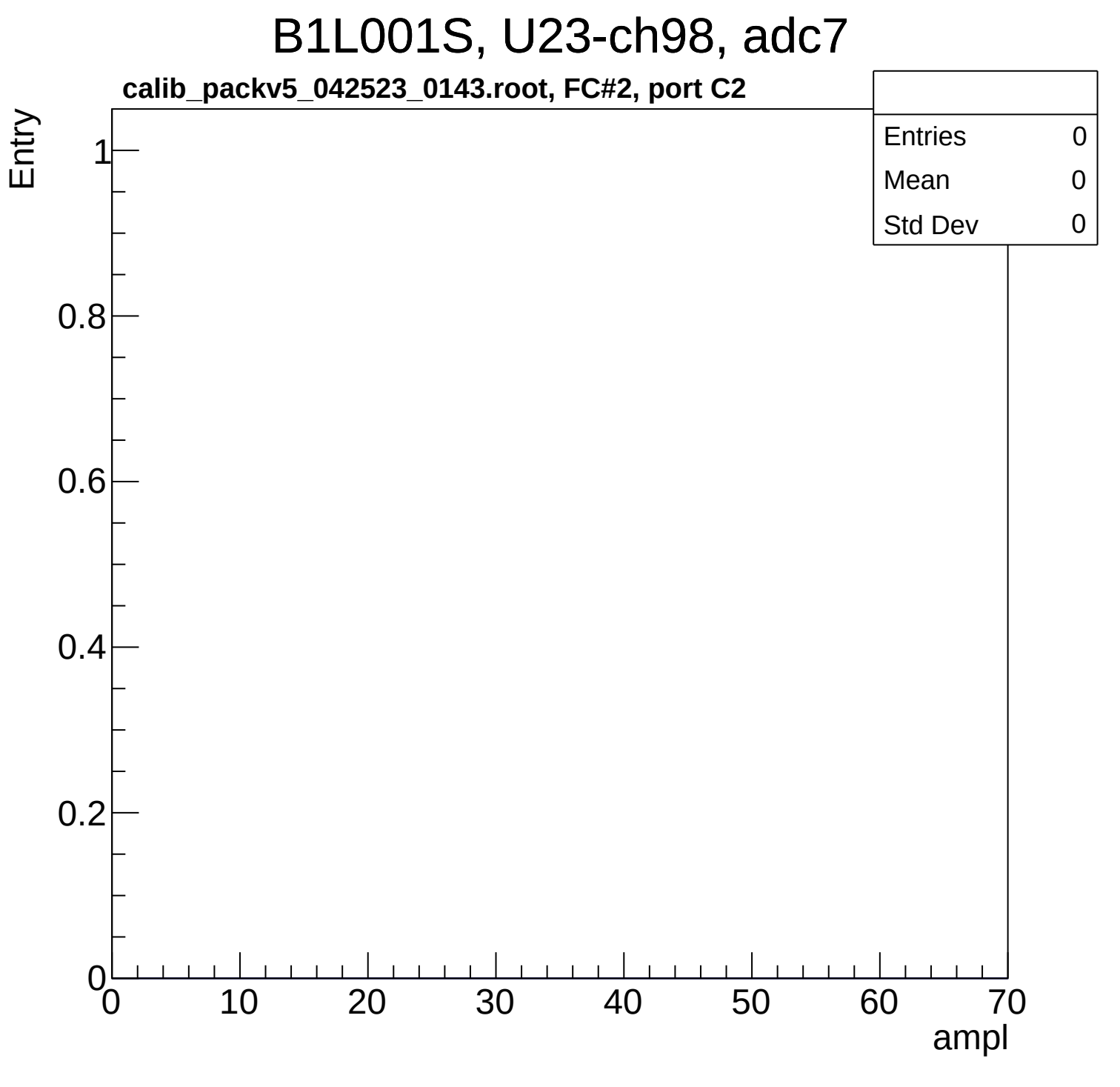
B1L001S, U23-ch98, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
20
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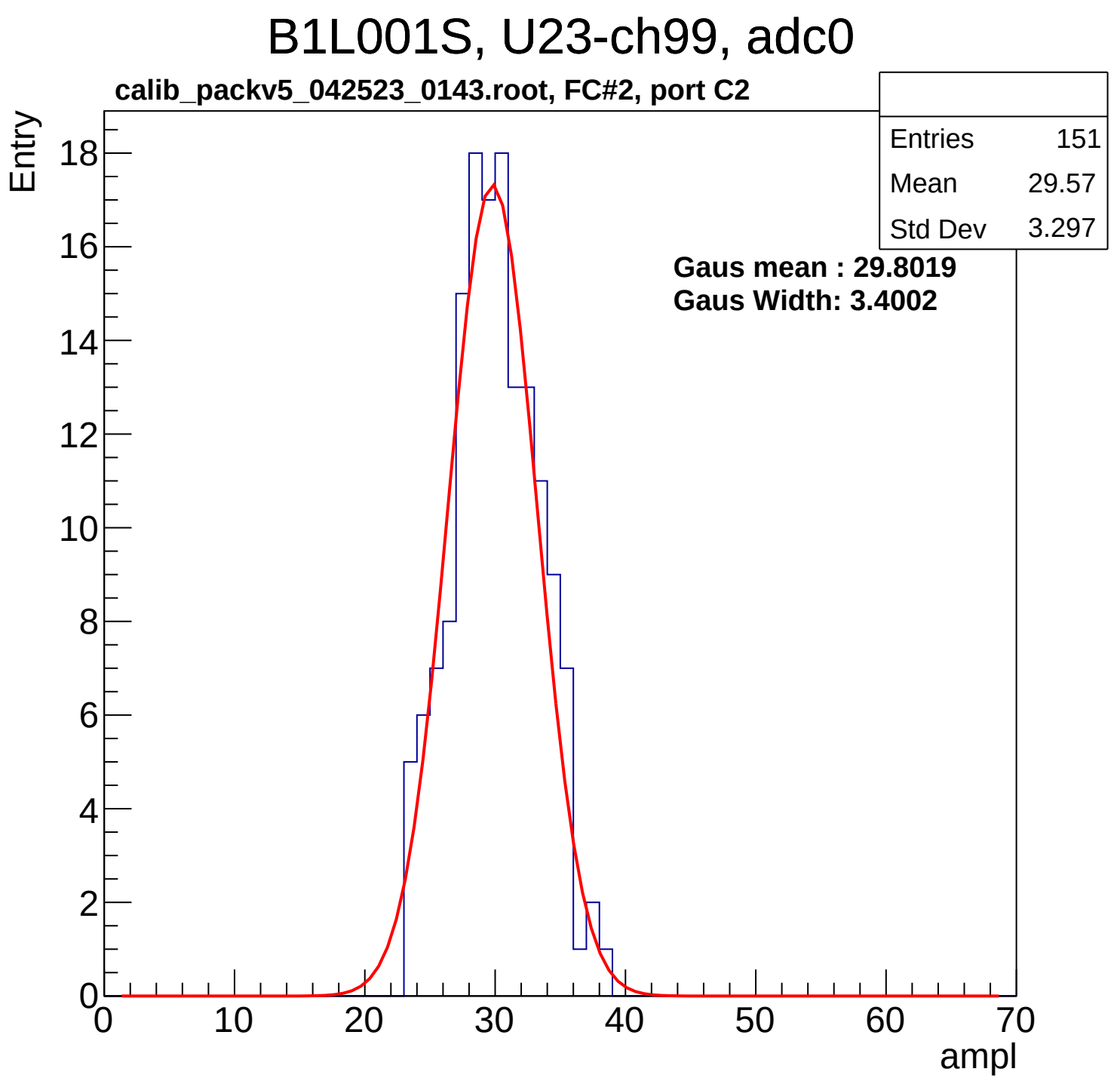
40
50
60
70
ampl


B1L001S, U23-ch99, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 29.57
Std Dev 3.297
Gaus mean : 29.8019
Gaus Width: 3.4002
Entry
ampl
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4
6
8
10
12
14
16
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0
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40
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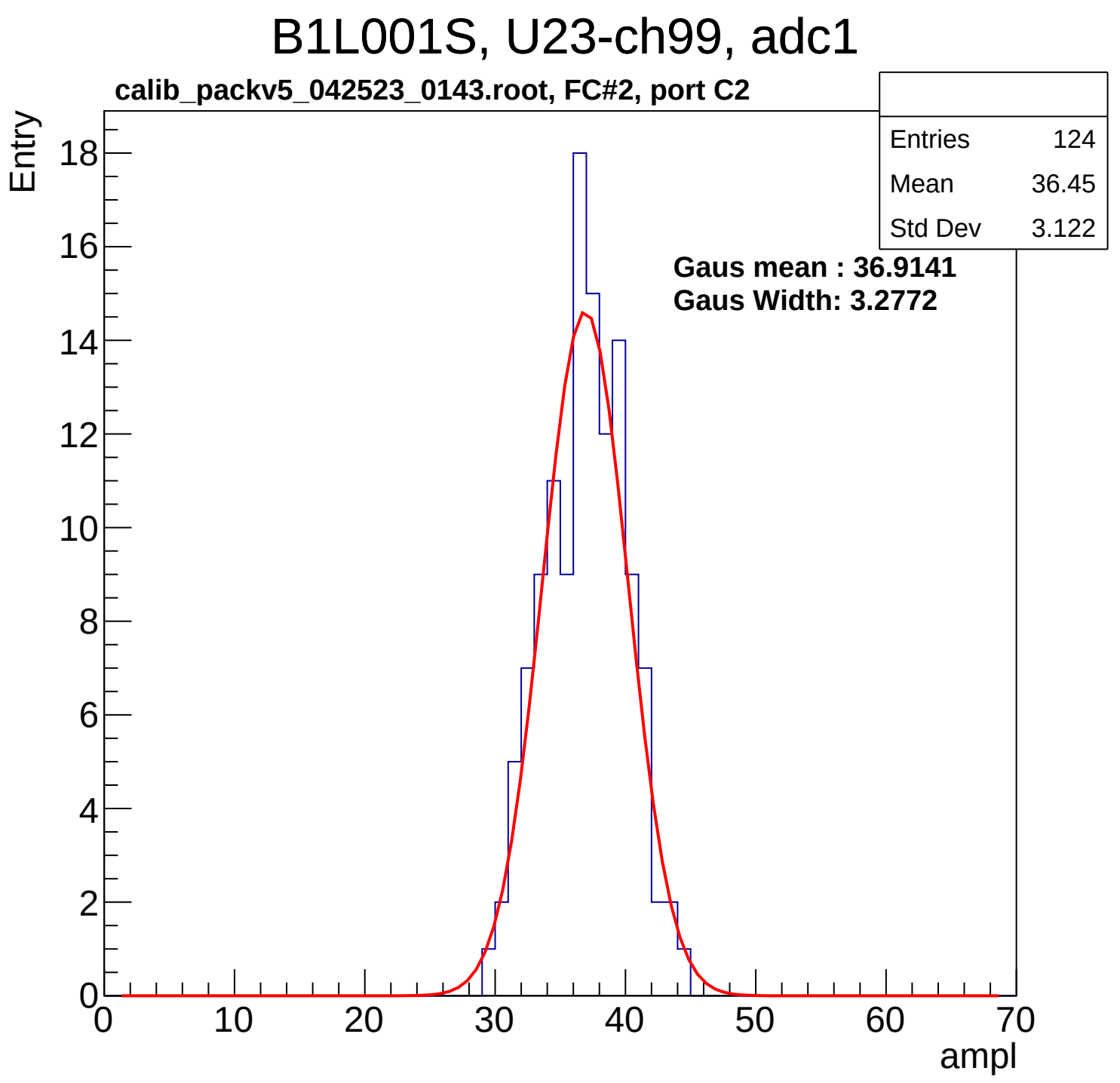
B1L001S, U23-ch99, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 36.45
Std Dev 3.122
Gaus mean : 36.9141
Gaus Width: 3.2772
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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60
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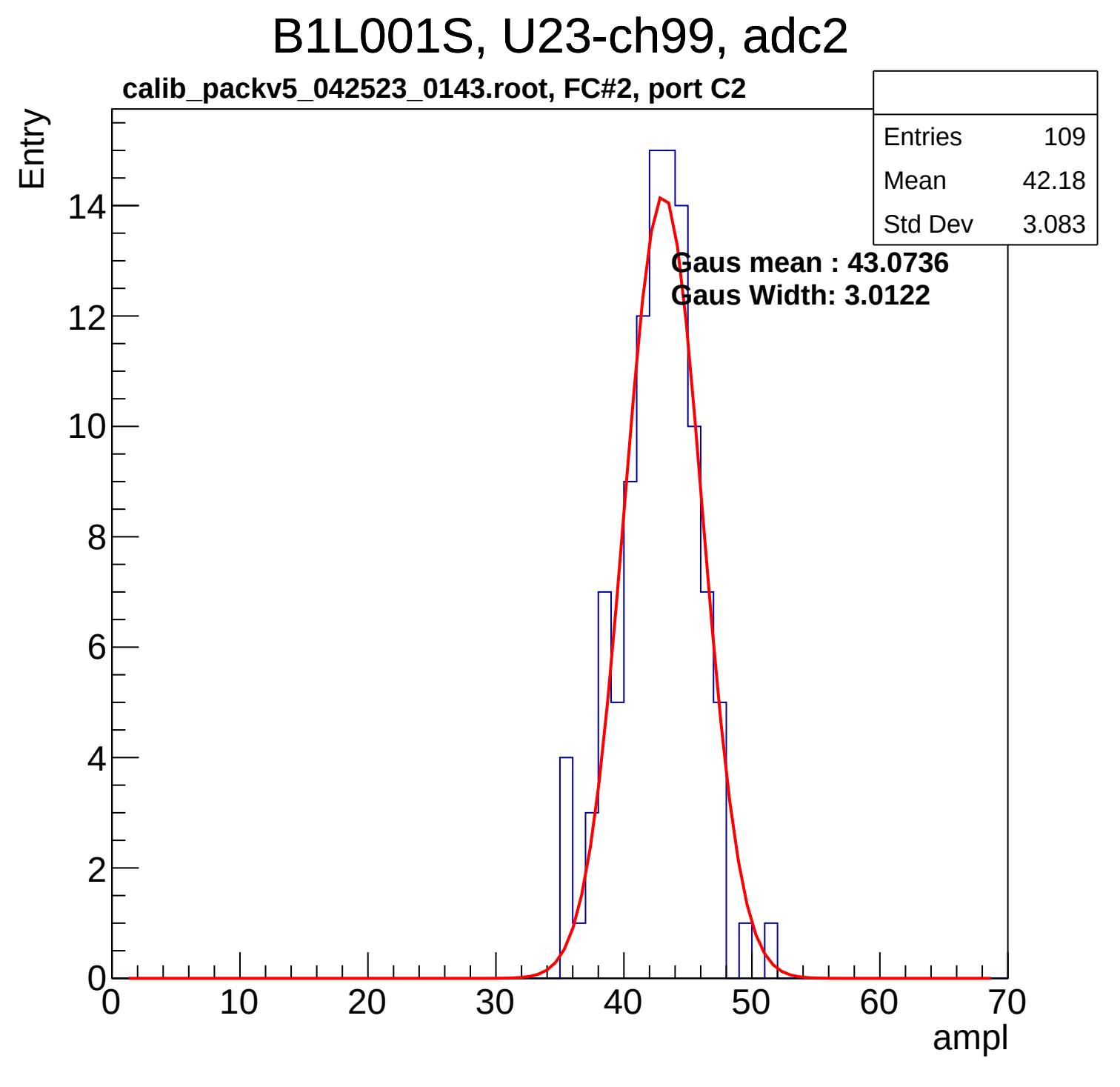
B1L001S, U23-ch99, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 109
Mean 42.18
Std Dev 3.083
Gaus mean : 43.0736
Gaus Width: 3.0122
Entry
ampl
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2
4
6
8
10
12
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0
10
20
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60
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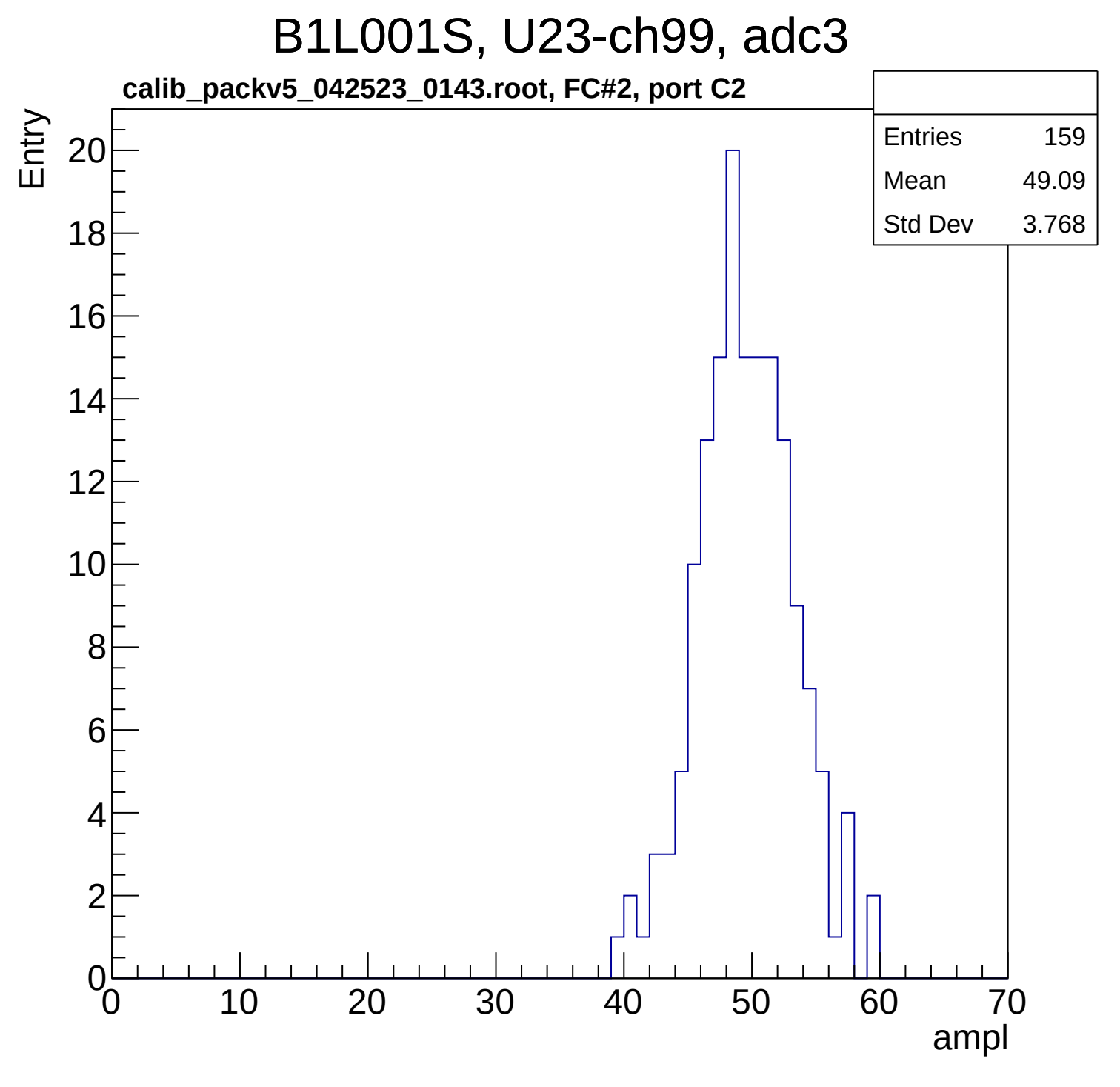
B1L001S, U23-ch99, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 49.09
Std Dev 3.768
Entry
ampl
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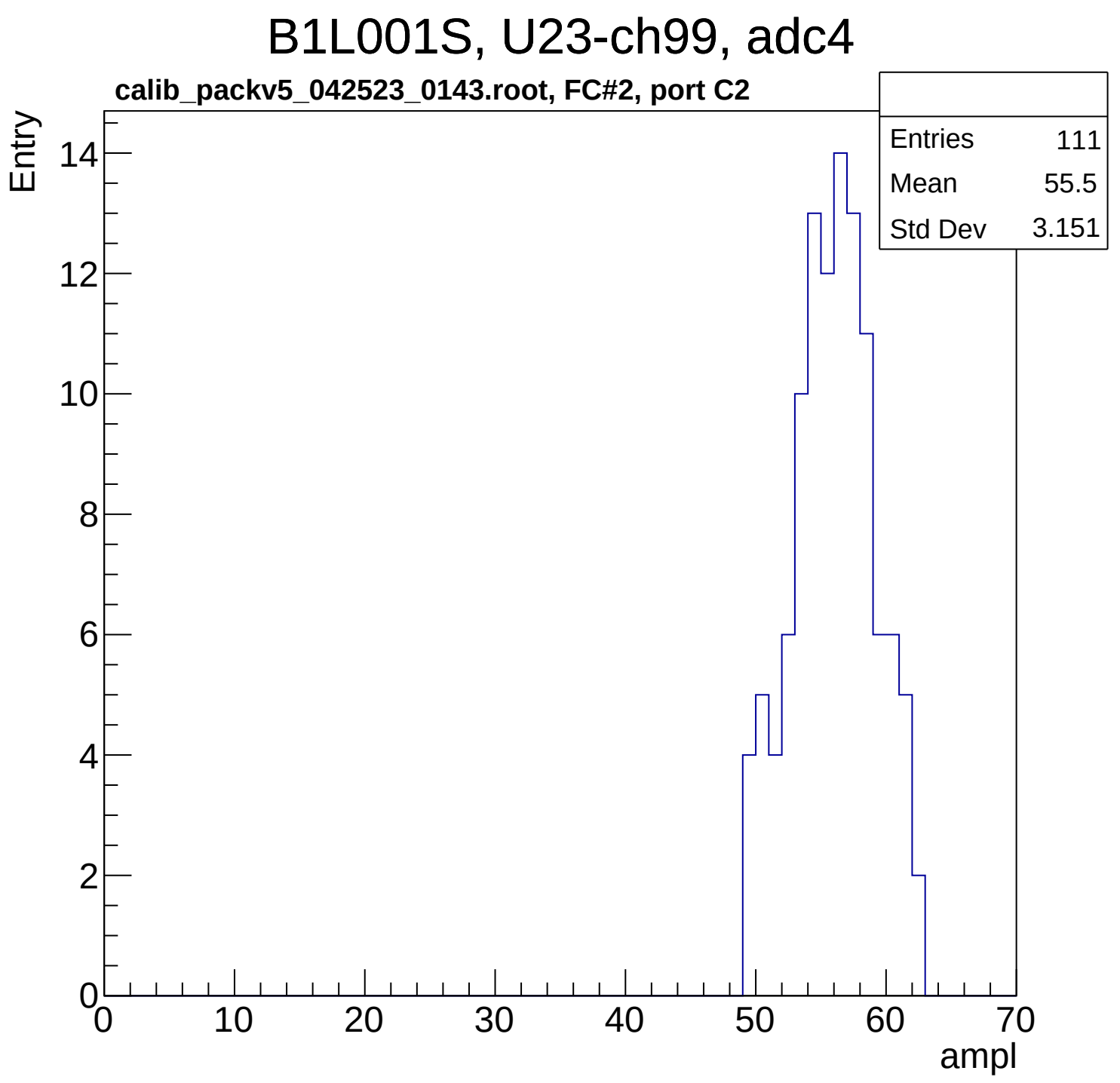
B1L001S, U23-ch99, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 111
Mean 55.5
Std Dev 3.151
Entry
ampl
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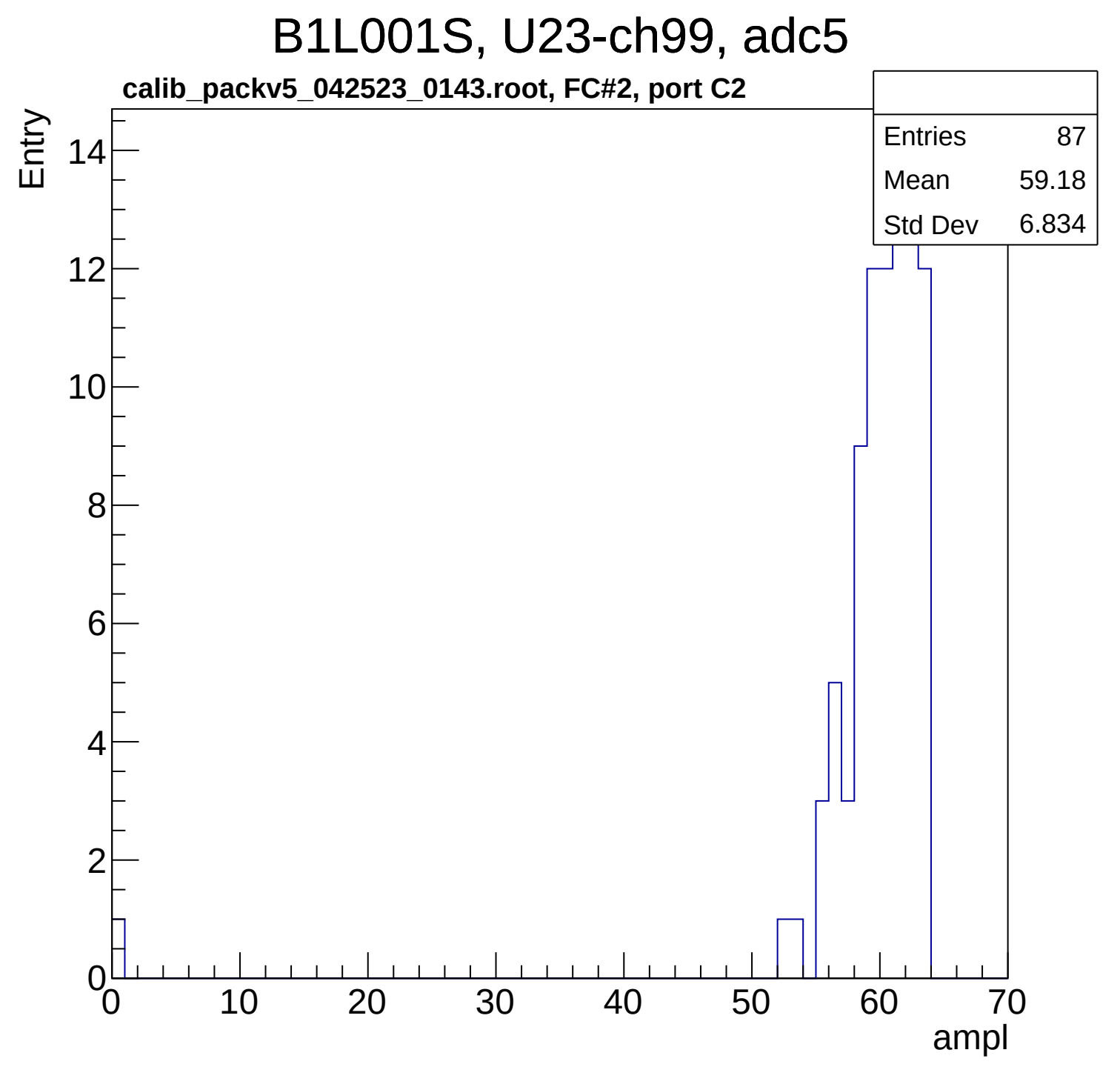
B1L001S, U23-ch99, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 87
Mean 59.18
Std Dev 6.834
Entry
ampl
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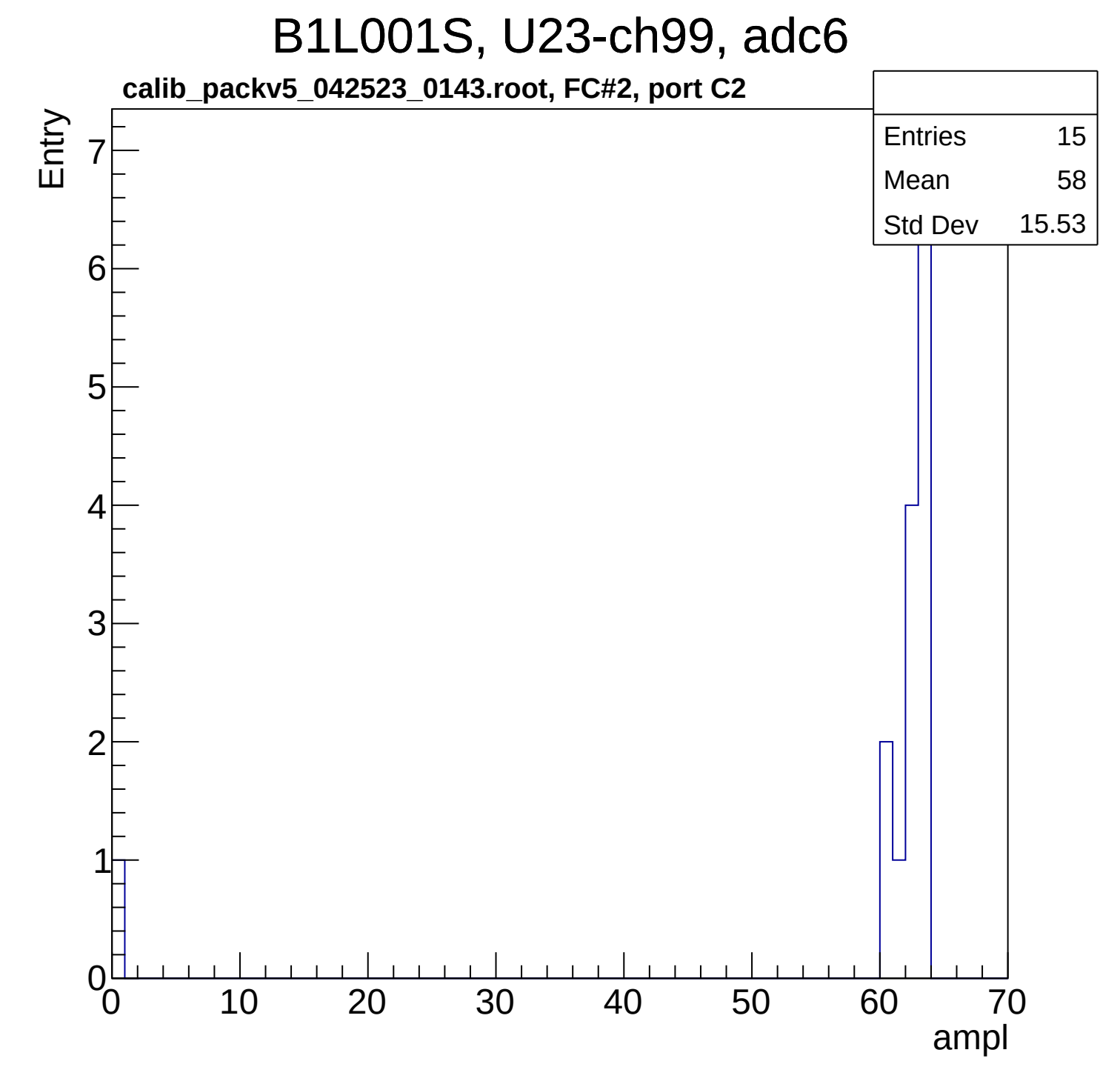
B1L001S, U23-ch99, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 58
Std Dev 15.53
Entry
ampl
0
1
2
3
4
5
6
7
0
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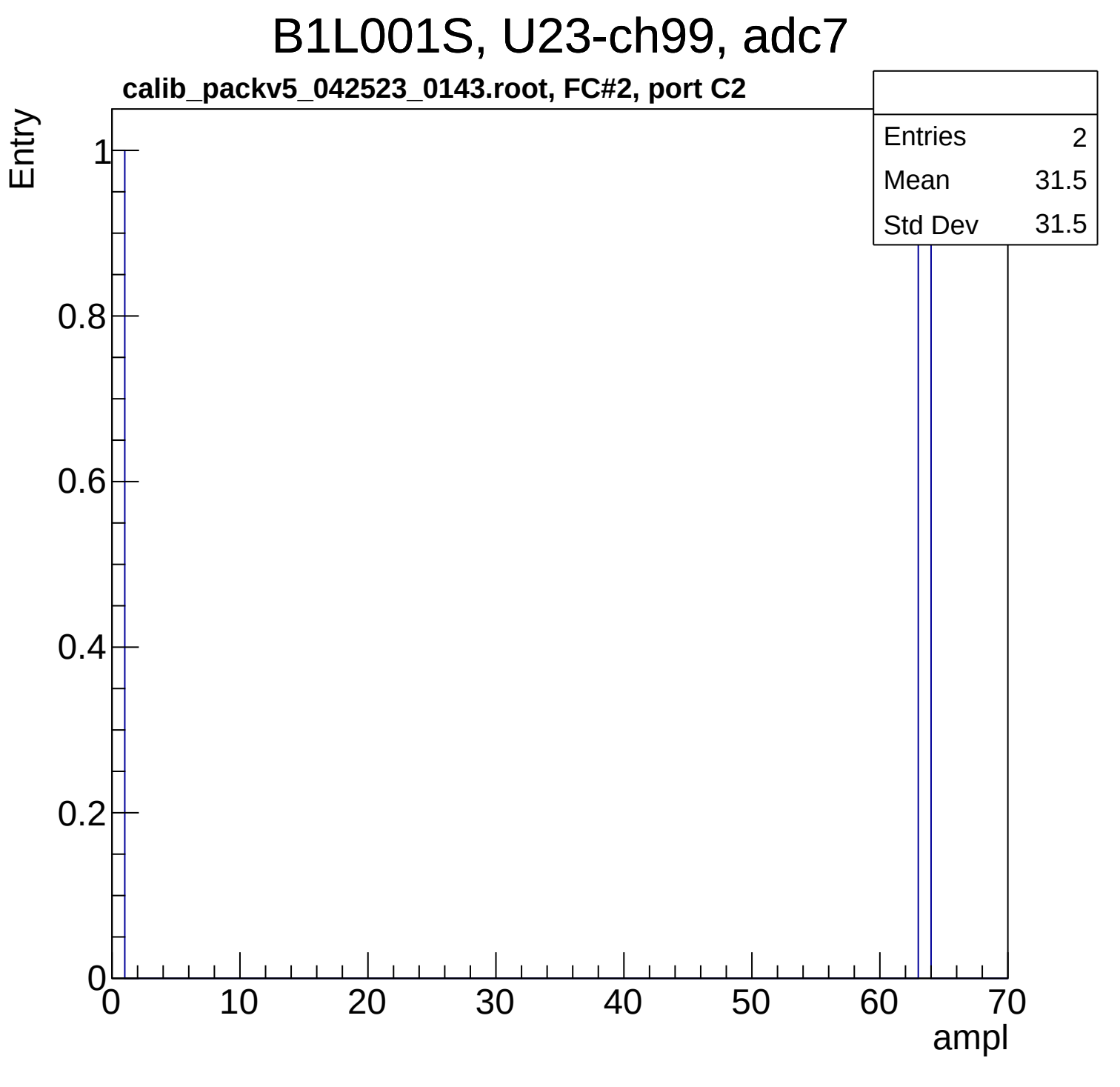
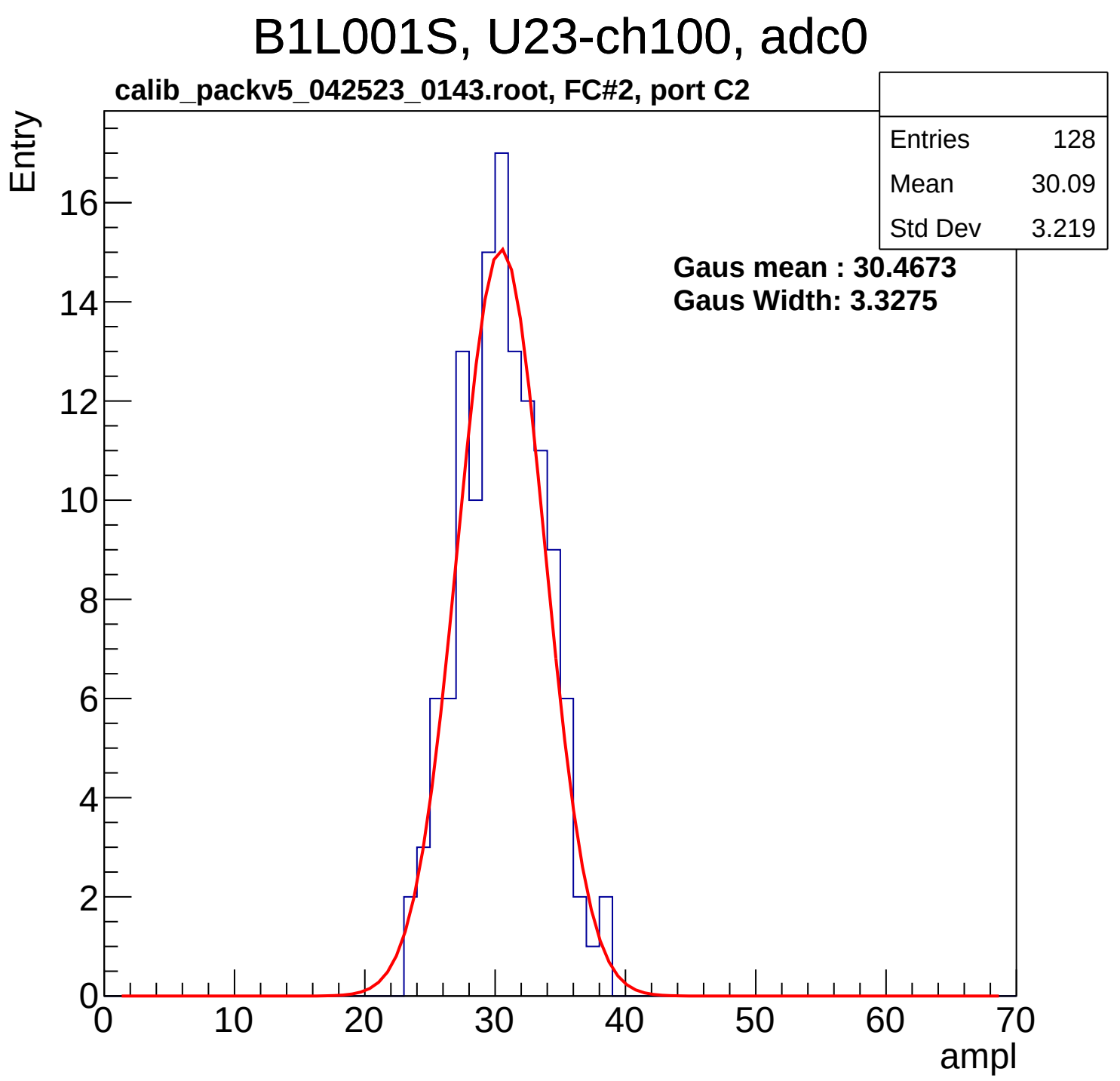
20
30
40
50
60
70


B1L001S, U23-ch99, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch100, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 30.09
Std Dev 3.219
Gaus mean : 30.4673
Gaus Width: 3.3275
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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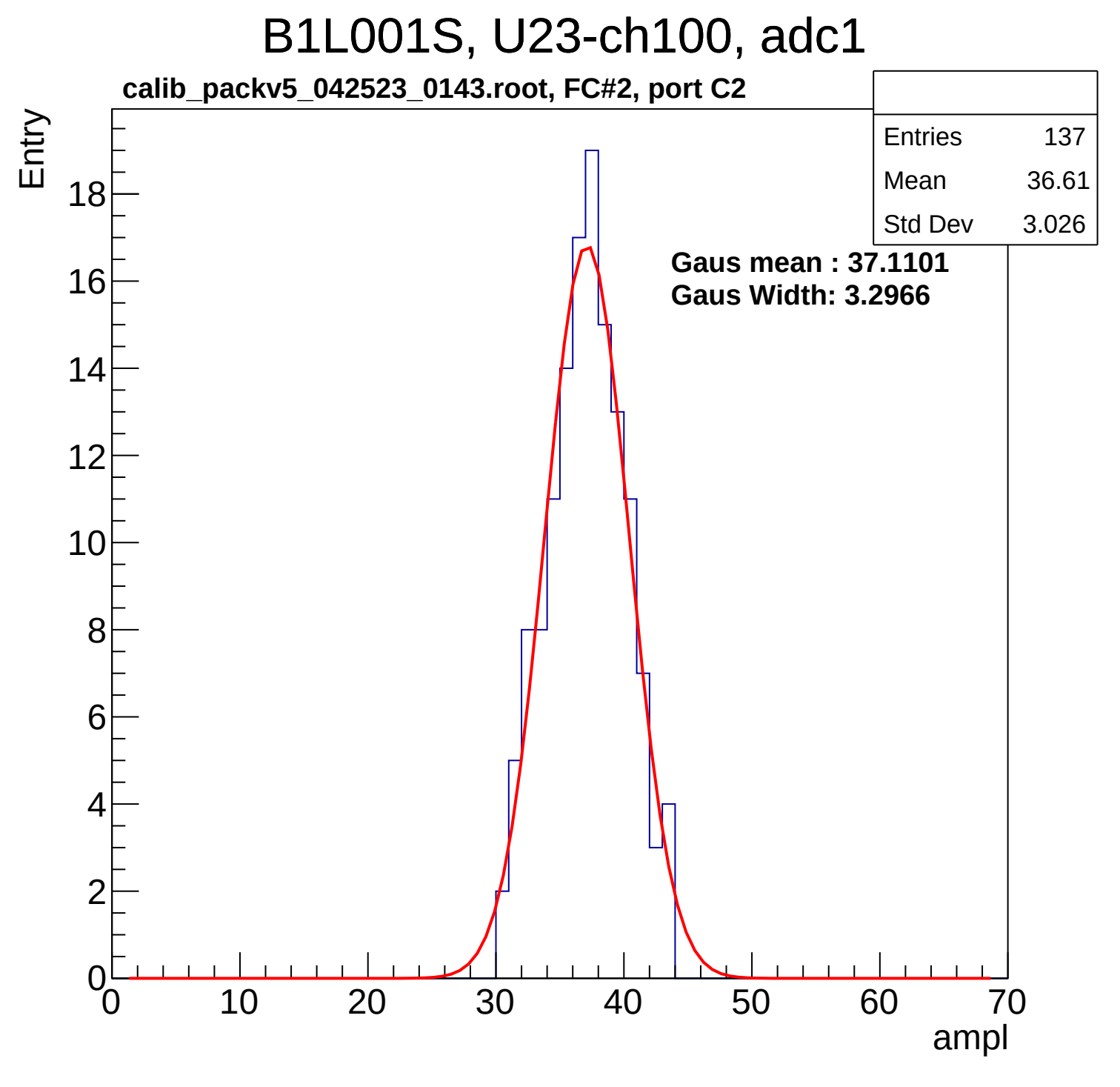
B1L001S, U23-ch100, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 36.61
Std Dev 3.026
Gaus mean : 37.1101
Gaus Width: 3.2966
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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30
40
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60
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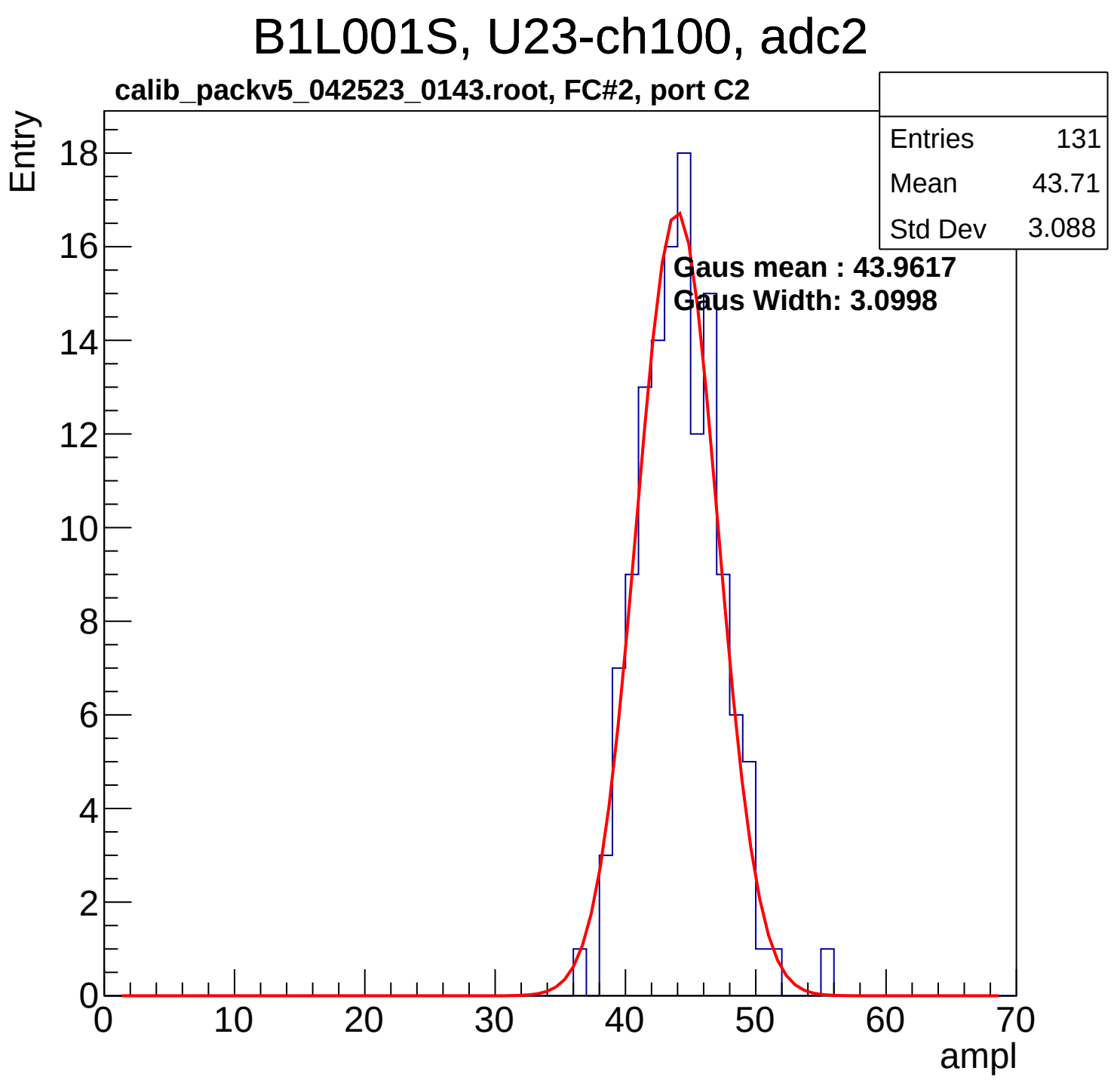
B1L001S, U23-ch100, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 43.71
Std Dev 3.088
Gaus mean : 43.9617
Gaus Width: 3.0998
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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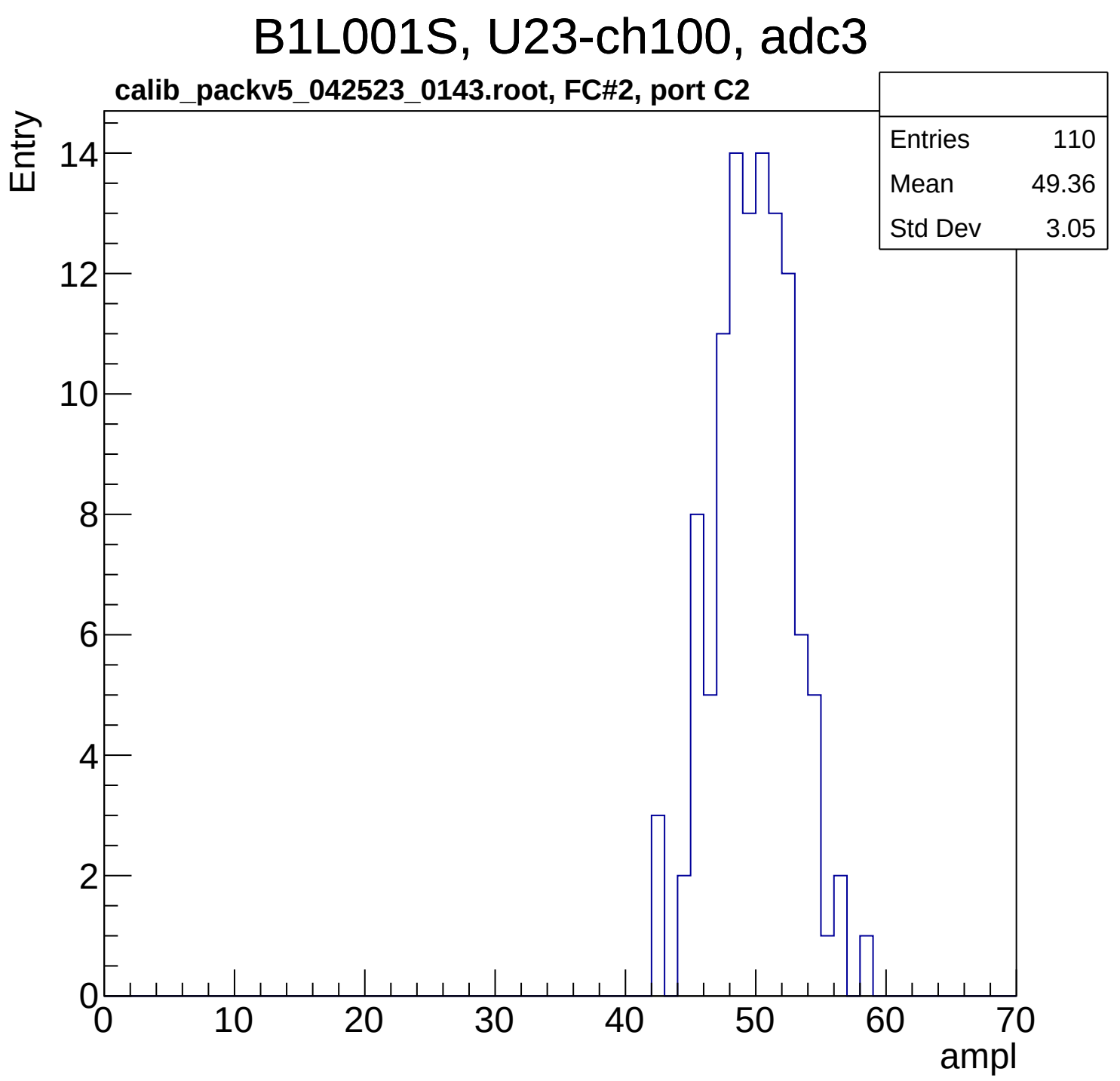
B1L001S, U23-ch100, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 49.36
Std Dev 3.05
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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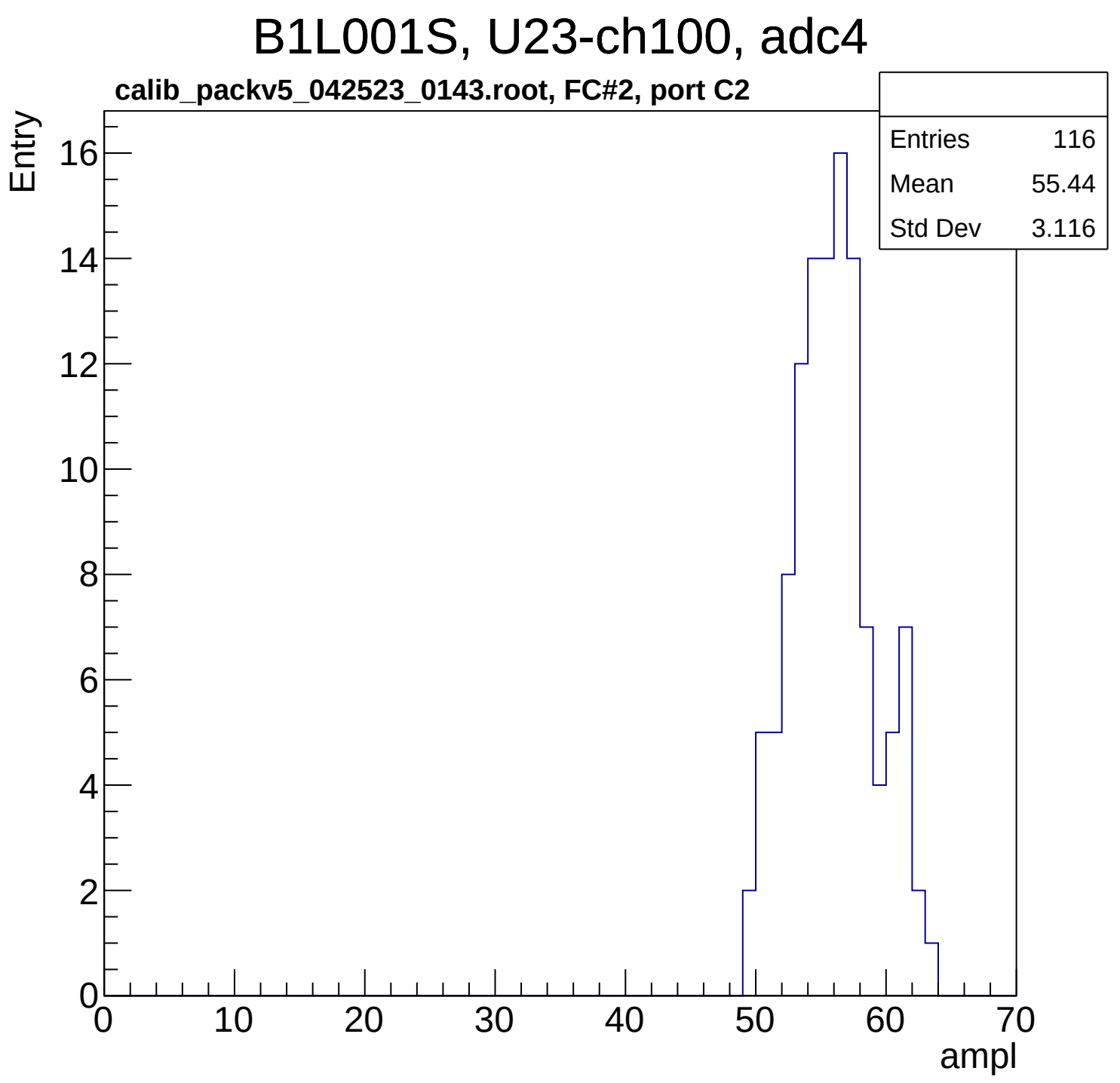
B1L001S, U23-ch100, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 55.44
Std Dev 3.116
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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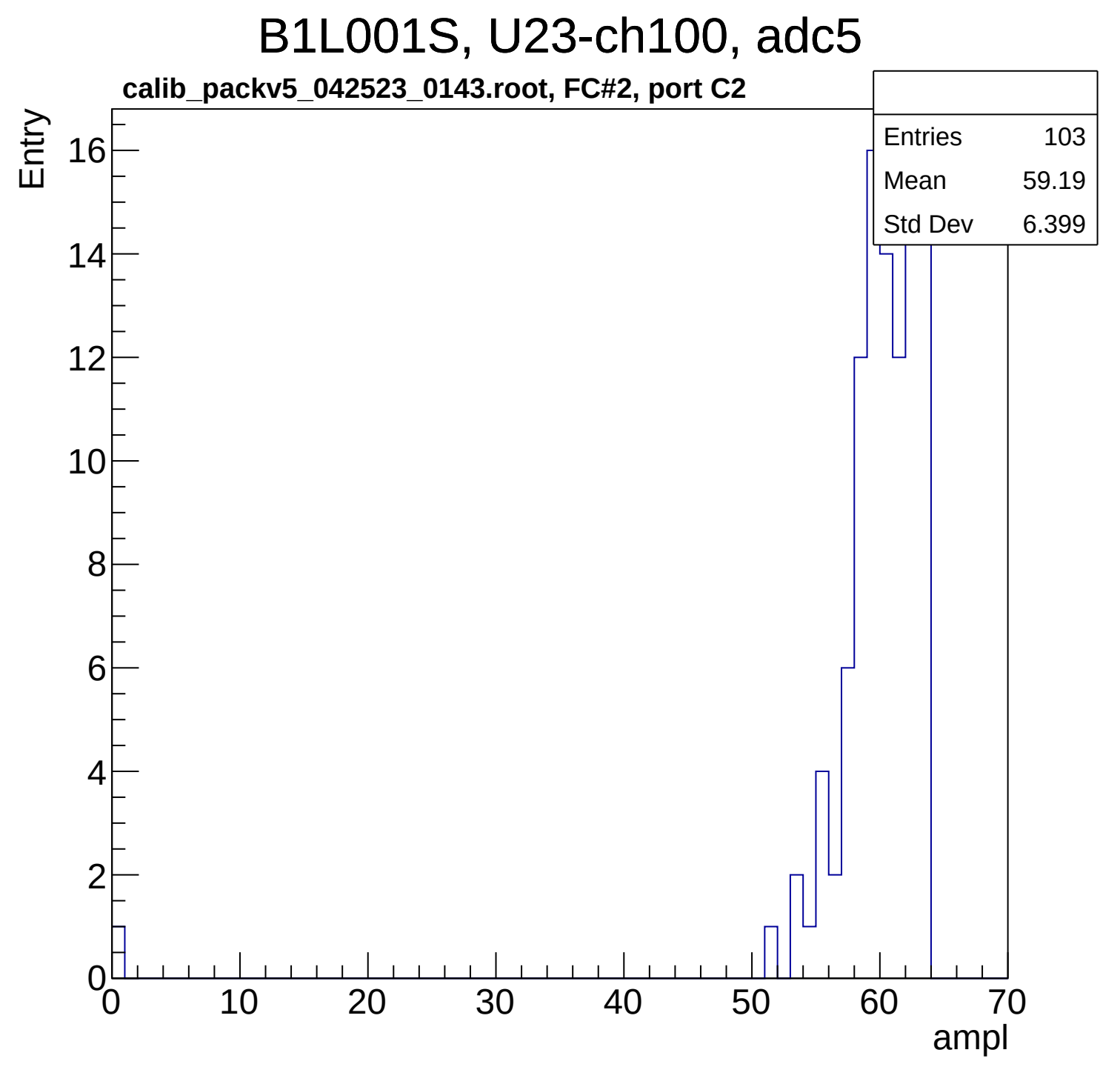
B1L001S, U23-ch100, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 59.19
Std Dev 6.399
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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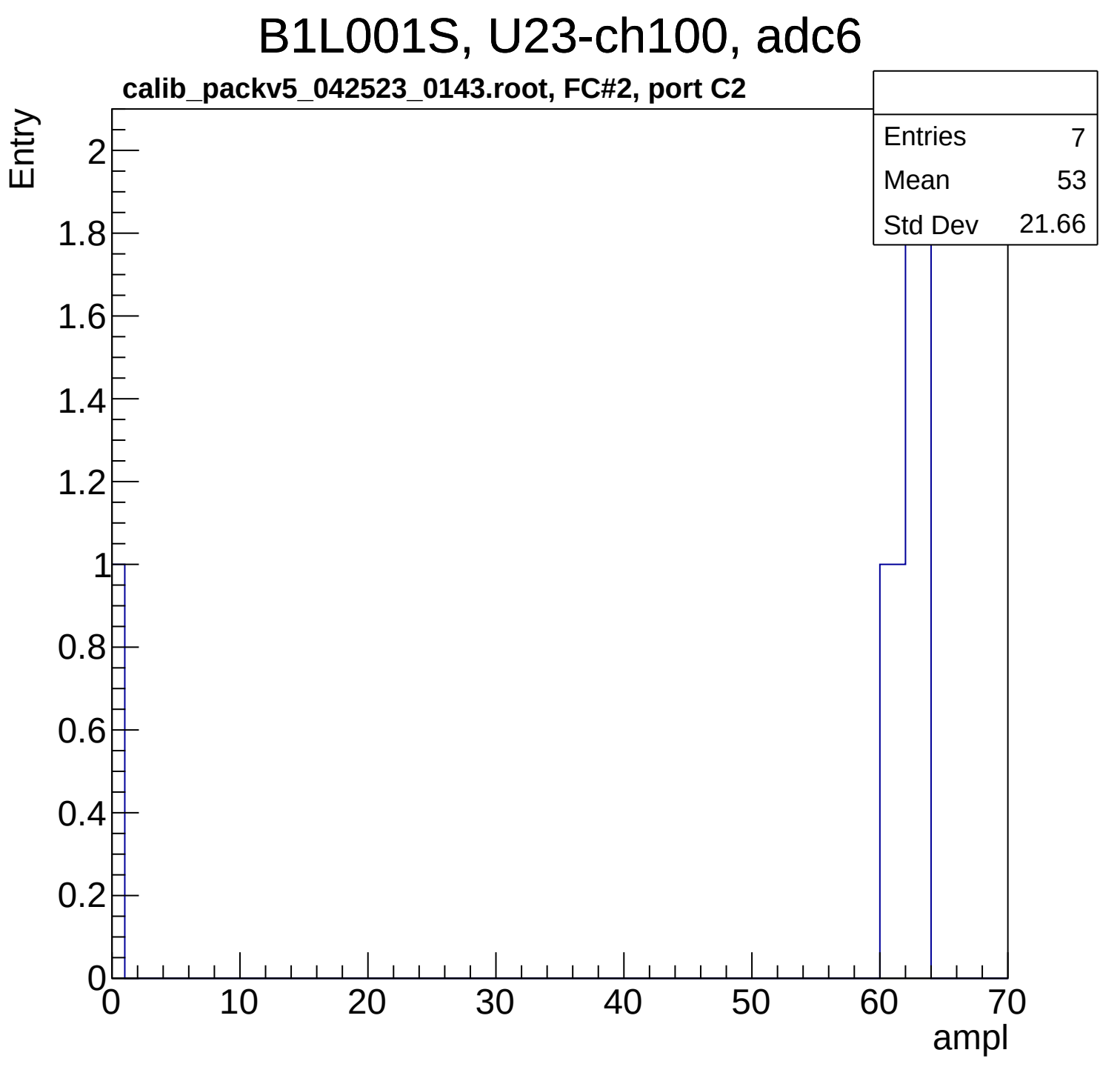
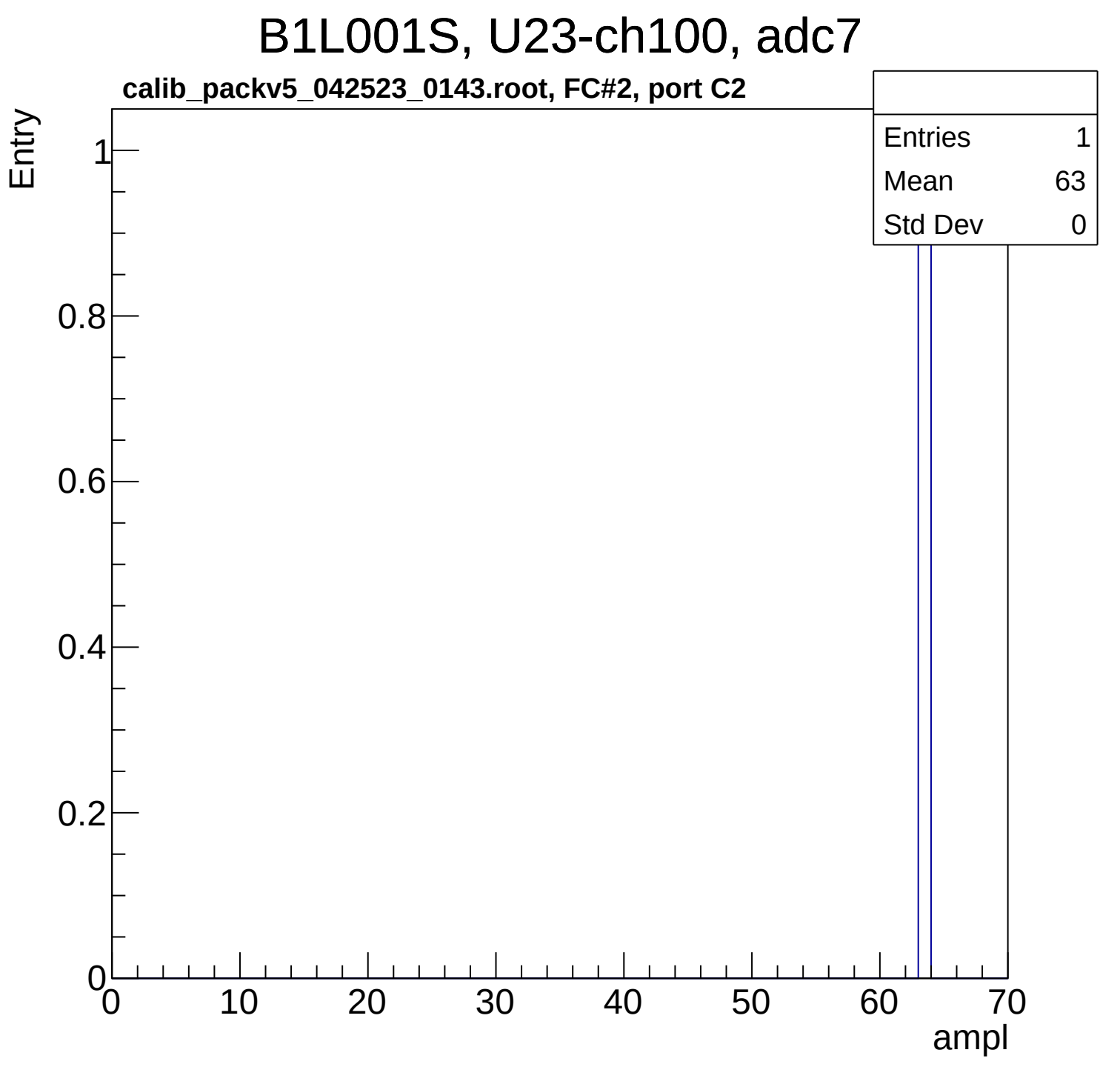

B1L001S, U23-ch100, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53
Std Dev 21.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch100, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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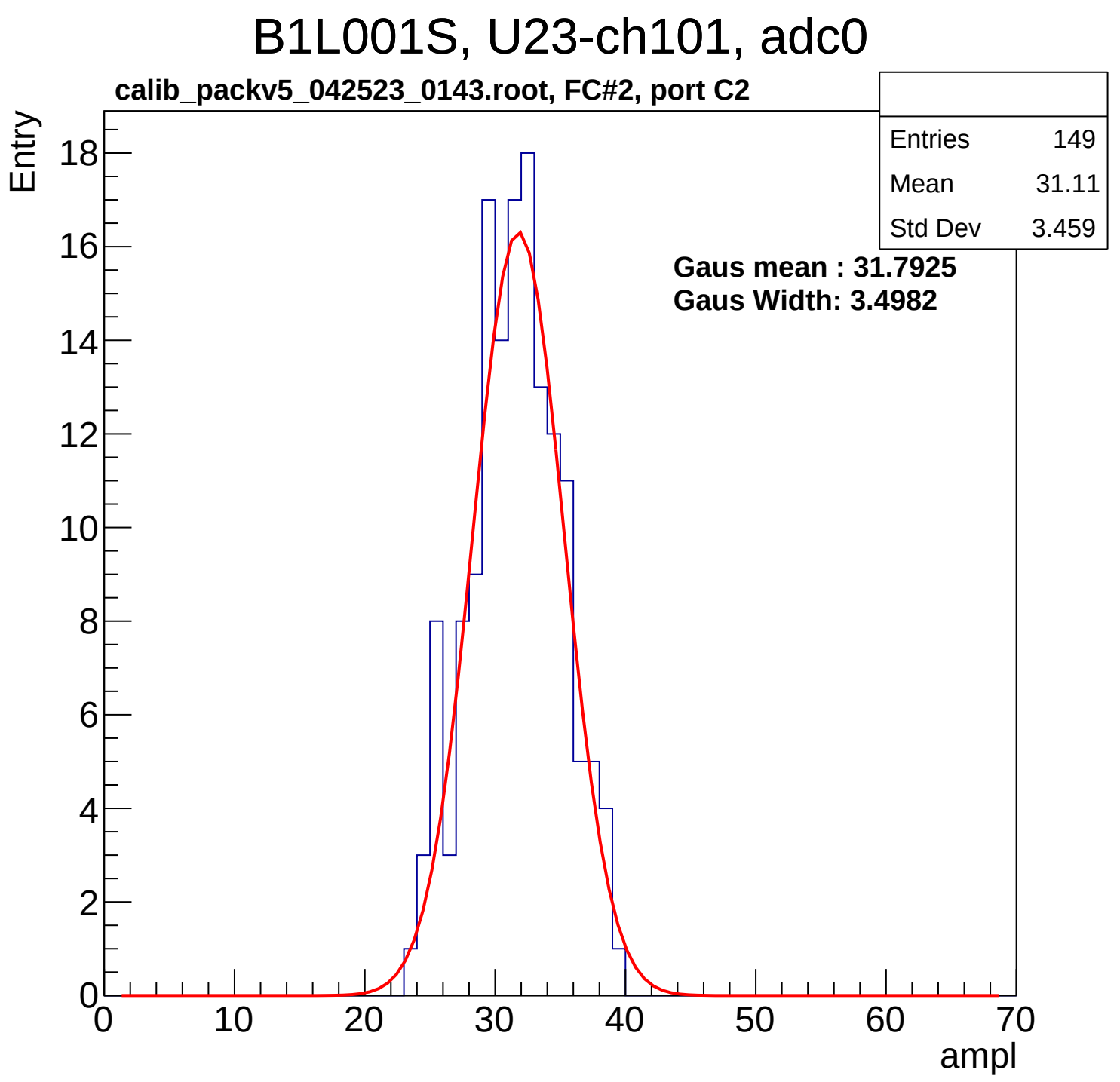
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch101, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 31.11
Std Dev 3.459
Gaus mean : 31.7925
Gaus Width: 3.4982
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
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20
30
40
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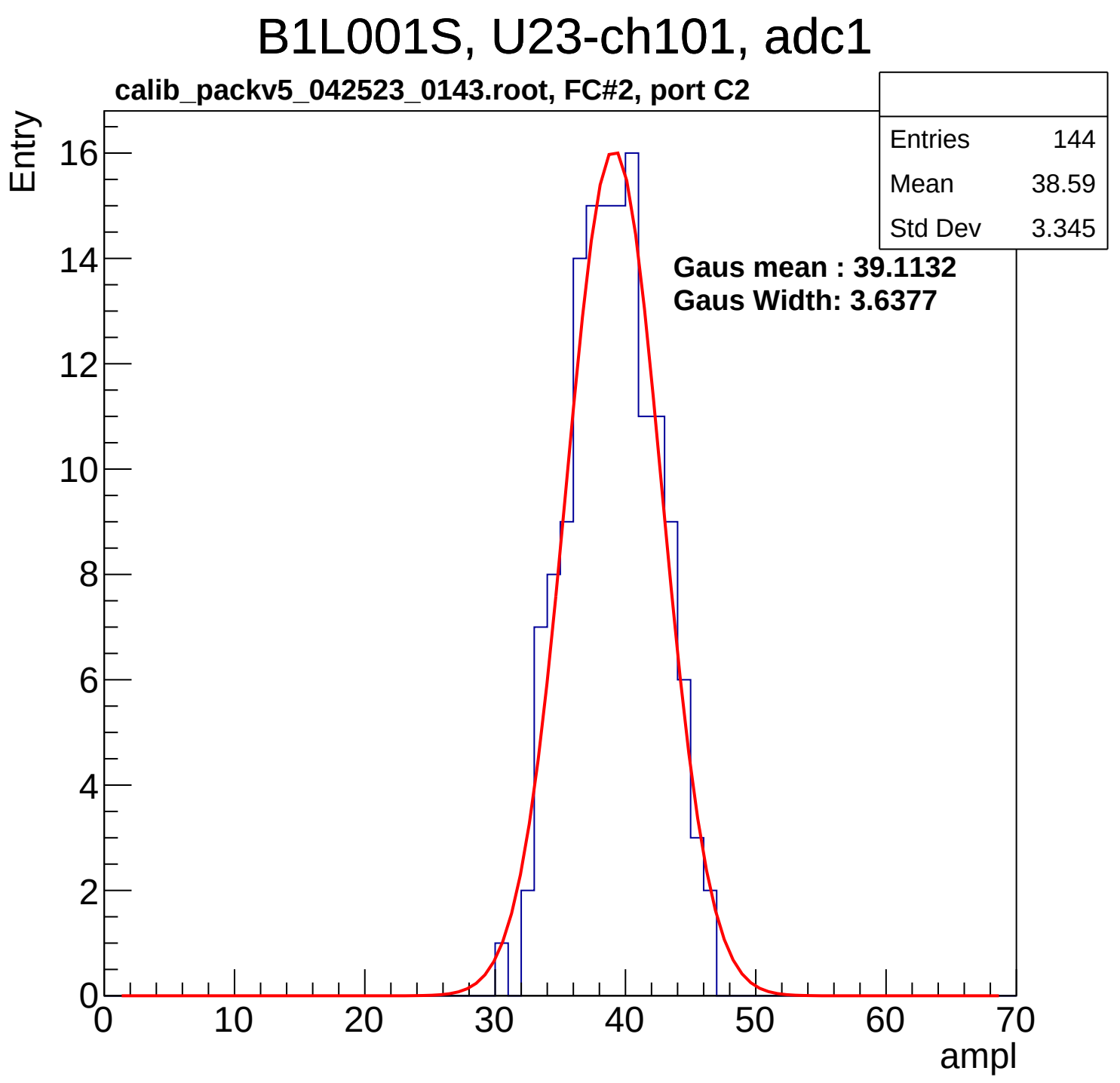
B1L001S, U23-ch101, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 38.59
Std Dev 3.345
Gaus mean : 39.1132
Gaus Width: 3.6377
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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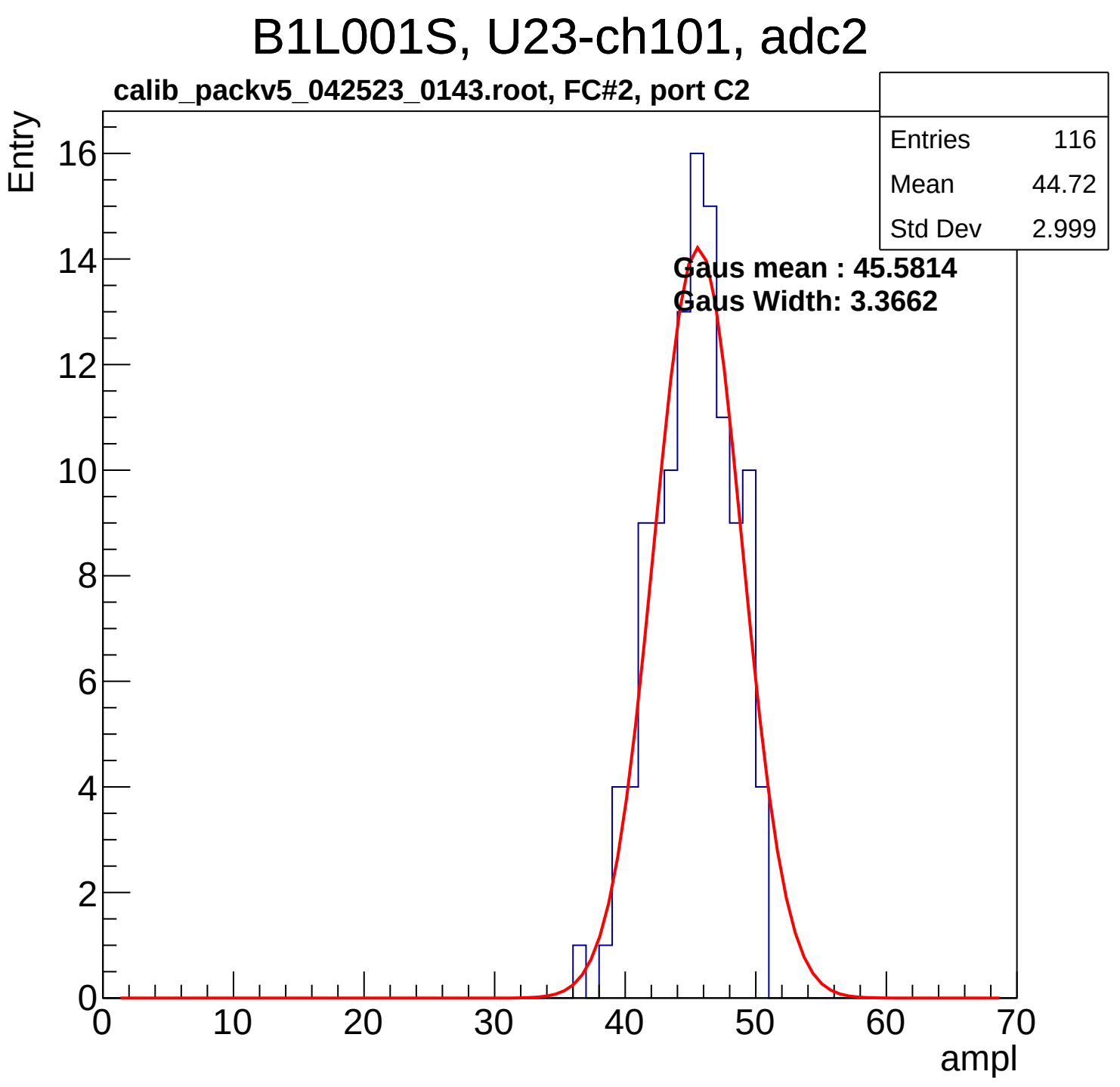
B1L001S, U23-ch101, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 44.72
Std Dev 2.999
Gaus mean : 45.5814
Gaus Width: 3.3662
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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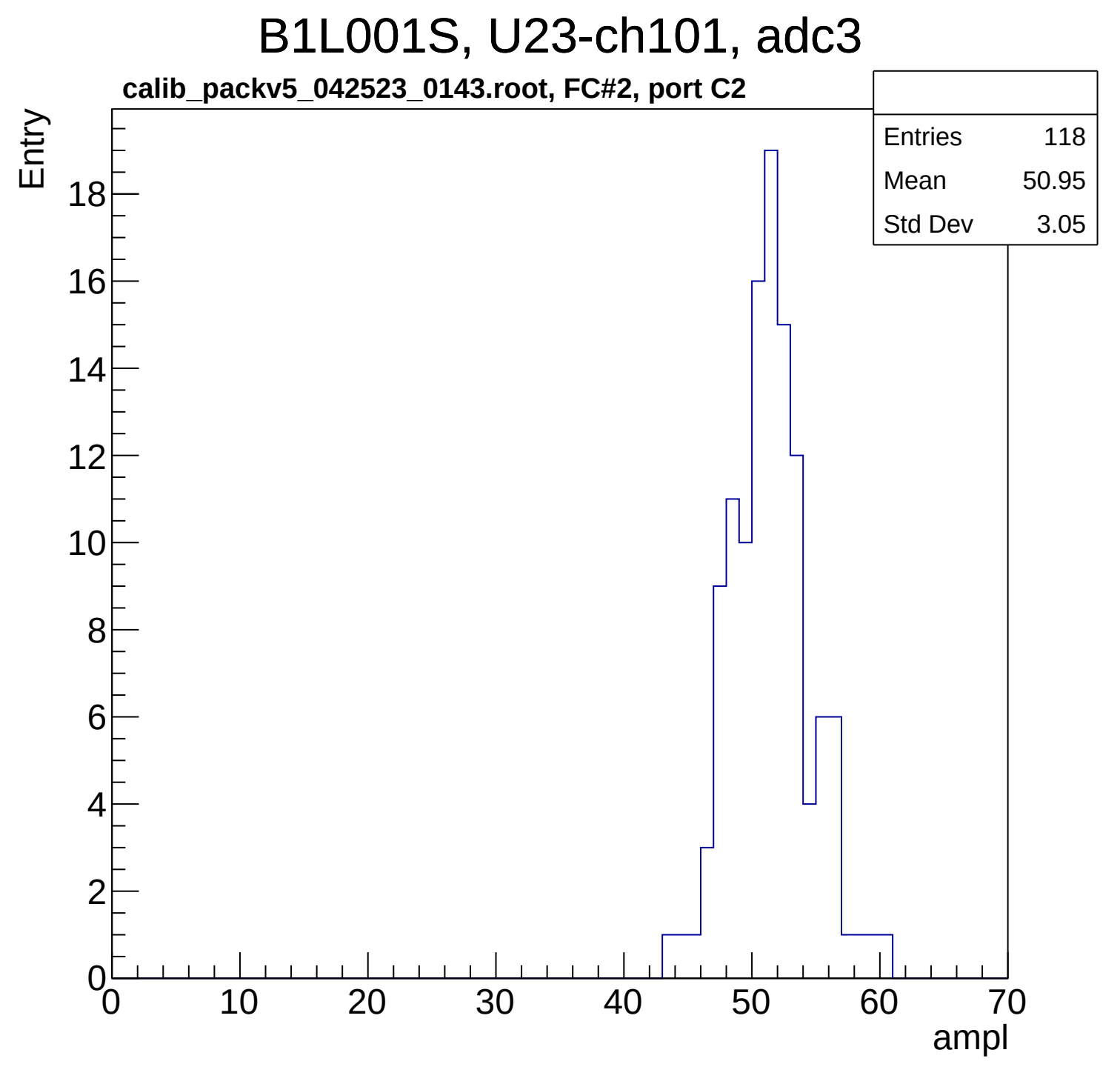
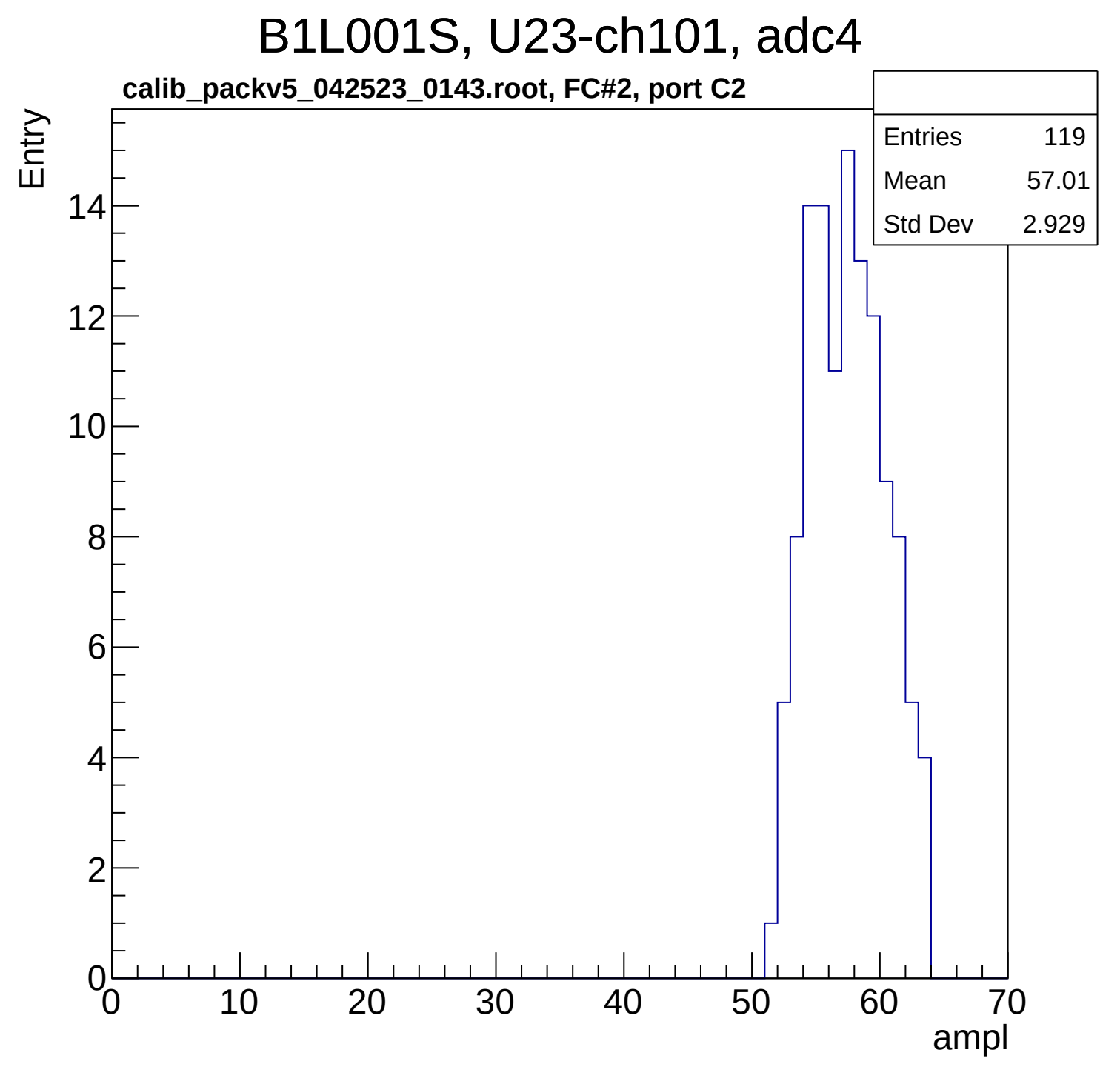

B1L001S, U23-ch101, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 50.95
Std Dev 3.05
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch101, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 57.01
Std Dev 2.929
Entry
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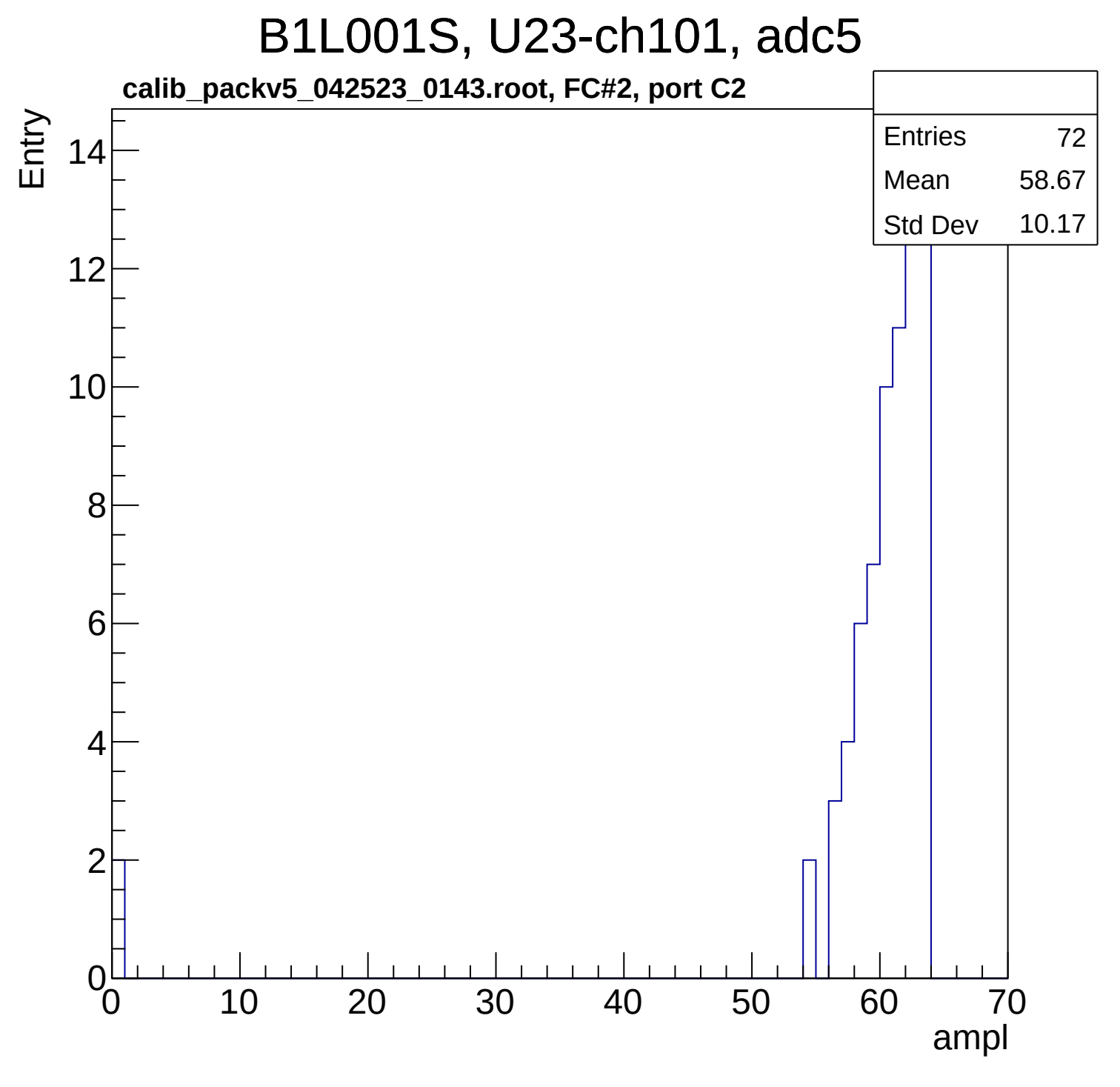
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch101, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 58.67
Std Dev 10.17
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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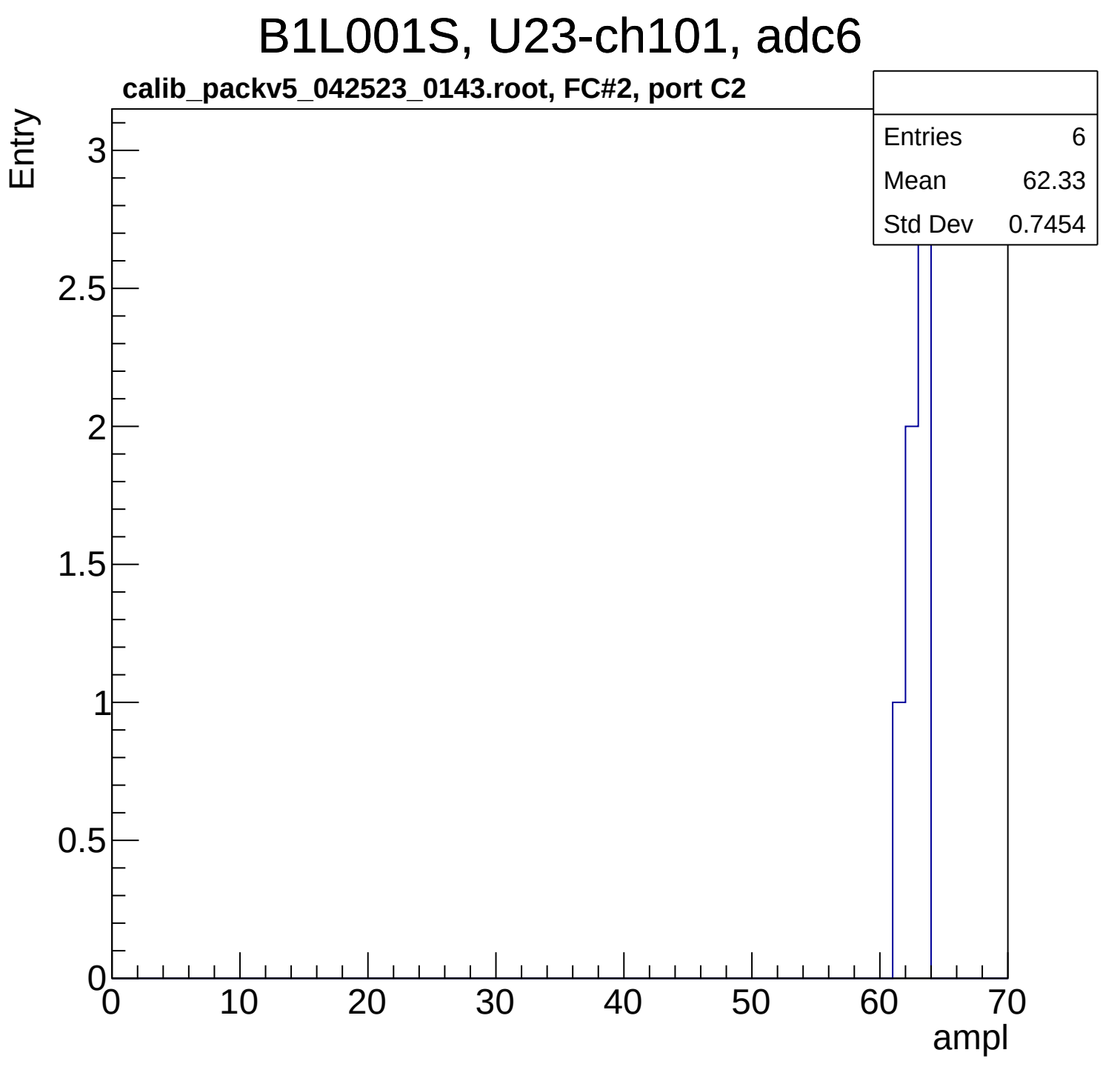
B1L001S, U23-ch101, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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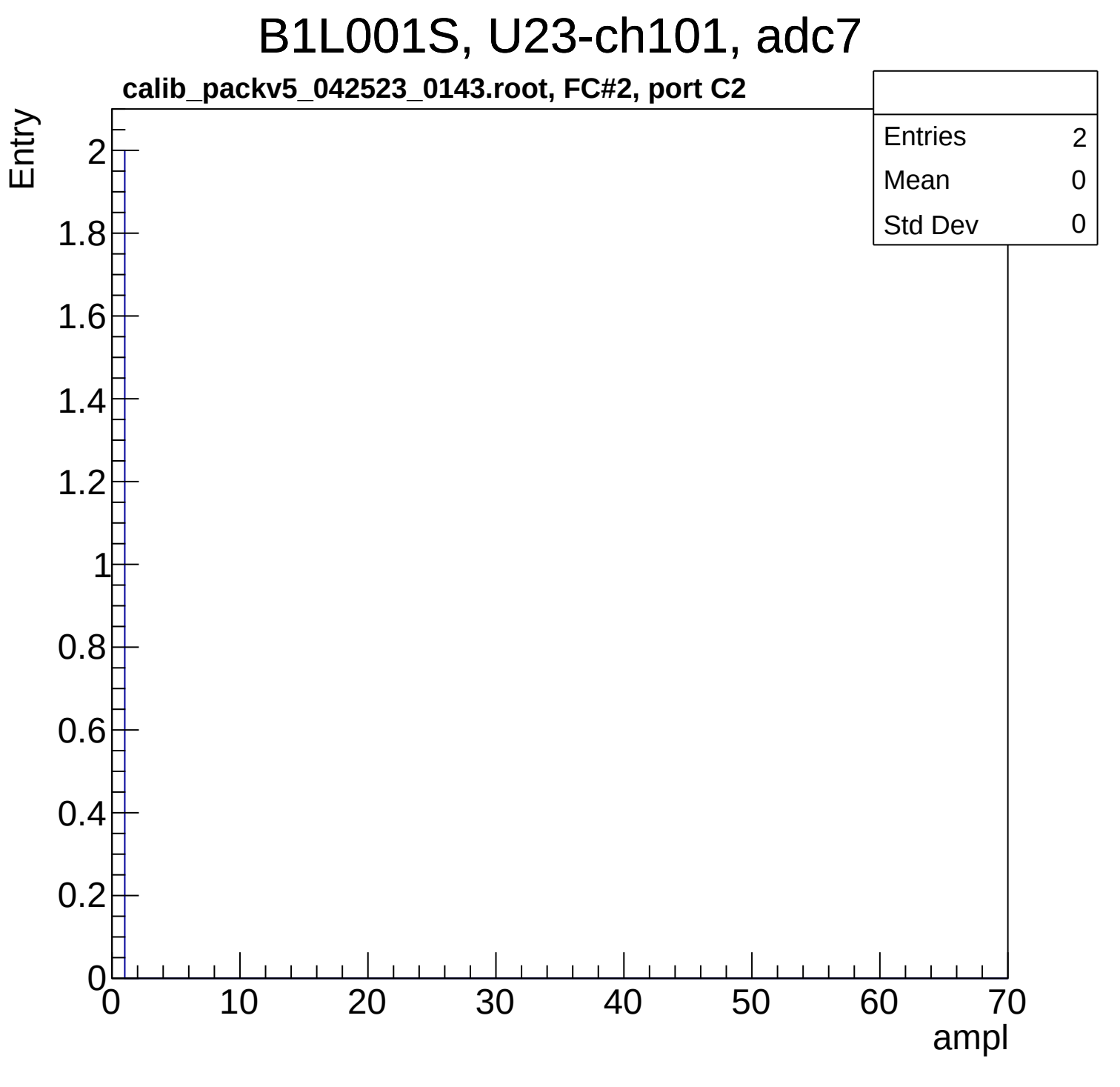
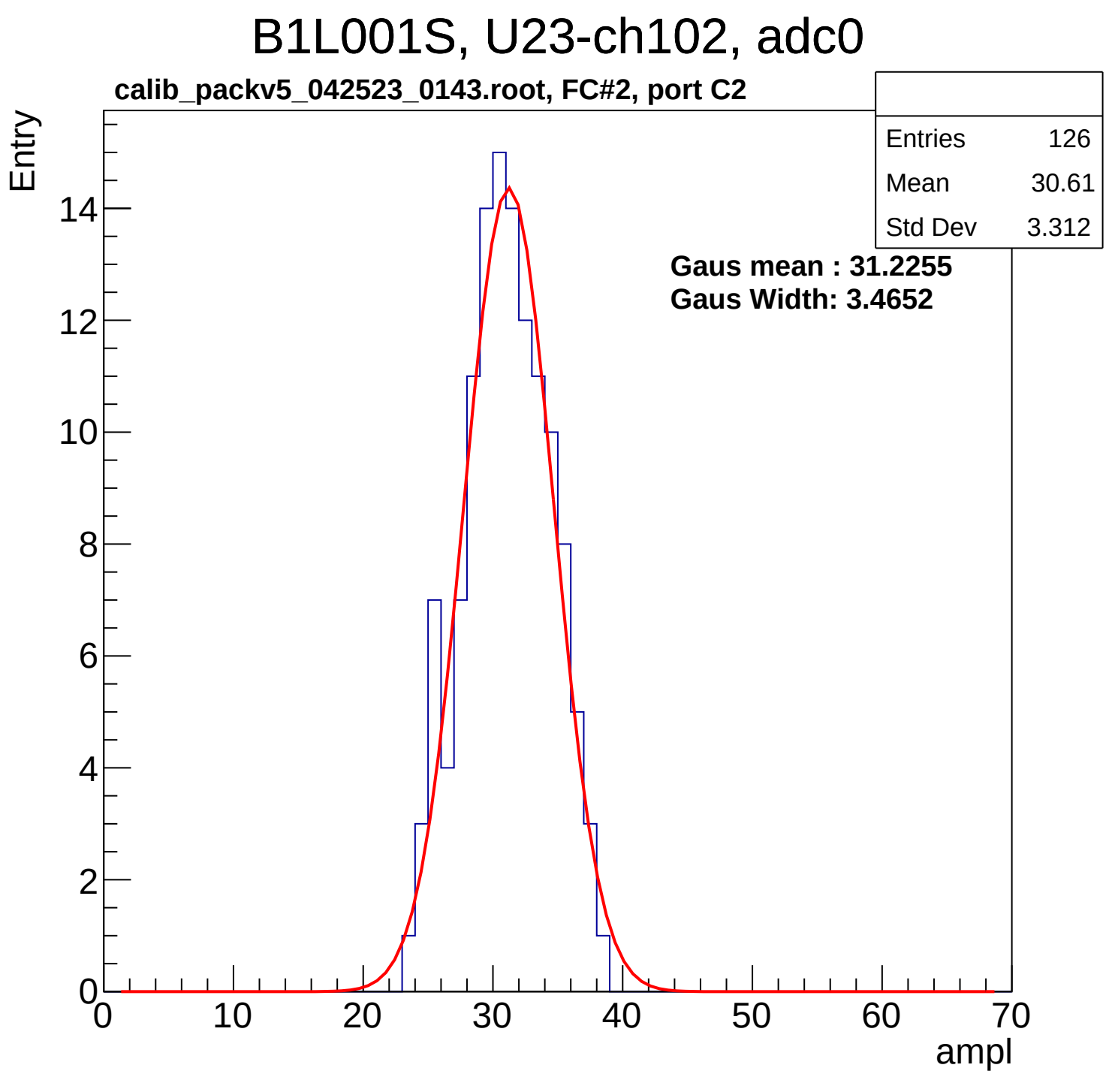
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch101, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch102, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 30.61
Std Dev 3.312
Gaus mean : 31.2255
Gaus Width: 3.4652
Entry
ampl
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6
8
10
12
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0
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20
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60
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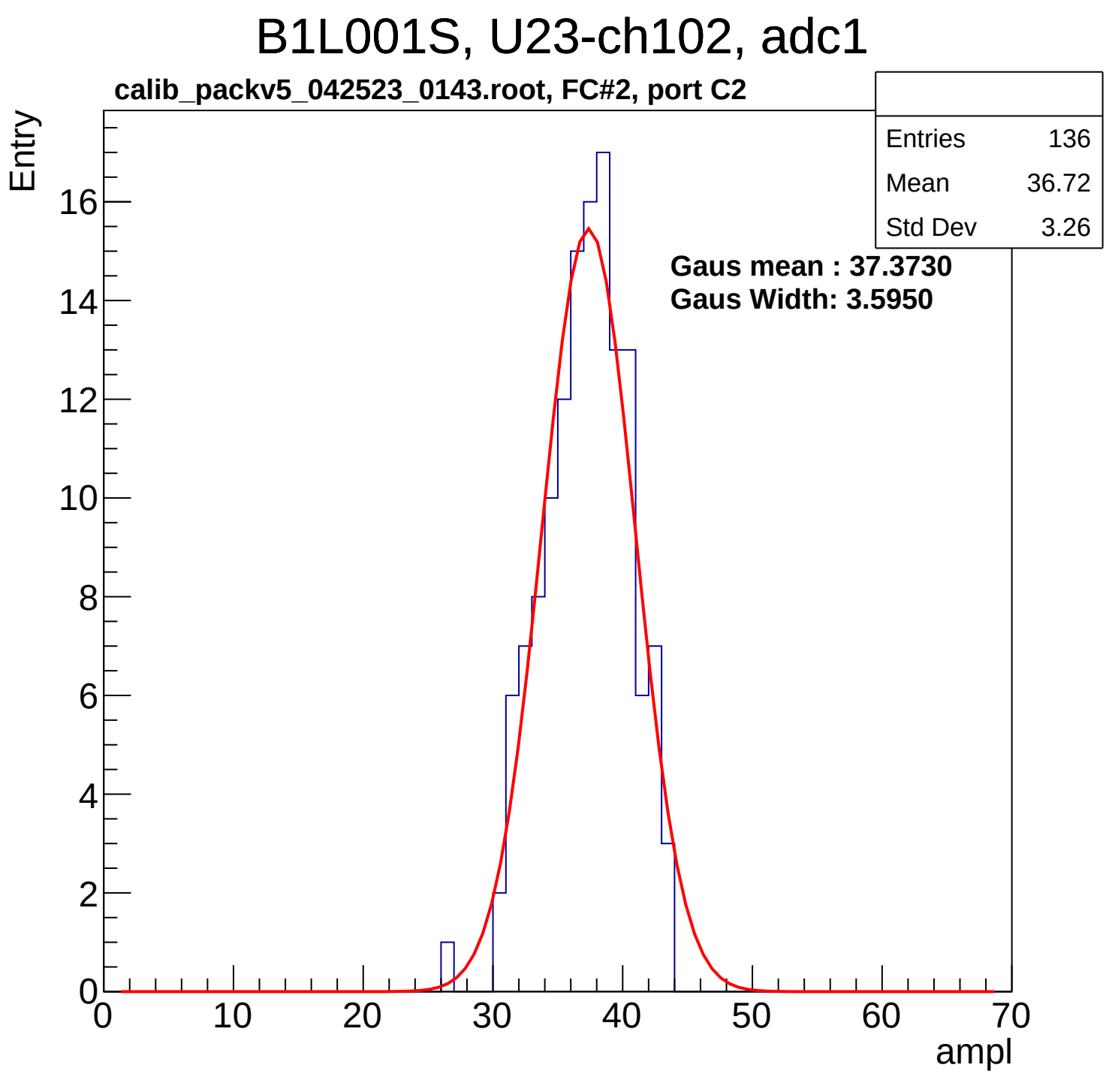
B1L001S, U23-ch102, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 36.72
Std Dev 3.26
Gaus mean : 37.3730
Gaus Width: 3.5950
Entry
ampl
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2
4
6
8
10
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0
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20
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40
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60
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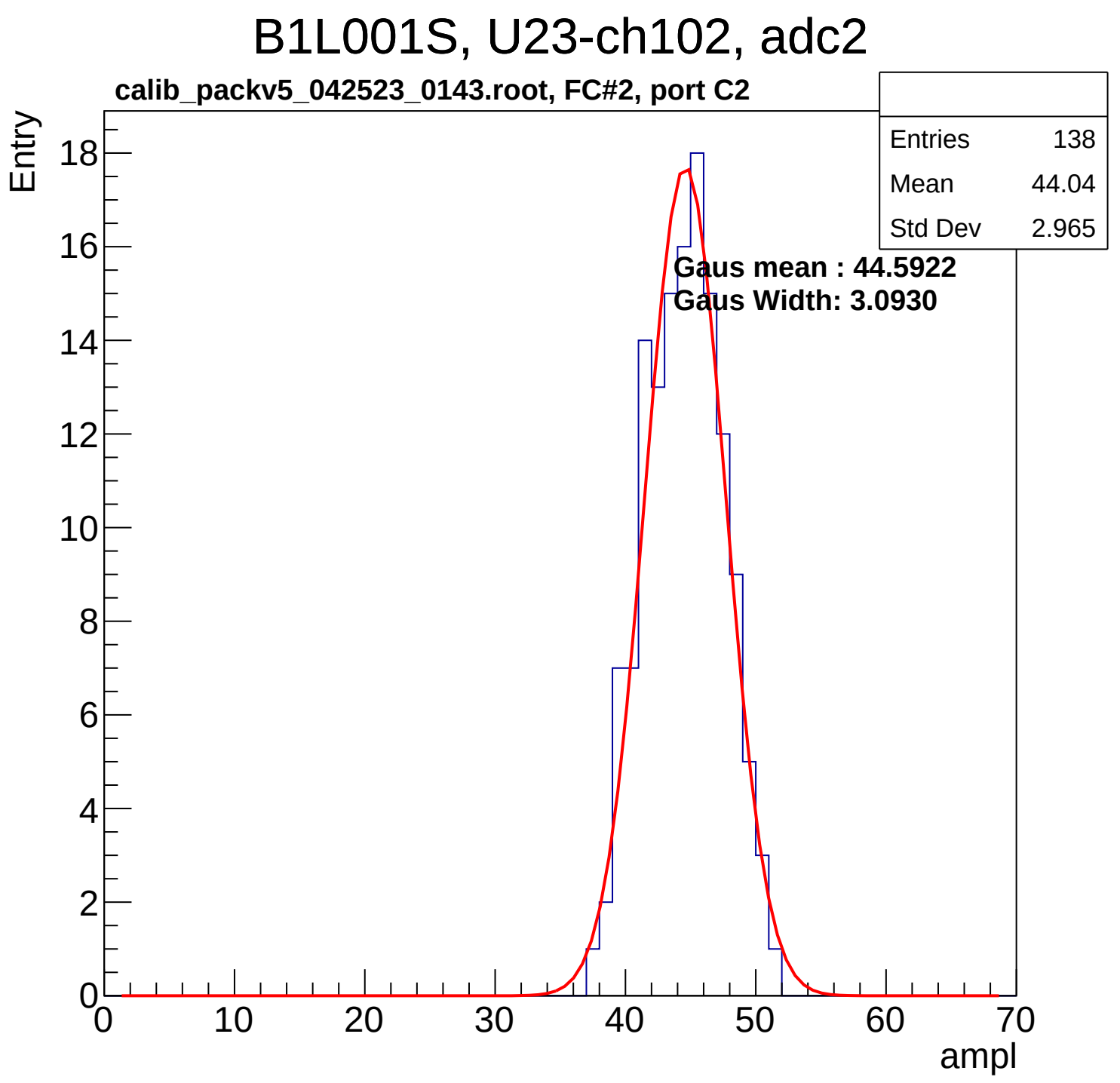
B1L001S, U23-ch102, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 44.04
Std Dev 2.965
Gaus mean : 44.5922
Gaus Width: 3.0930
Entry
ampl
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6
8
10
12
14
16
18
0
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20
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60
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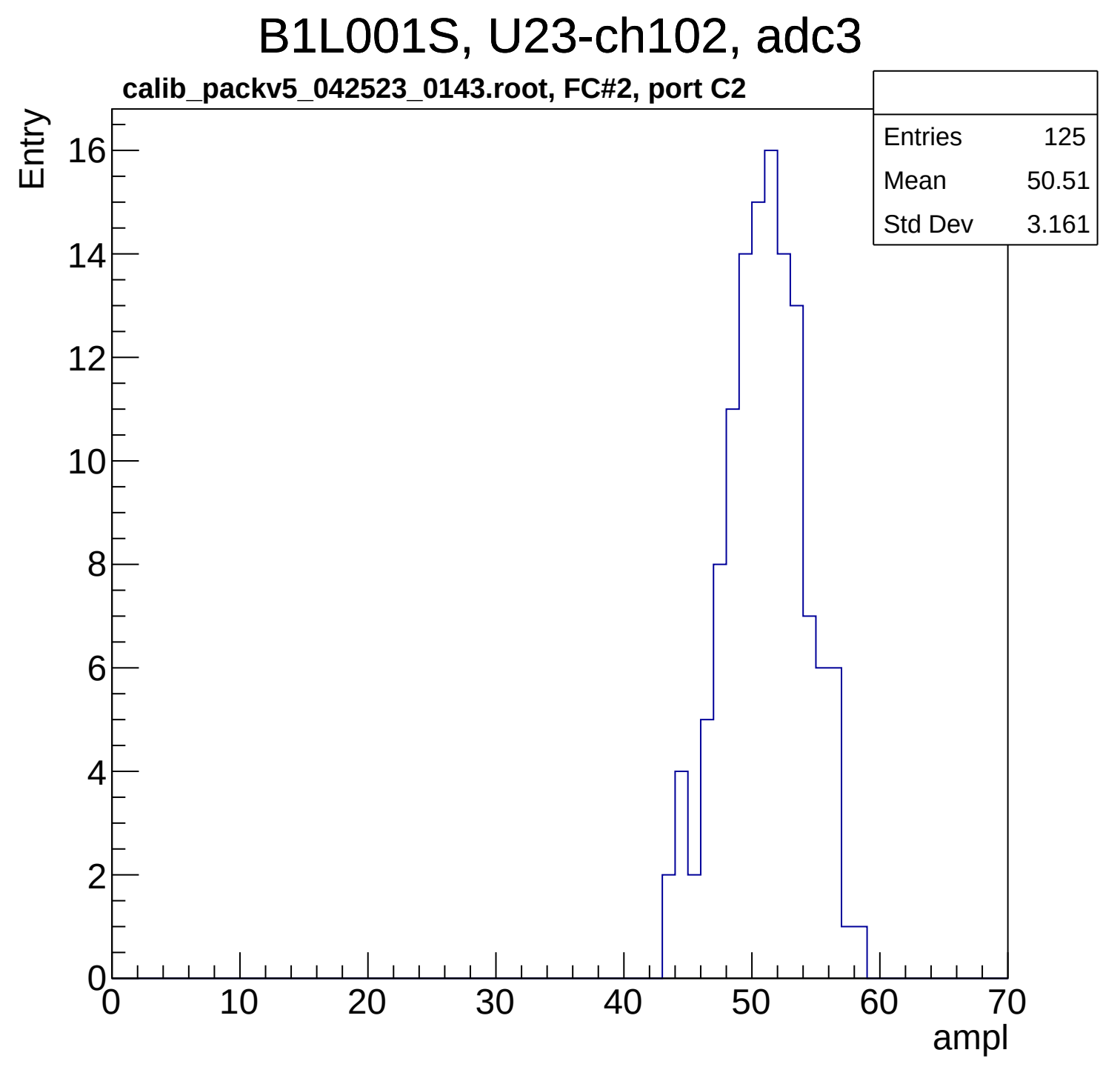
B1L001S, U23-ch102, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 50.51
Std Dev 3.161
Entry
ampl
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10
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16
0
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60
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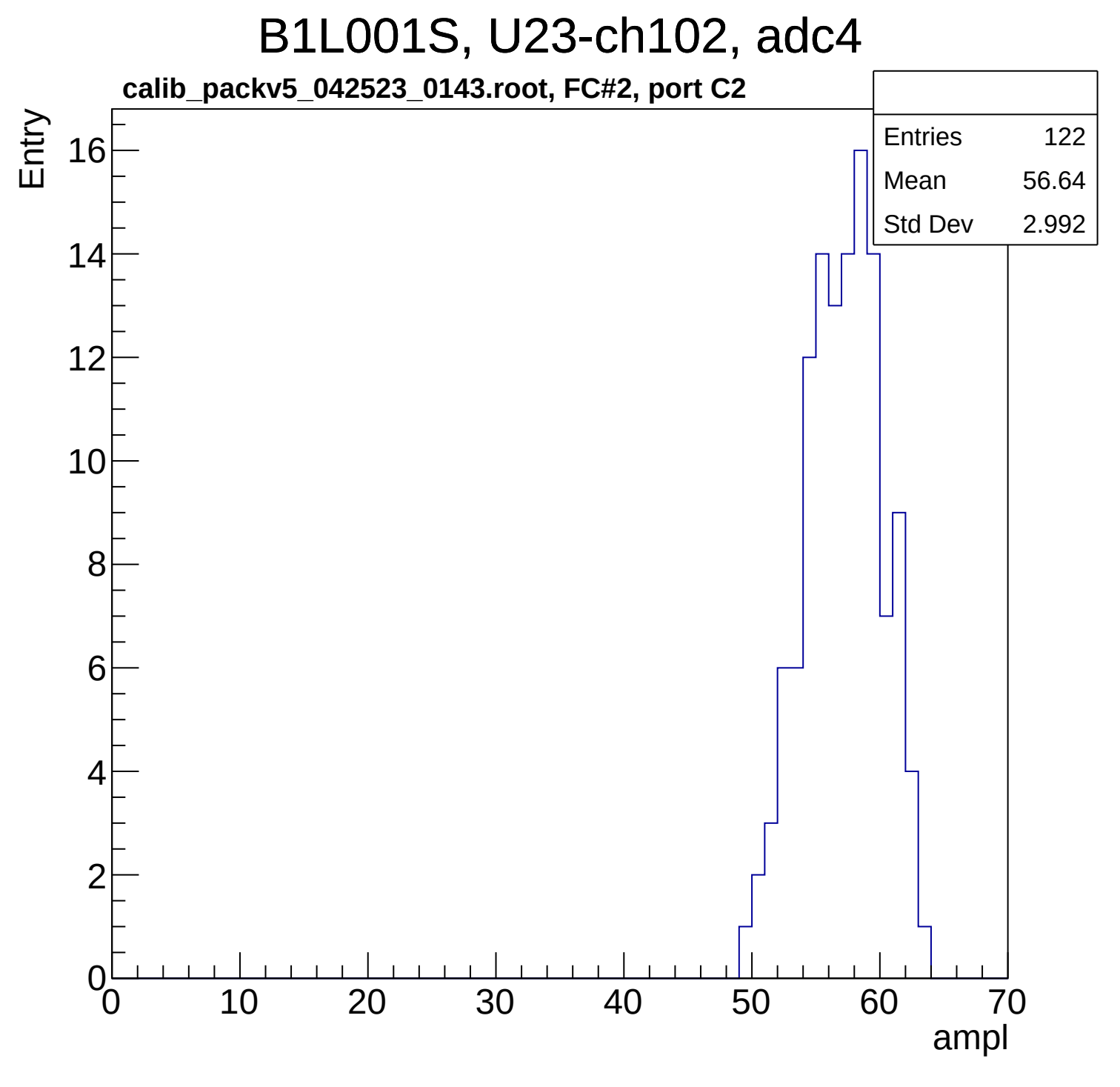
B1L001S, U23-ch102, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 56.64
Std Dev 2.992
Entry
ampl
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10
12
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0
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20
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60
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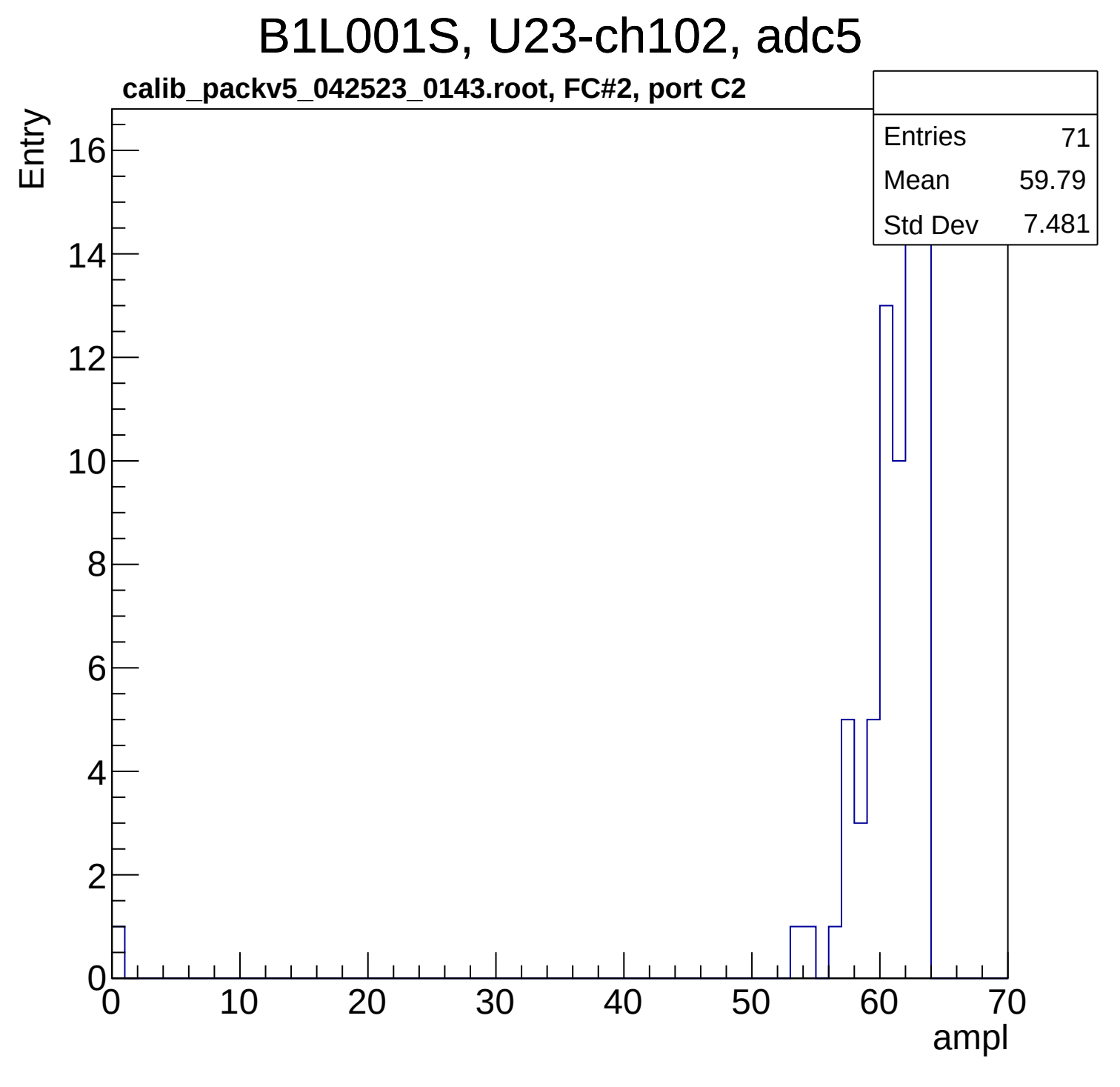
B1L001S, U23-ch102, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 71
Mean 59.79
Std Dev 7.481
Entry
ampl
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0
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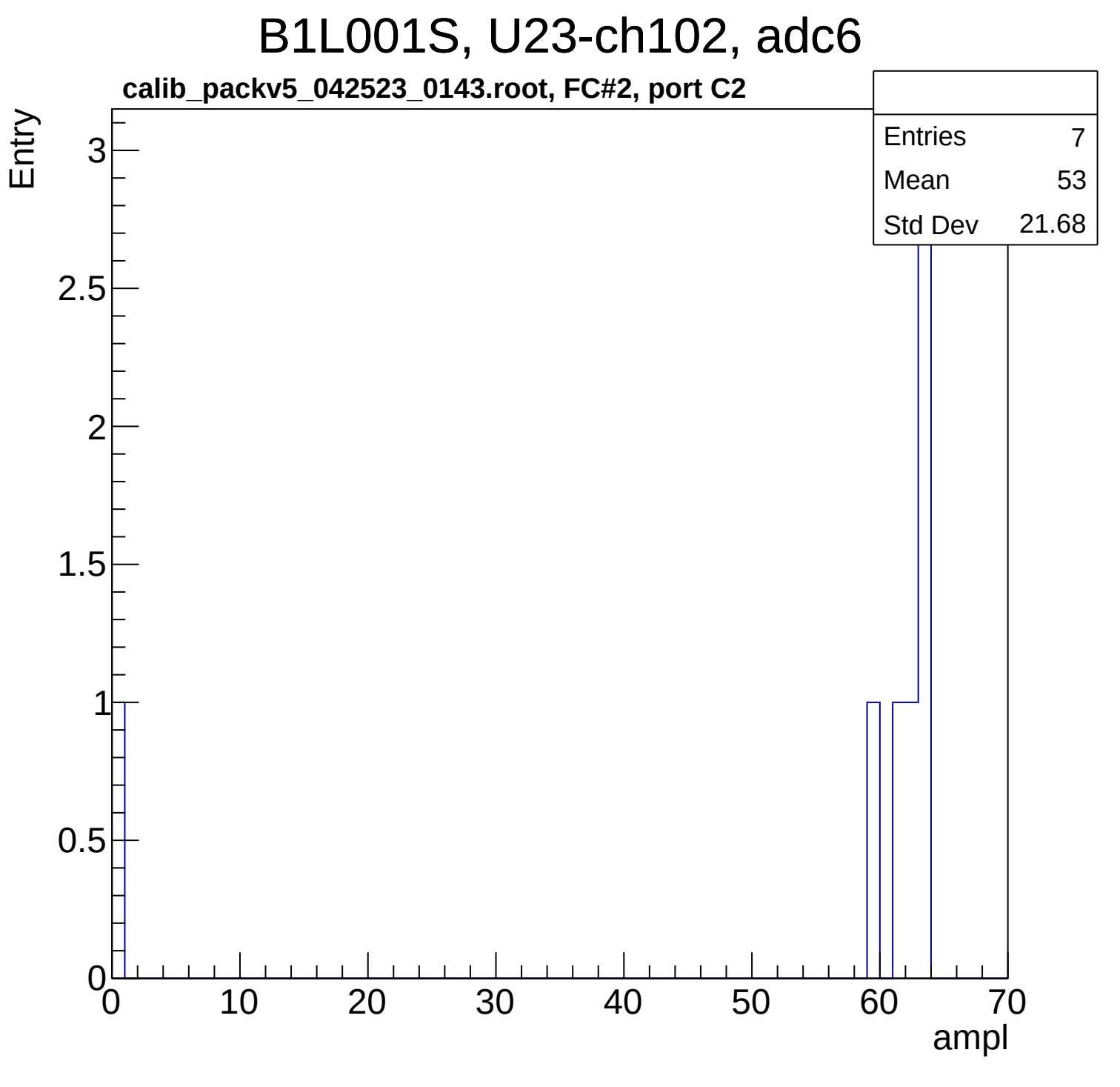
B1L001S, U23-ch102, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 7
Mean 53
Std Dev 21.68
Entry
ampl
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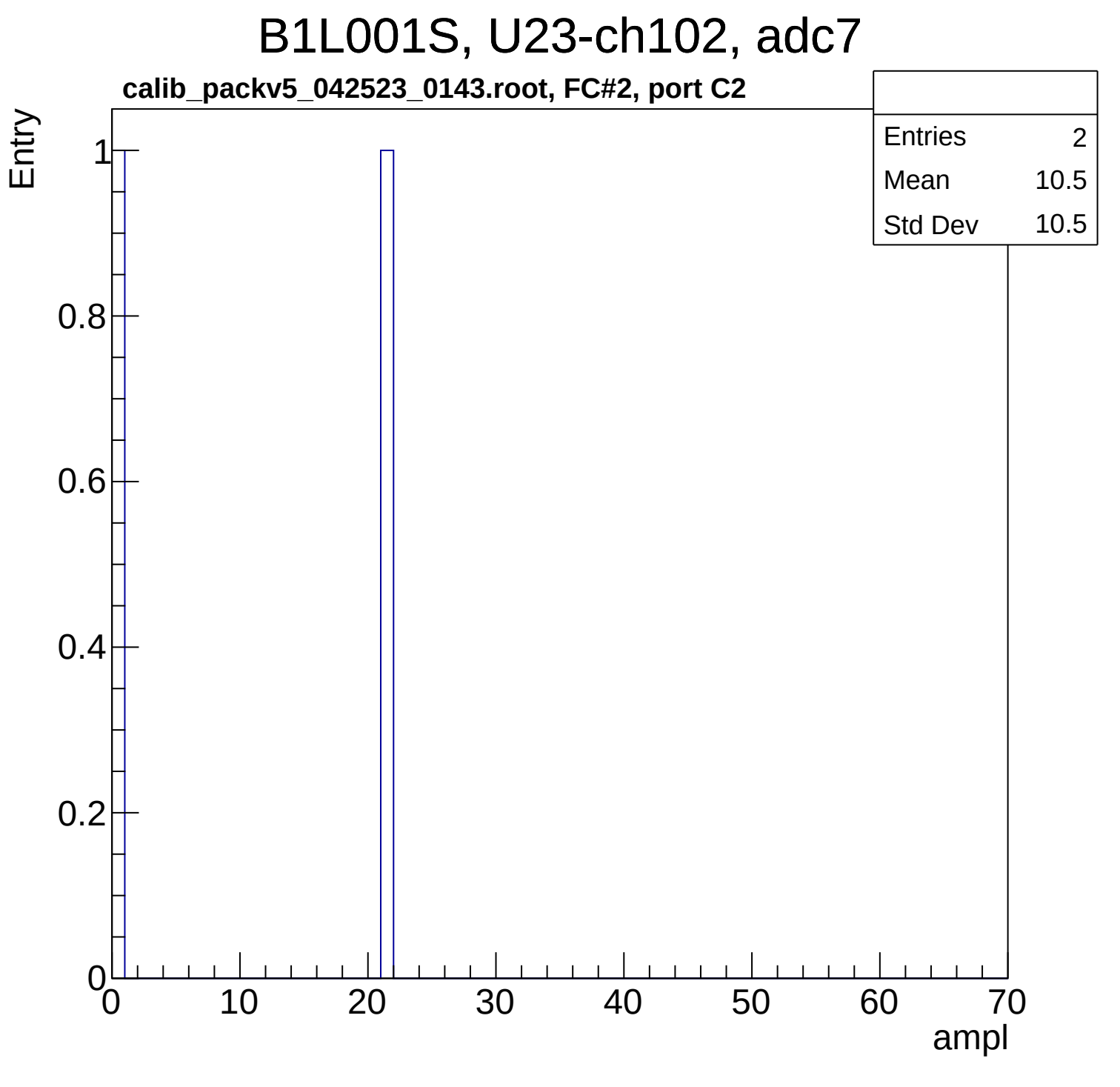
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch102, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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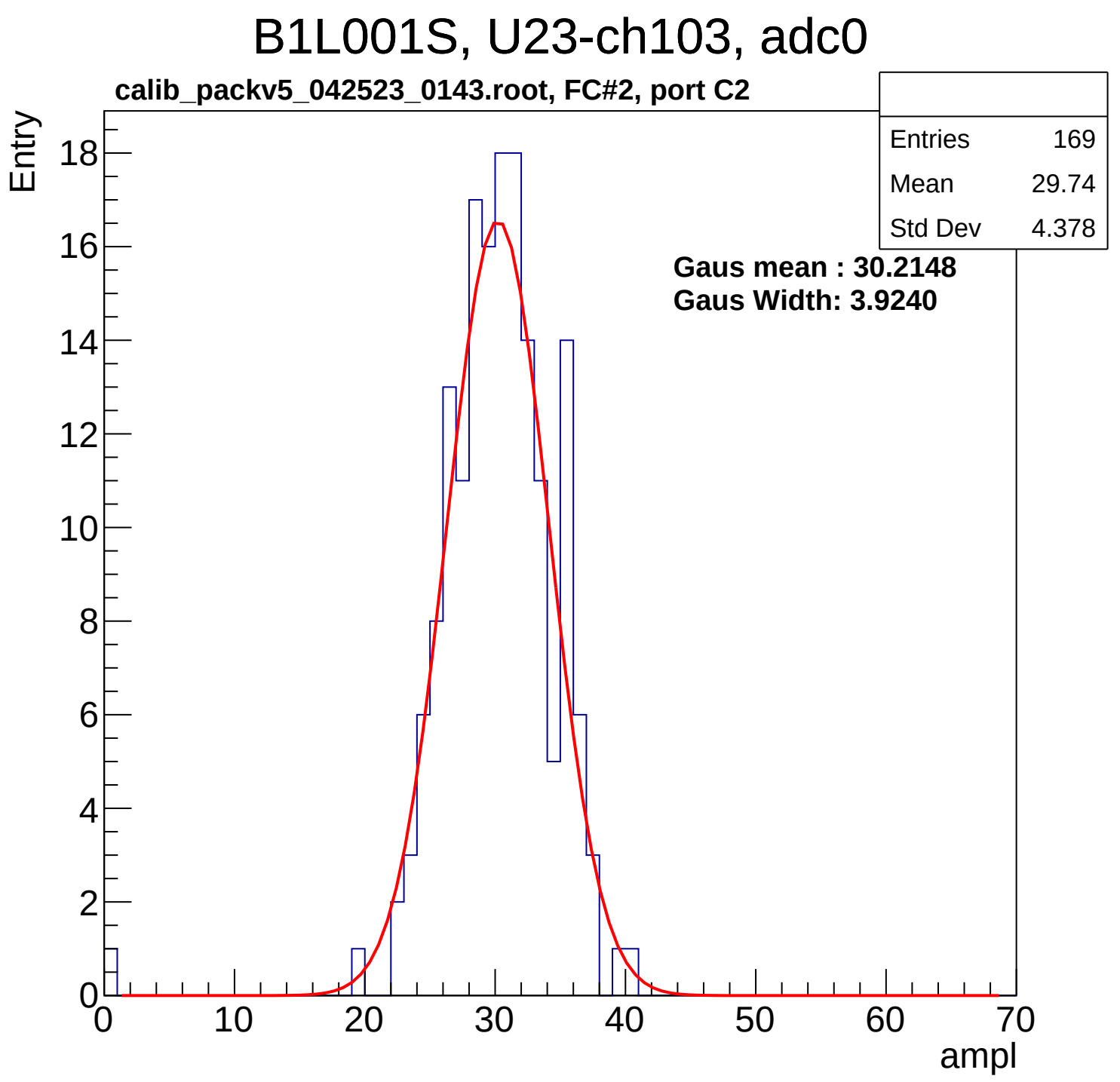
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch103, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 169
Mean 29.74
Std Dev 4.378
Gaus mean : 30.2148
Gaus Width: 3.9240
Entry
ampl
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6
8
10
12
14
16
18
0
10
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40
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60
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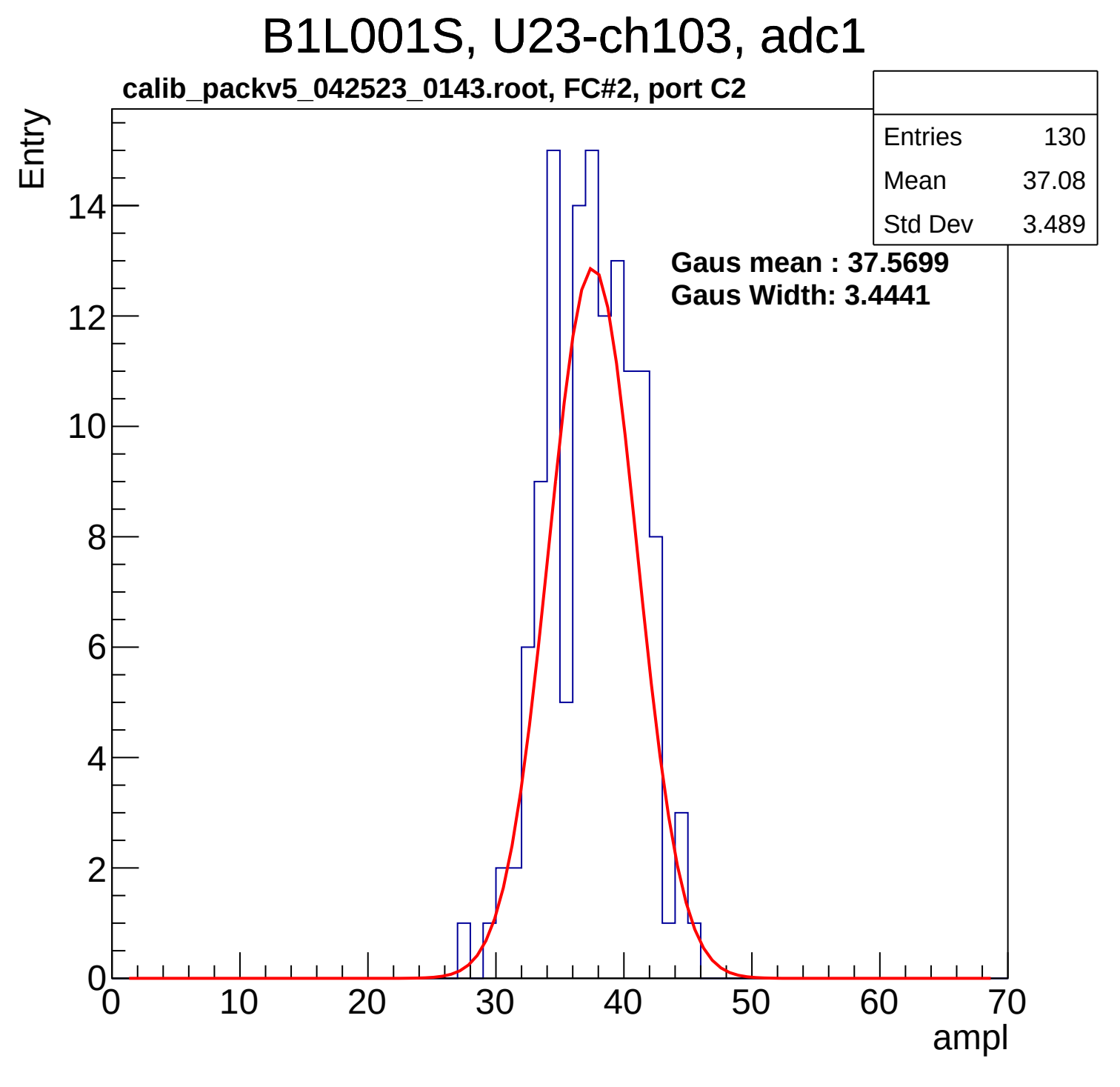
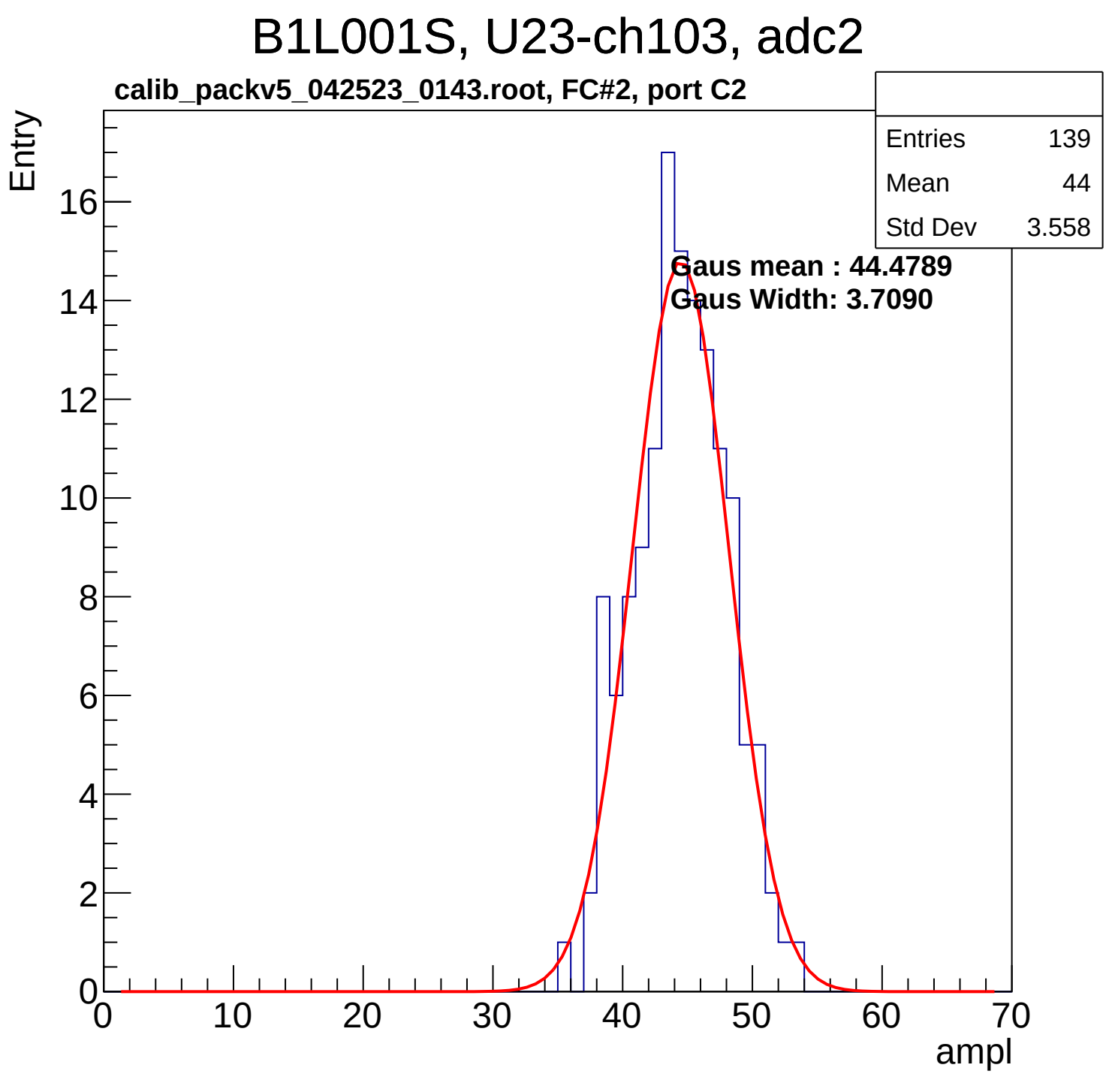

B1L001S, U23-ch103, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 37.08
Std Dev 3.489
Gaus mean : 37.5699
Gaus Width: 3.4441
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch103, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 44
Std Dev 3.558
Gaus mean : 44.4789
Gaus Width: 3.7090
Entry
ampl
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4
6
8
10
12
14
16
0
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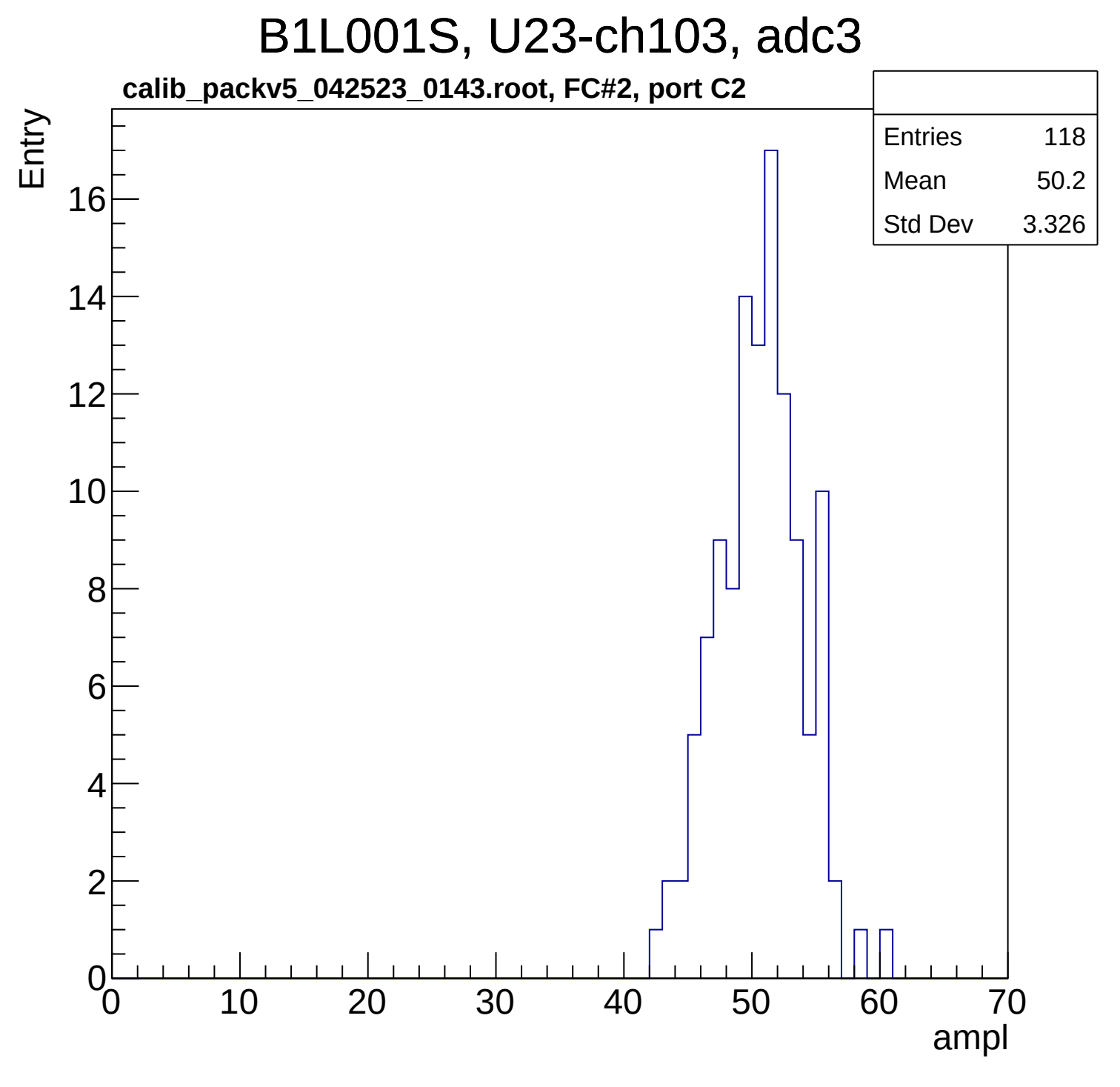
B1L001S, U23-ch103, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 50.2
Std Dev 3.326
Entry
ampl
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10
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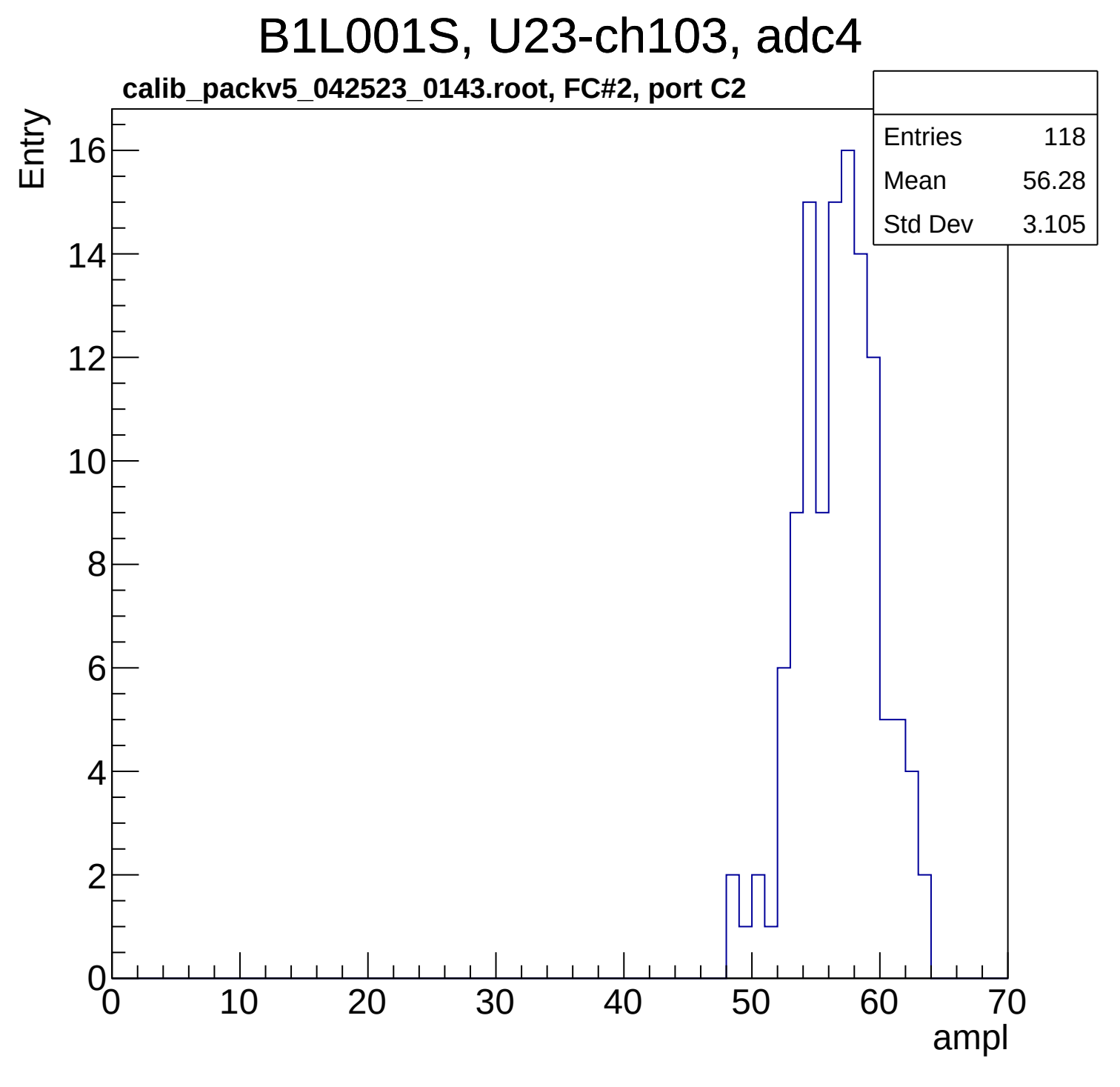
B1L001S, U23-ch103, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 56.28
Std Dev 3.105
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
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60
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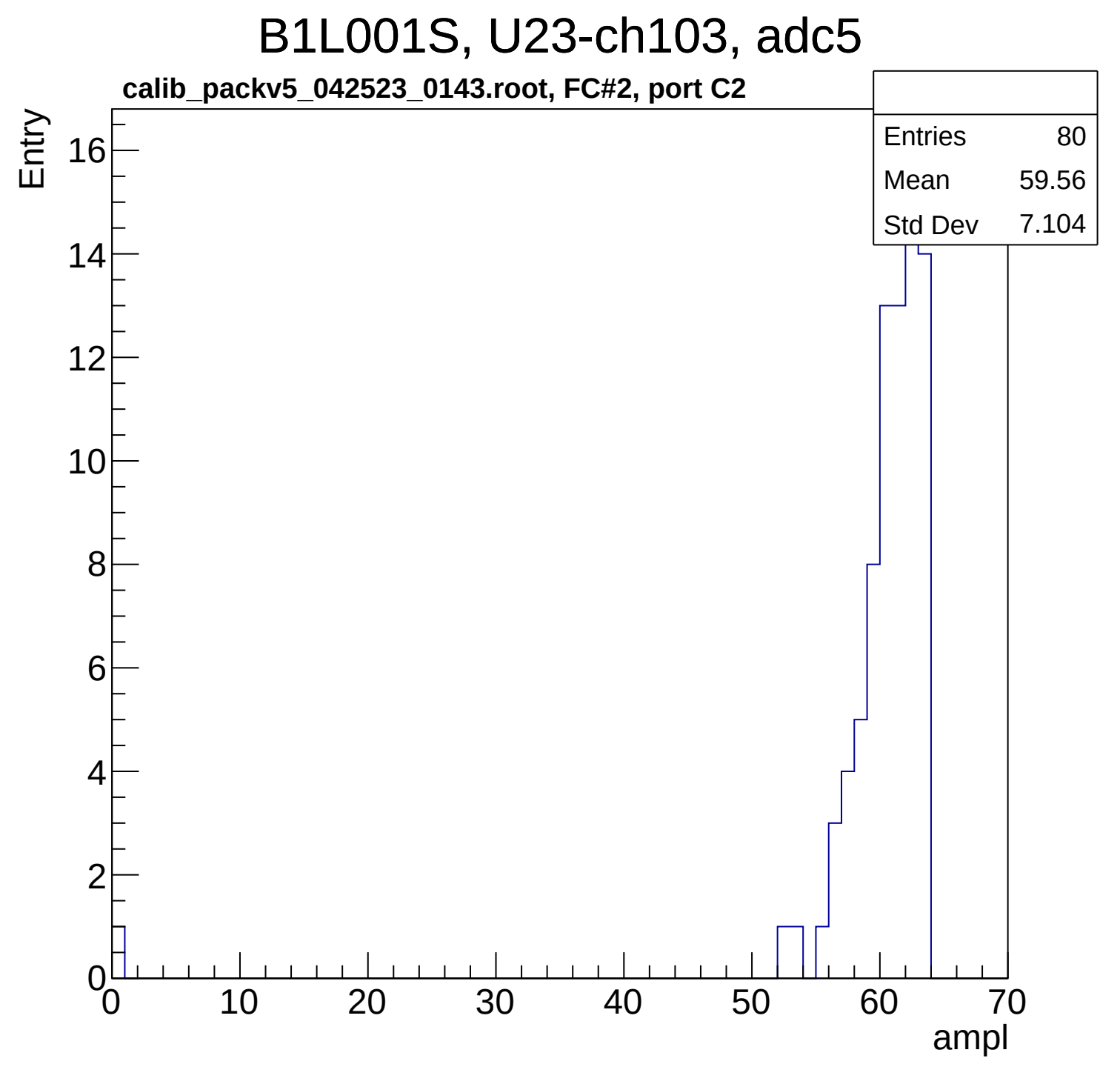
B1L001S, U23-ch103, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 59.56
Std Dev 7.104
Entry
ampl
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2
4
6
8
10
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0
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60
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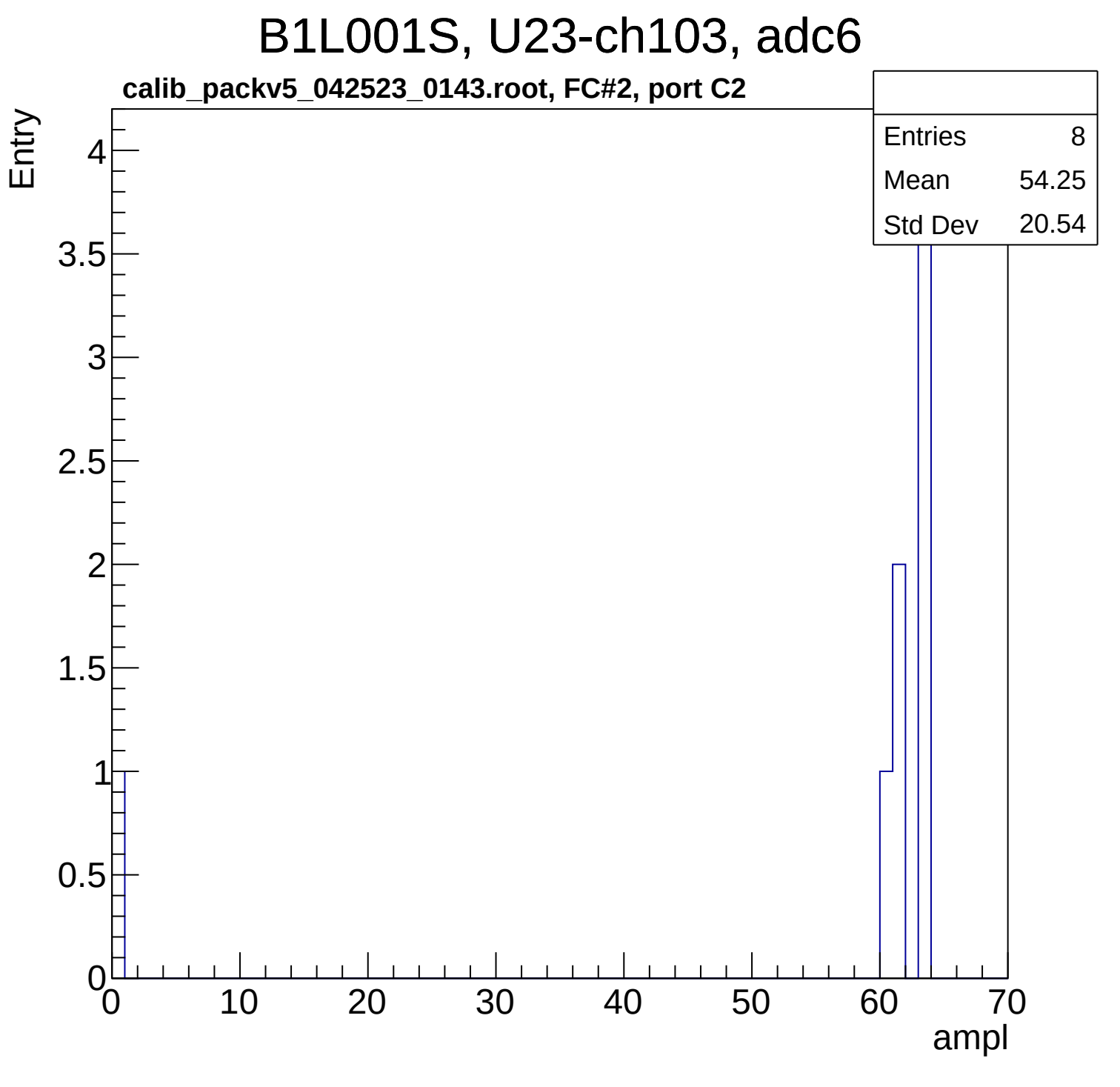
B1L001S, U23-ch103, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 54.25
Std Dev 20.54
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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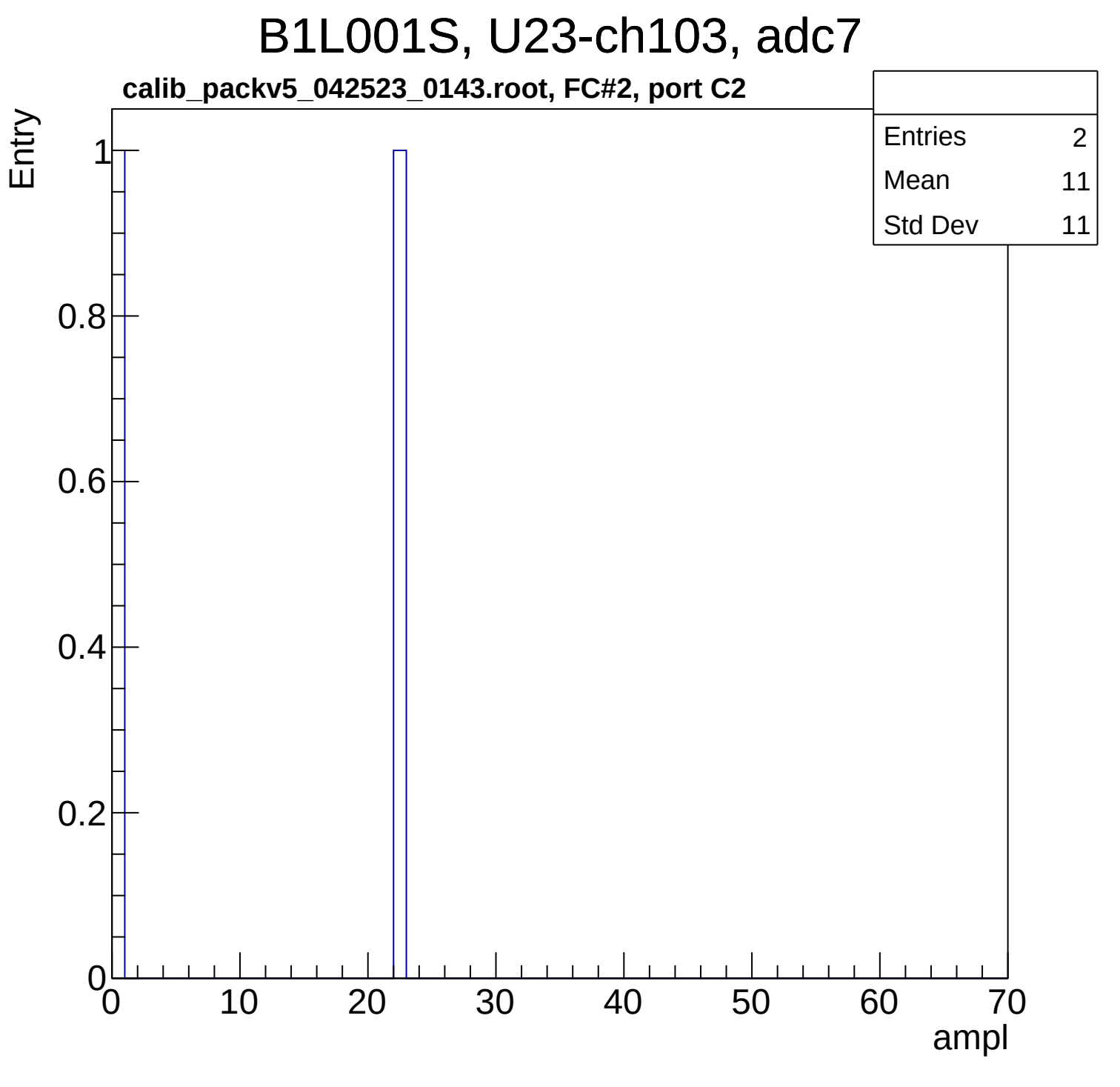
B1L001S, U23-ch103, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11
Std Dev 11
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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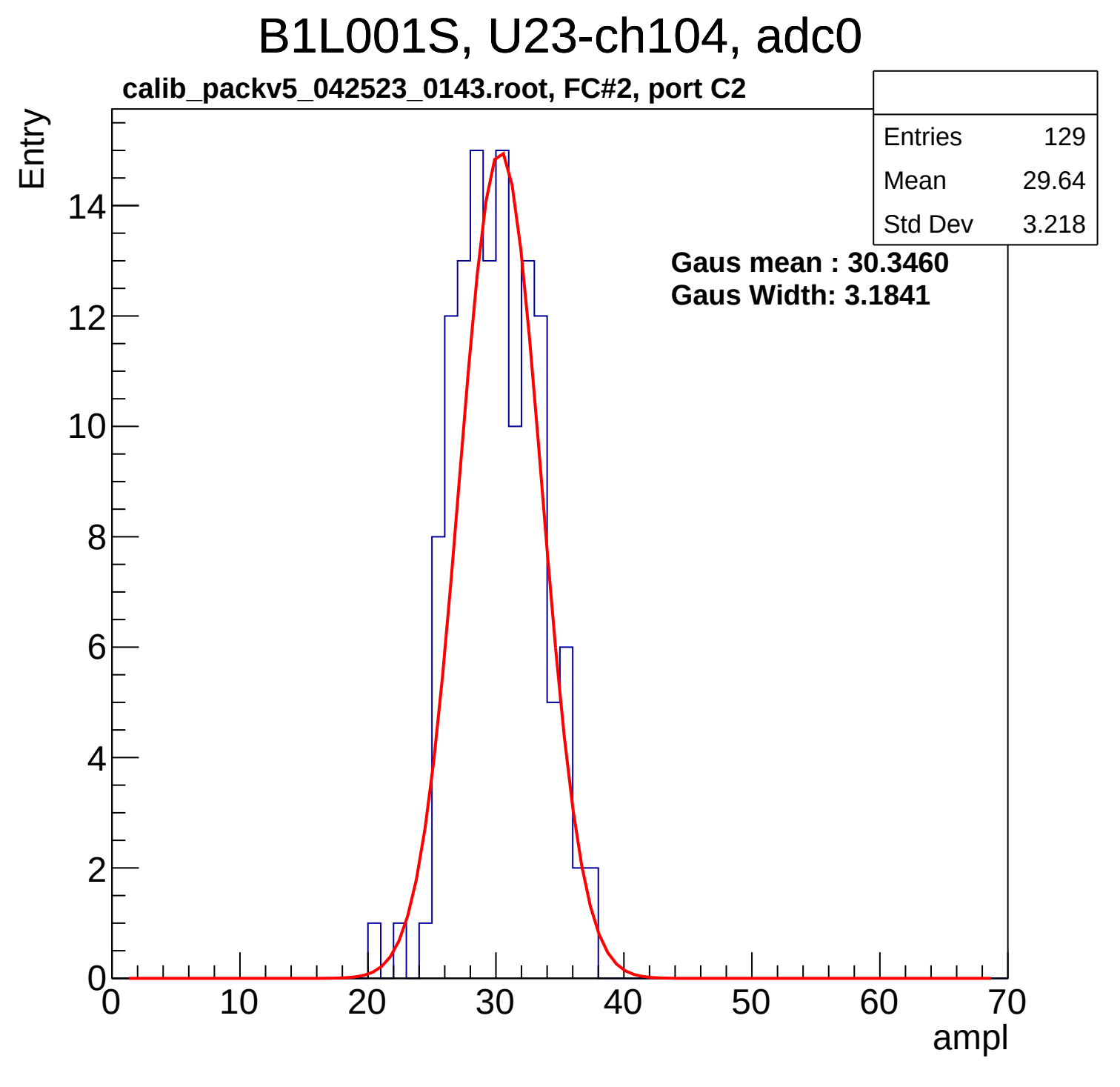
50
60
70
ampl


B1L001S, U23-ch104, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 29.64
Std Dev 3.218
Gaus mean : 30.3460
Gaus Width: 3.1841
Entry
ampl
0
2
4
6
8
10
12
14
0
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20
30
40
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60
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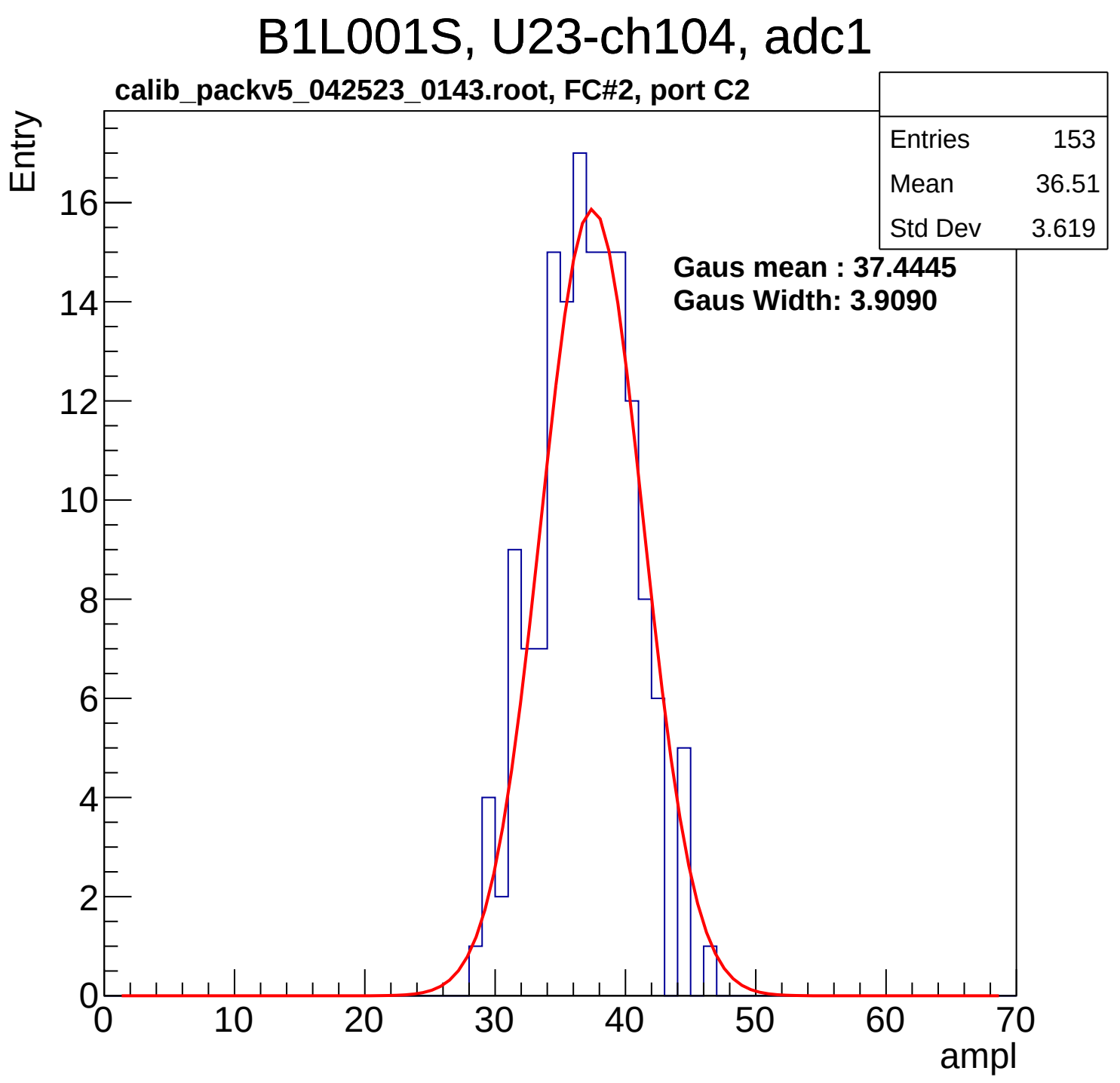
B1L001S, U23-ch104, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 36.51
Std Dev 3.619
Gaus mean : 37.4445
Gaus Width: 3.9090
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
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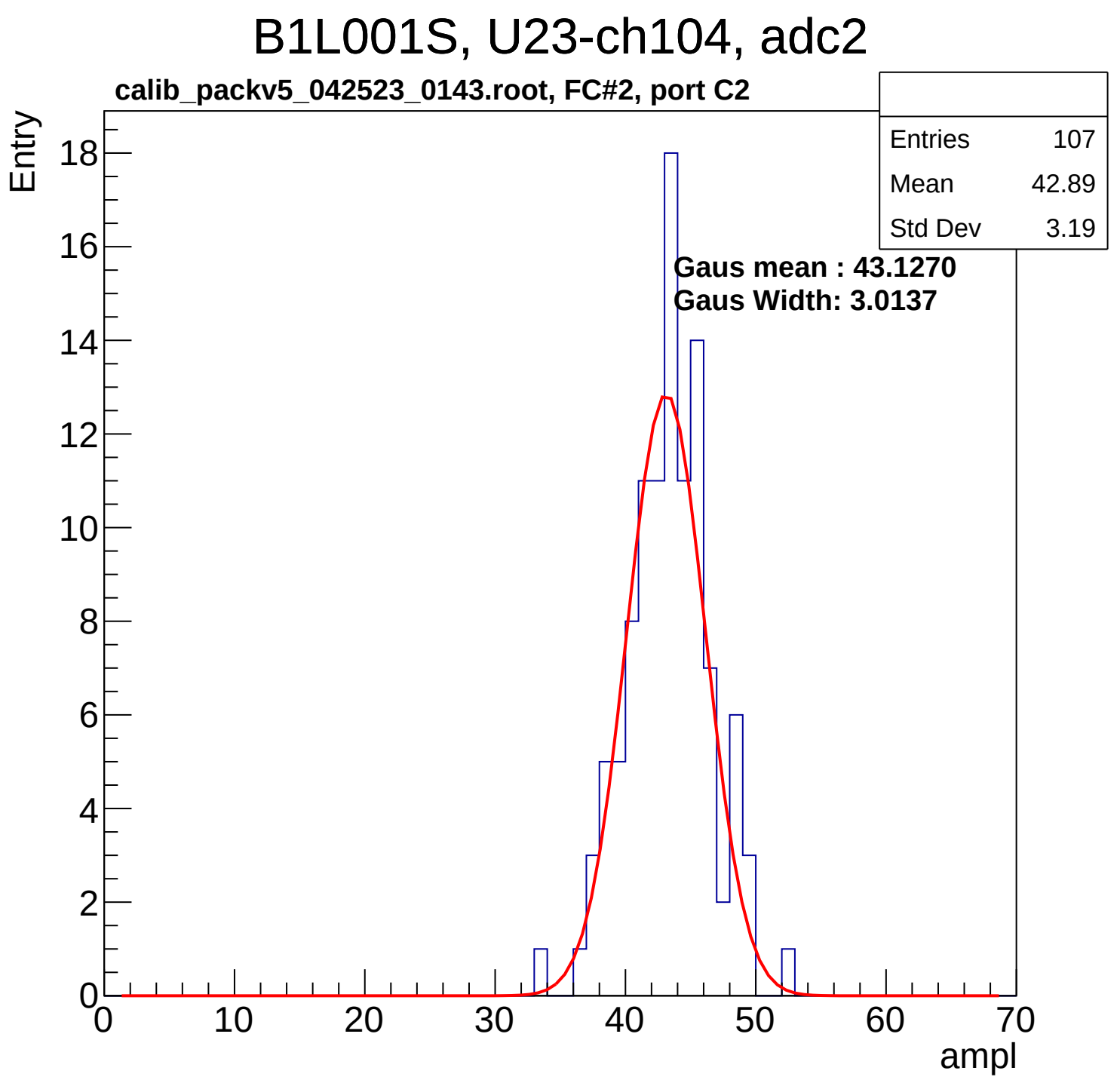
B1L001S, U23-ch104, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 42.89
Std Dev 3.19
Gaus mean : 43.1270
Gaus Width: 3.0137
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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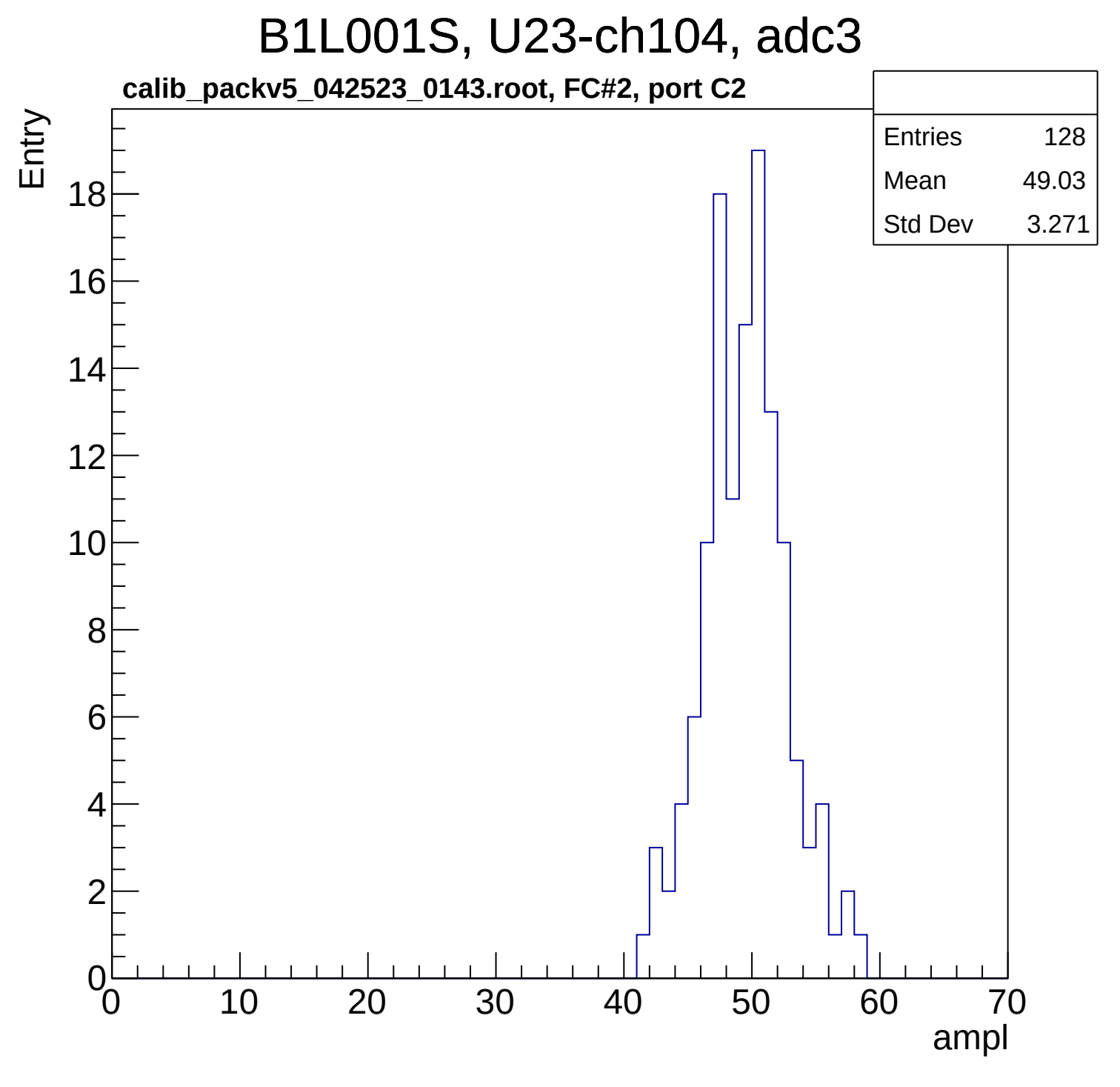
B1L001S, U23-ch104, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 49.03
Std Dev 3.271
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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20
30
40
50
60
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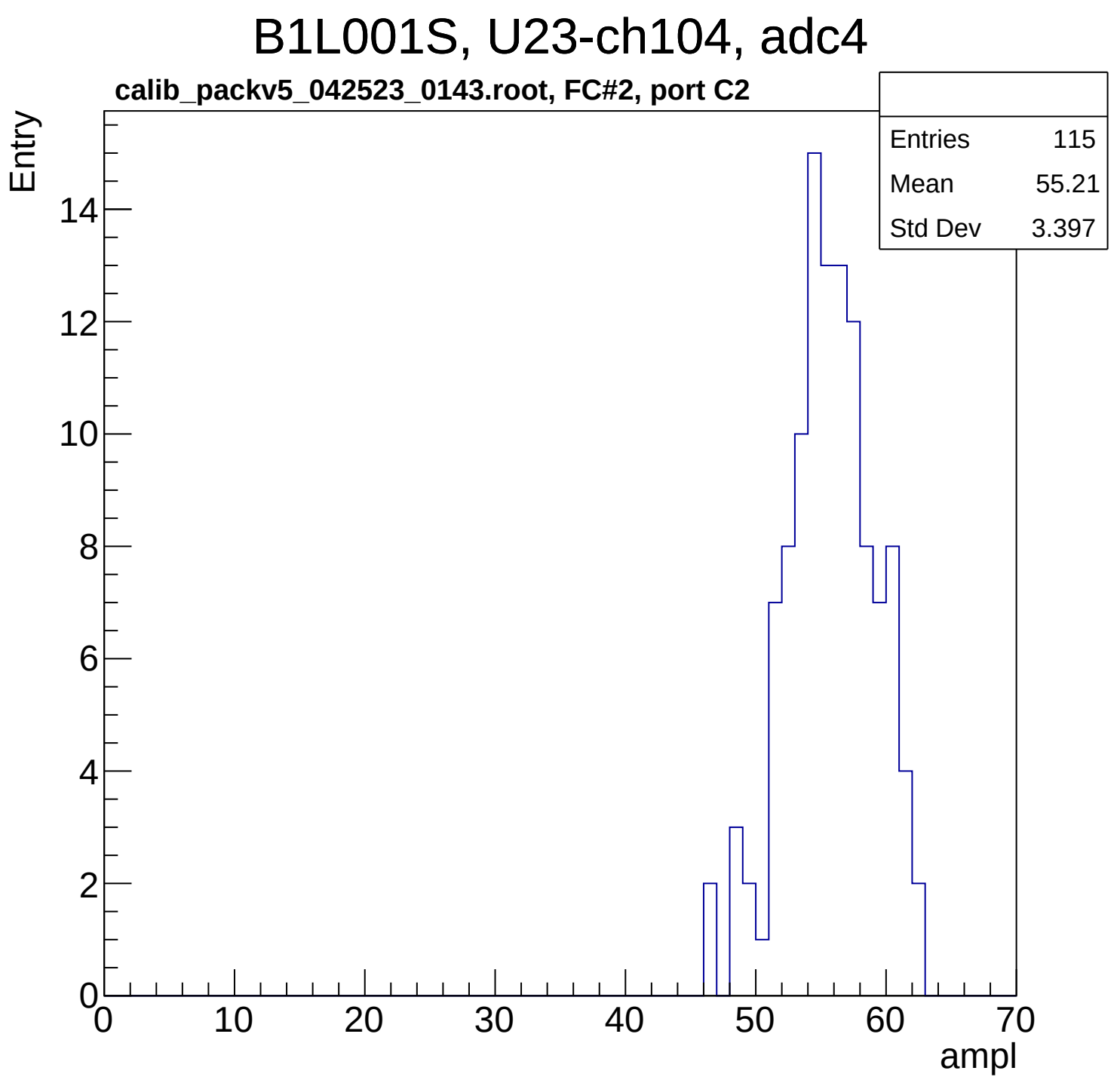
B1L001S, U23-ch104, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 55.21
Std Dev 3.397
Entry
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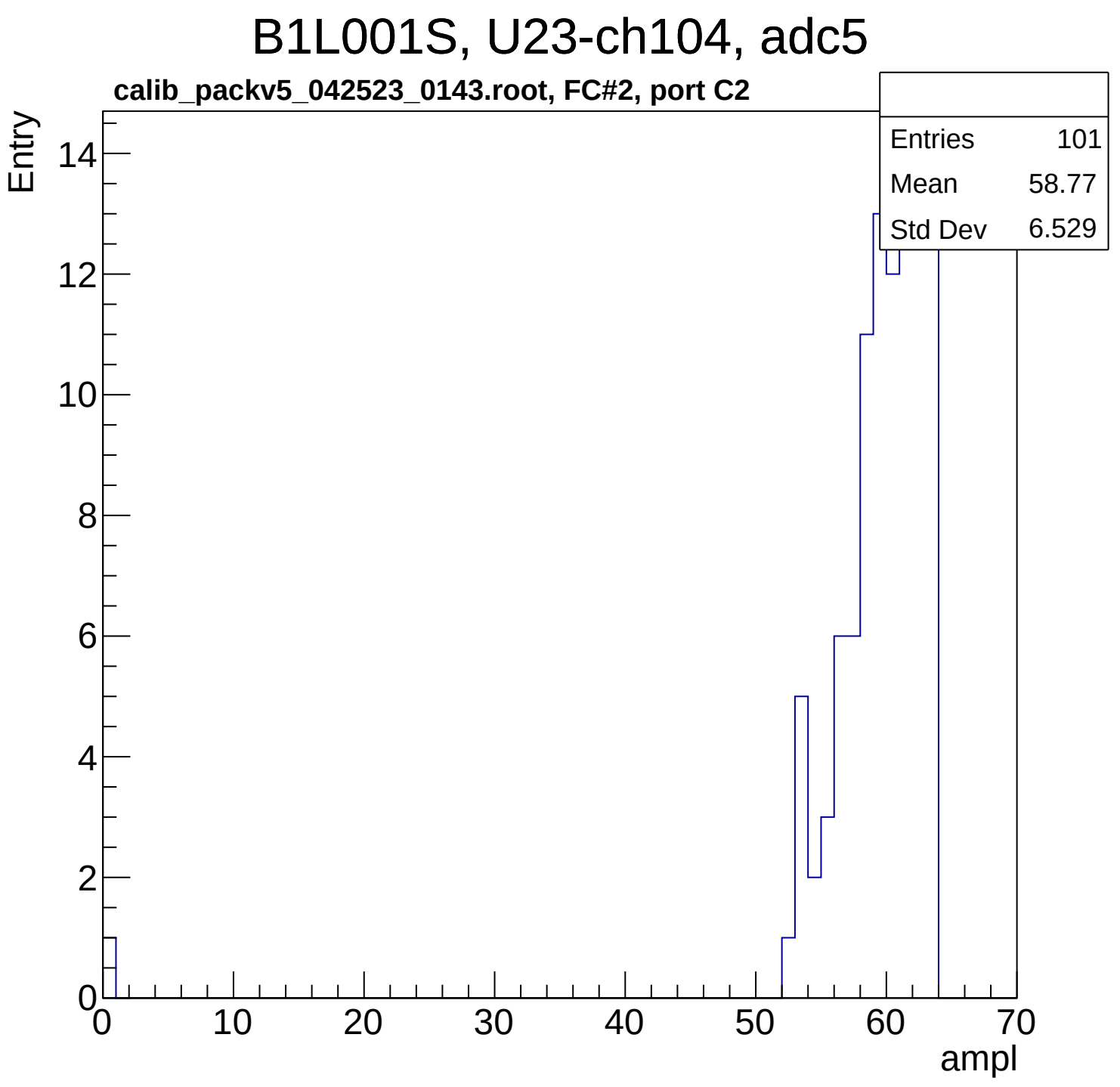
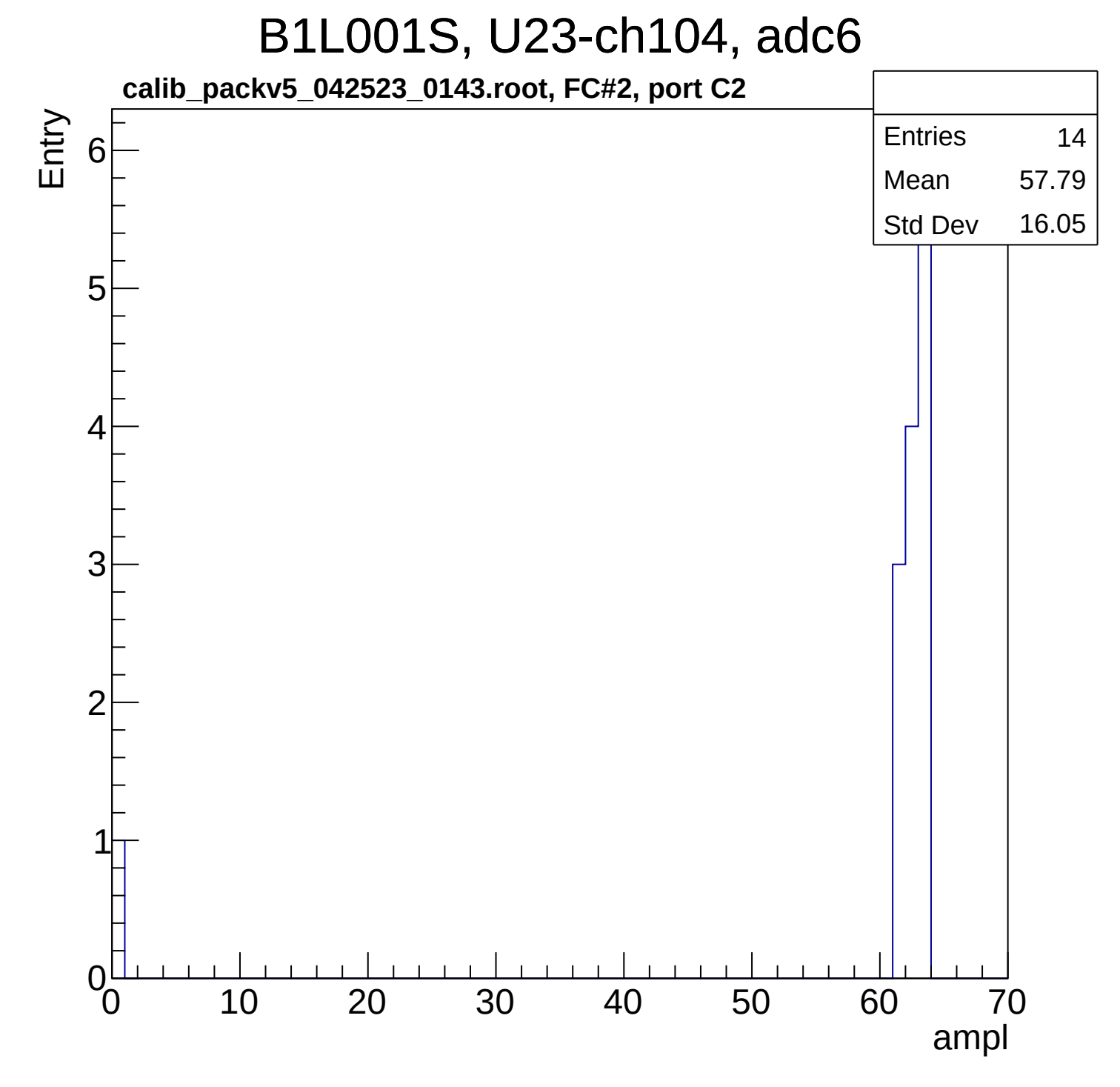
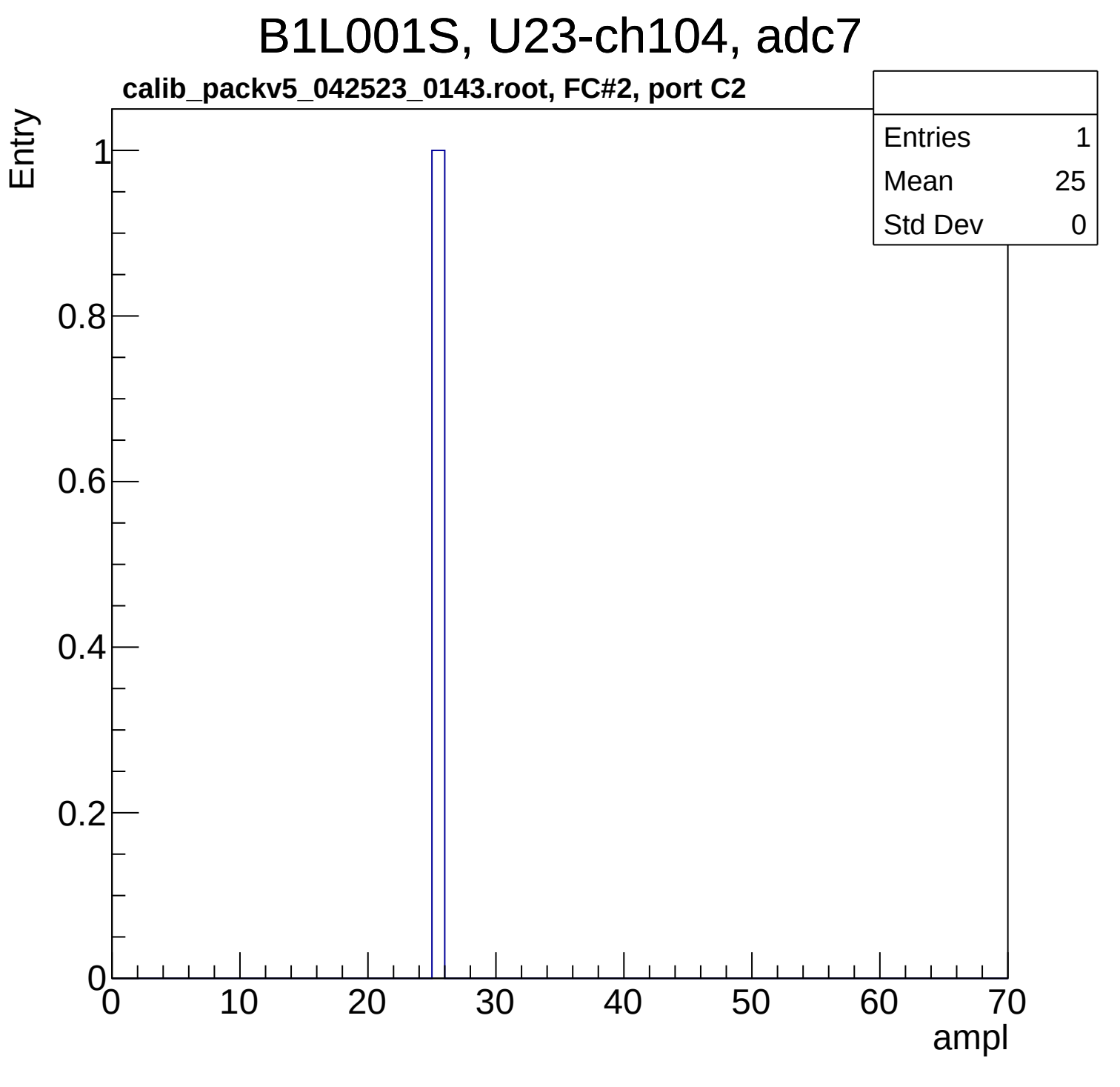
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch104, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 58.77
Std Dev 6.529
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch104, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 14
Mean 57.79
Std Dev 16.05
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch104, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 25
Std Dev 0
Entry
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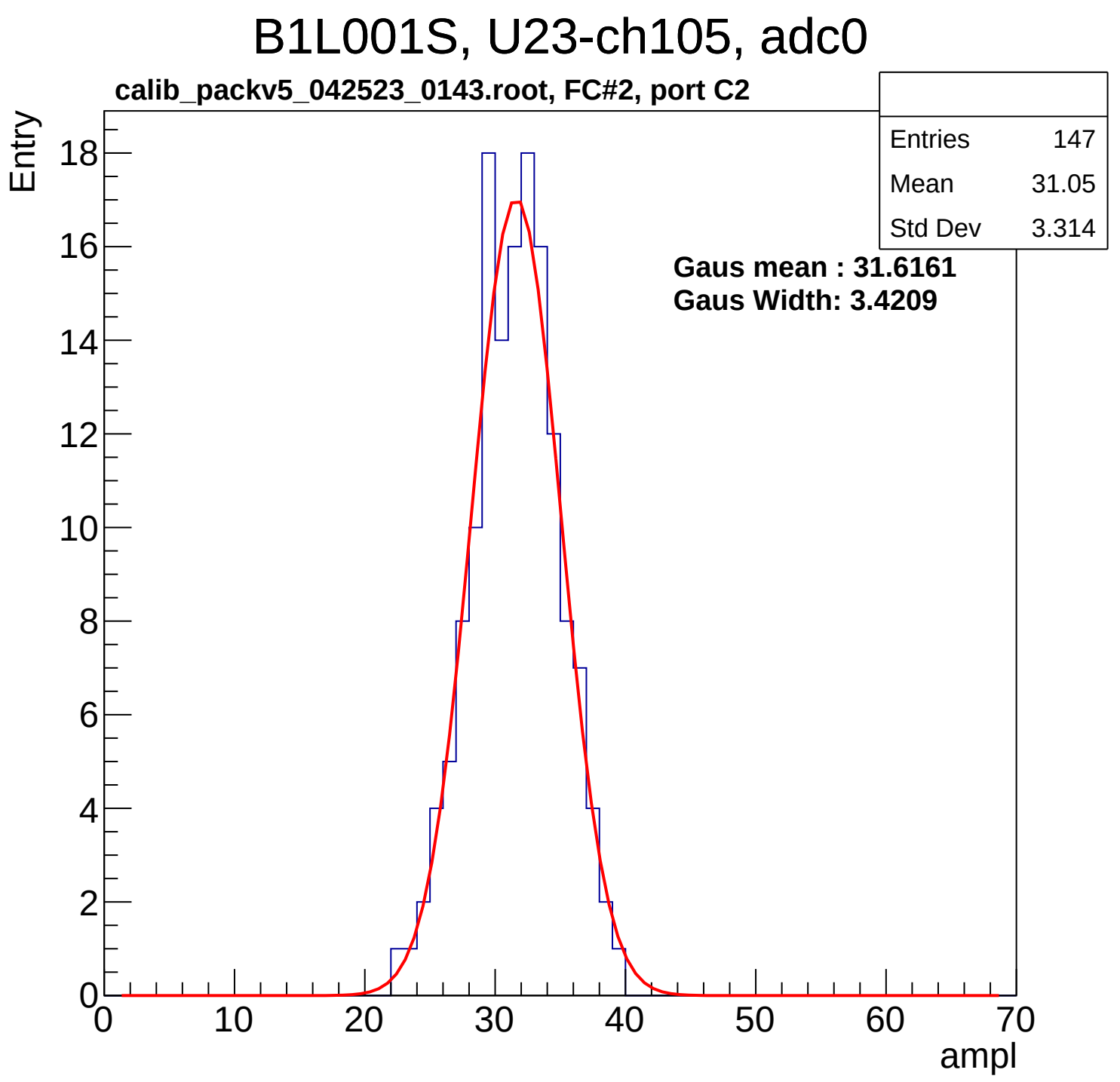
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch105, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 147
Mean 31.05
Std Dev 3.314
Gaus mean : 31.6161
Gaus Width: 3.4209
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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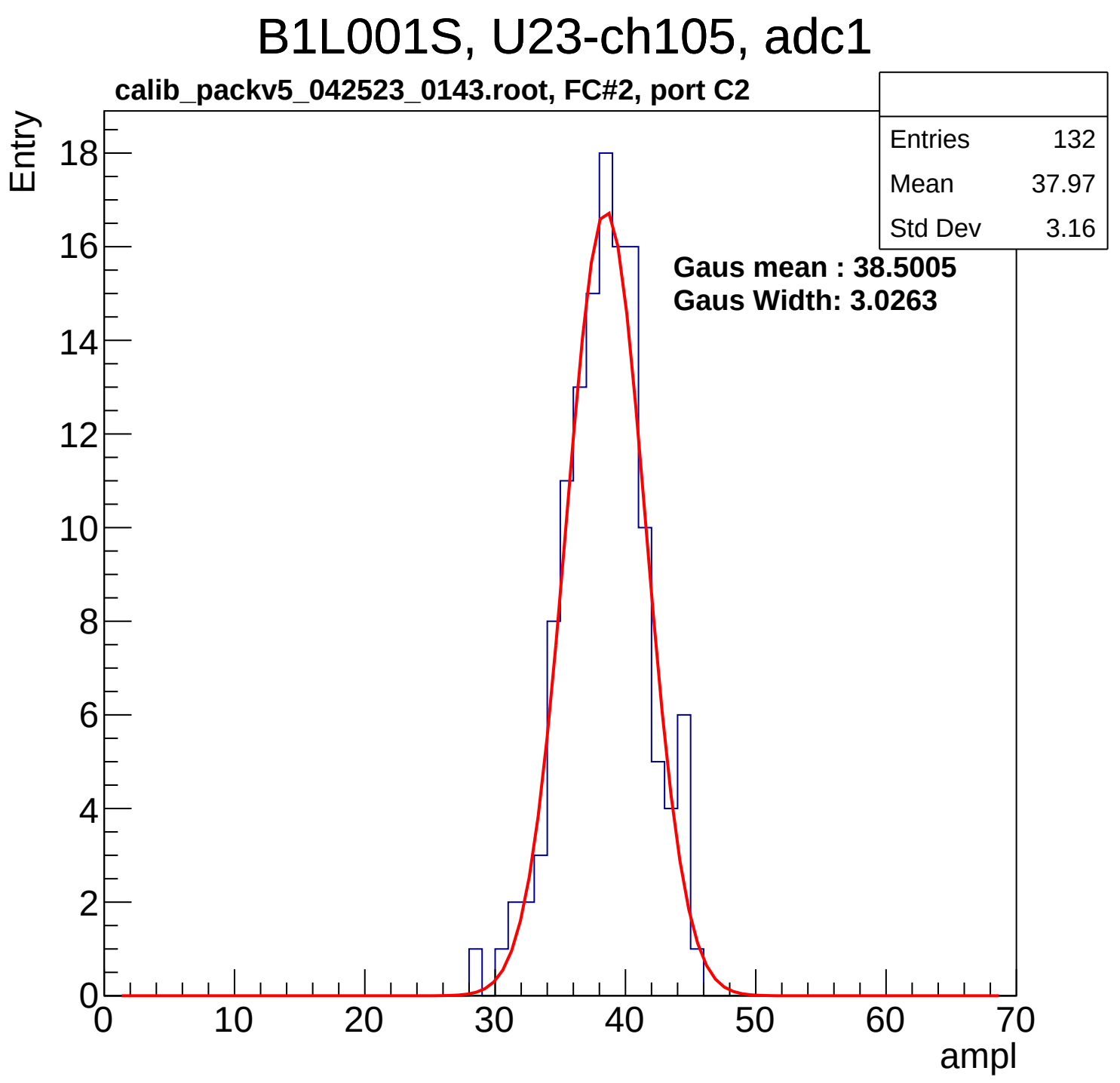
B1L001S, U23-ch105, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 132
Mean 37.97
Std Dev 3.16
Gaus mean : 38.5005
Gaus Width: 3.0263
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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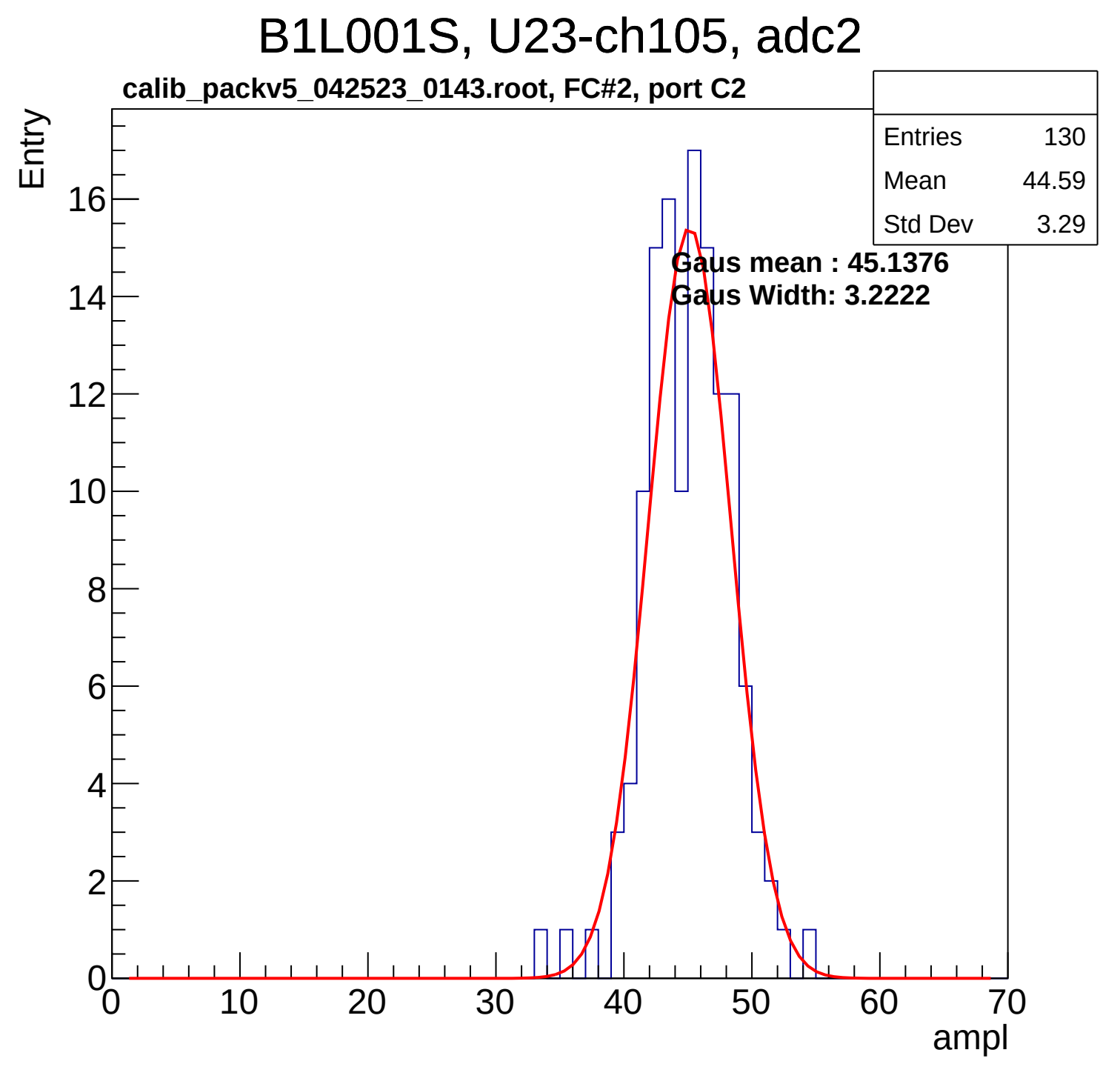
B1L001S, U23-ch105, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 44.59
Std Dev 3.29
Gaus mean : 45.1376
Gaus Width: 3.2222
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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20
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40
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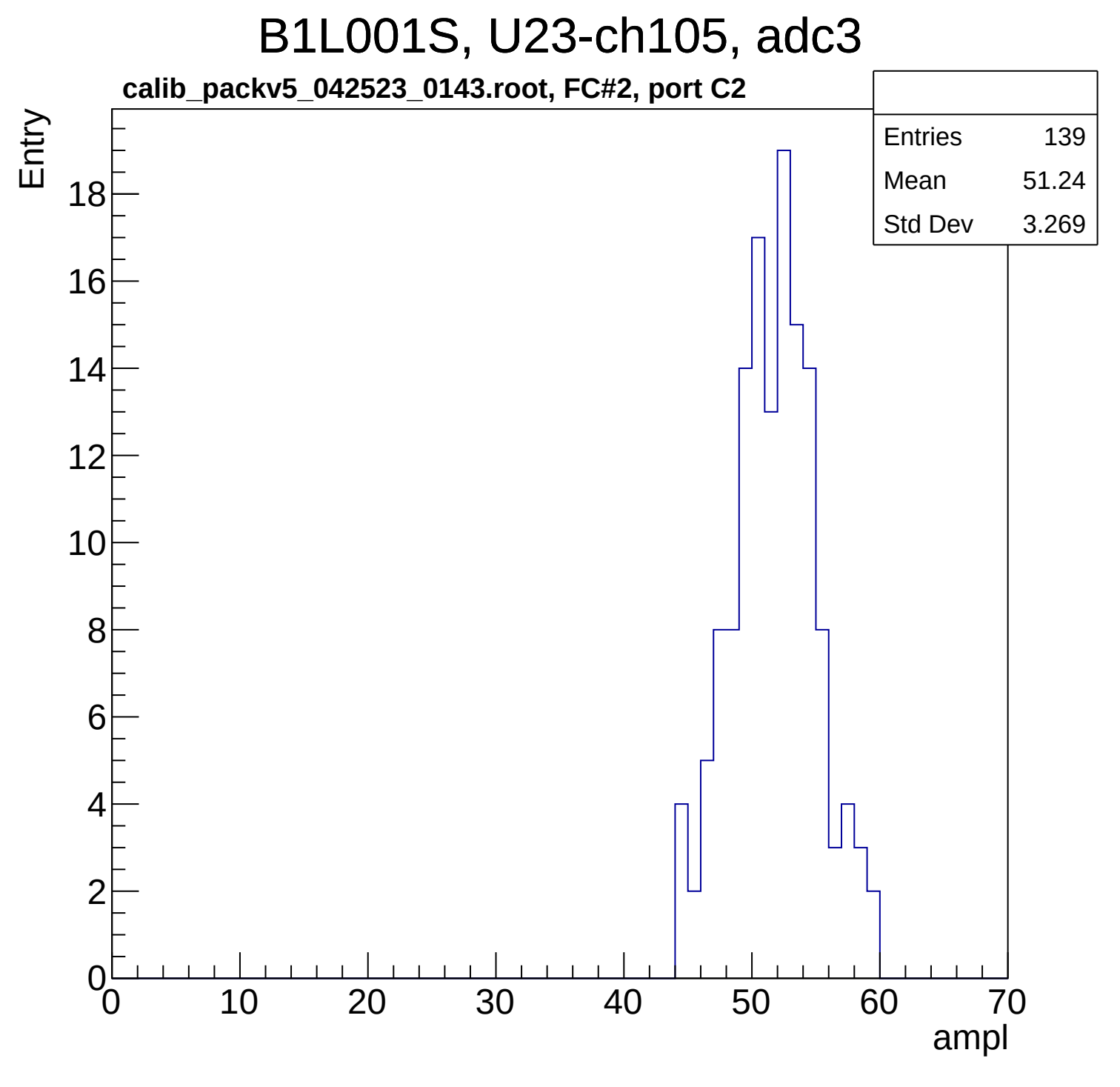
B1L001S, U23-ch105, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 139
Mean 51.24
Std Dev 3.269
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
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40
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60
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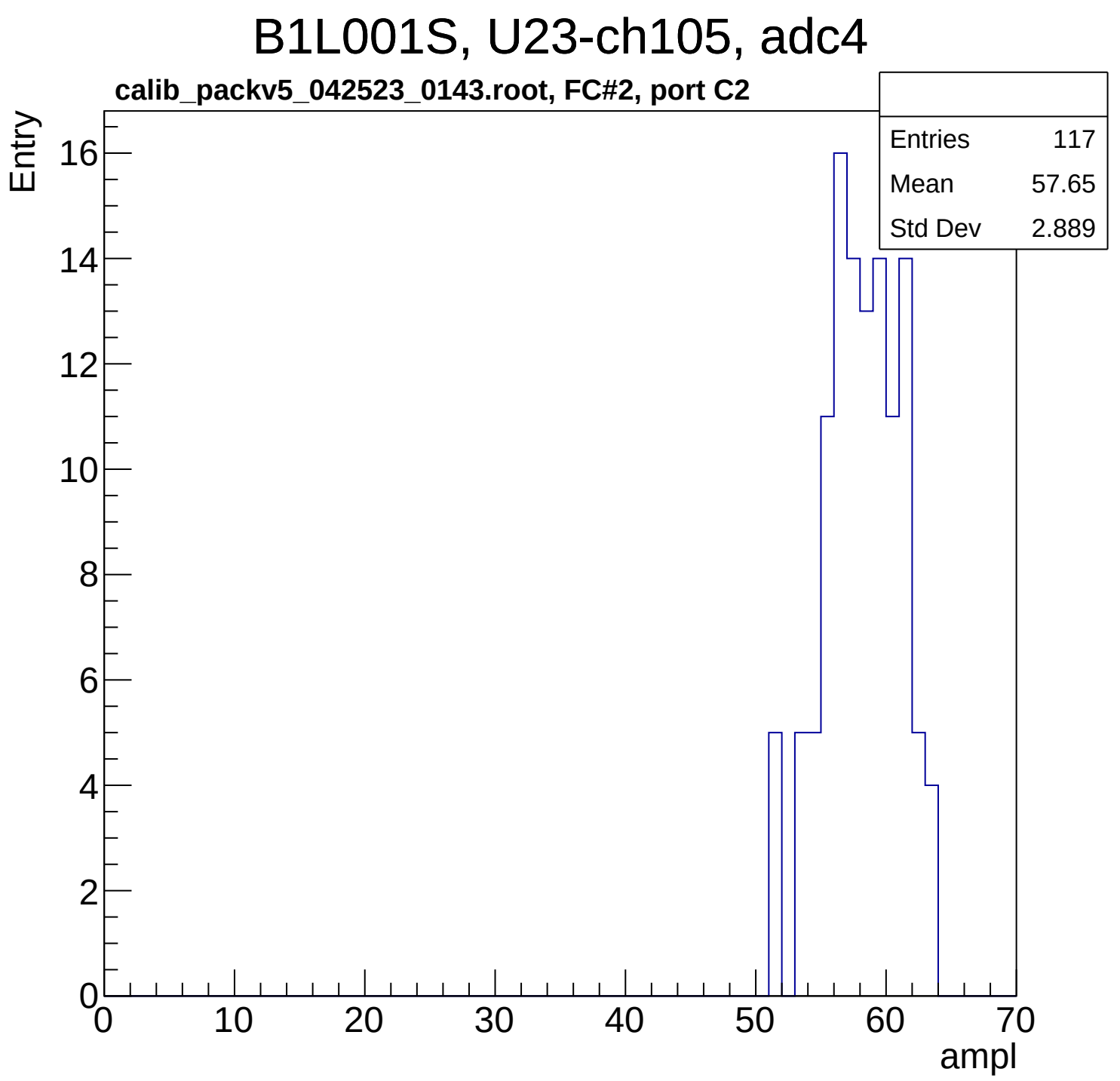
B1L001S, U23-ch105, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 57.65
Std Dev 2.889
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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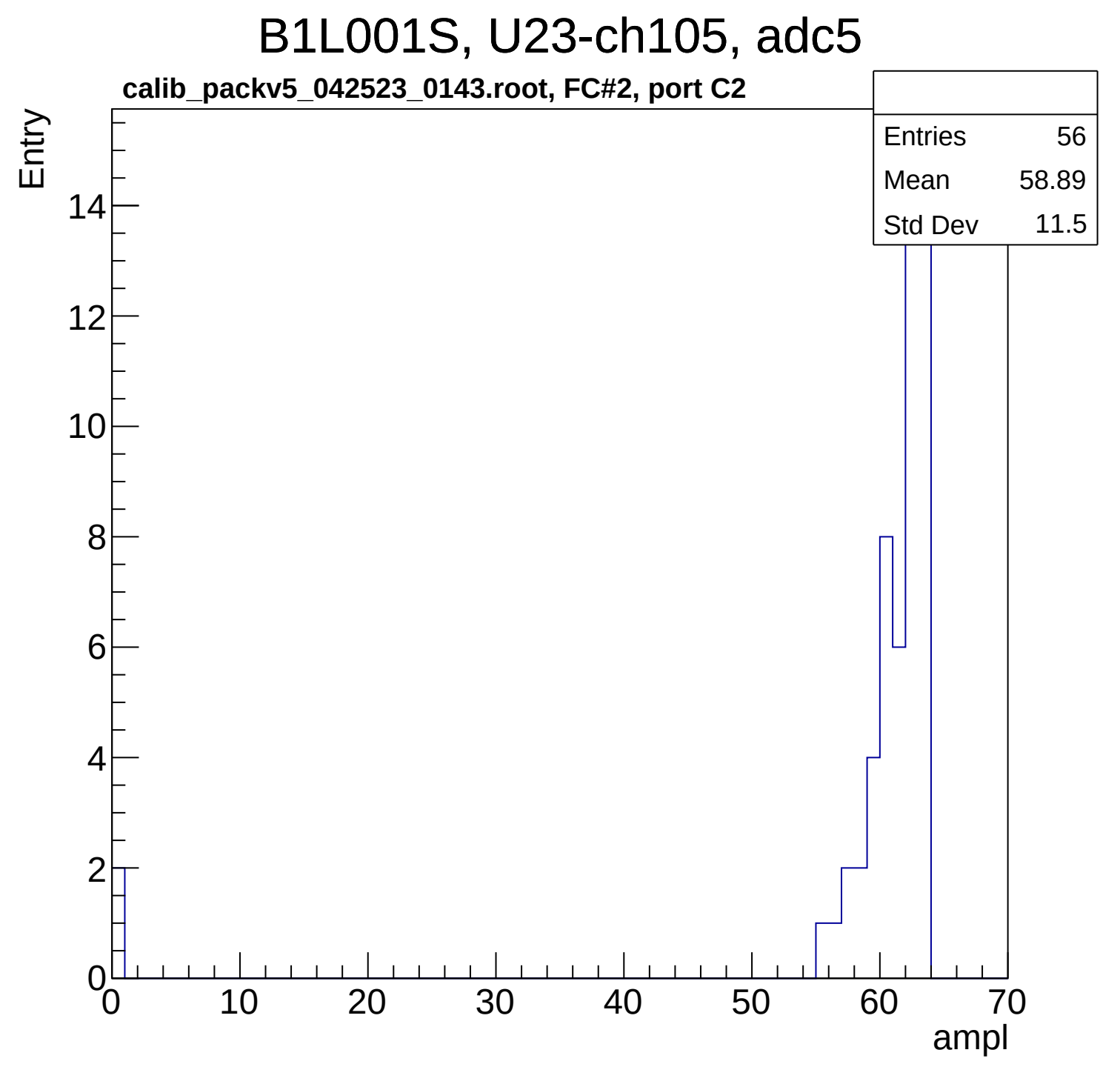
B1L001S, U23-ch105, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 56
Mean 58.89
Std Dev 11.5
Entry
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2
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6
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14
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10
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30
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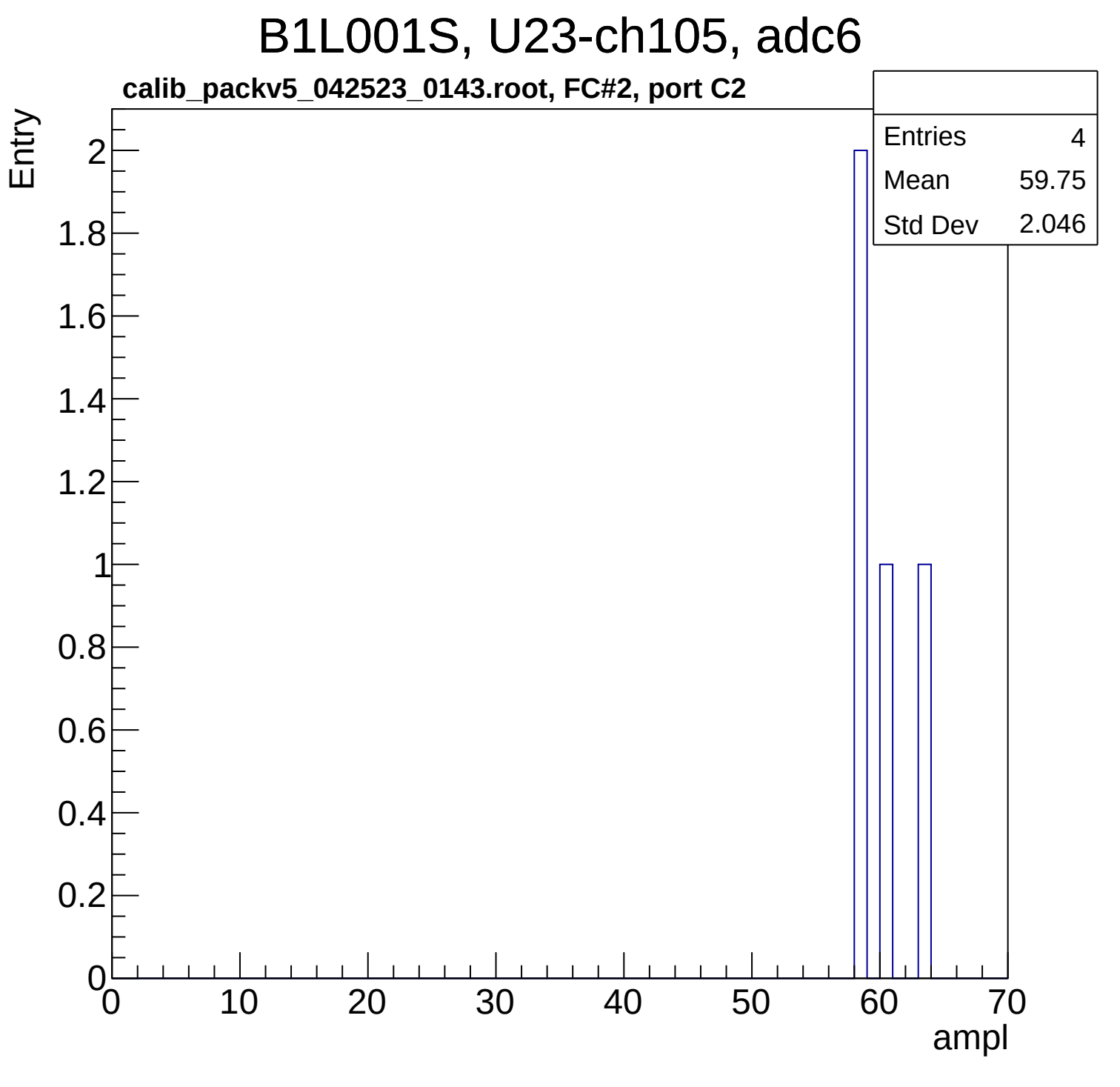
50
60
70
ampl


B1L001S, U23-ch105, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 59.75
Std Dev 2.046
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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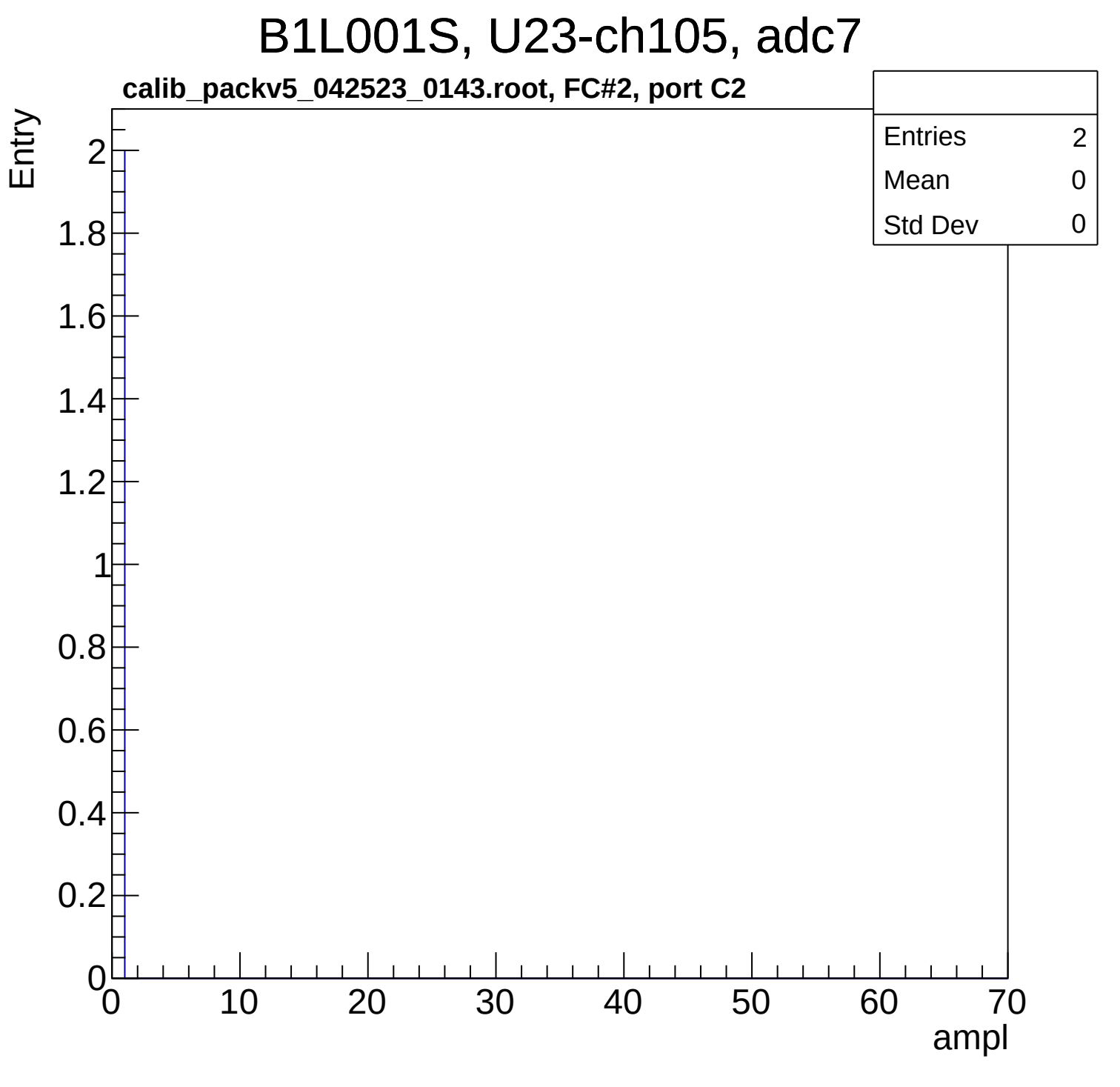
B1L001S, U23-ch105, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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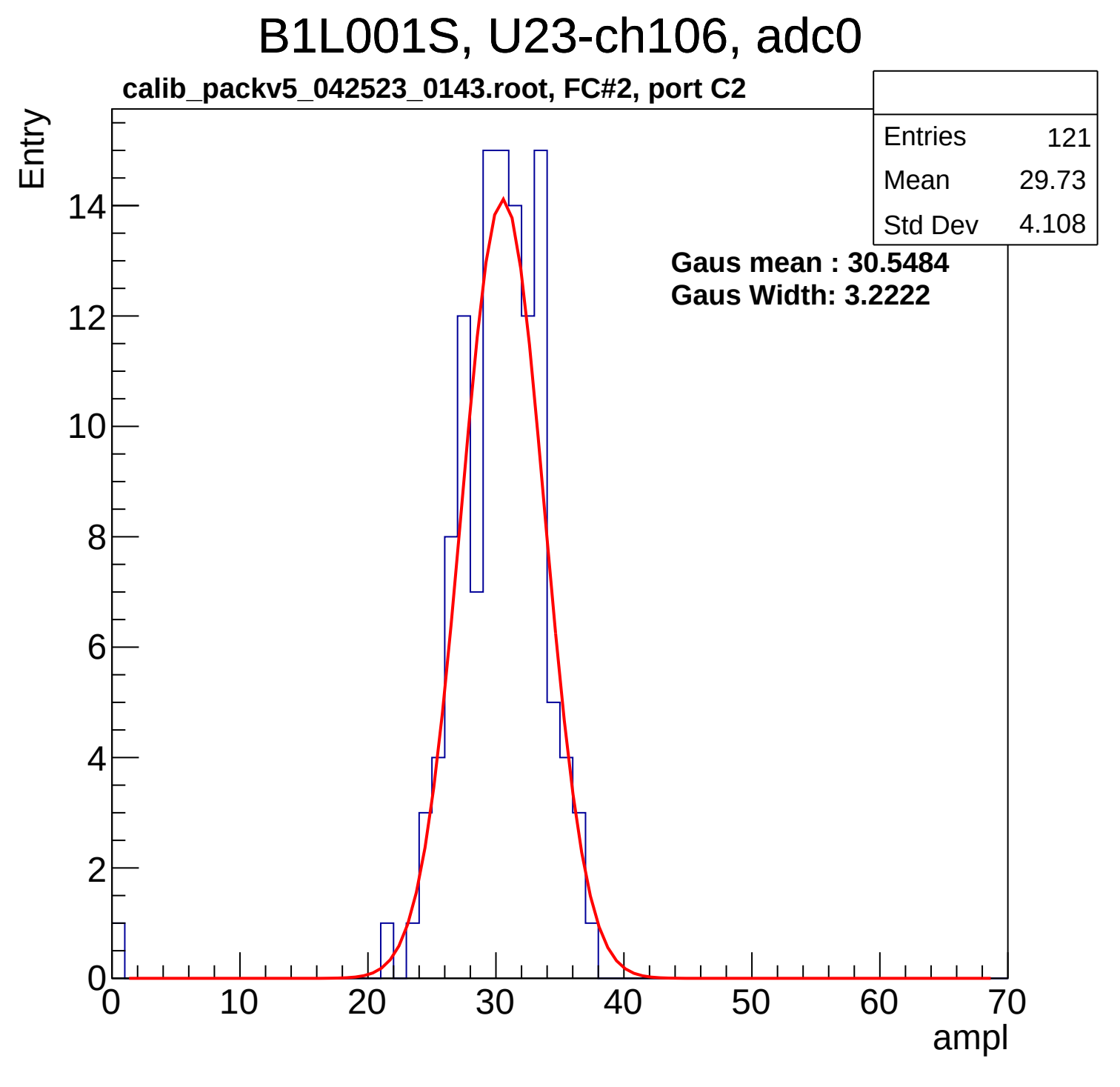
50
60
70
ampl


B1L001S, U23-ch106, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 29.73
Std Dev 4.108
Gaus mean : 30.5484
Gaus Width: 3.2222
Entry
ampl
0
2
4
6
8
10
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14
0
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20
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40
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60
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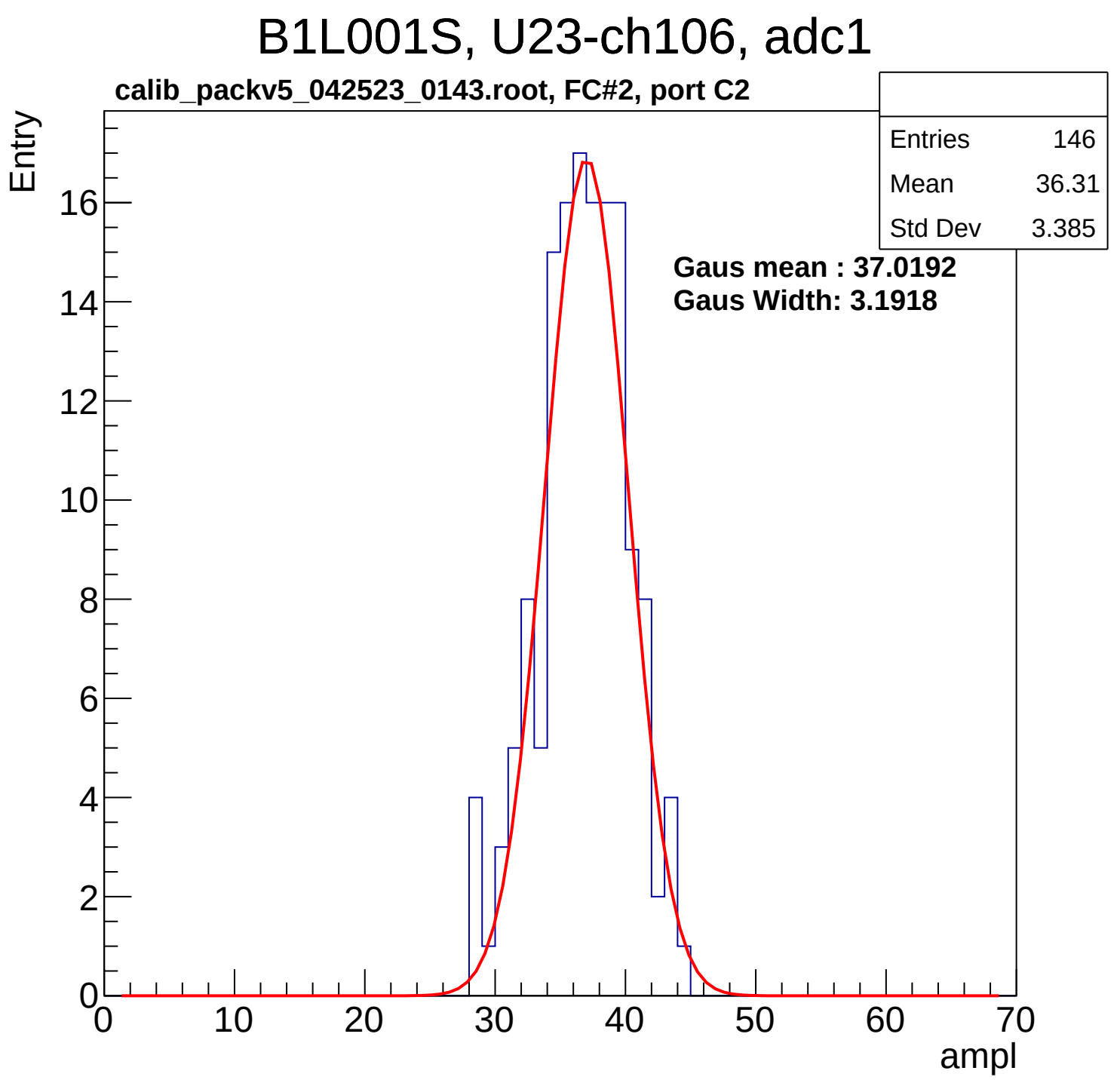
B1L001S, U23-ch106, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 36.31
Std Dev 3.385
Gaus mean : 37.0192
Gaus Width: 3.1918
Entry
ampl
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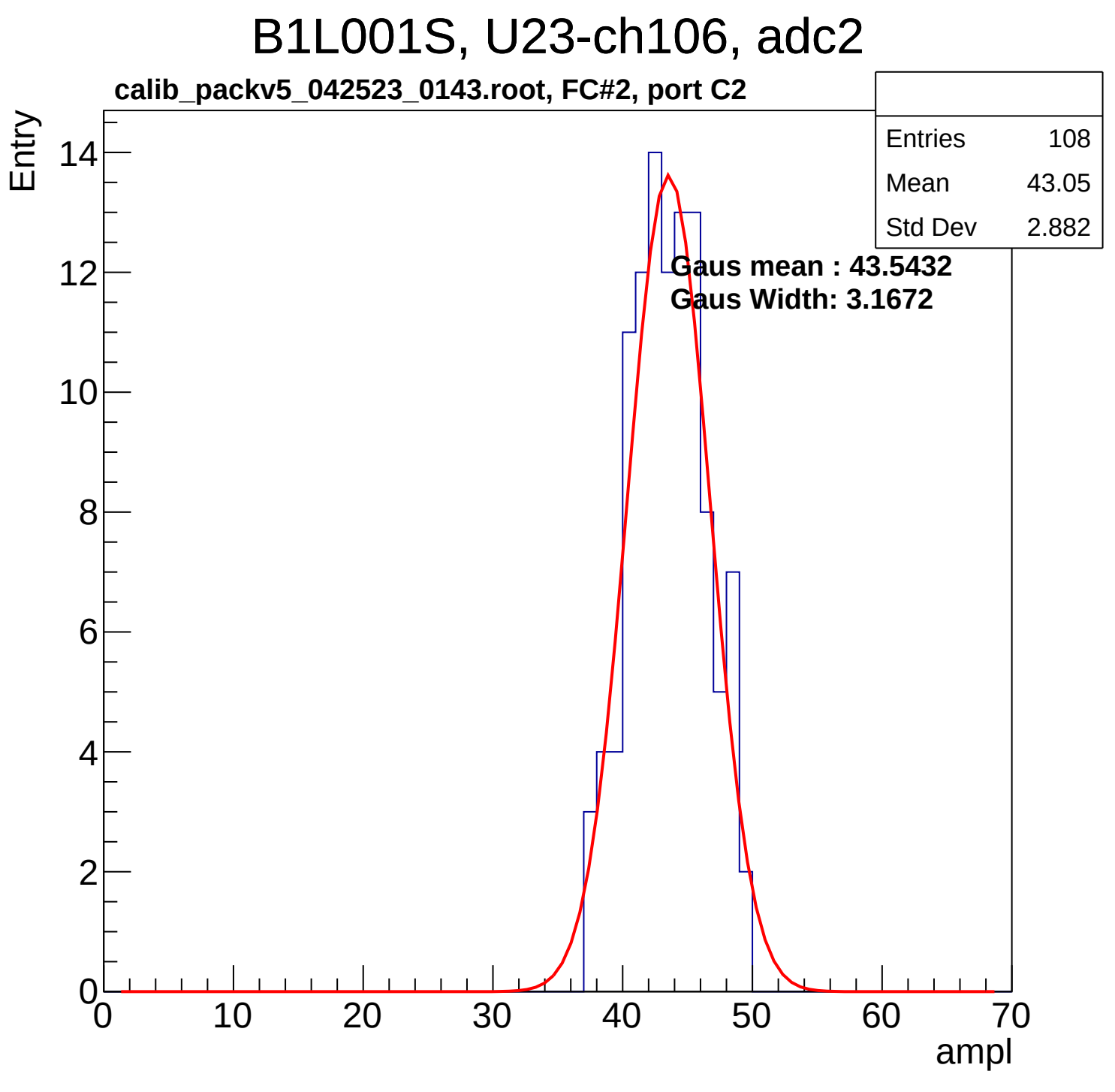
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch106, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 43.05
Std Dev 2.882
Gaus mean : 43.5432
Gaus Width: 3.1672
Entry
ampl
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2
4
6
8
10
12
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0
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60
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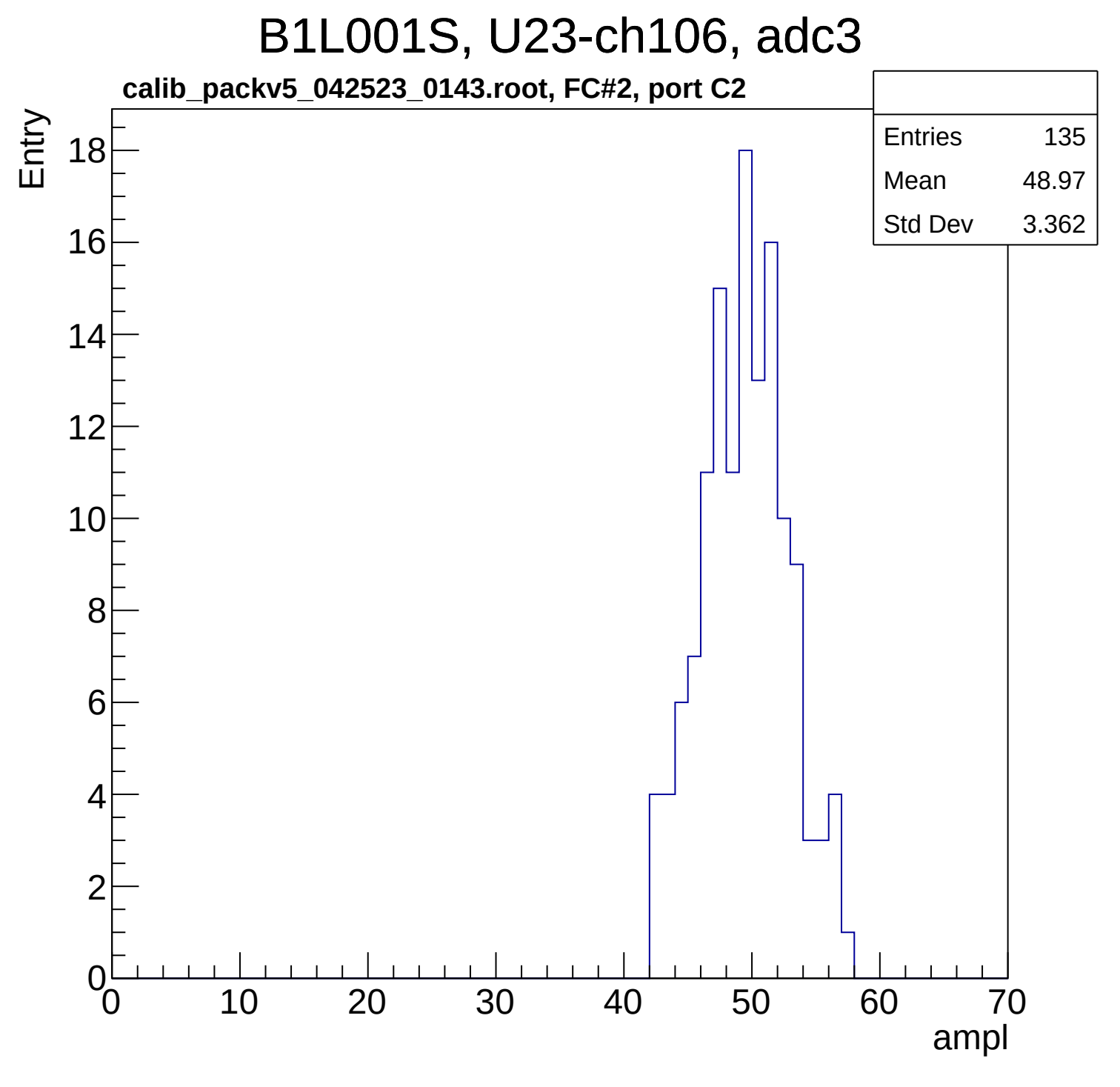
B1L001S, U23-ch106, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 48.97
Std Dev 3.362
Entry
ampl
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8
10
12
14
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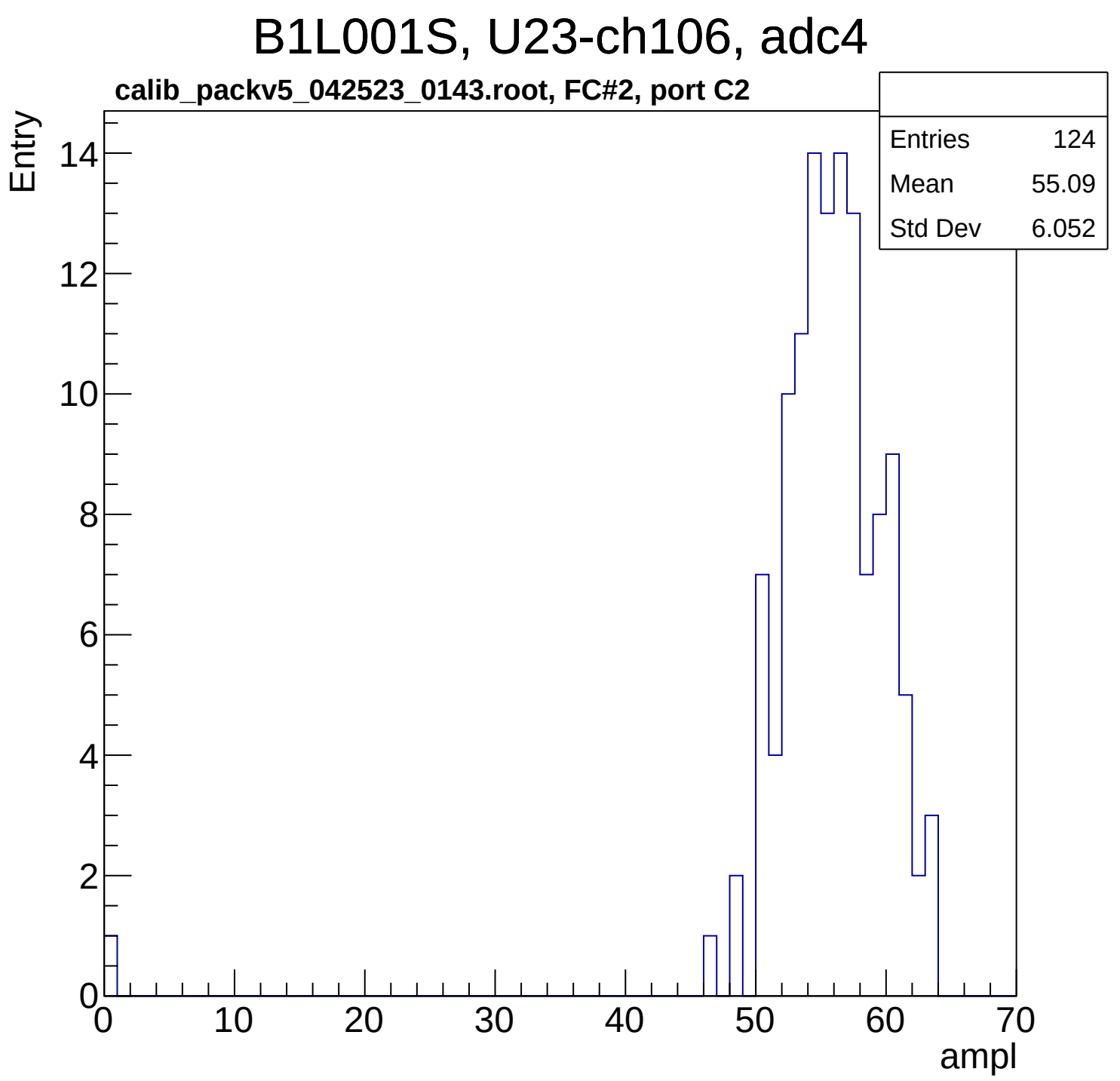
B1L001S, U23-ch106, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 55.09
Std Dev 6.052
Entry
ampl
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4
6
8
10
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0
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40
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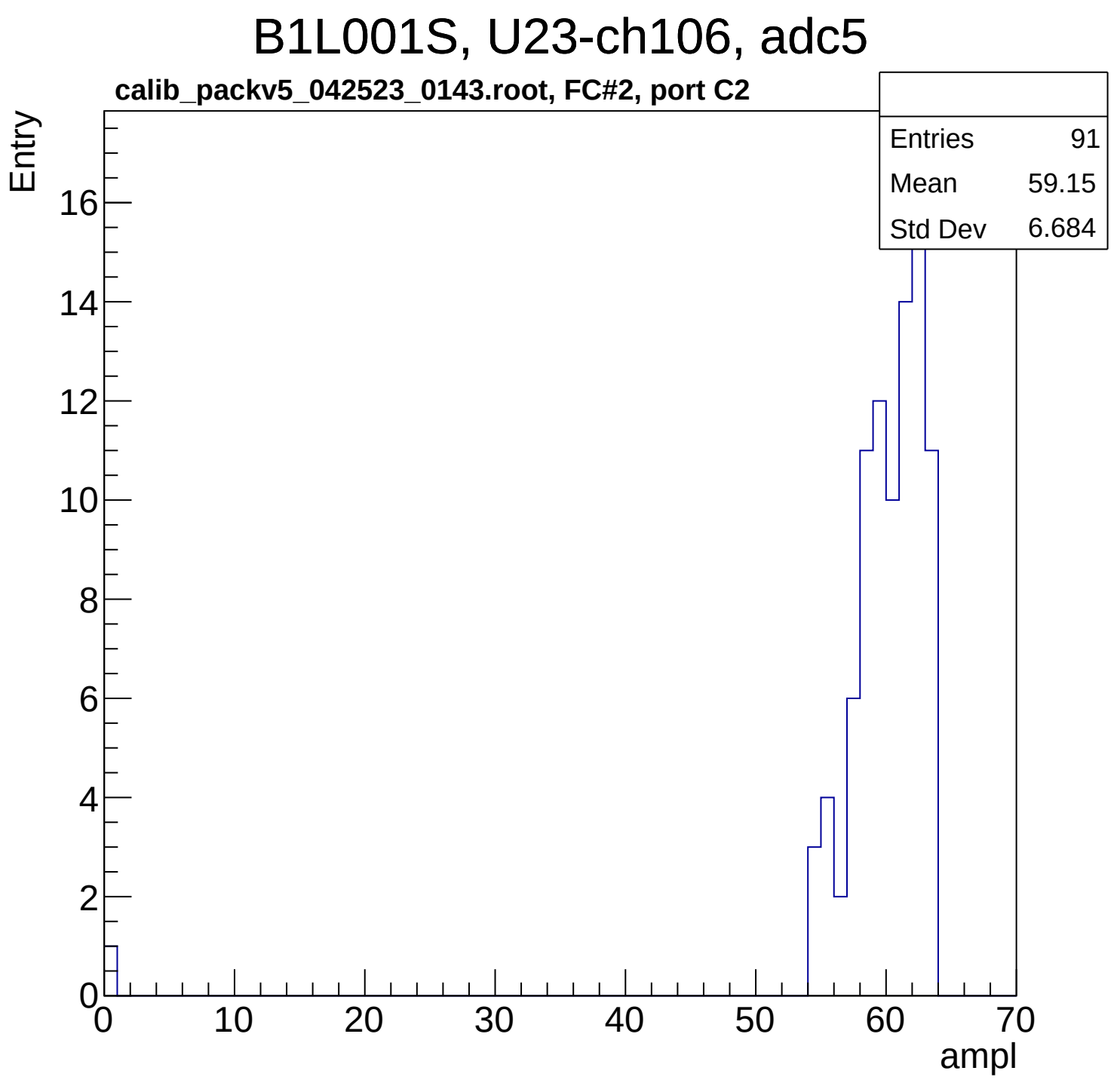
B1L001S, U23-ch106, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.15
Std Dev 6.684
Entry
ampl
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6
8
10
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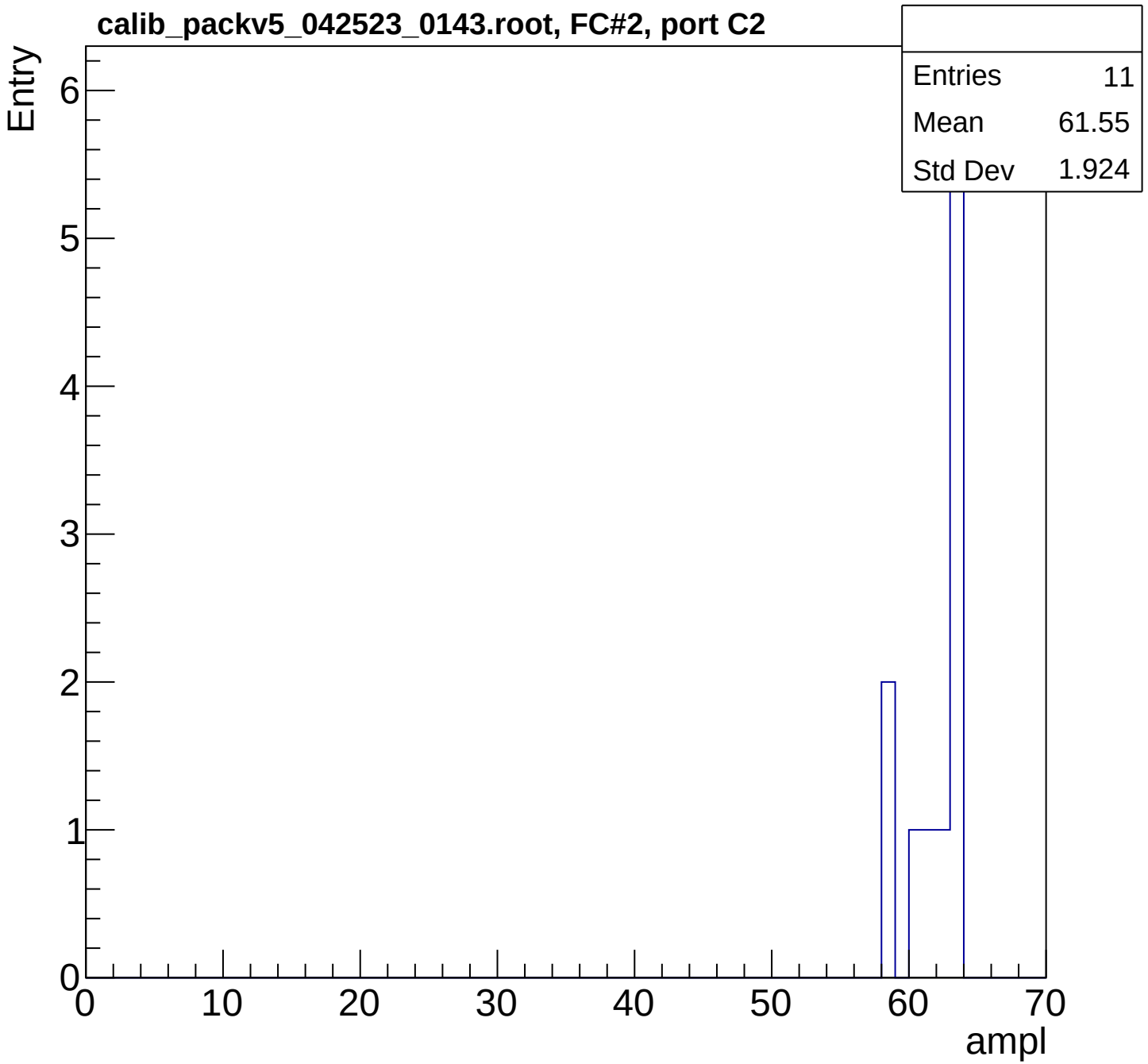
B1L001S, U23-ch106, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 61.55
Std Dev 1.924
Entry
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2
3
4
5
6
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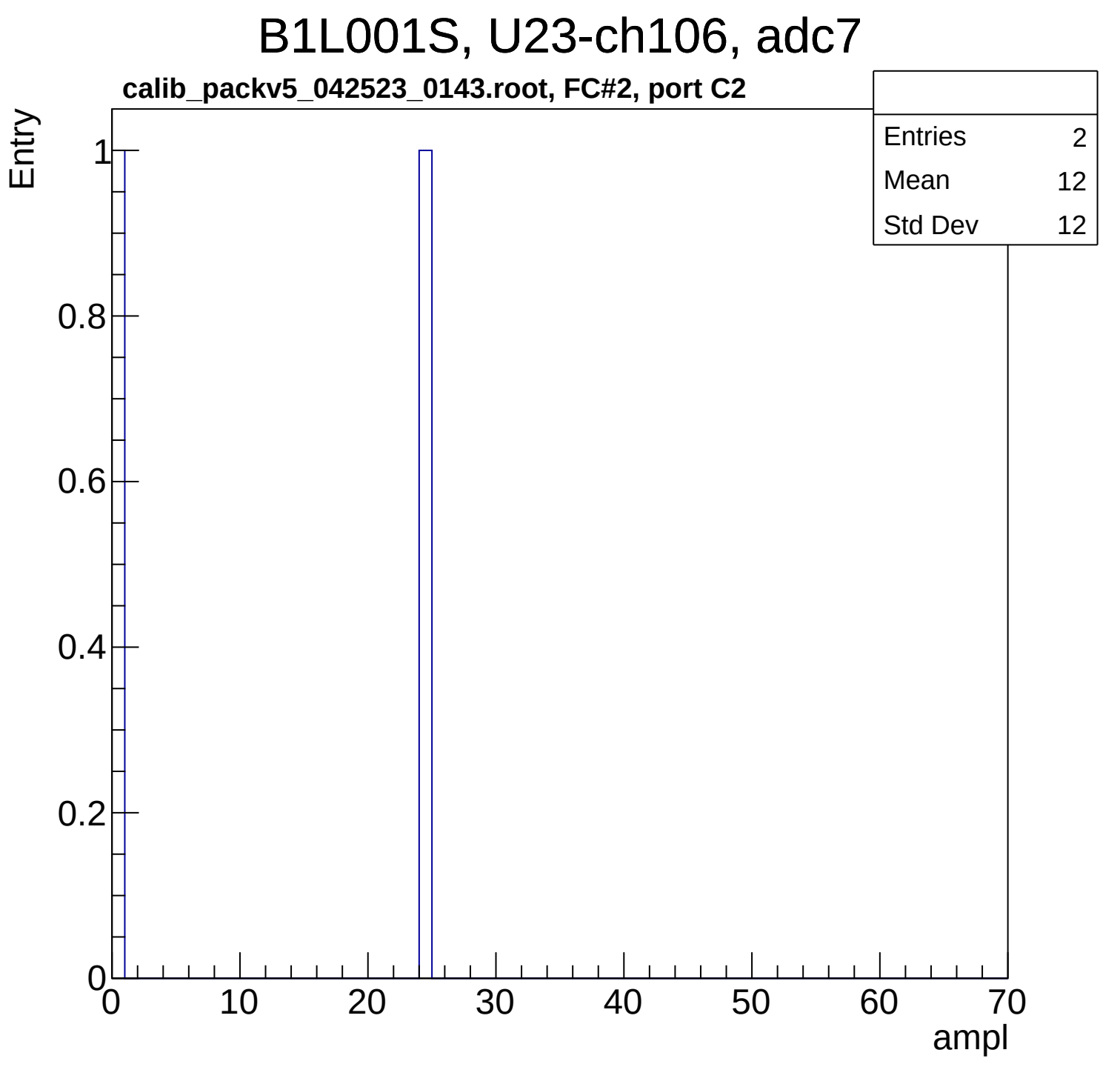
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch106, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 12
Std Dev 12
Entry
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0.6
0.4
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10
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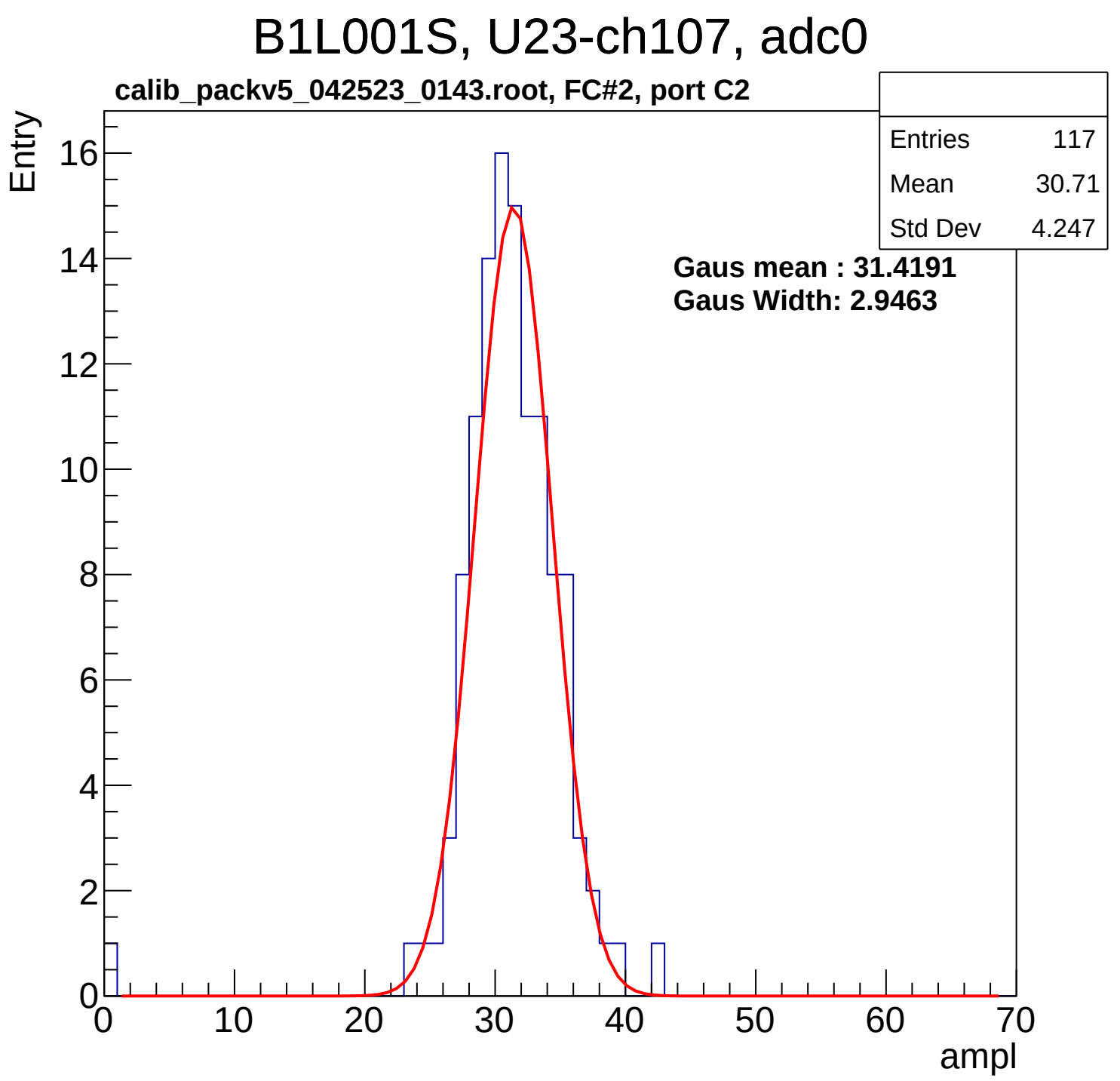
50
60
70
ampl


B1L001S, U23-ch107, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 30.71
Std Dev 4.247
Gaus mean : 31.4191
Gaus Width: 2.9463
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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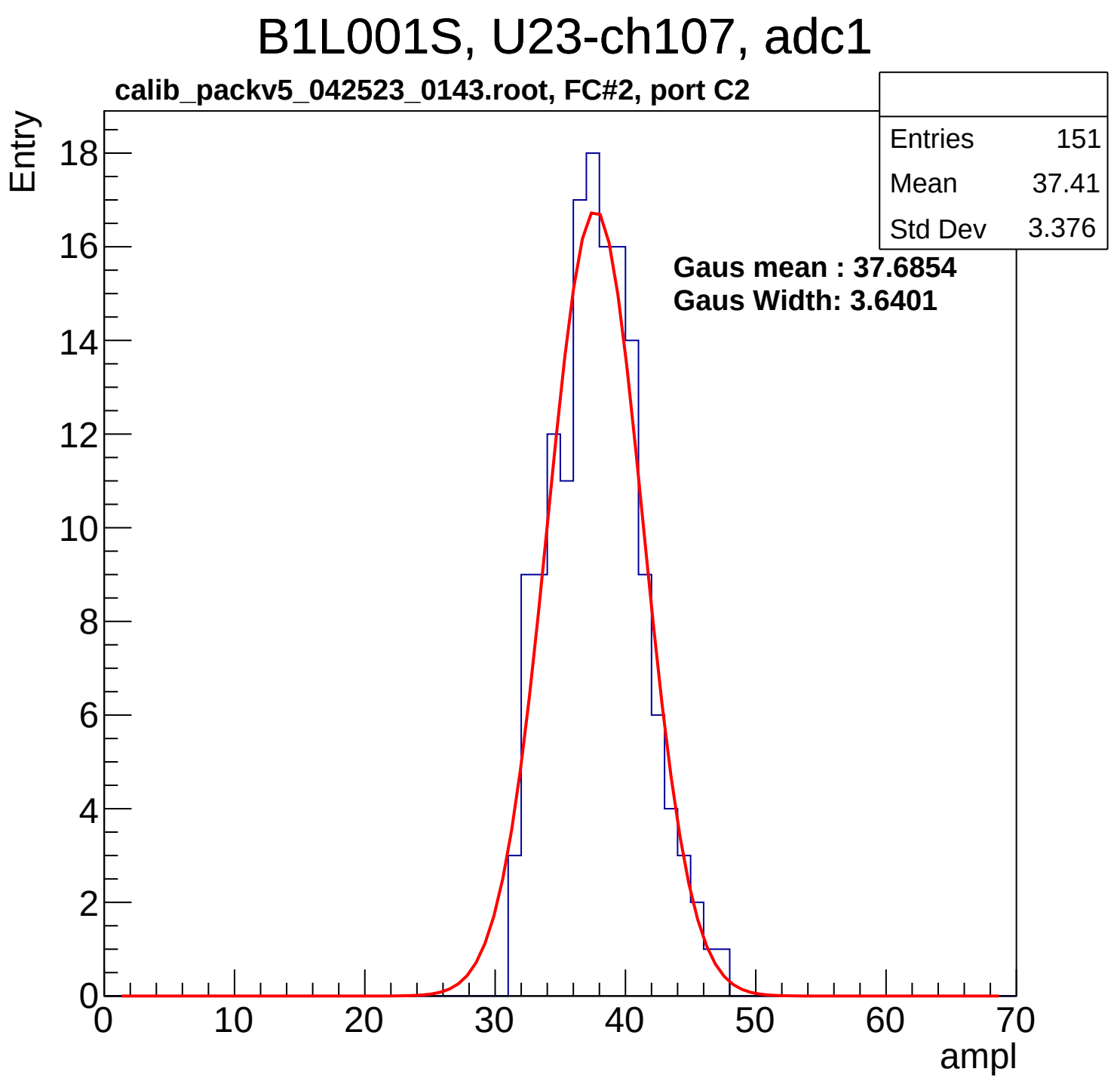
B1L001S, U23-ch107, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 37.41
Std Dev 3.376
Gaus mean : 37.6854
Gaus Width: 3.6401
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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60
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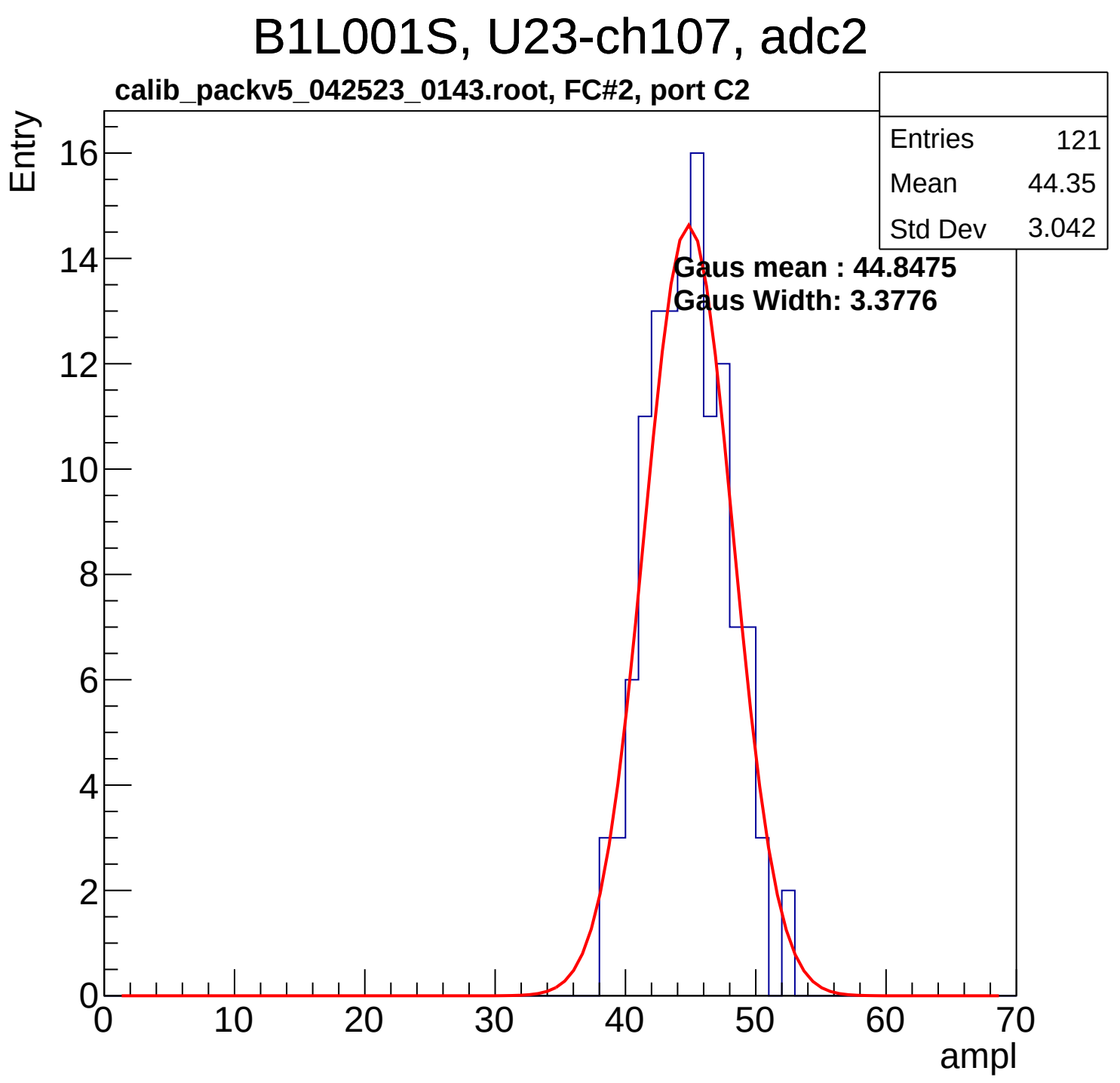
B1L001S, U23-ch107, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 44.35
Std Dev 3.042
Gaus mean : 44.8475
Gaus Width: 3.3776
Entry
ampl
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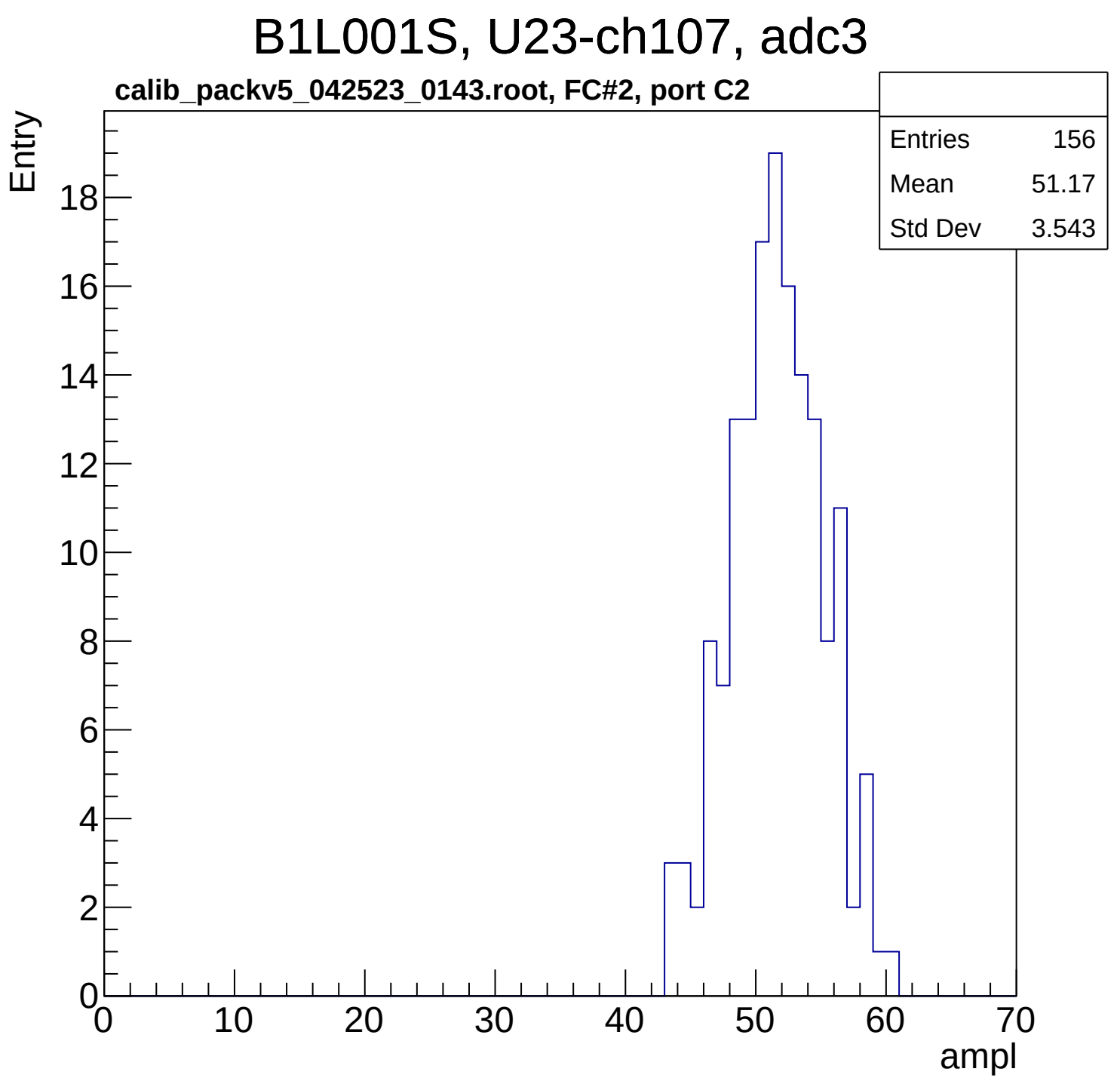
B1L001S, U23-ch107, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 156
Mean 51.17
Std Dev 3.543
Entry
ampl
0
2
4
6
8
10
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16
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0
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40
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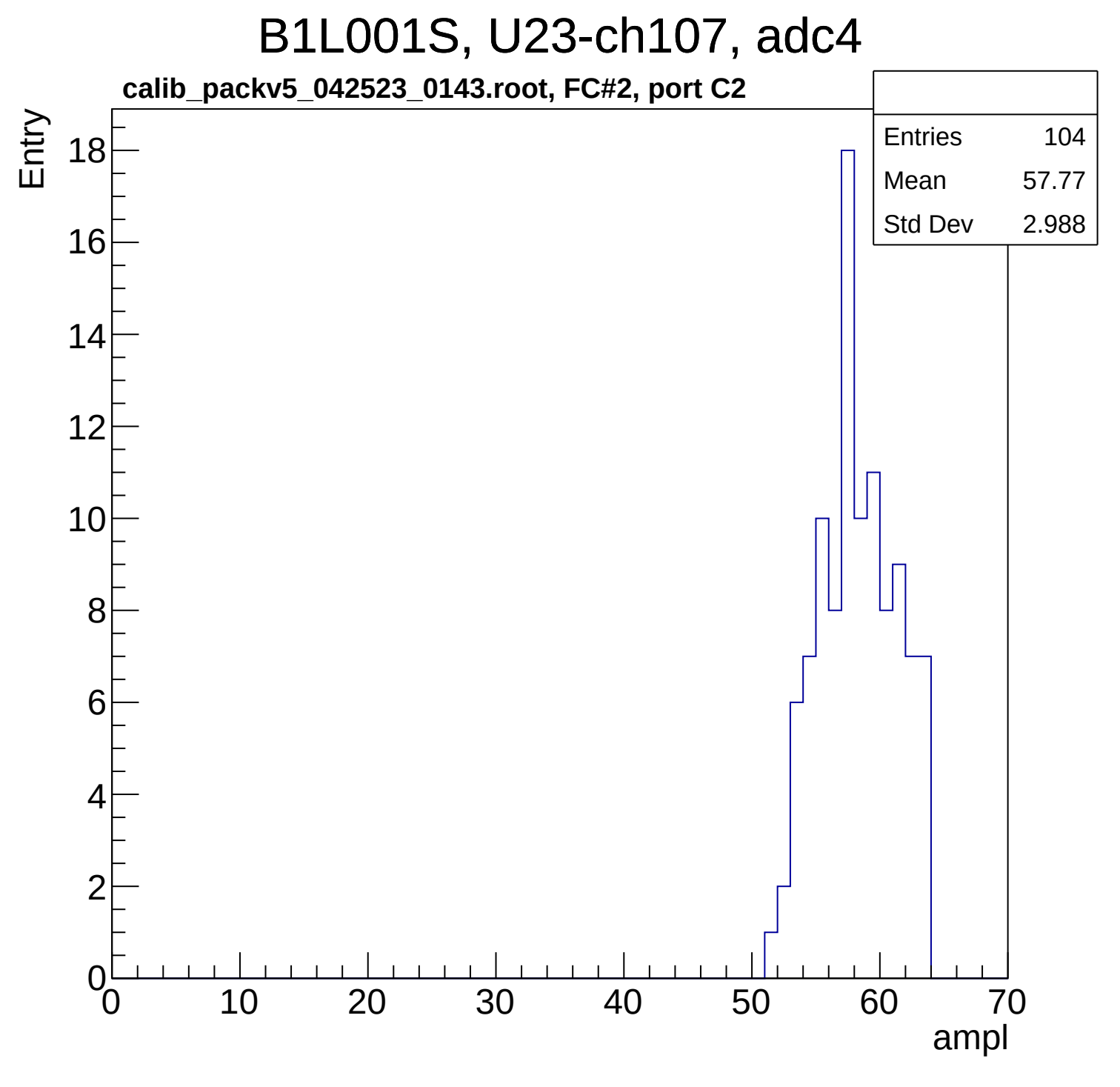
60
70


B1L001S, U23-ch107, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 104
Mean 57.77
Std Dev 2.988
Entry
ampl
0
2
4
6
8
10
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14
16
18
0
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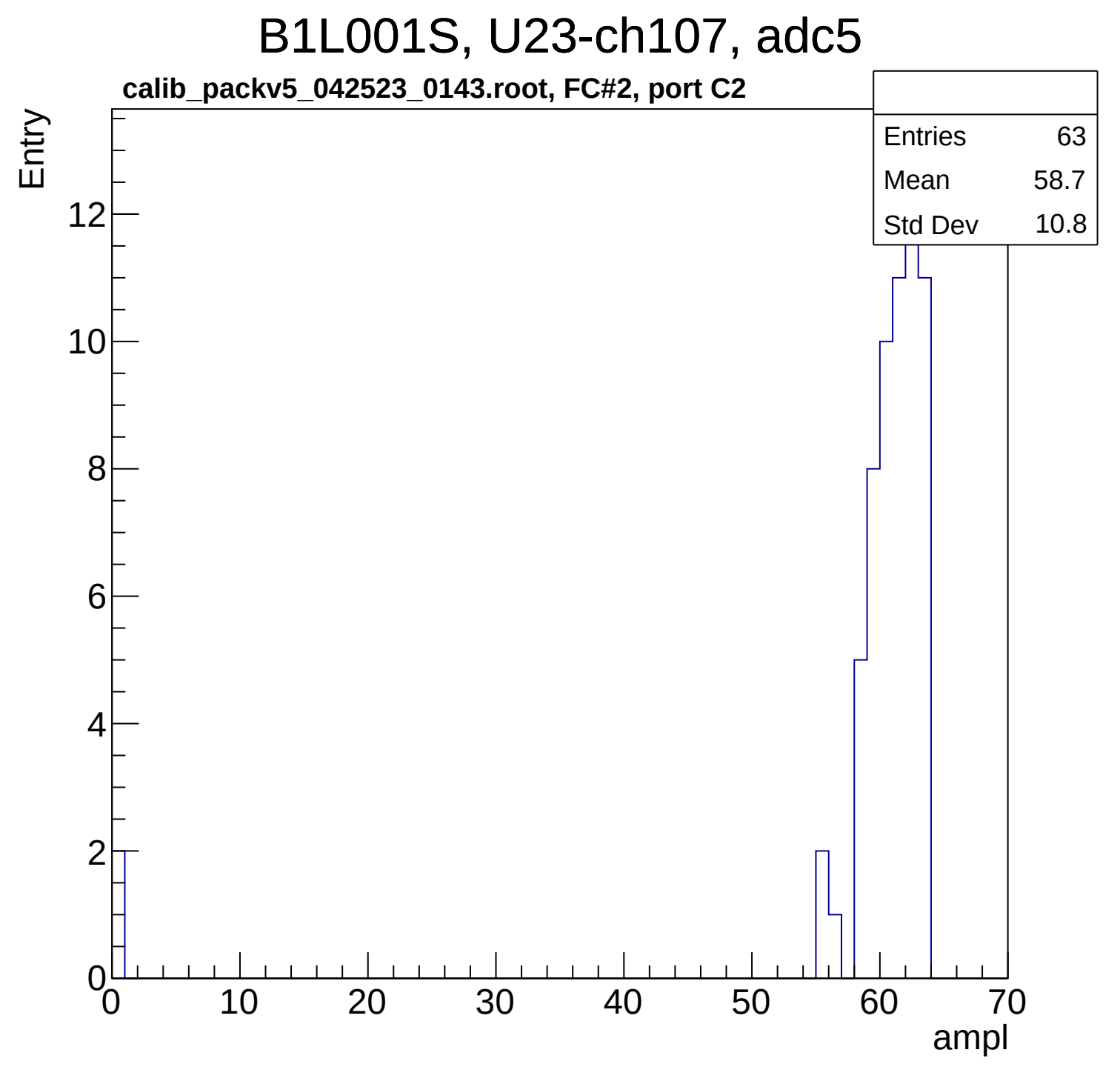
B1L001S, U23-ch107, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 63
Mean 58.7
Std Dev 10.8
Entry
0
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4
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8
10
12
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10
20
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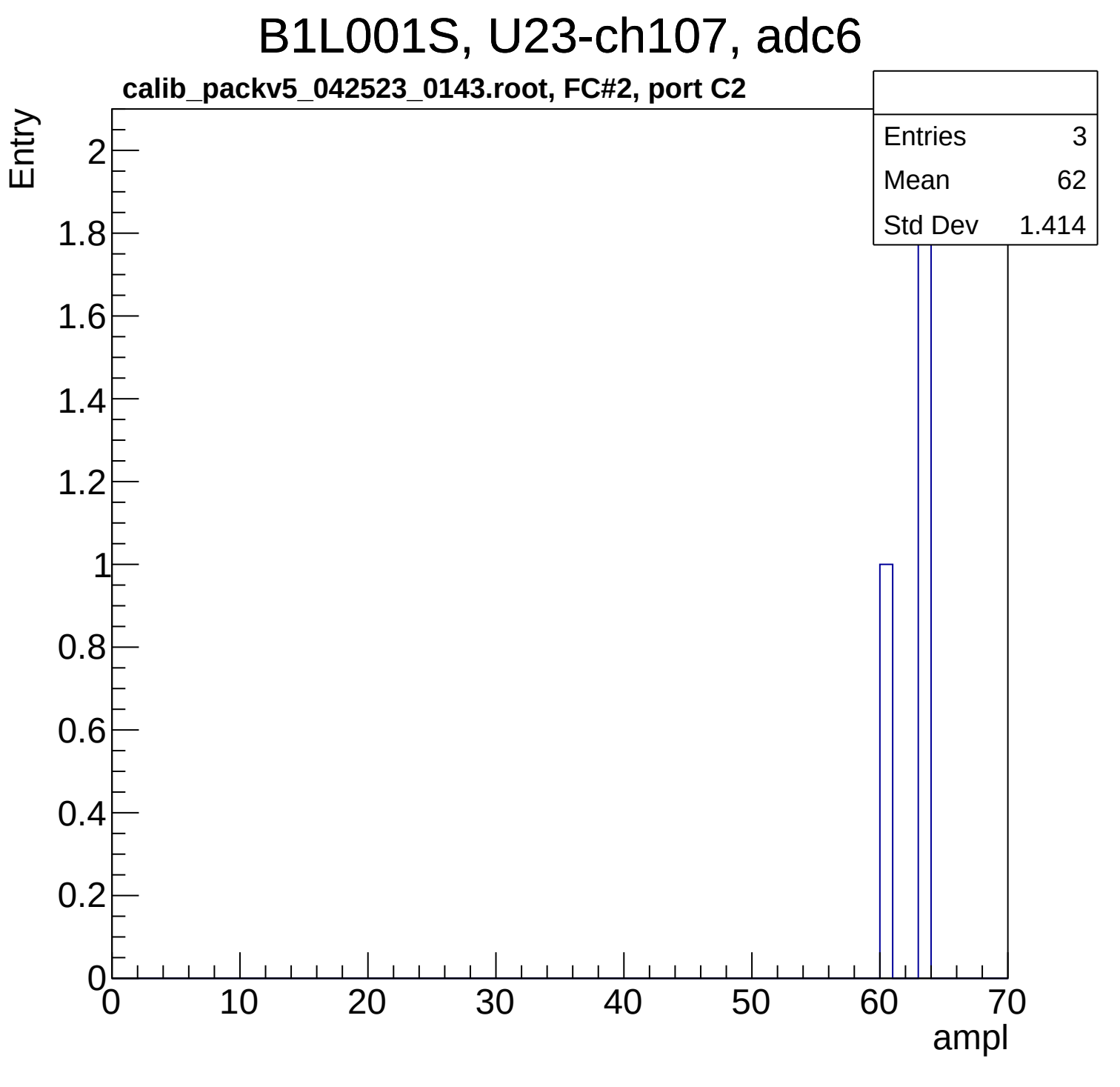
50
60
70
ampl


B1L001S, U23-ch107, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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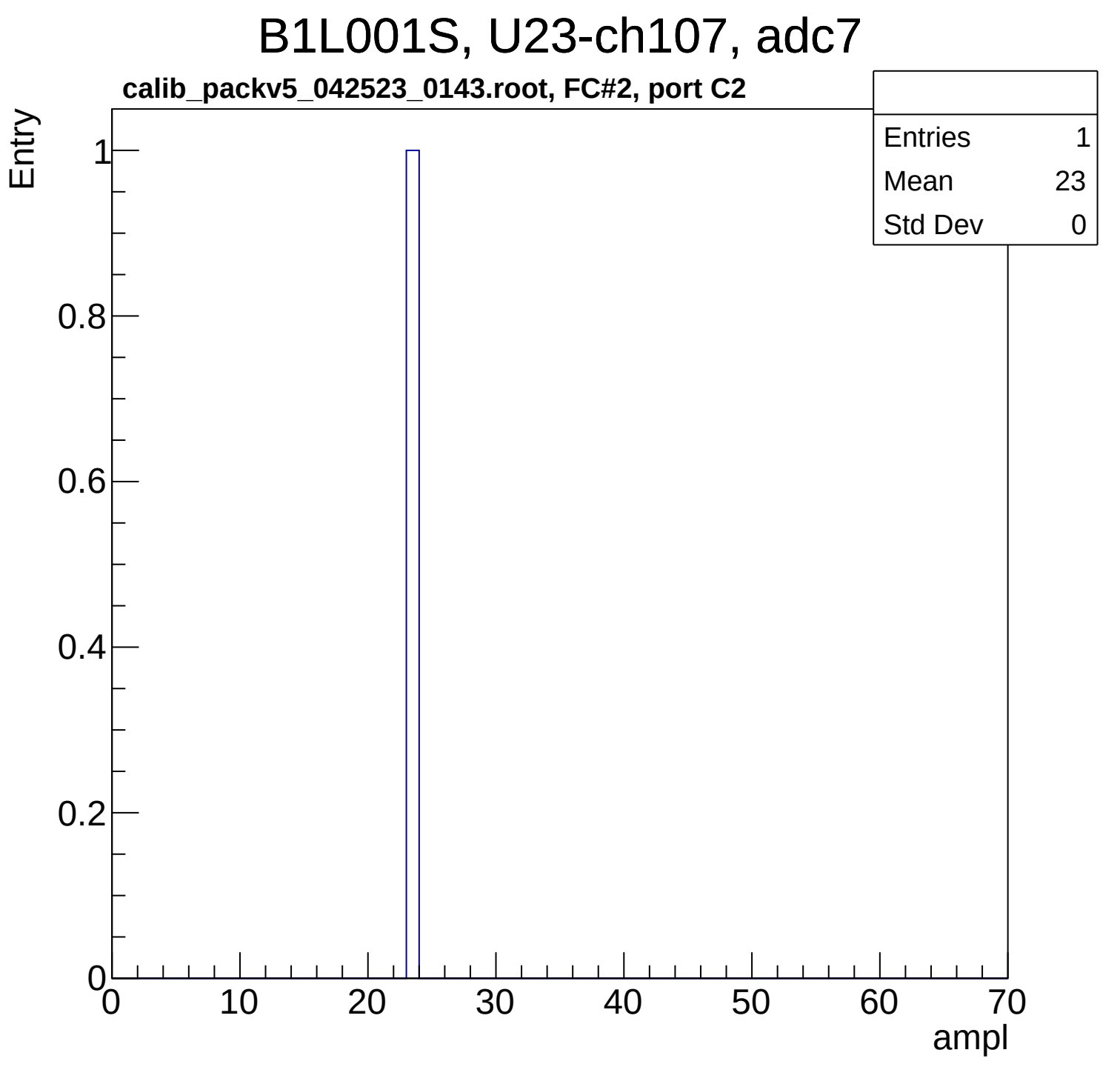
B1L001S, U23-ch107, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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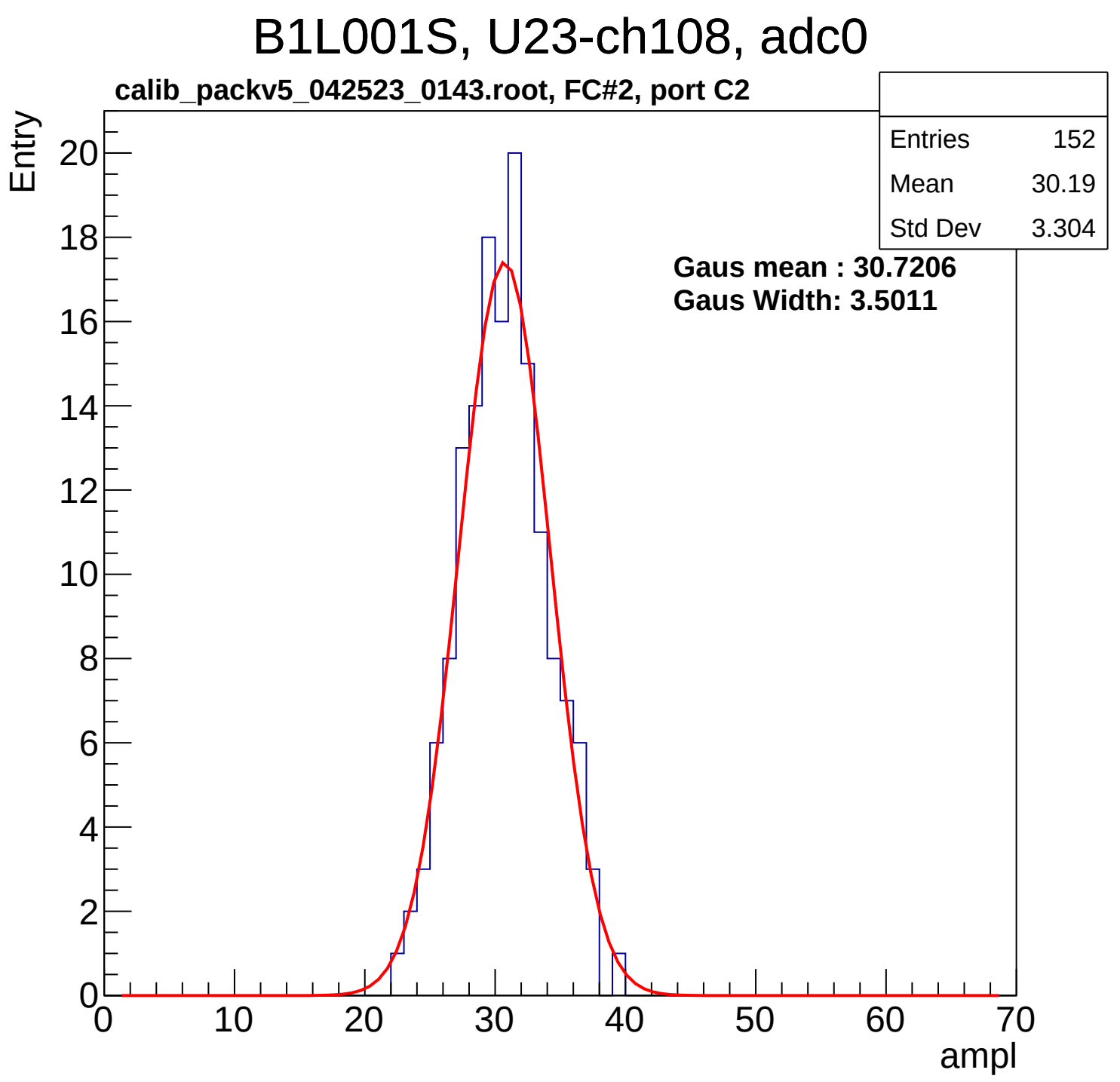
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch108, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 152
Mean 30.19
Std Dev 3.304
Gaus mean : 30.7206
Gaus Width: 3.5011
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
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20
30
40
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60
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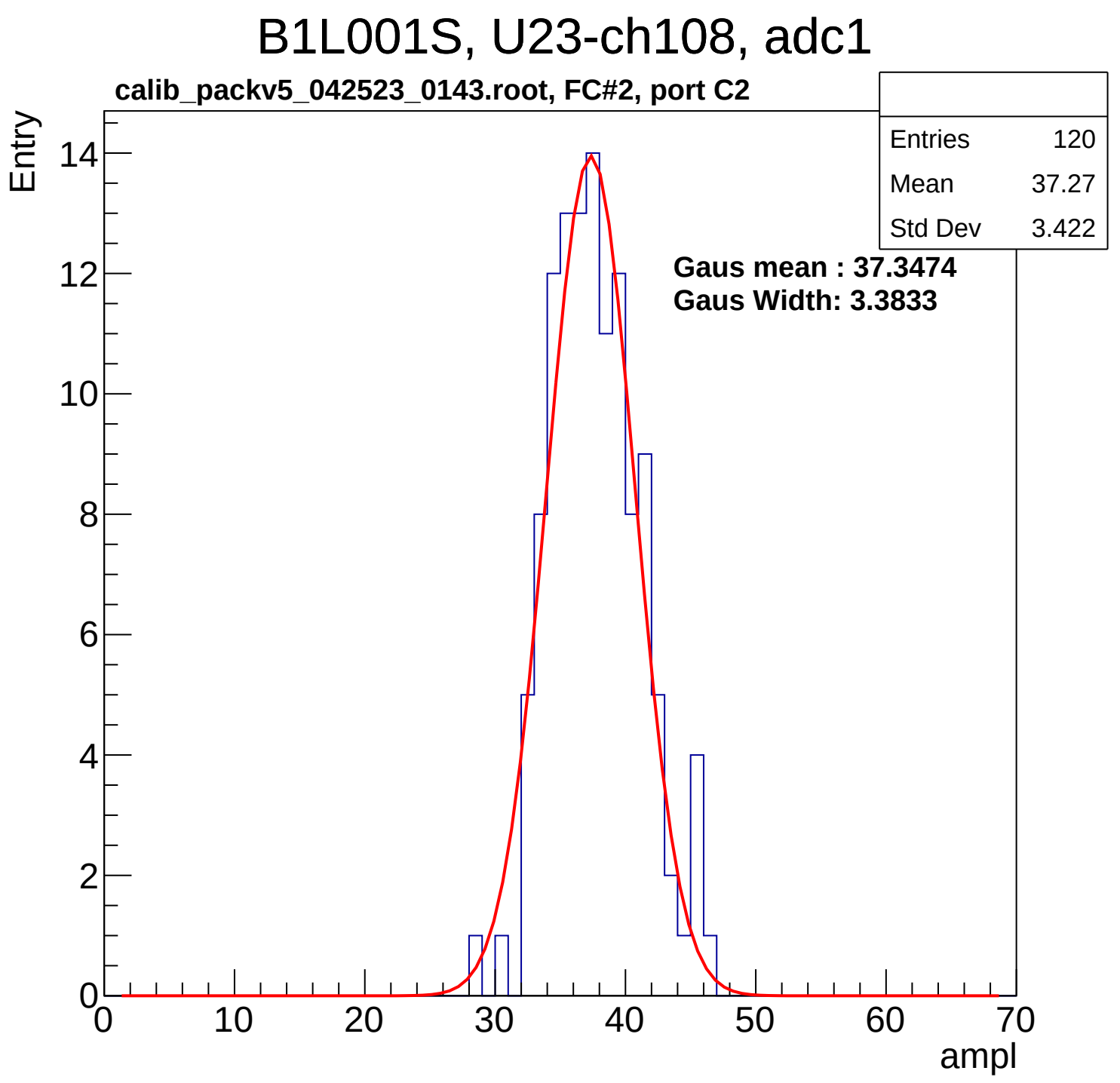
B1L001S, U23-ch108, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 37.27
Std Dev 3.422
Gaus mean : 37.3474
Gaus Width: 3.3833
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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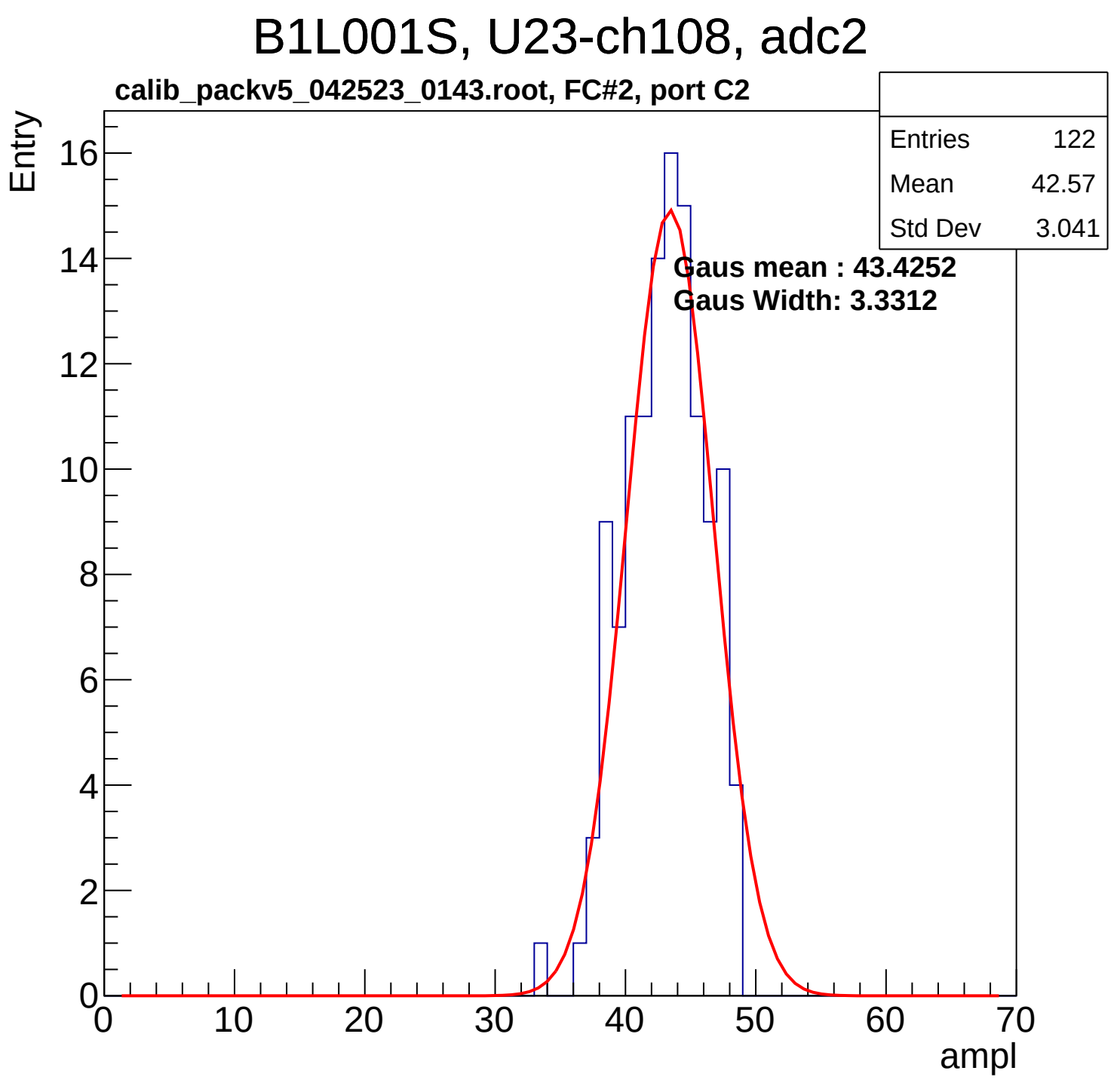
B1L001S, U23-ch108, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 42.57
Std Dev 3.041
Gaus mean : 43.4252
Gaus Width: 3.3312
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
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40
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60
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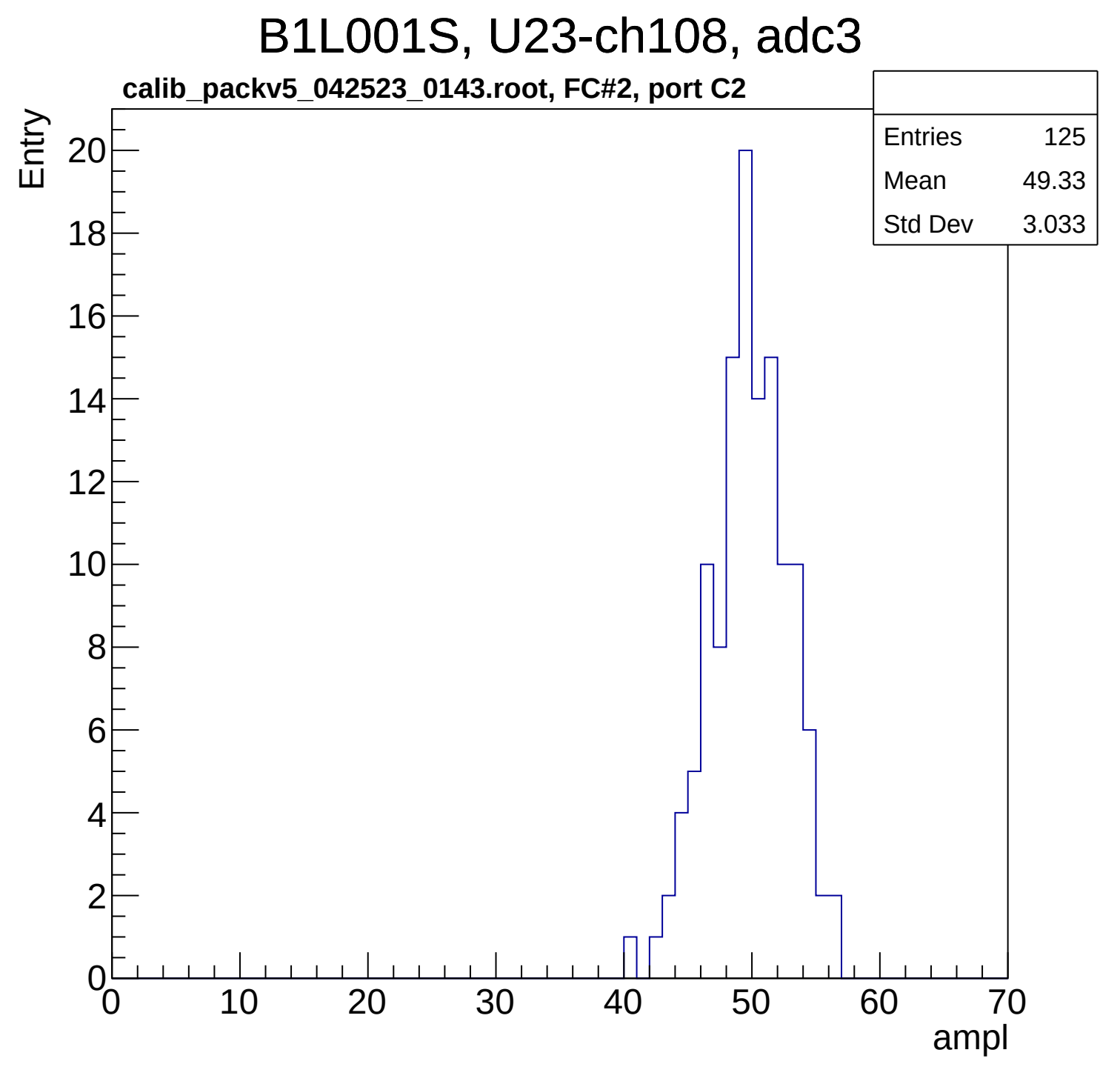
B1L001S, U23-ch108, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 49.33
Std Dev 3.033
Entry
ampl
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2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
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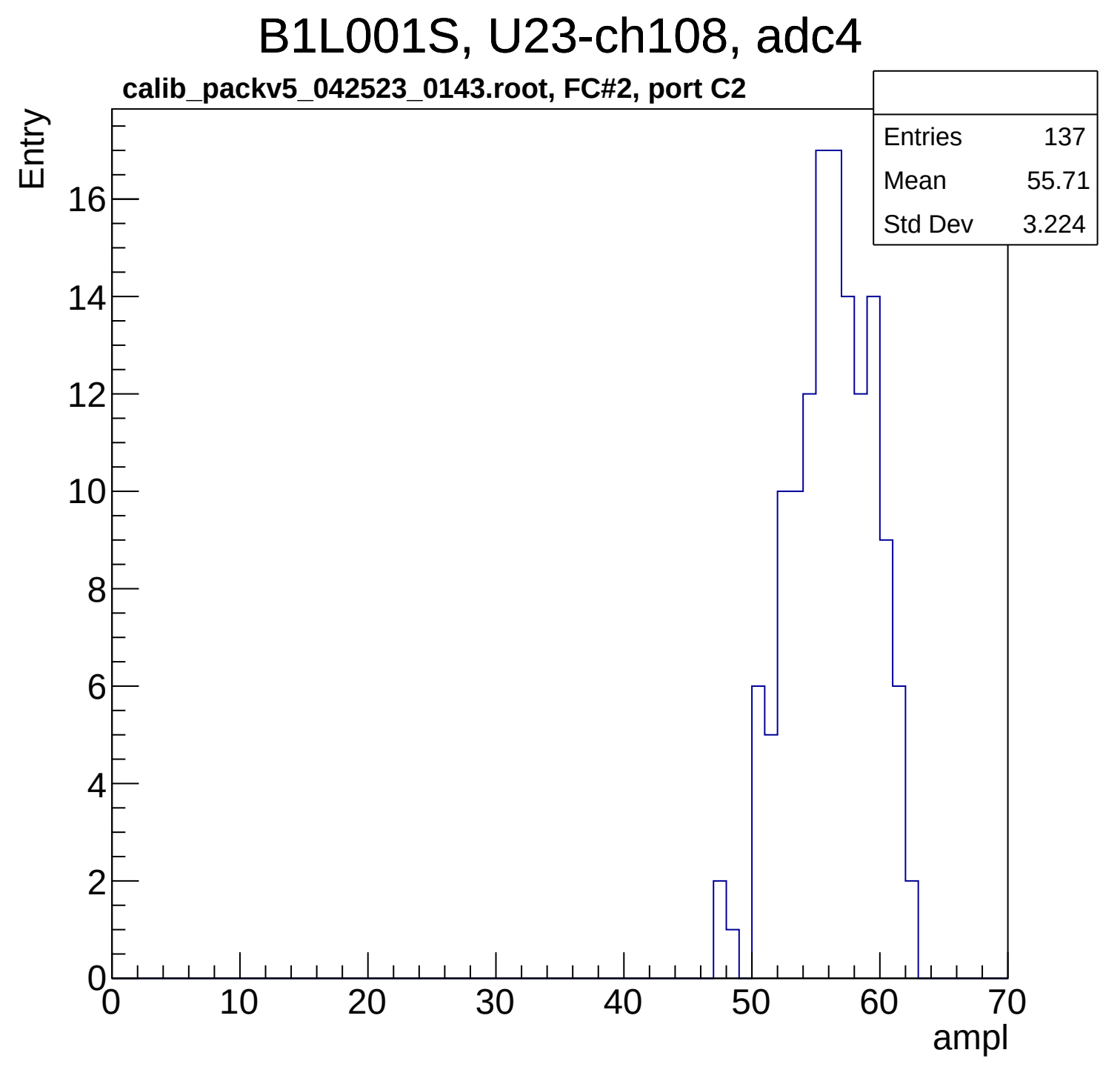
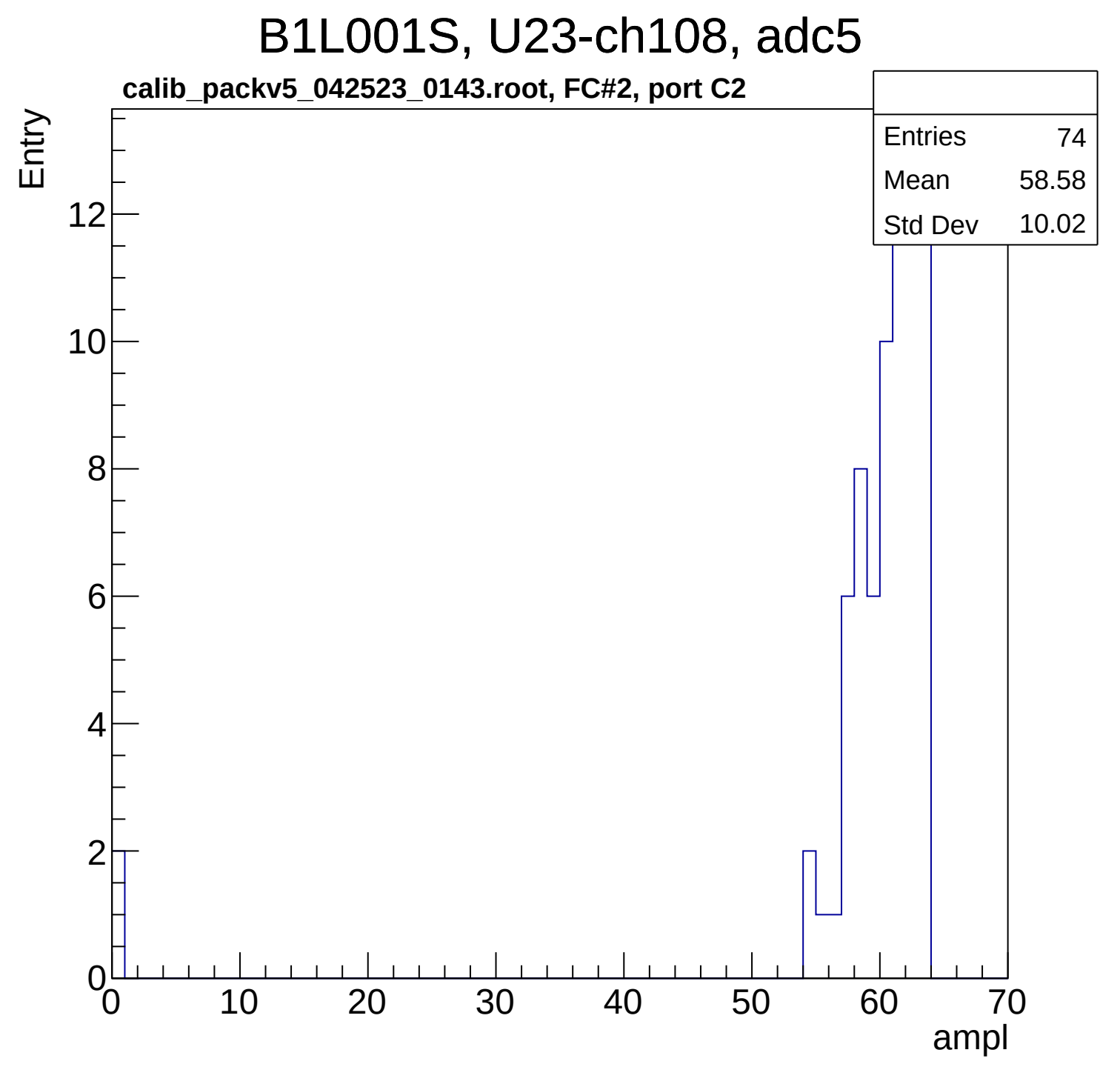
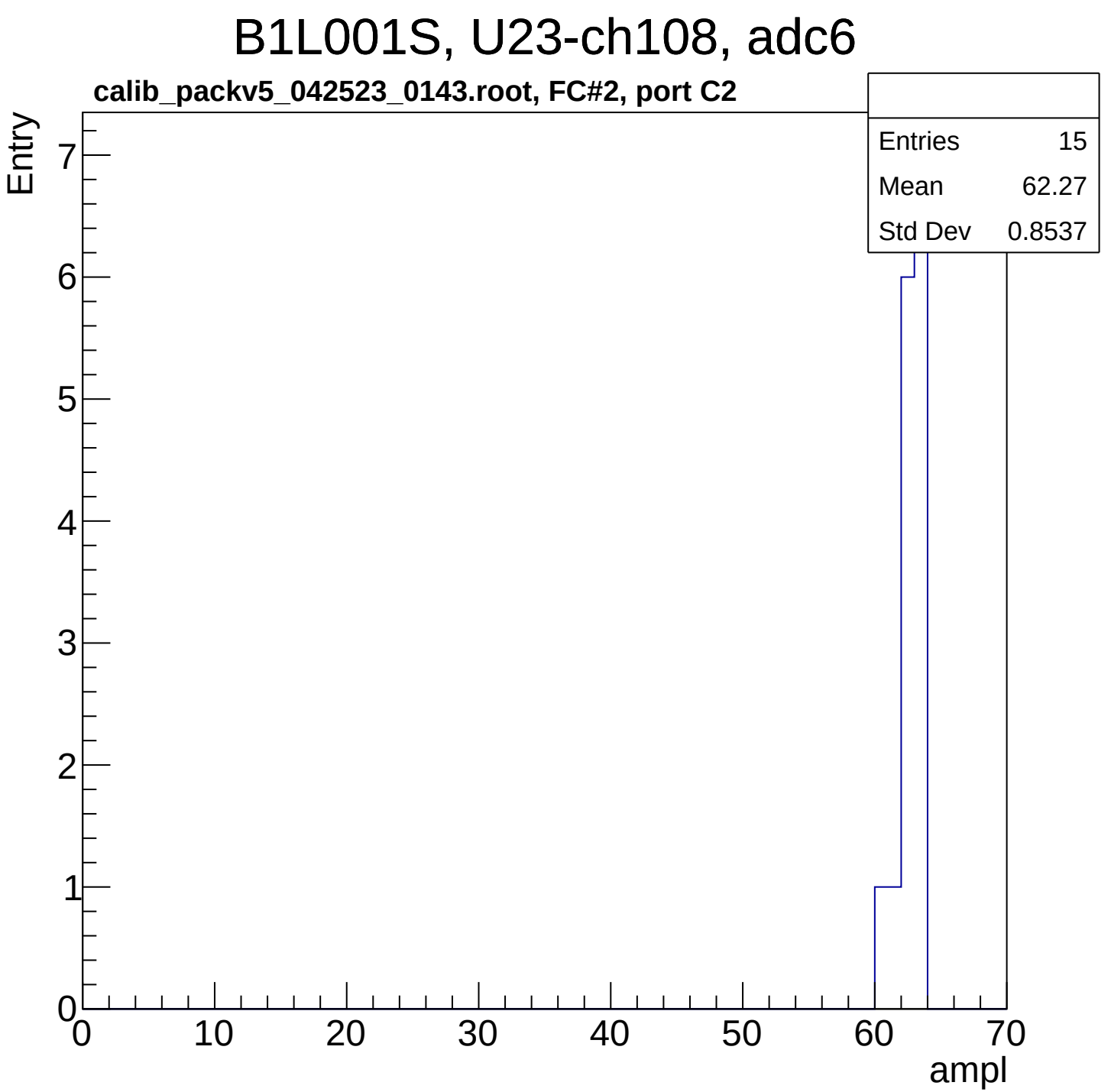
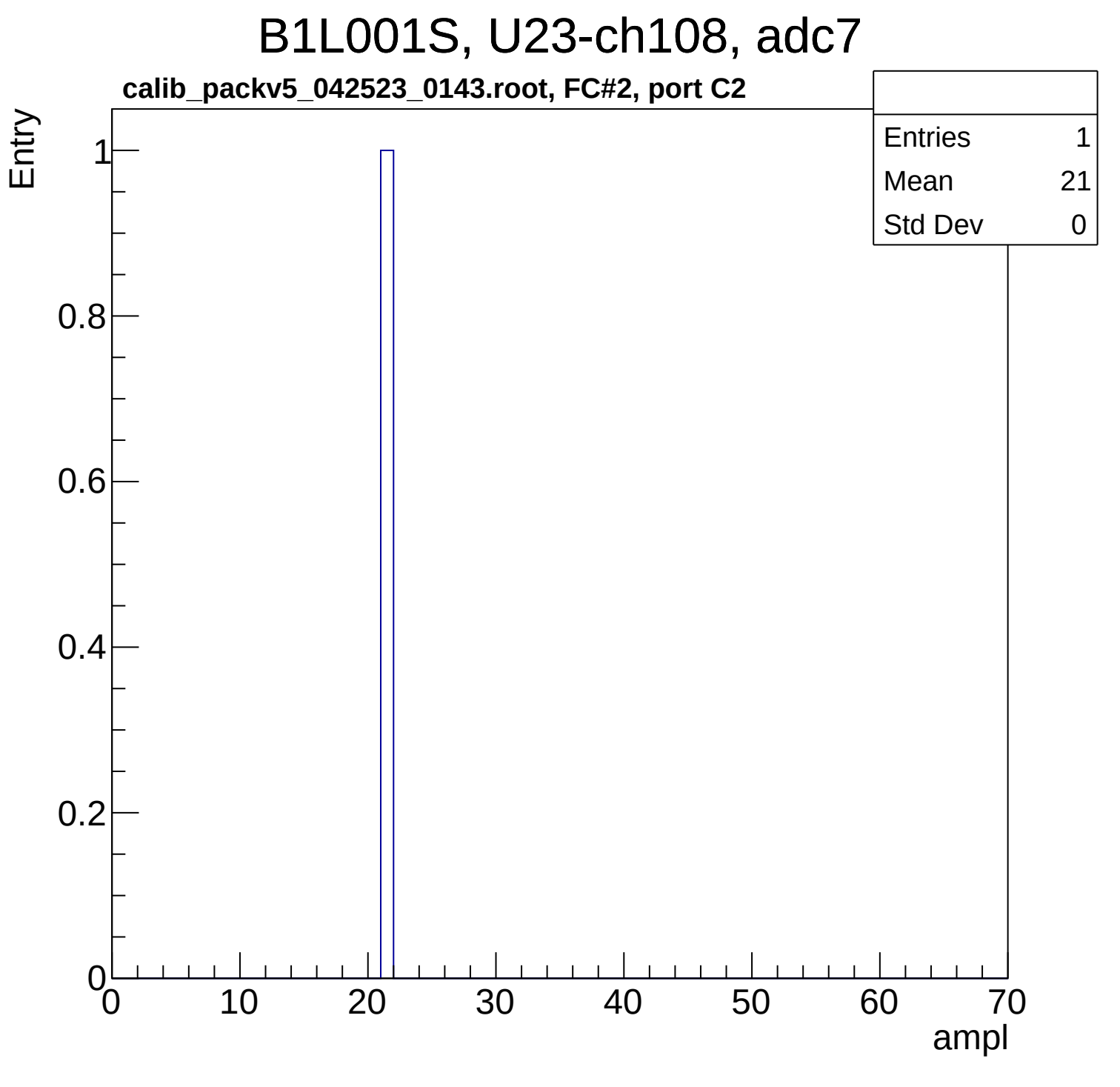
60
70


B1L001S, U23-ch108, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 55.71
Std Dev 3.224
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch108, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 58.58
Std Dev 10.02
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch108, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 62.27
Std Dev 0.8537
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L001S, U23-ch108, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 21
Std Dev 0
Entry
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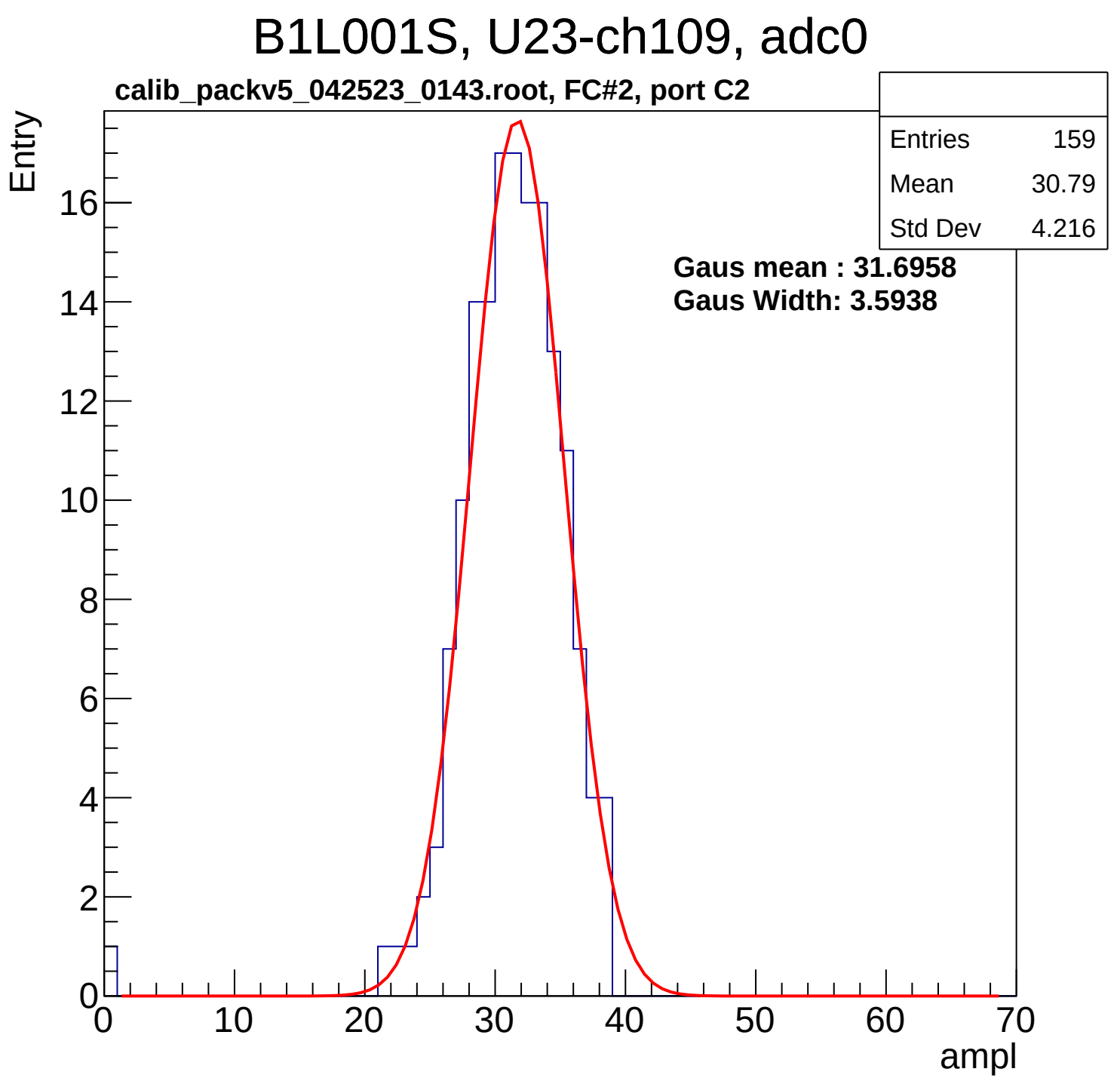
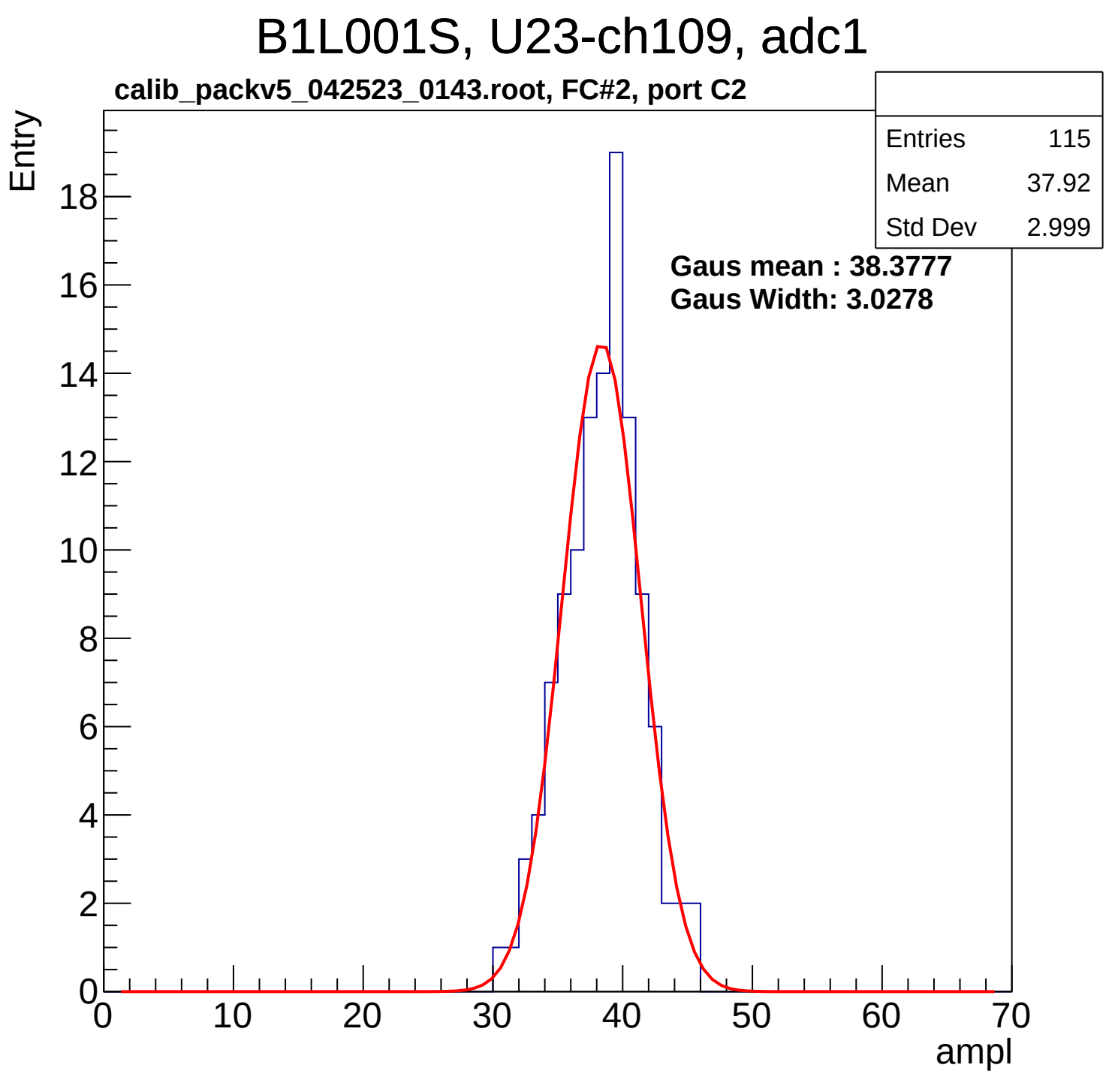
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch109, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 30.79
Std Dev 4.216
Gaus mean : 31.6958
Gaus Width: 3.5938
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch109, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 37.92
Std Dev 2.999
Gaus mean : 38.3777
Gaus Width: 3.0278
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
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60
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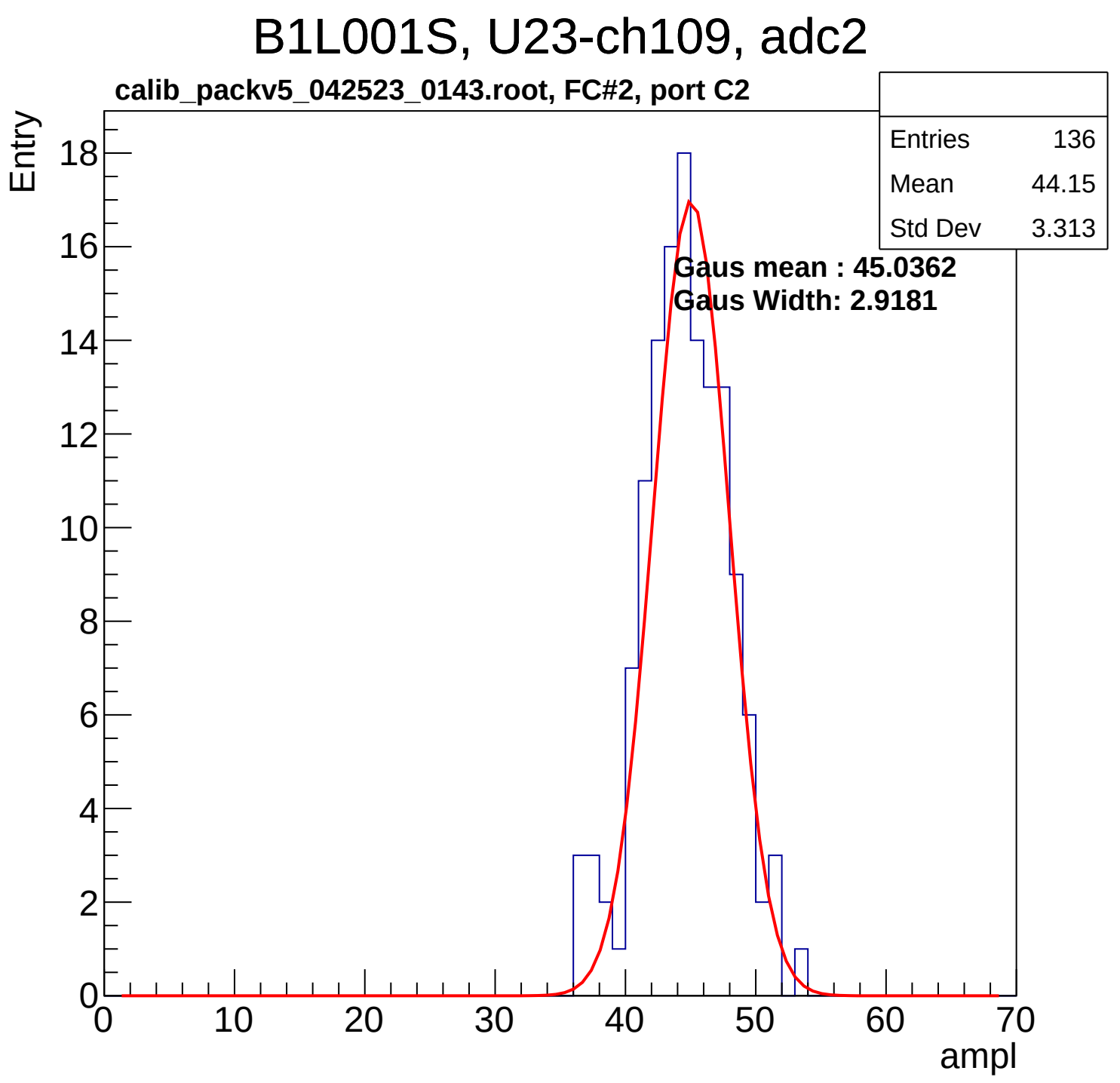
B1L001S, U23-ch109, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 136
Mean 44.15
Std Dev 3.313
Gaus mean : 45.0362
Gaus Width: 2.9181
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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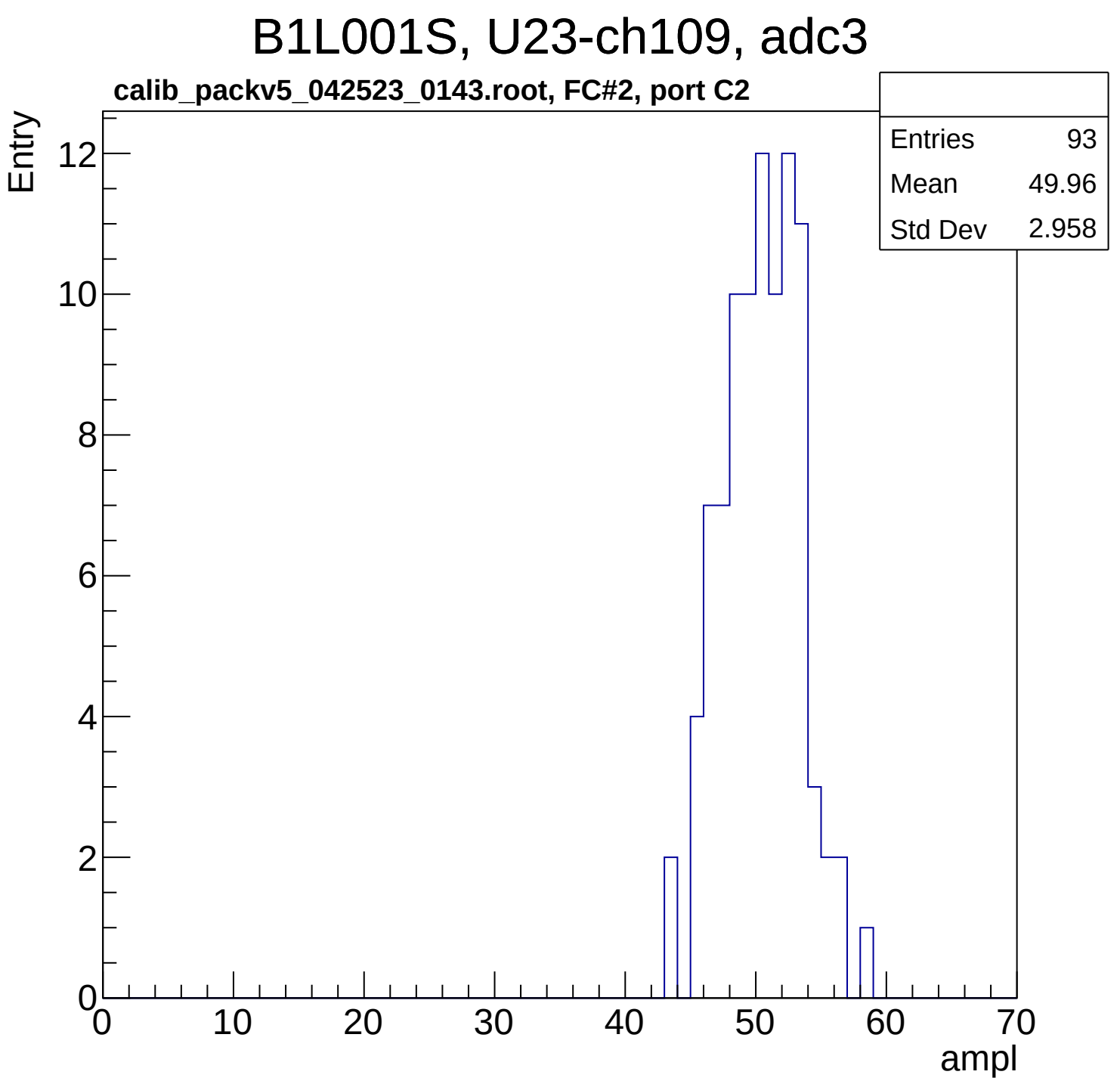
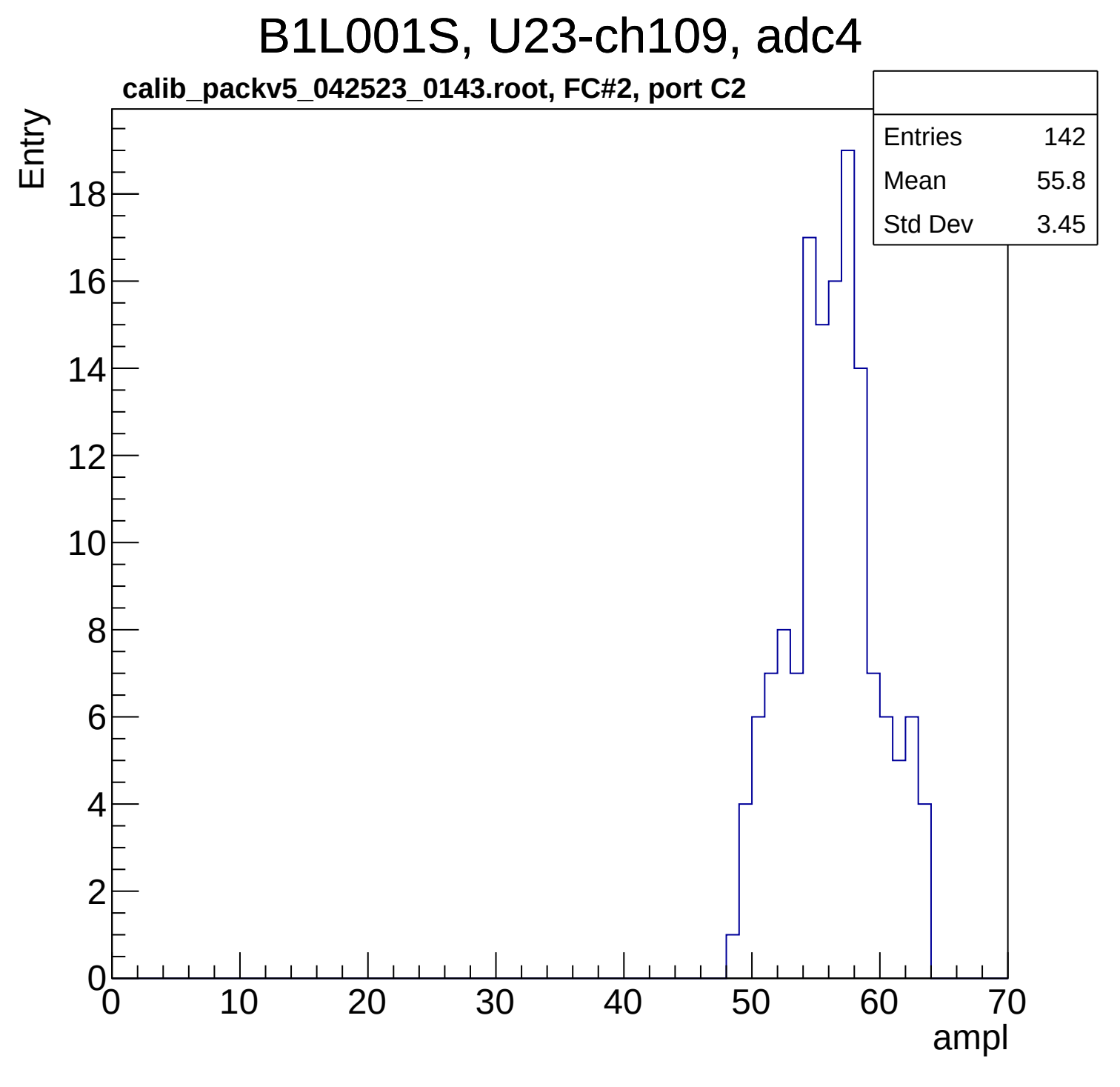

B1L001S, U23-ch109, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 49.96
Std Dev 2.958
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch109, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 55.8
Std Dev 3.45
Entry
0
2
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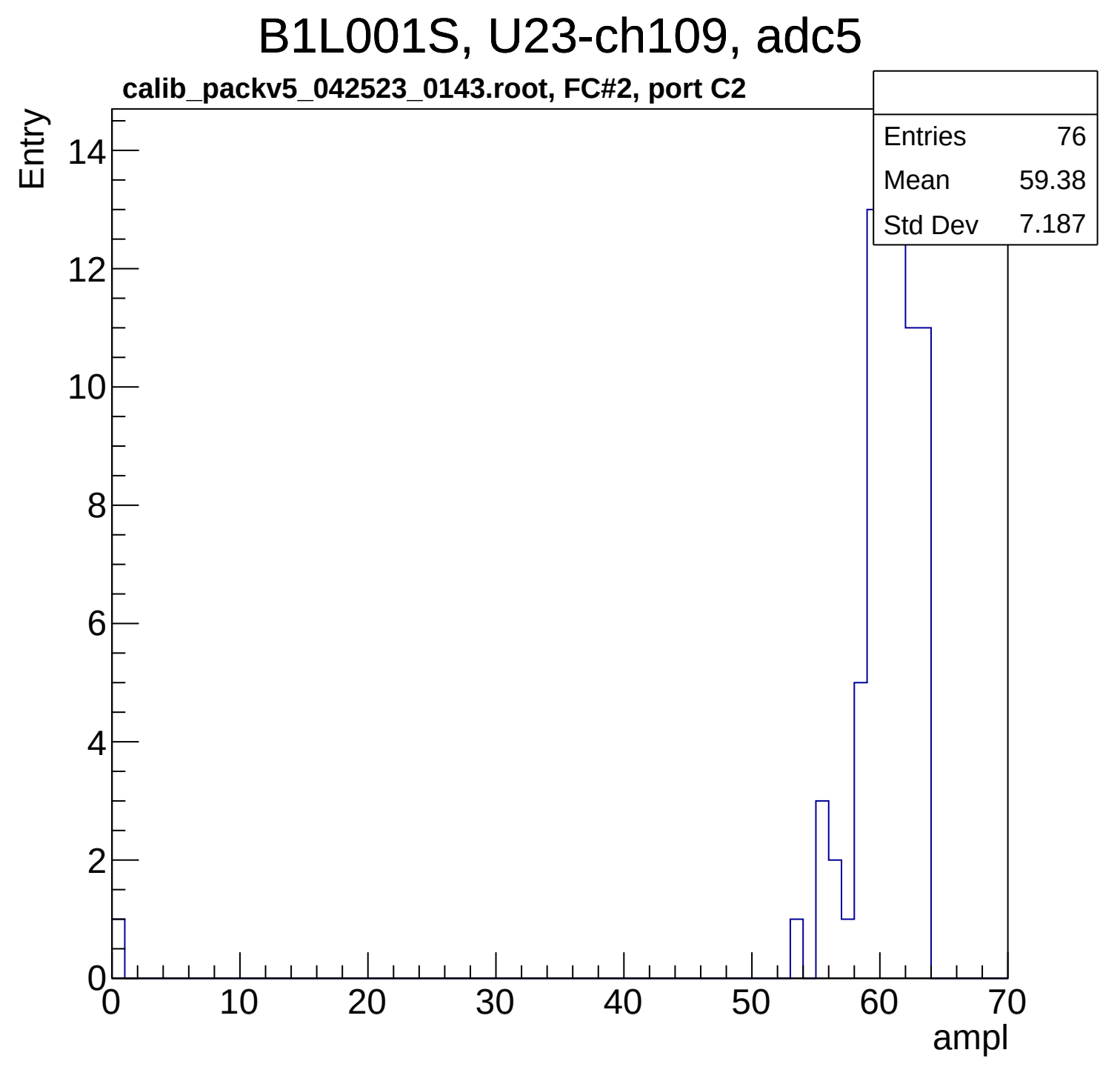
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch109, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 76
Mean 59.38
Std Dev 7.187
Entry
ampl
0
2
4
6
8
10
12
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0
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20
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60
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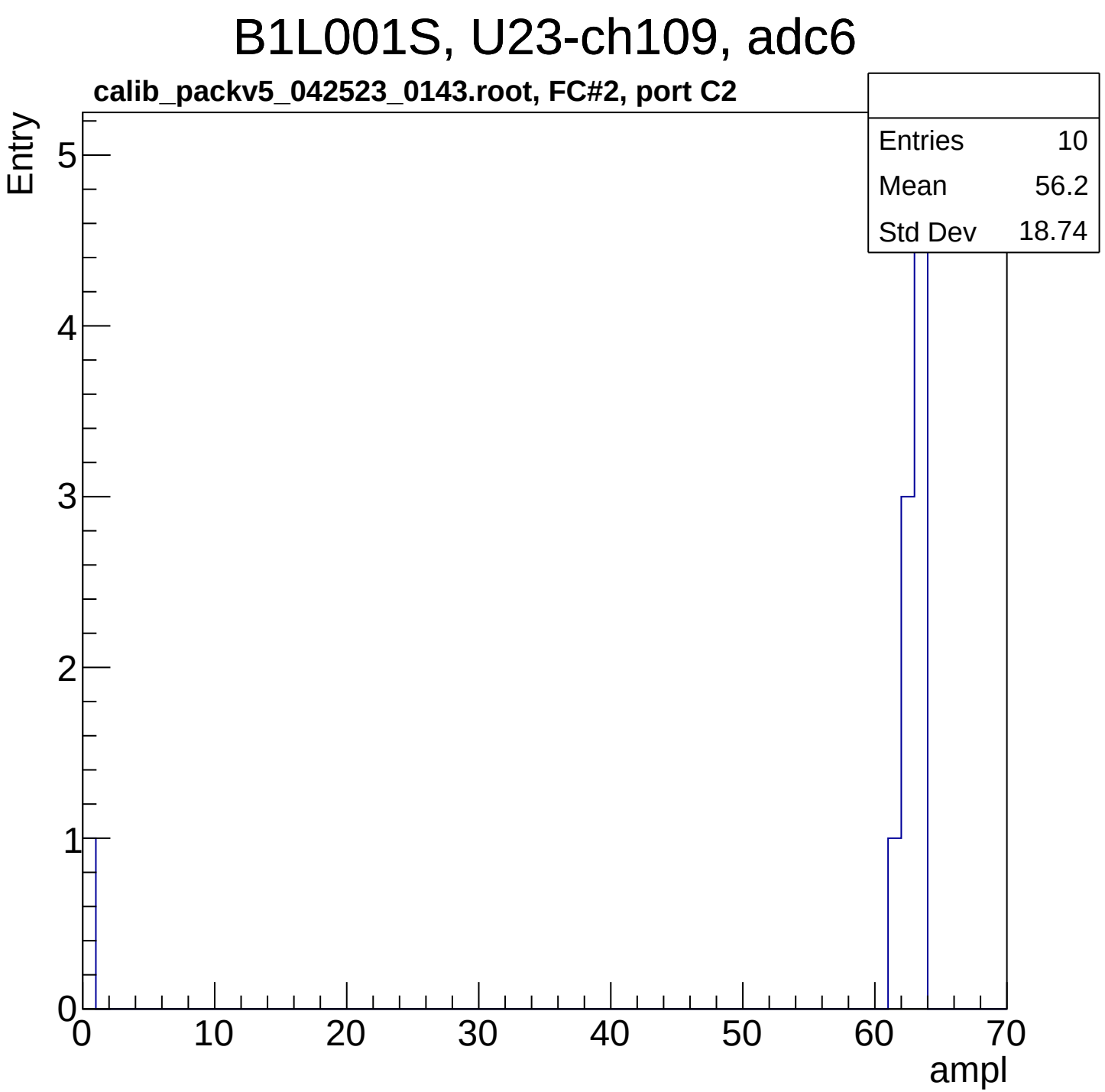
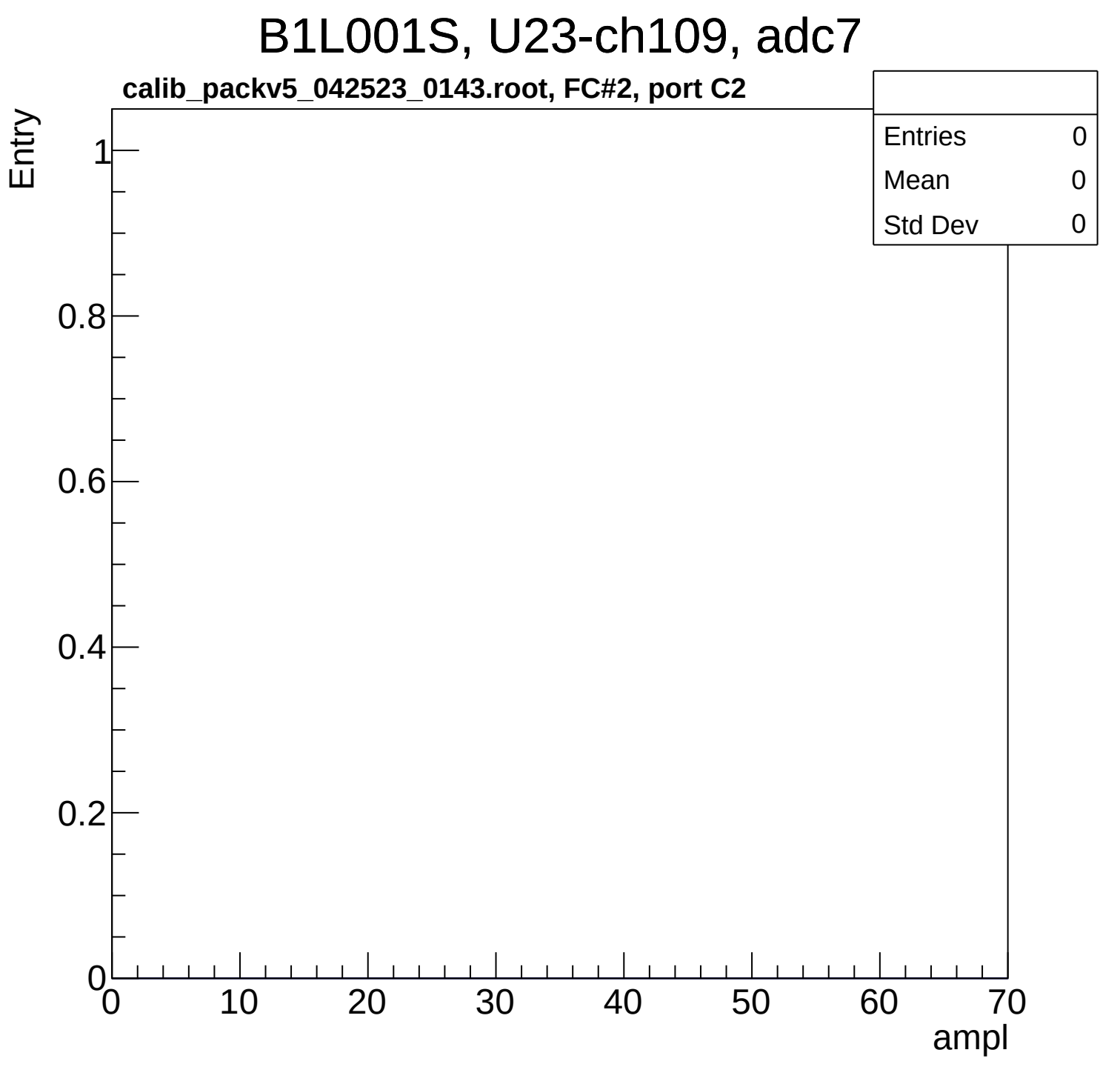

B1L001S, U23-ch109, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 10
Mean 56.2
Std Dev 18.74
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch109, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
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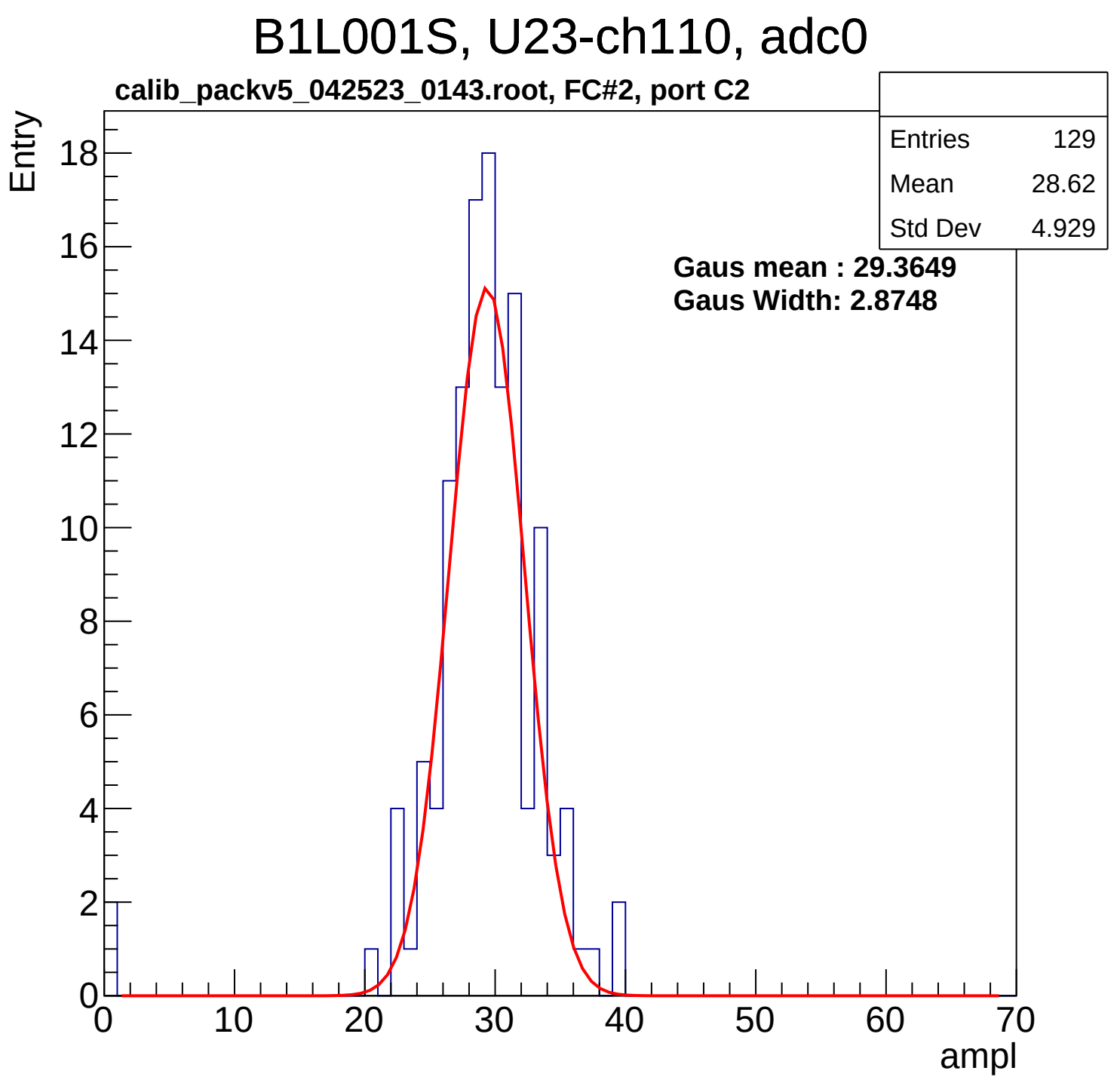
30
40
50
60
70
ampl


B1L001S, U23-ch110, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 28.62
Std Dev 4.929
Gaus mean : 29.3649
Gaus Width: 2.8748
Entry
ampl
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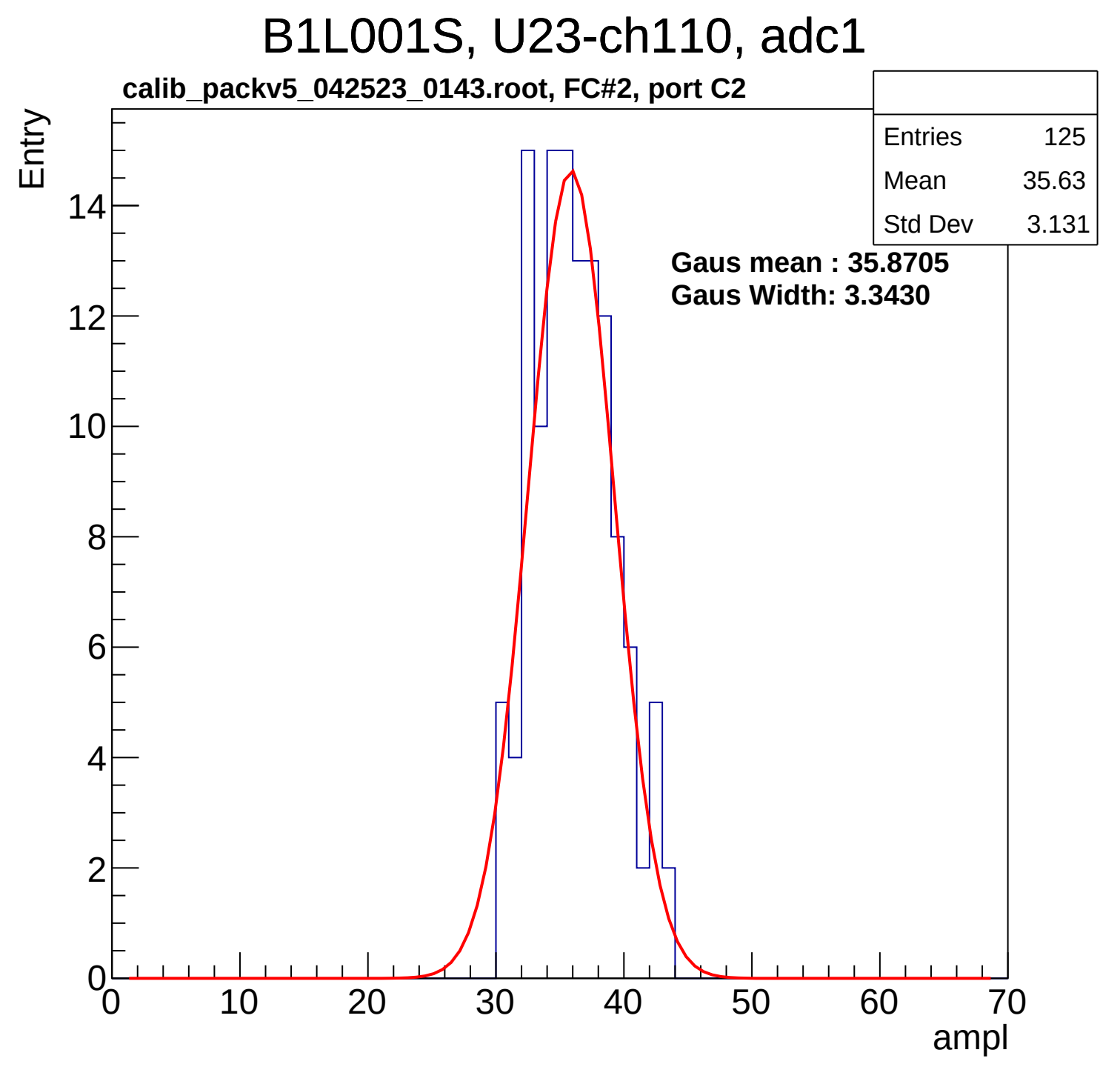
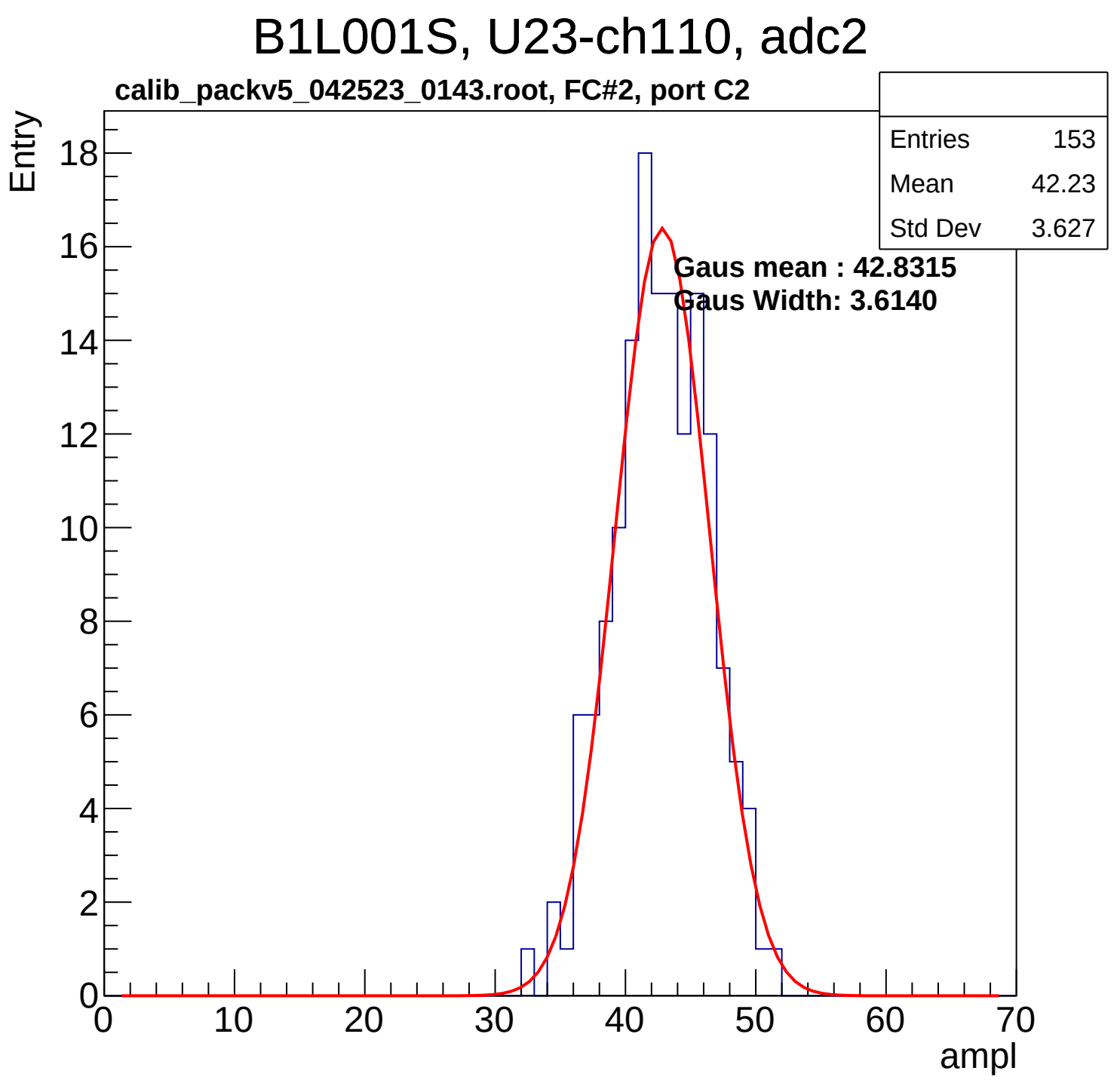
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch110, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 125
Mean 35.63
Std Dev 3.131
Gaus mean : 35.8705
Gaus Width: 3.3430
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch110, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 42.23
Std Dev 3.627
Gaus mean : 42.8315
Gaus Width: 3.6140
Entry
ampl
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4
6
8
10
12
14
16
18
0
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60
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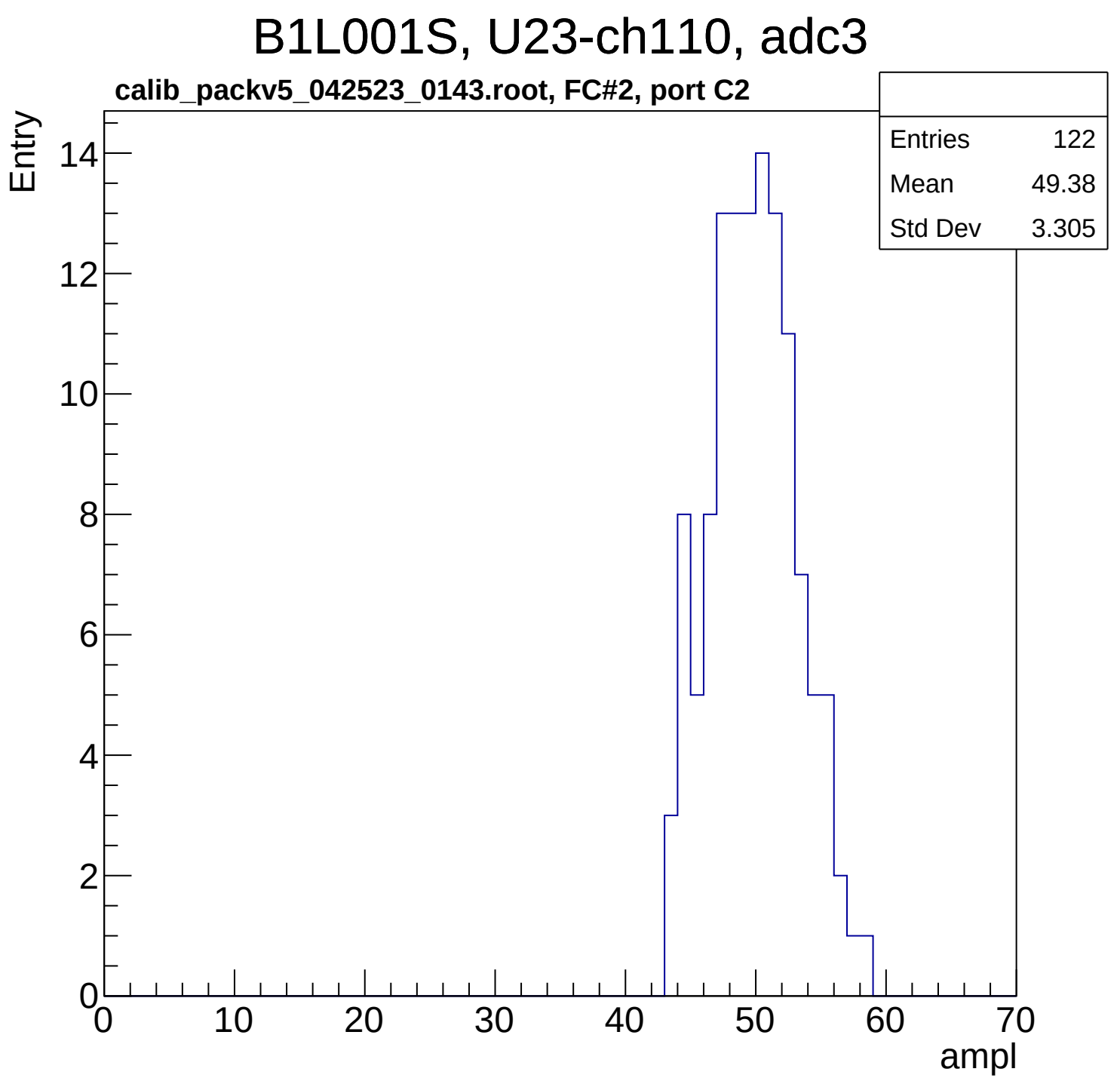
B1L001S, U23-ch110, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 122
Mean 49.38
Std Dev 3.305
Entry
ampl
0
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4
6
8
10
12
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0
10
20
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50
60
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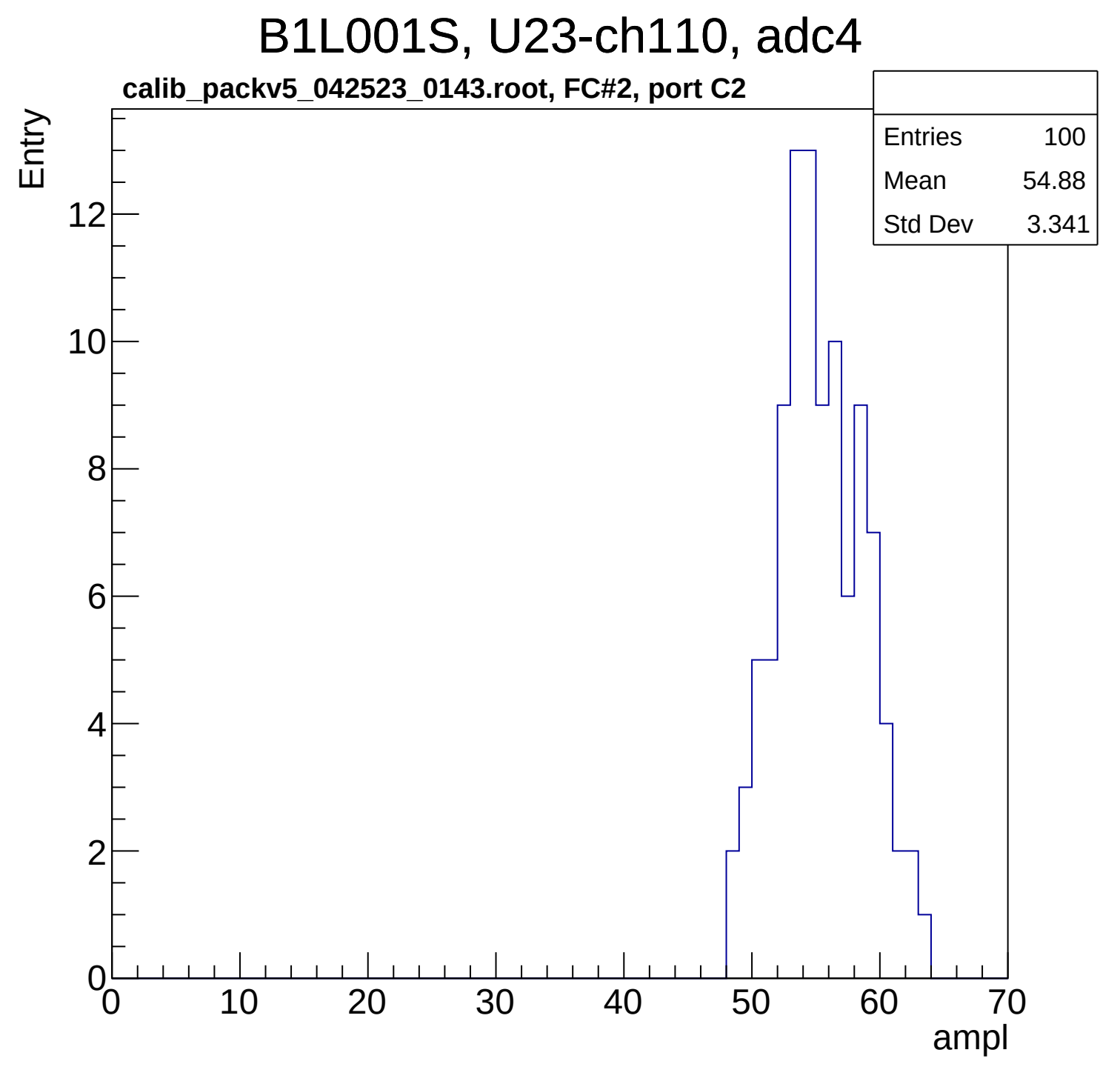
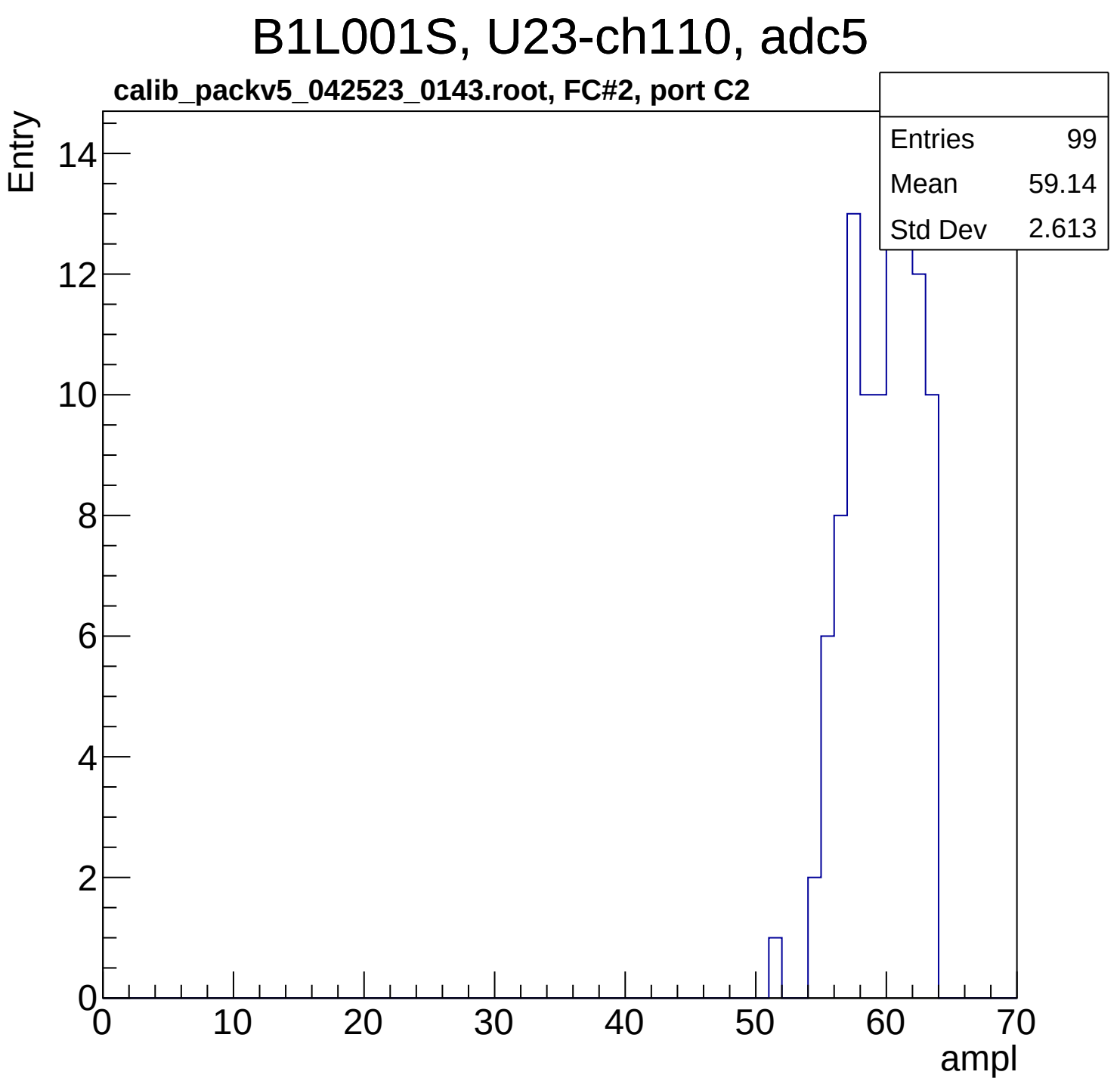

B1L001S, U23-ch110, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 100
Mean 54.88
Std Dev 3.341
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch110, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 99
Mean 59.14
Std Dev 2.613
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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40
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60
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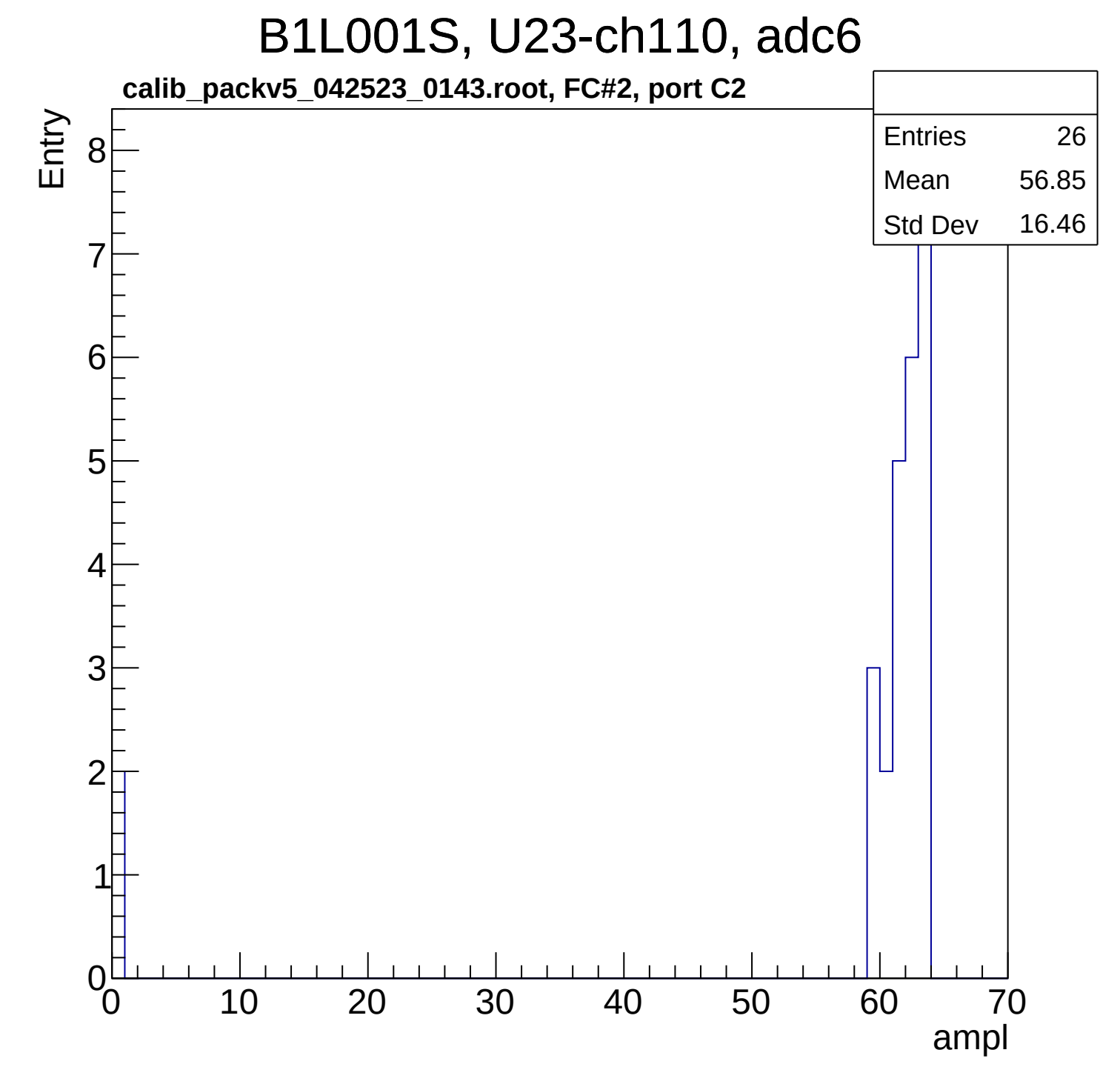
B1L001S, U23-ch110, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 26
Mean 56.85
Std Dev 16.46
Entry
ampl
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1
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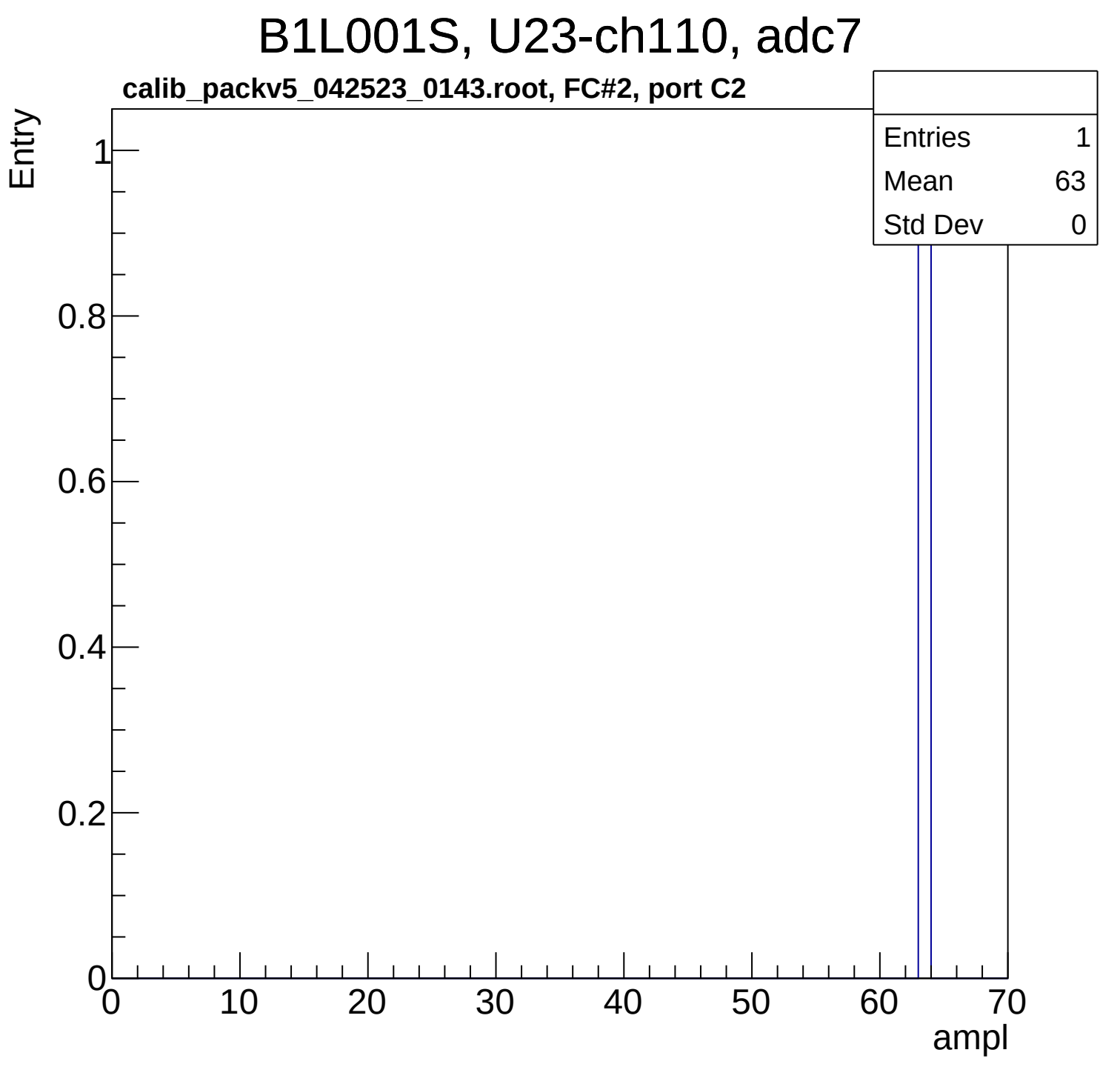
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L001S, U23-ch110, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
20
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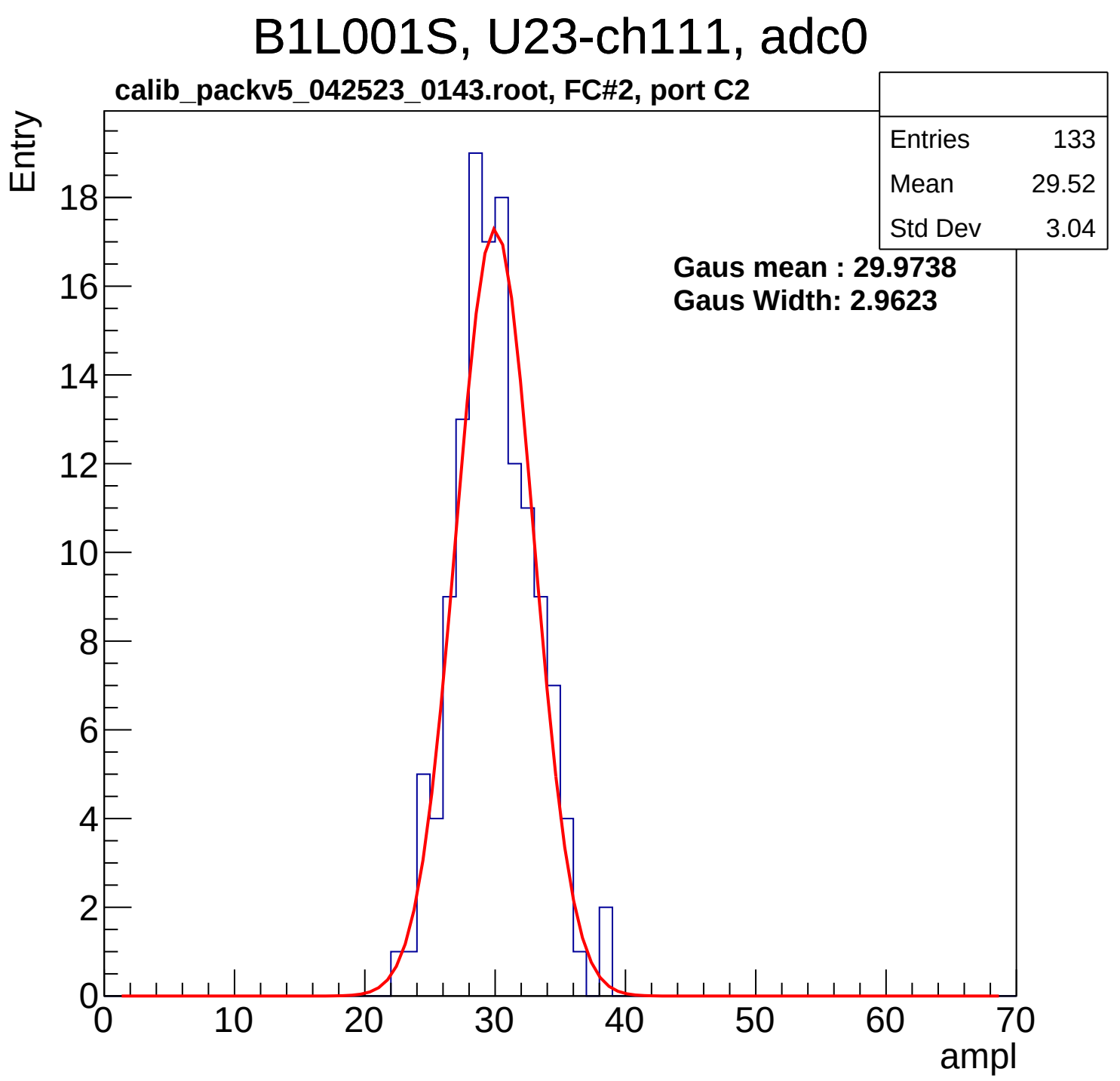
50
60
70
ampl


B1L001S, U23-ch111, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 29.52
Std Dev 3.04
Gaus mean : 29.9738
Gaus Width: 2.9623
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
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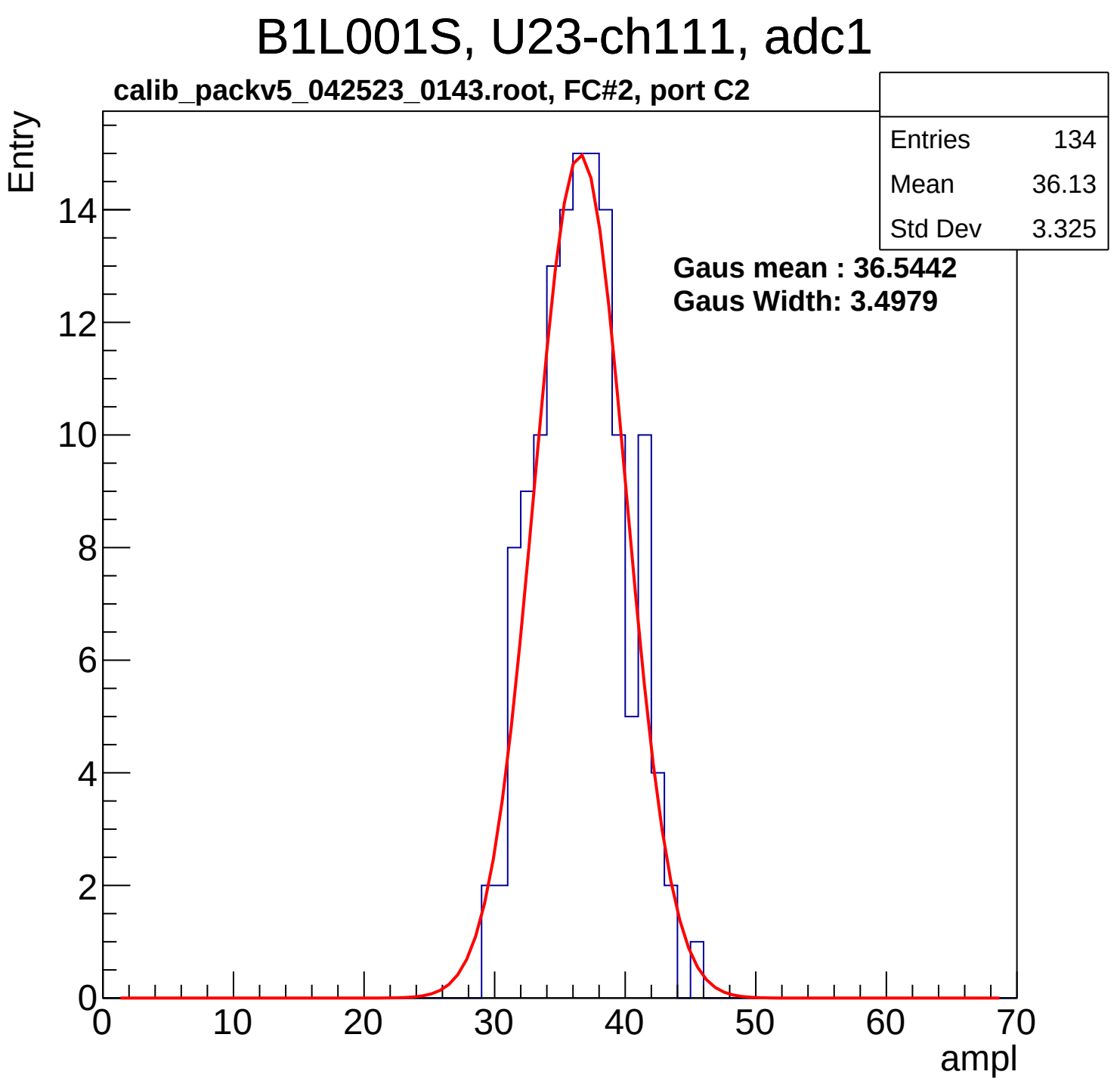
B1L001S, U23-ch111, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 36.13
Std Dev 3.325
Gaus mean : 36.5442
Gaus Width: 3.4979
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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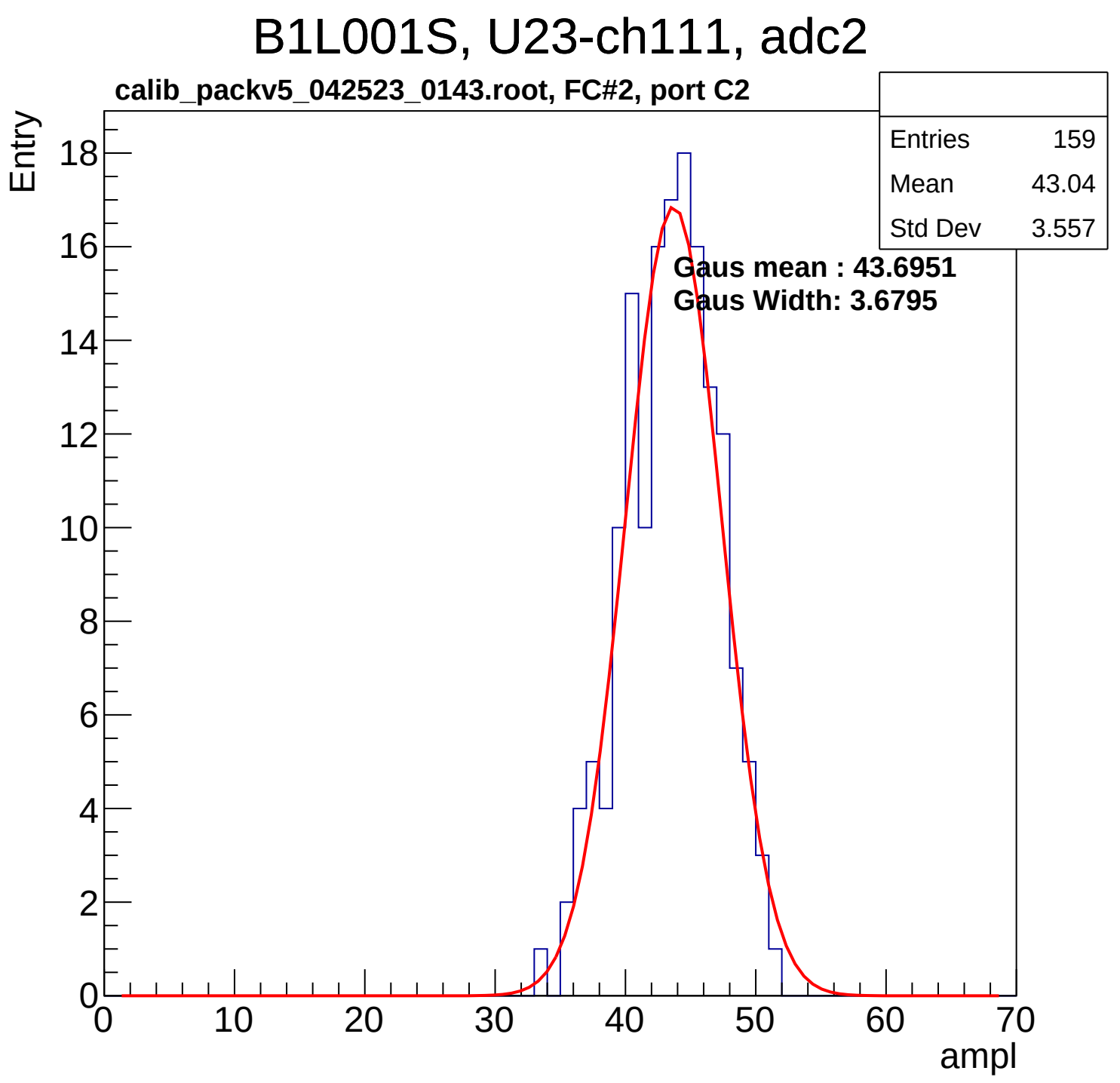
40
50
60
70


B1L001S, U23-ch111, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 159
Mean 43.04
Std Dev 3.557
Gaus mean : 43.6951
Gaus Width: 3.6795
Entry
ampl
0
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4
6
8
10
12
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16
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0
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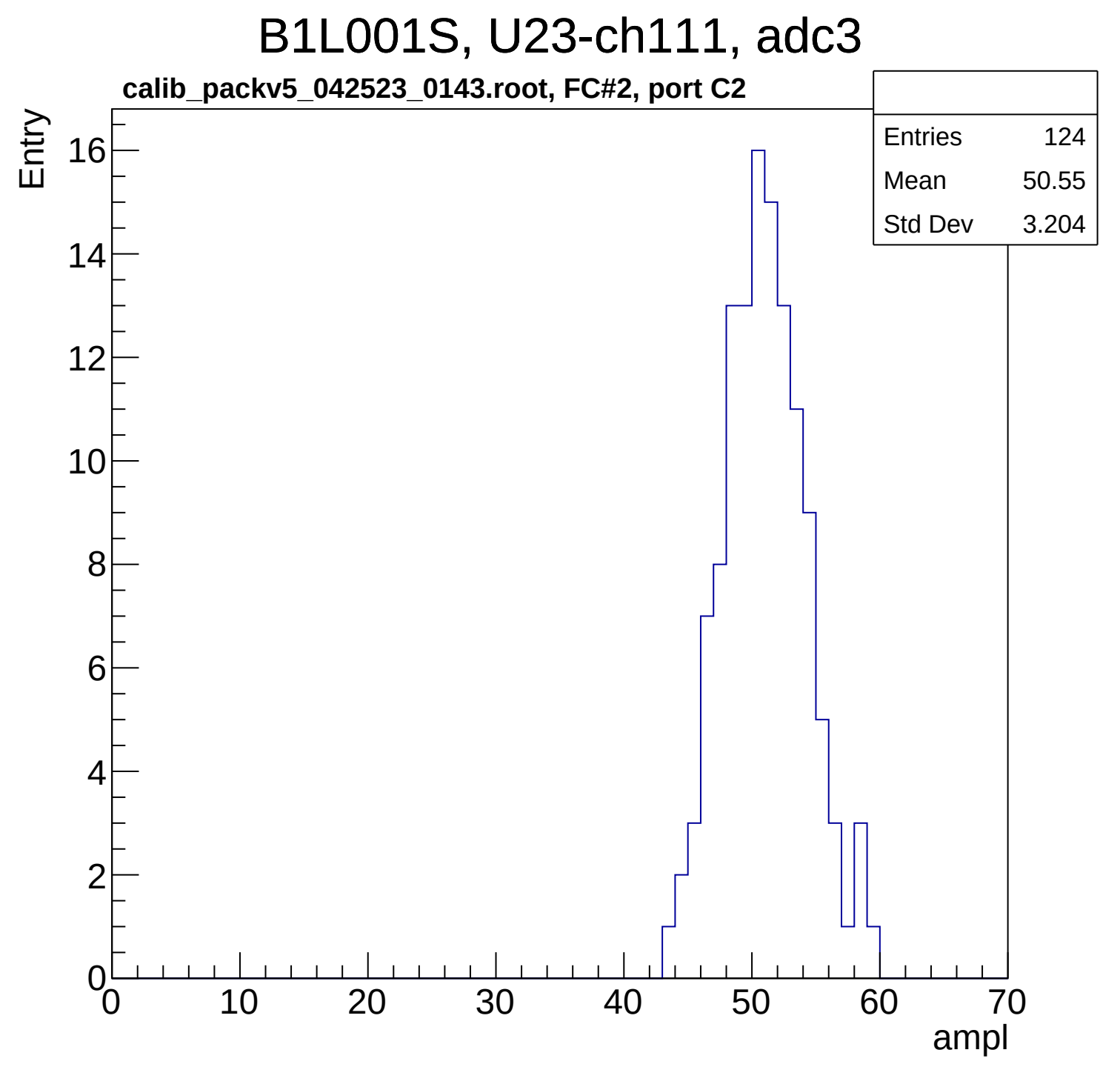
40
50
60
70


B1L001S, U23-ch111, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 50.55
Std Dev 3.204
Entry
ampl
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2
4
6
8
10
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60
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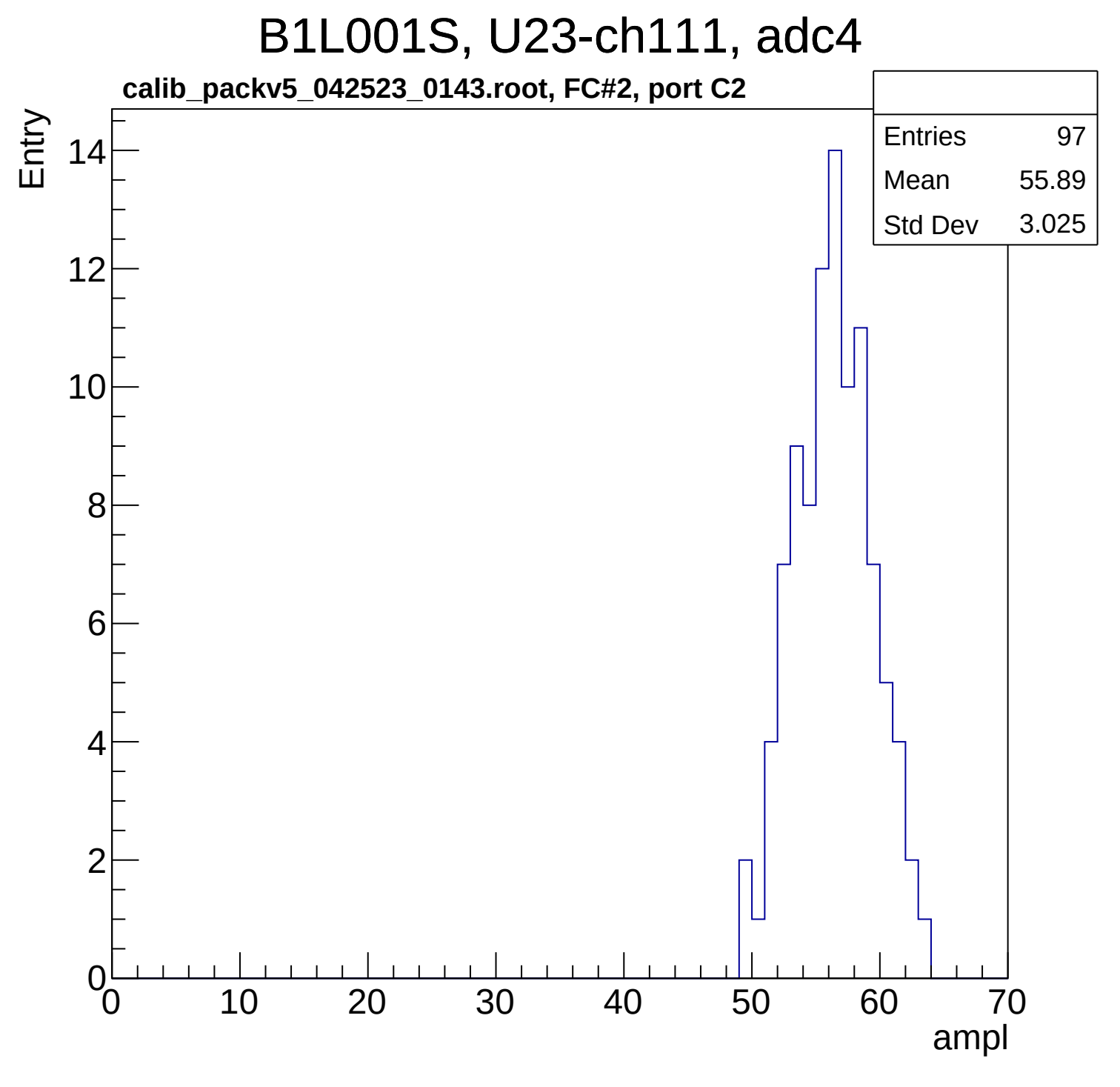
B1L001S, U23-ch111, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 55.89
Std Dev 3.025
Entry
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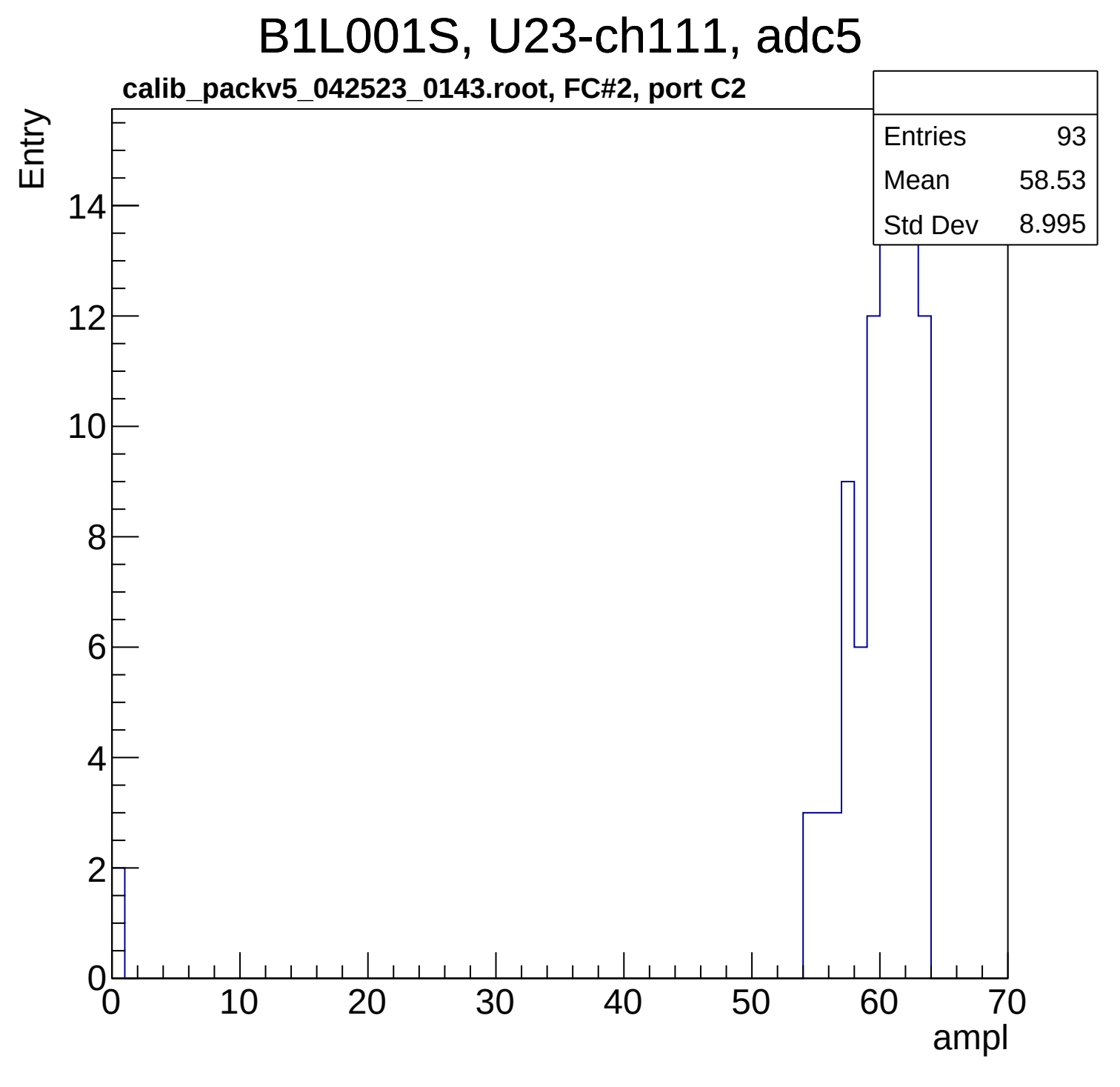
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch111, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 93
Mean 58.53
Std Dev 8.995
Entry
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2
4
6
8
10
12
14
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10
20
30
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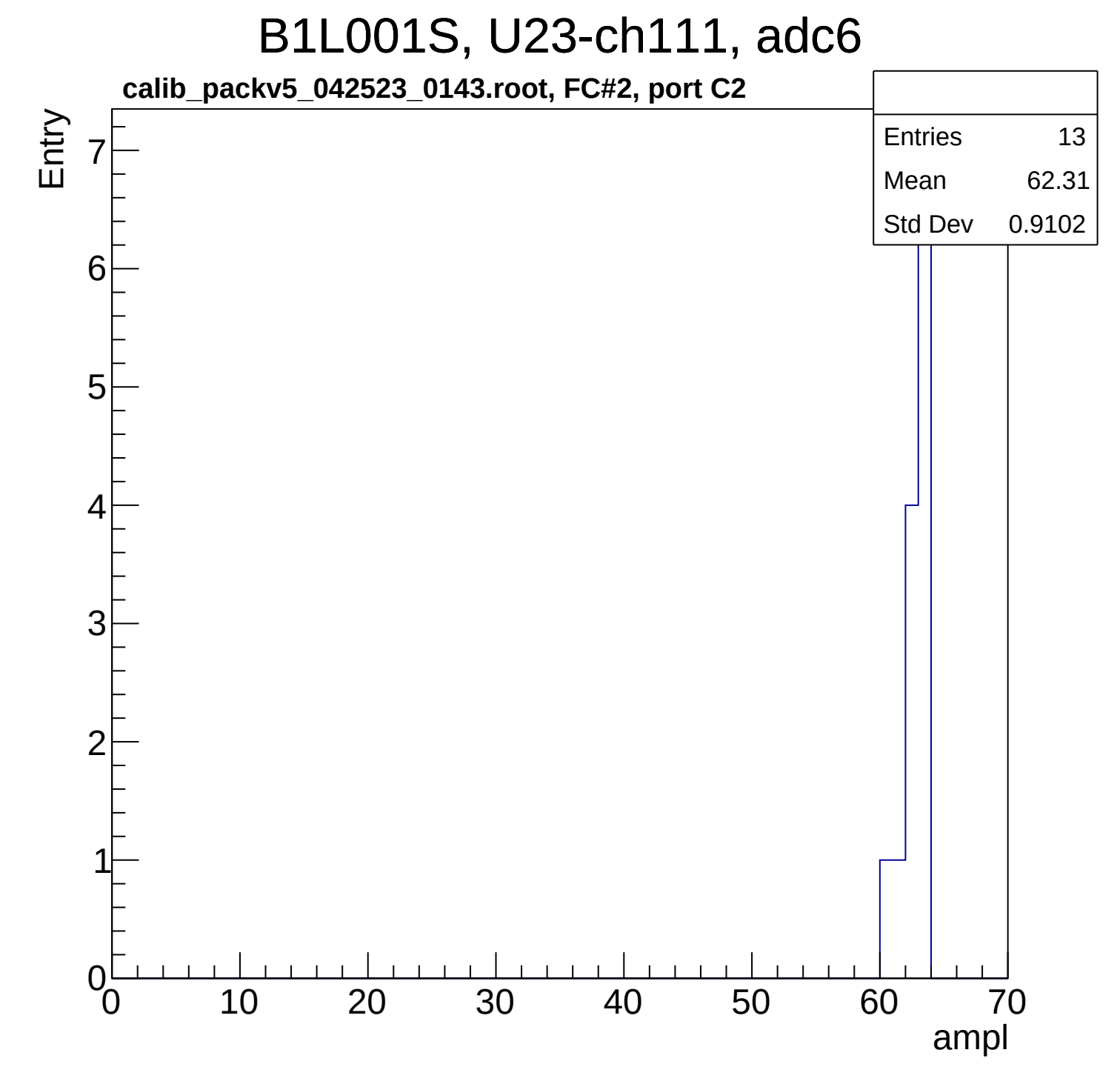
50
60
70
ampl


B1L001S, U23-ch111, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 13
Mean 62.31
Std Dev 0.9102
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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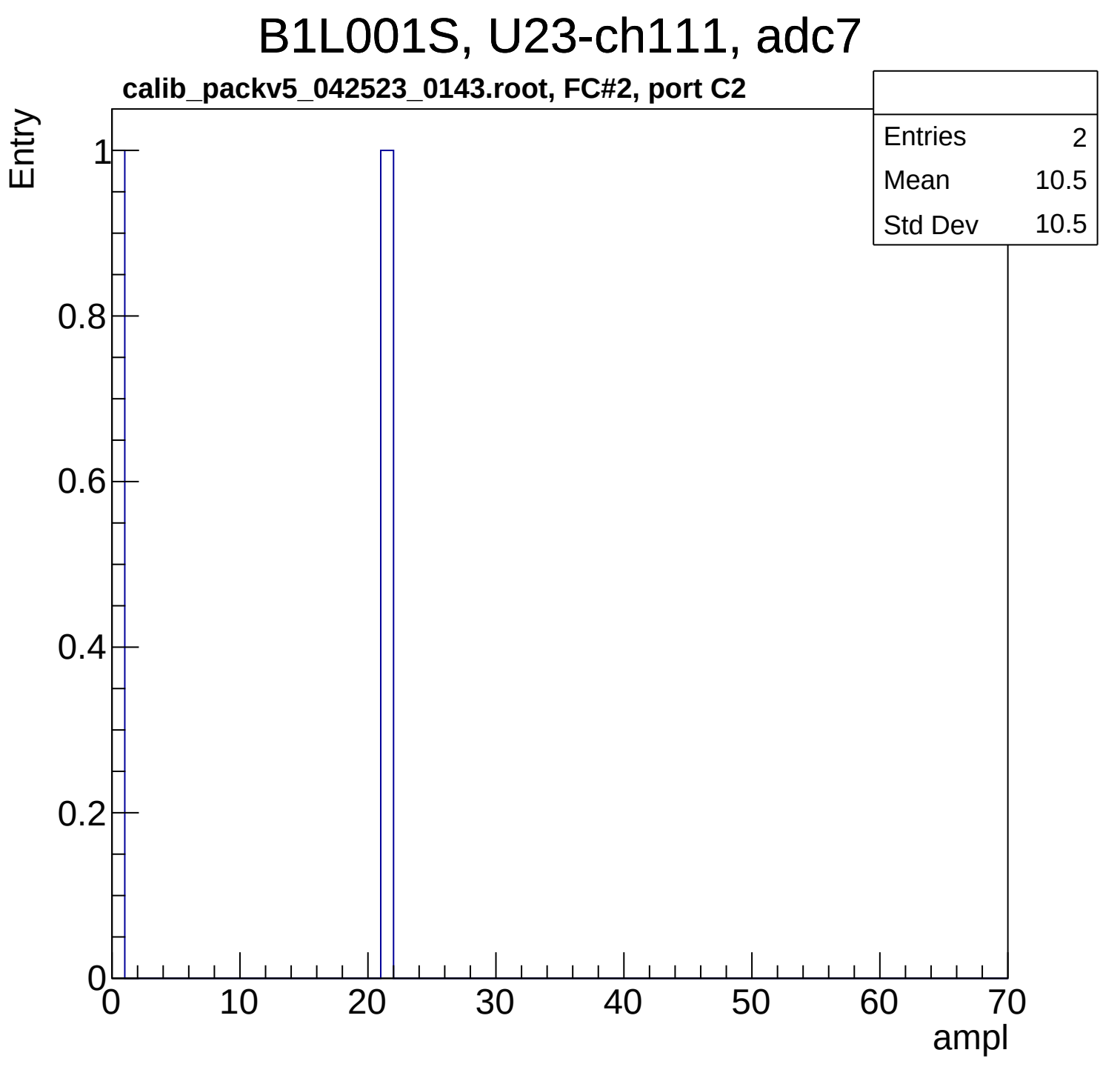
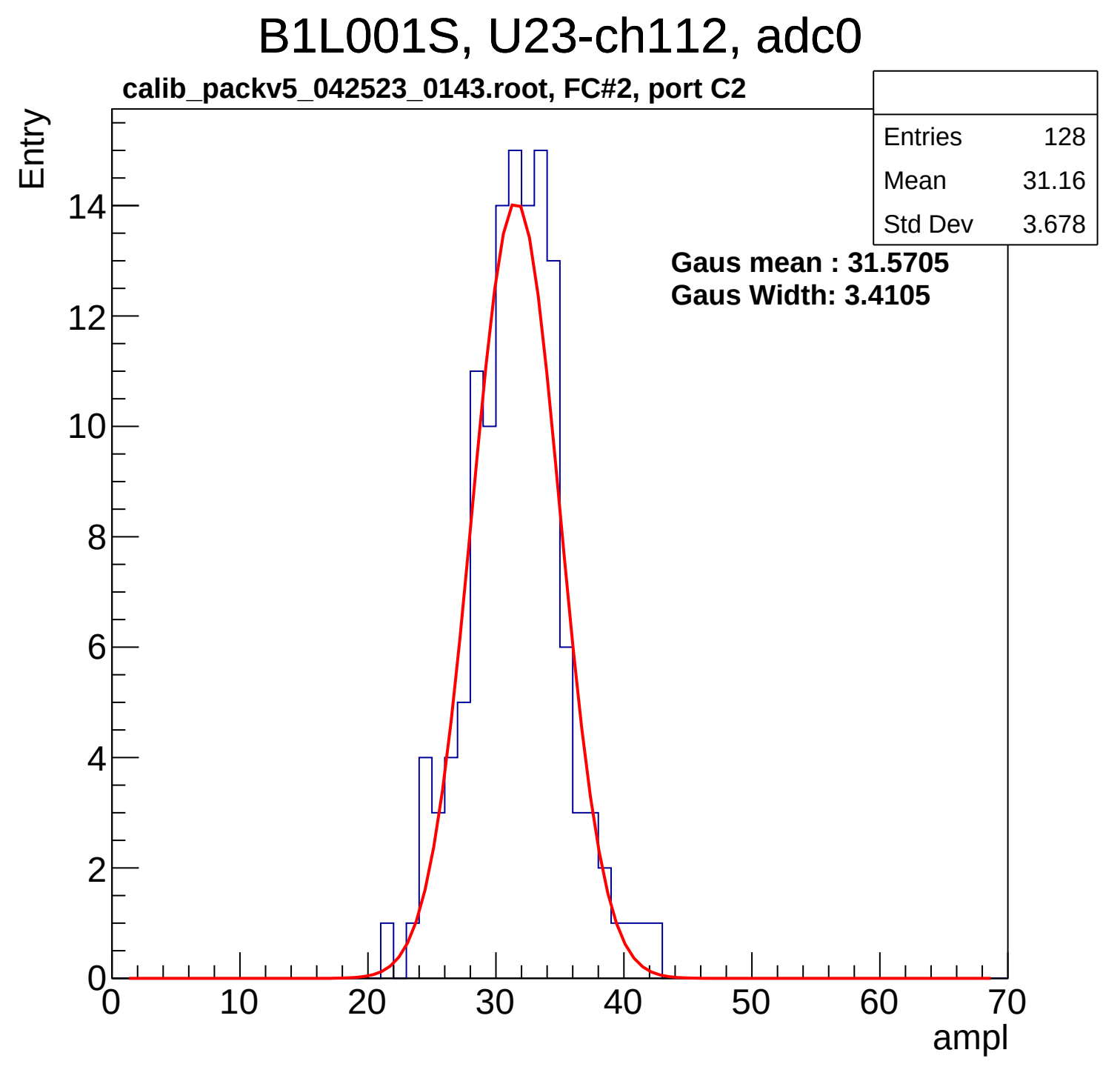

B1L001S, U23-ch111, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch112, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 31.16
Std Dev 3.678
Gaus mean : 31.5705
Gaus Width: 3.4105
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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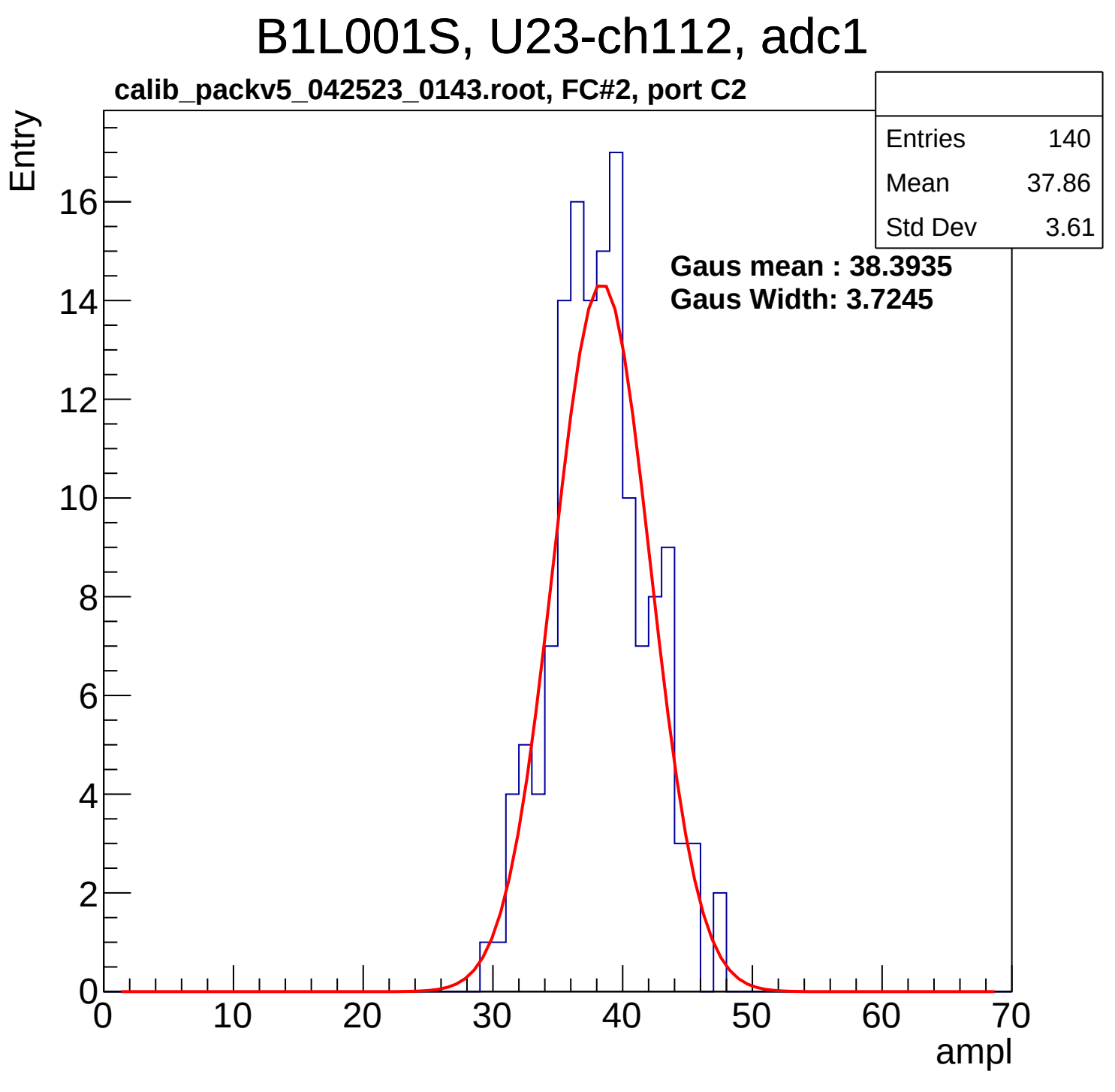
B1L001S, U23-ch112, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 37.86
Std Dev 3.61
Gaus mean : 38.3935
Gaus Width: 3.7245
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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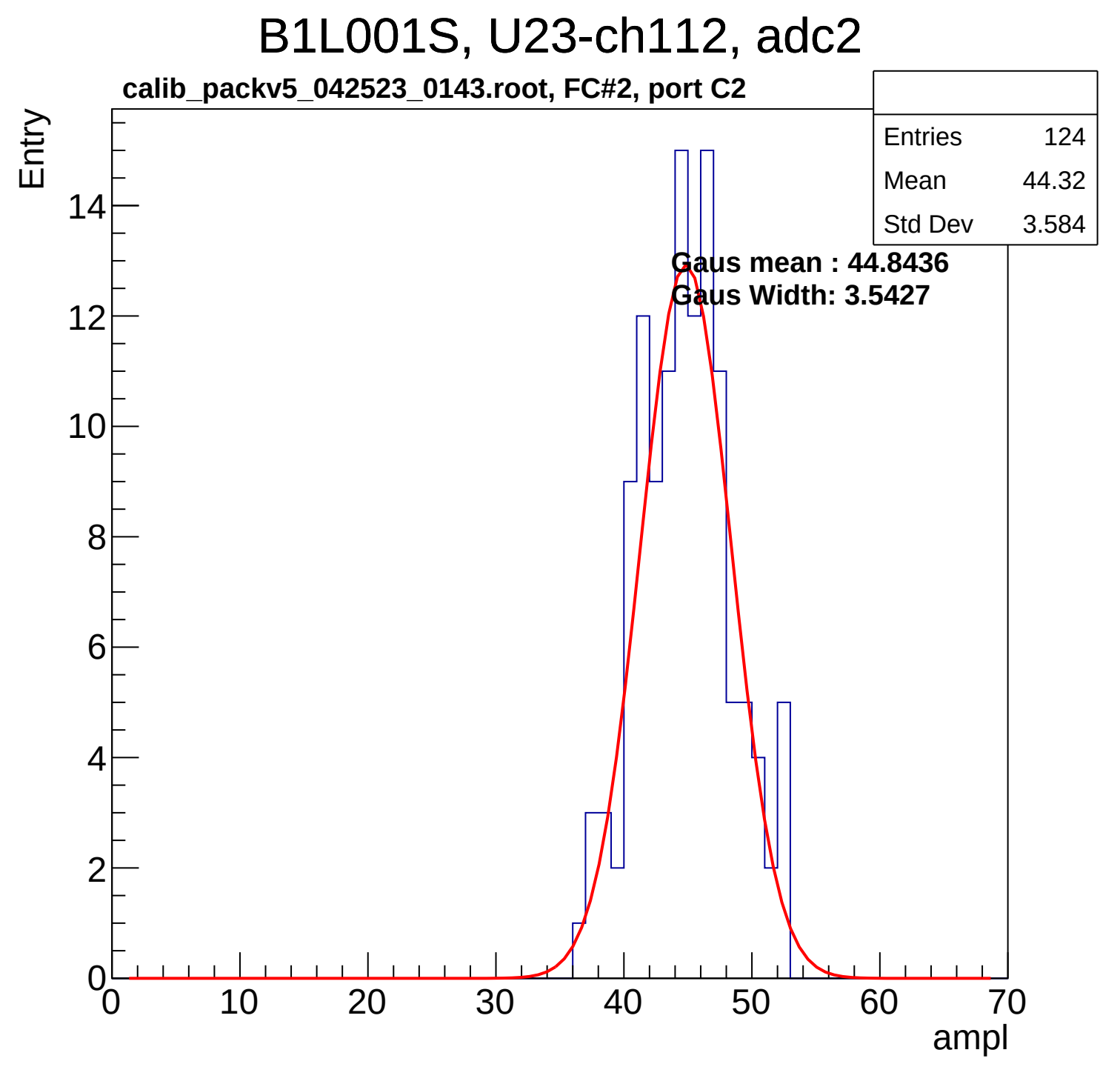
B1L001S, U23-ch112, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 44.32
Std Dev 3.584
Gaus mean : 44.8436
Gaus Width: 3.5427
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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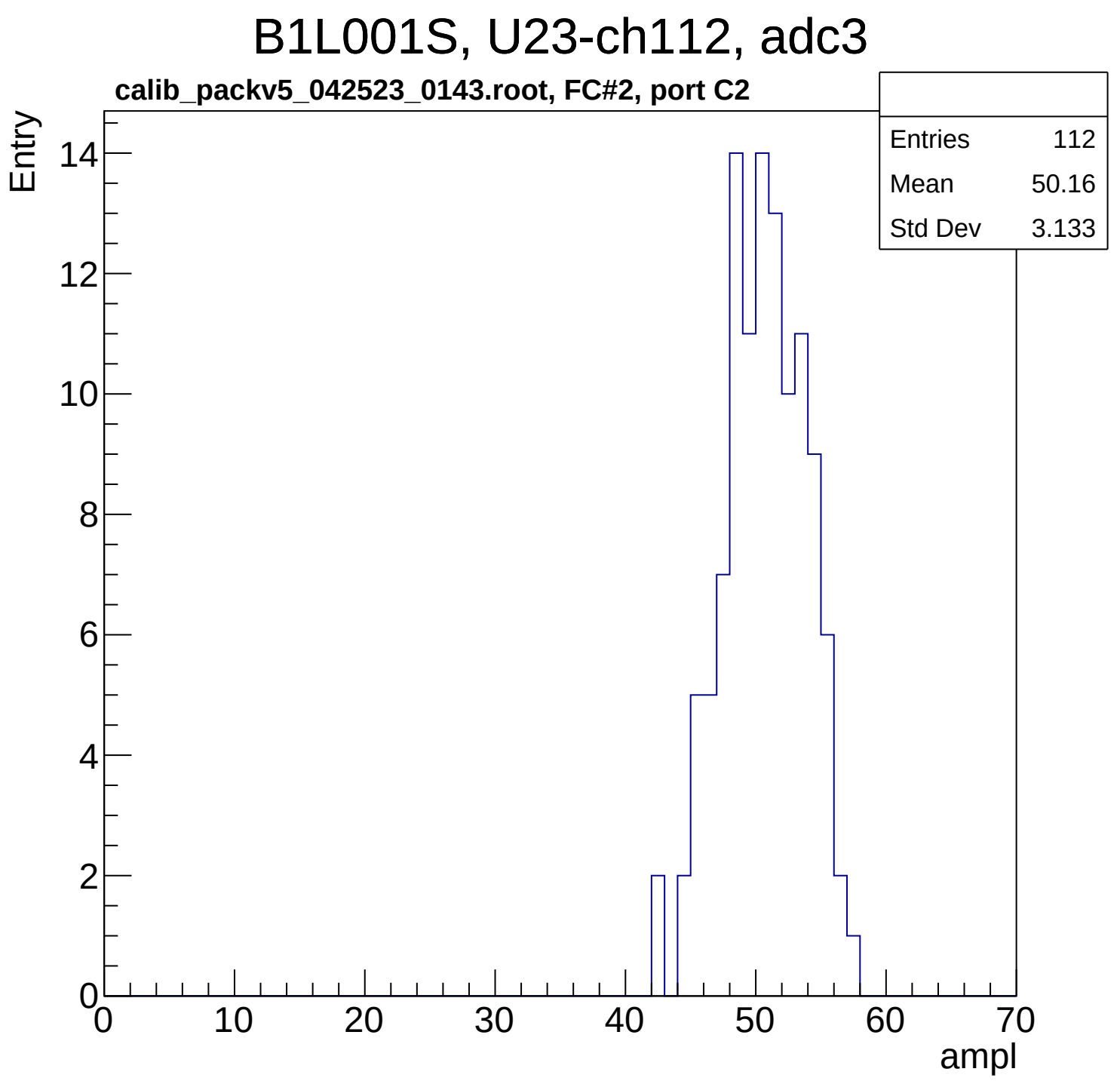
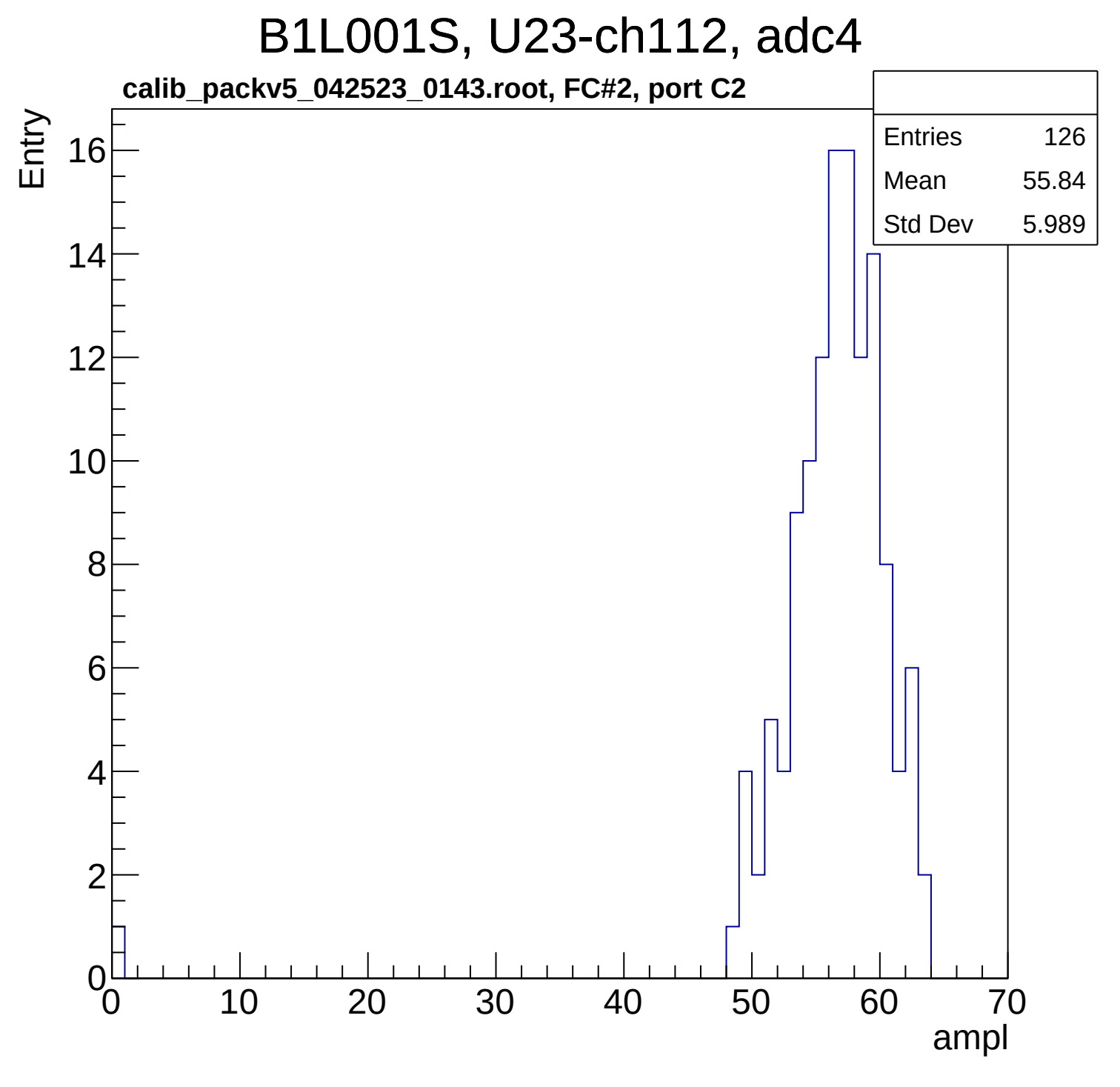
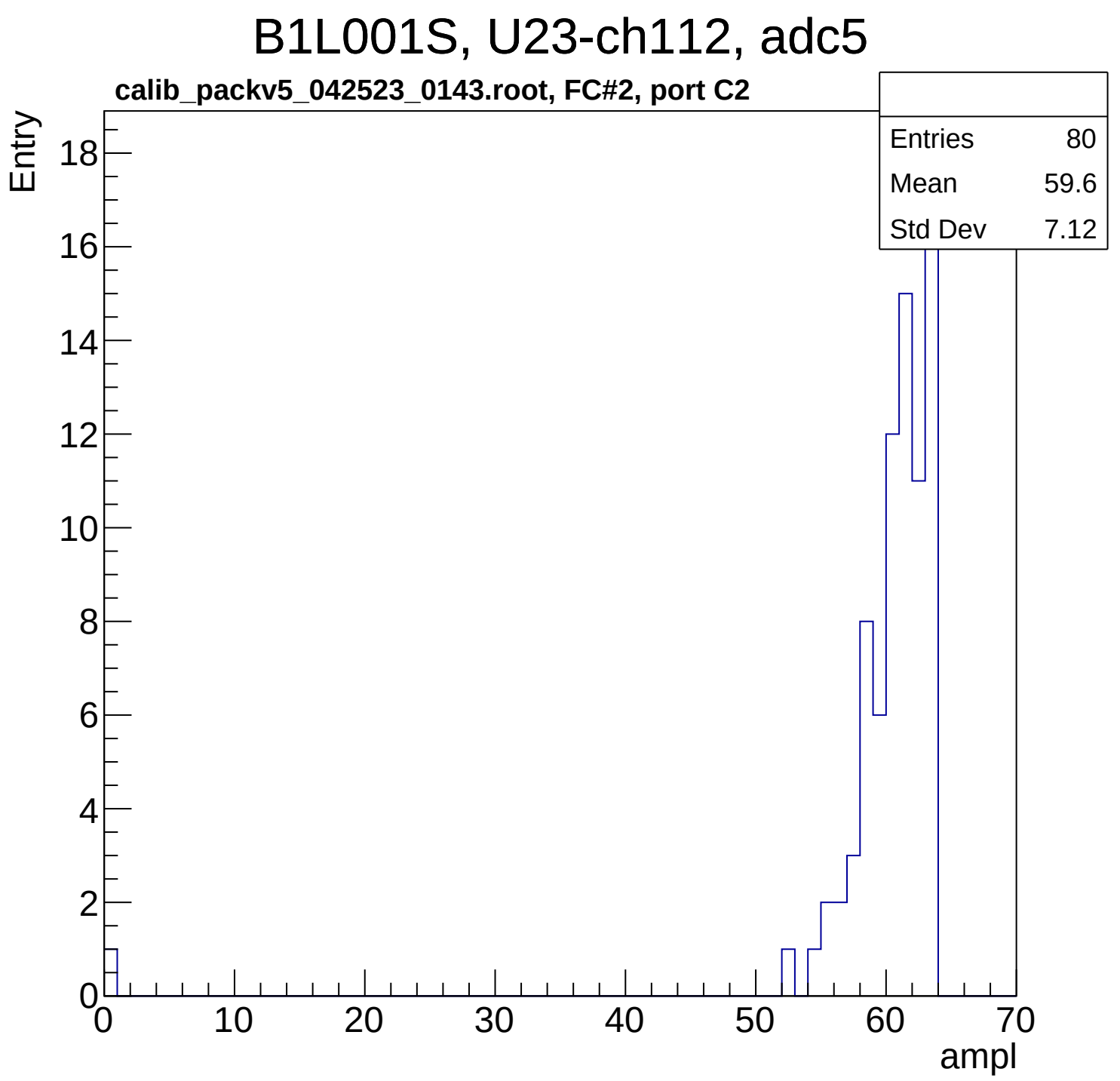
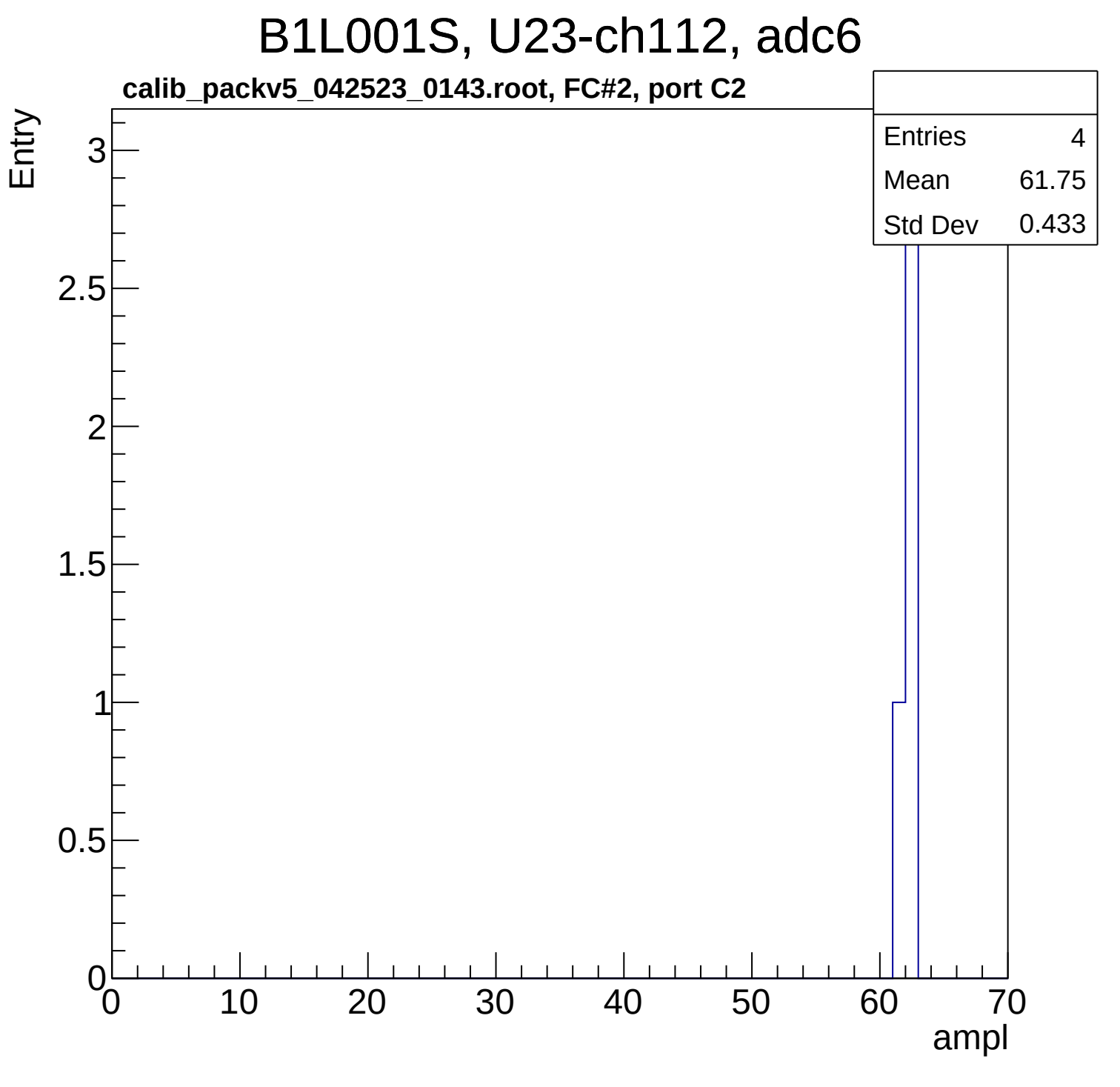

B1L001S, U23-ch112, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 112
Mean 50.16
Std Dev 3.133
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch112, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 55.84
Std Dev 5.989
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch112, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 80
Mean 59.6
Std Dev 7.12
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch112, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 0.433
Entry
ampl
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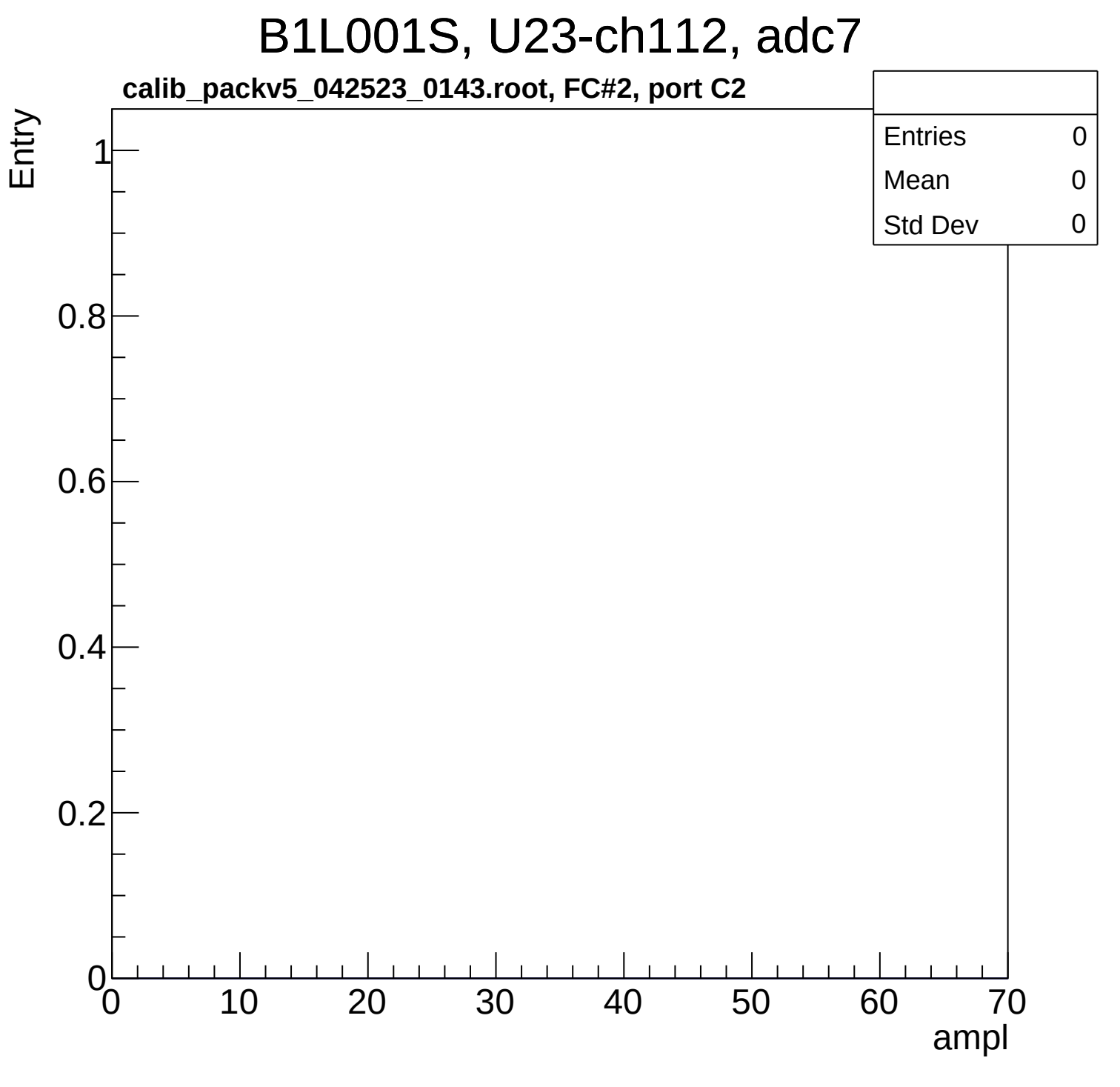
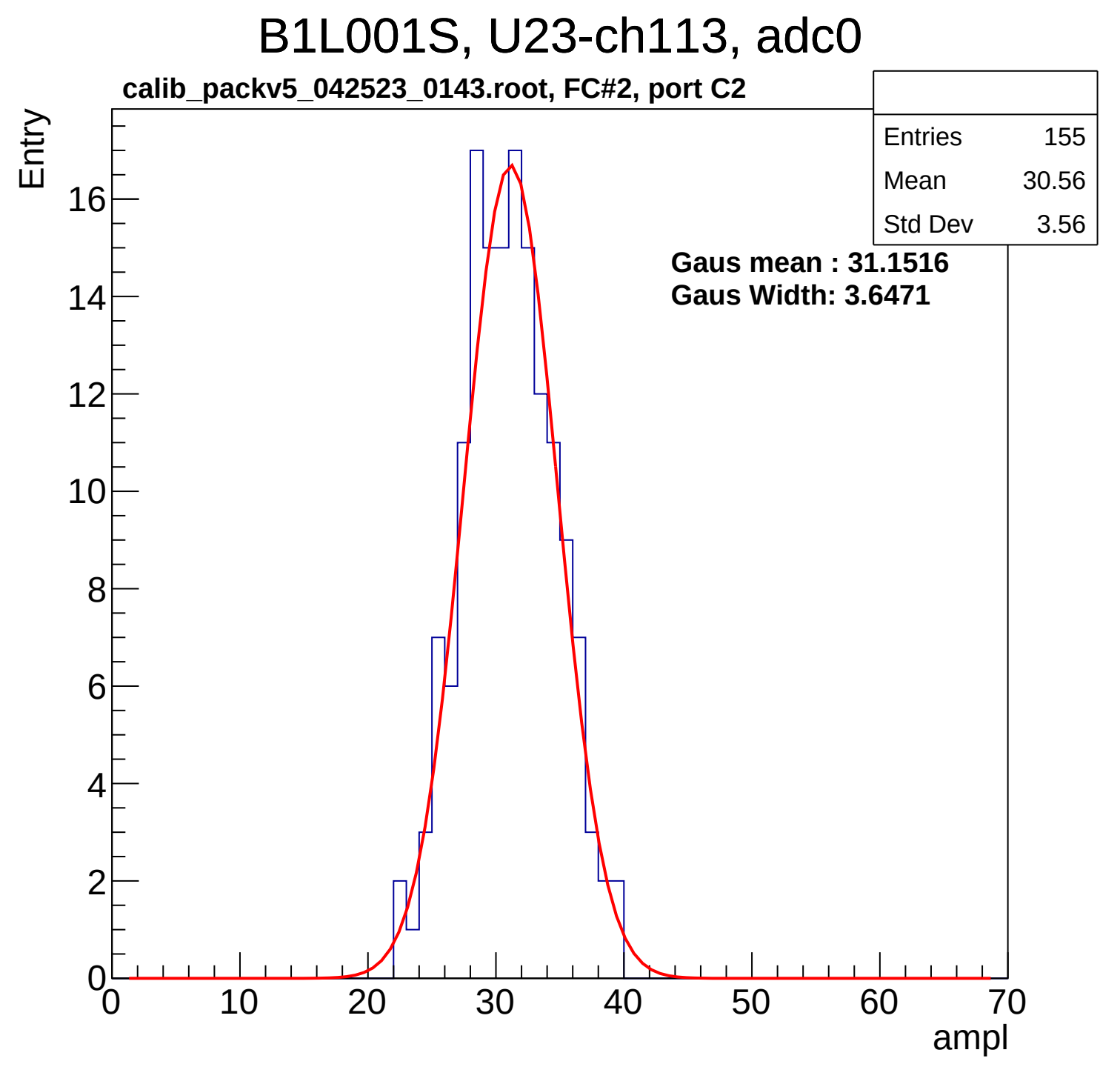
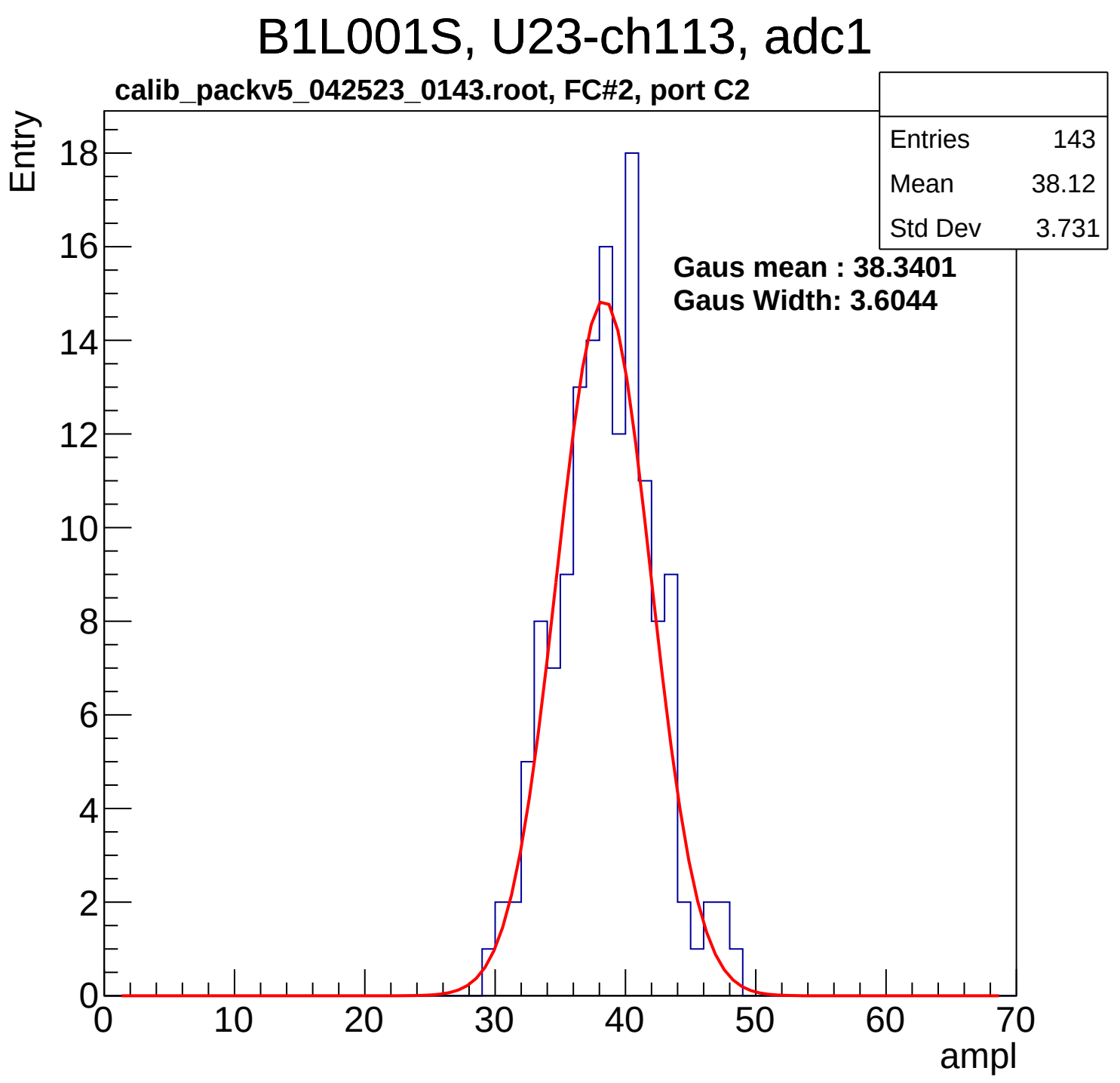
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch112, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch113, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 30.56
Std Dev 3.56
Gaus mean : 31.1516
Gaus Width: 3.6471
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch113, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 143
Mean 38.12
Std Dev 3.731
Gaus mean : 38.3401
Gaus Width: 3.6044
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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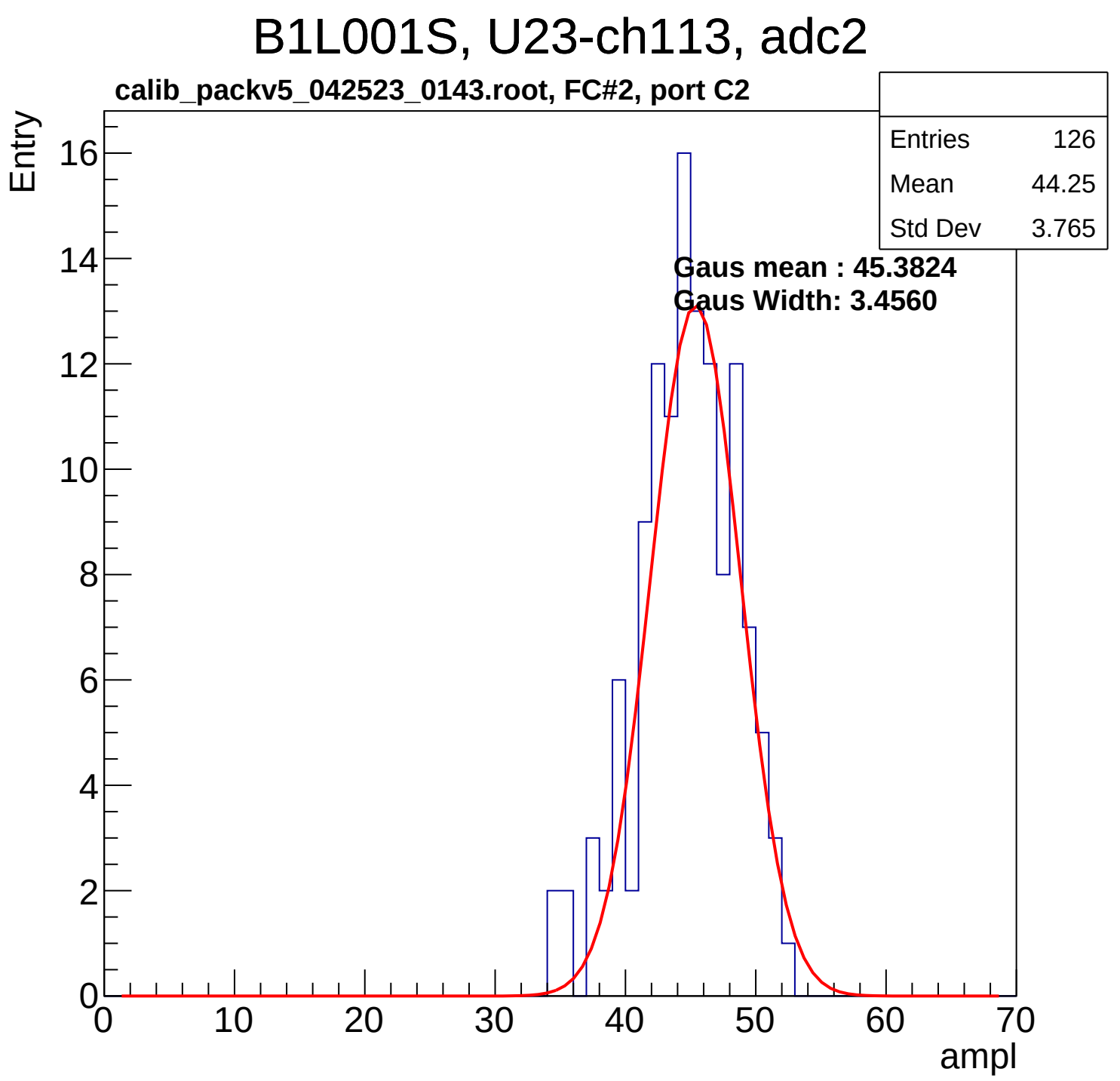
B1L001S, U23-ch113, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 126
Mean 44.25
Std Dev 3.765
Gaus mean : 45.3824
Gaus Width: 3.4560
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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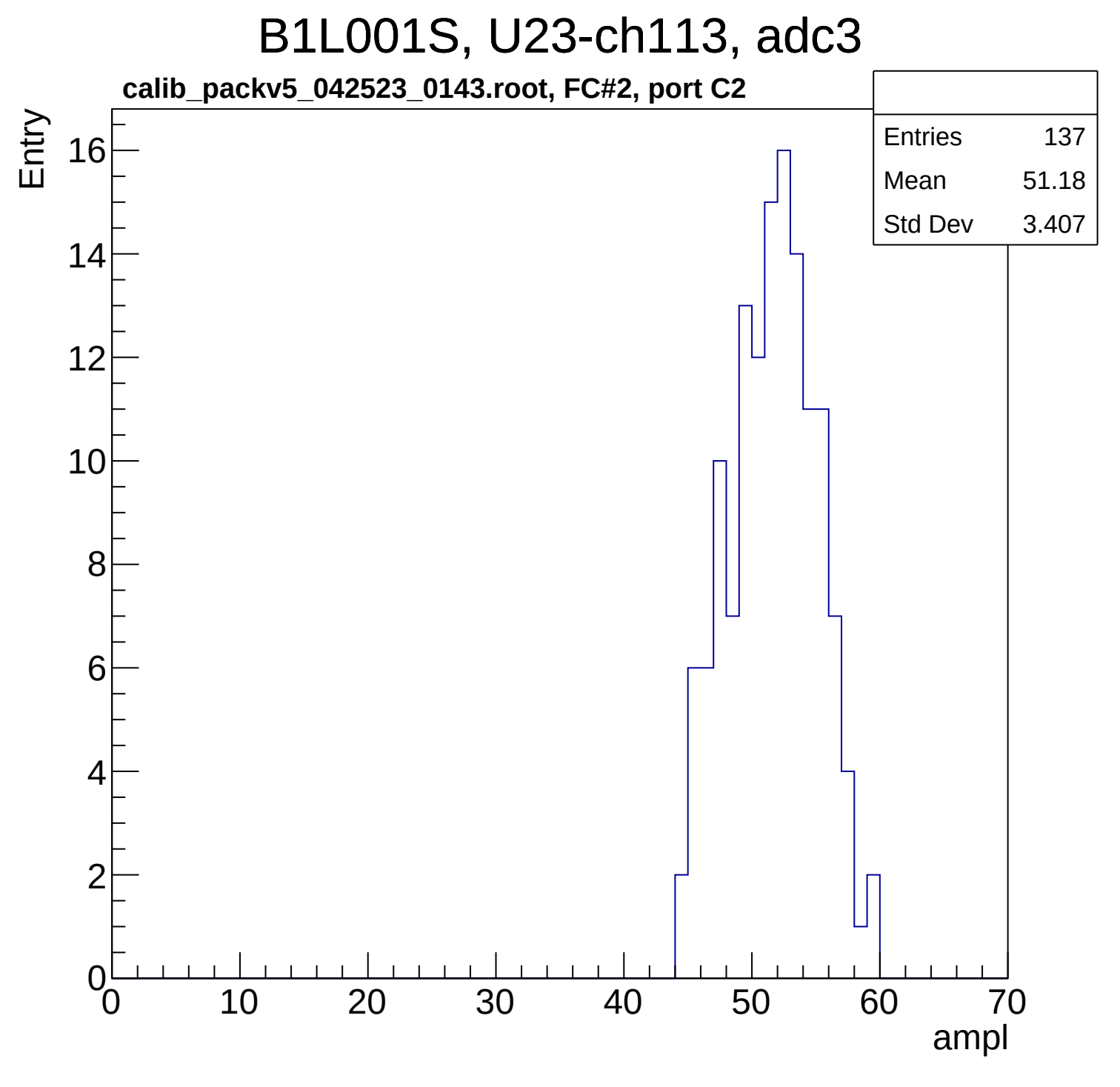
B1L001S, U23-ch113, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 137
Mean 51.18
Std Dev 3.407
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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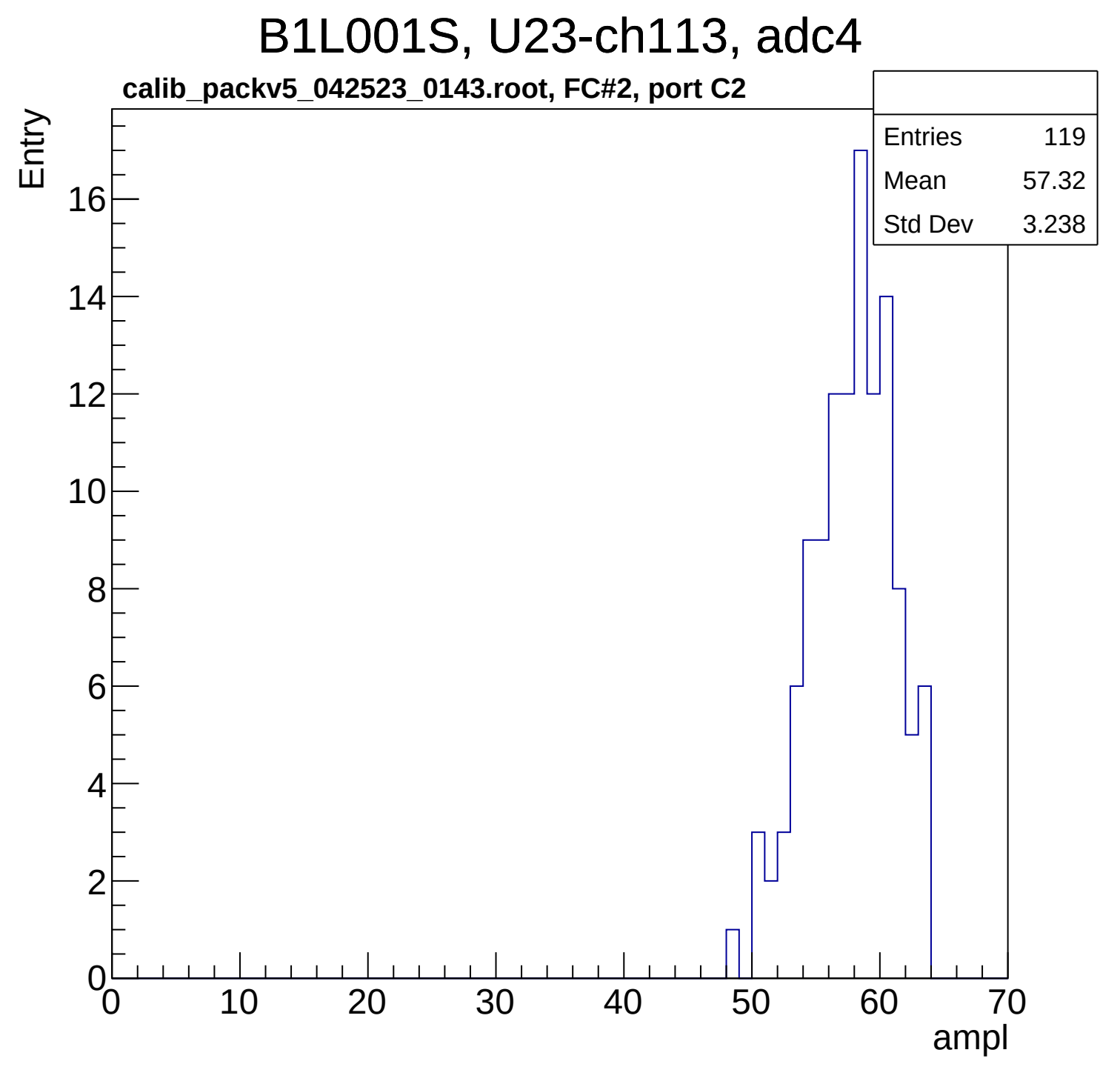
B1L001S, U23-ch113, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 119
Mean 57.32
Std Dev 3.238
Entry
ampl
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4
6
8
10
12
14
16
0
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20
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40
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60
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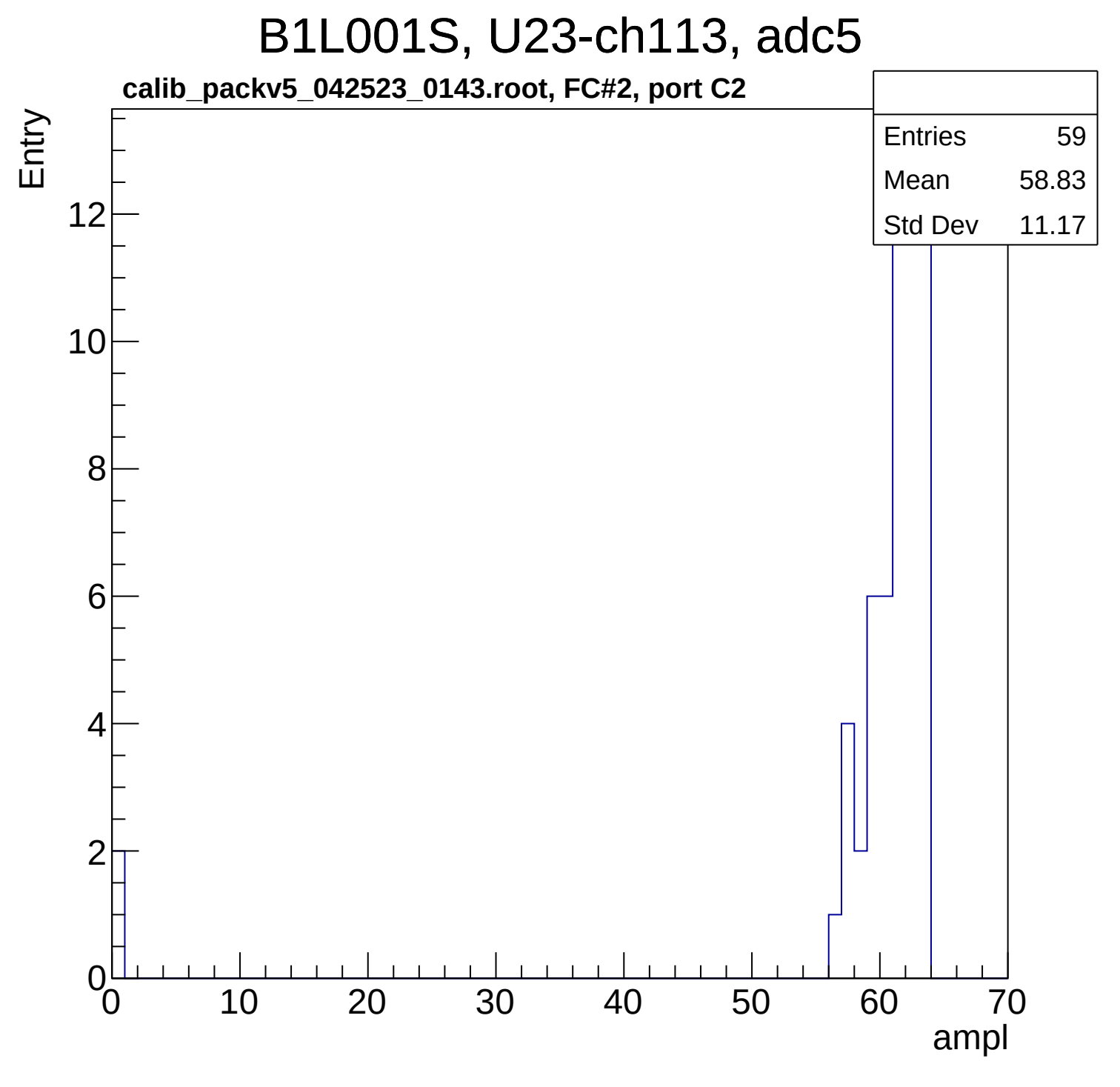
B1L001S, U23-ch113, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 59
Mean 58.83
Std Dev 11.17
Entry
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10
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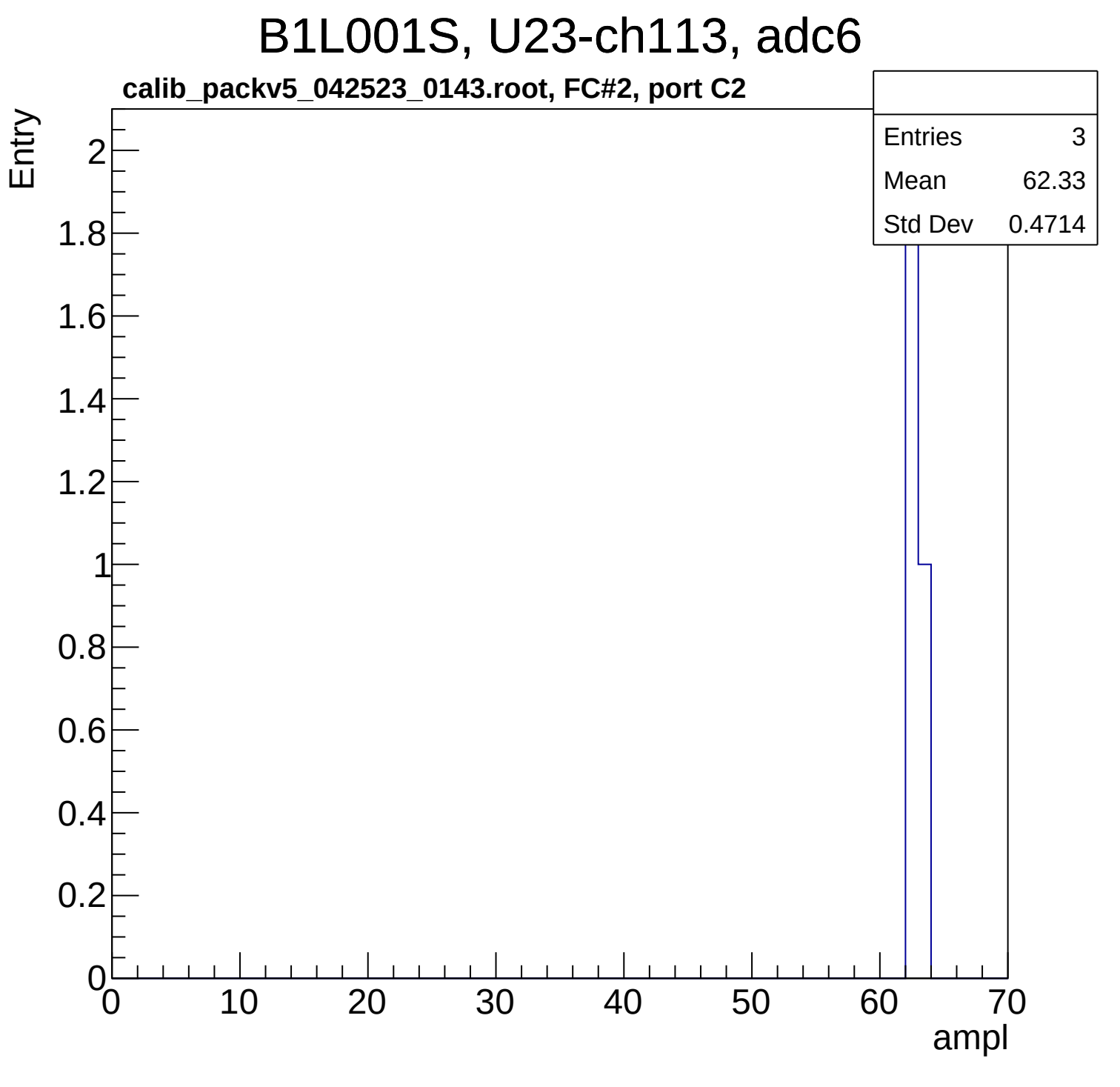
40
50
60
70
ampl


B1L001S, U23-ch113, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
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1.2
1.4
1.6
1.8
2
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60
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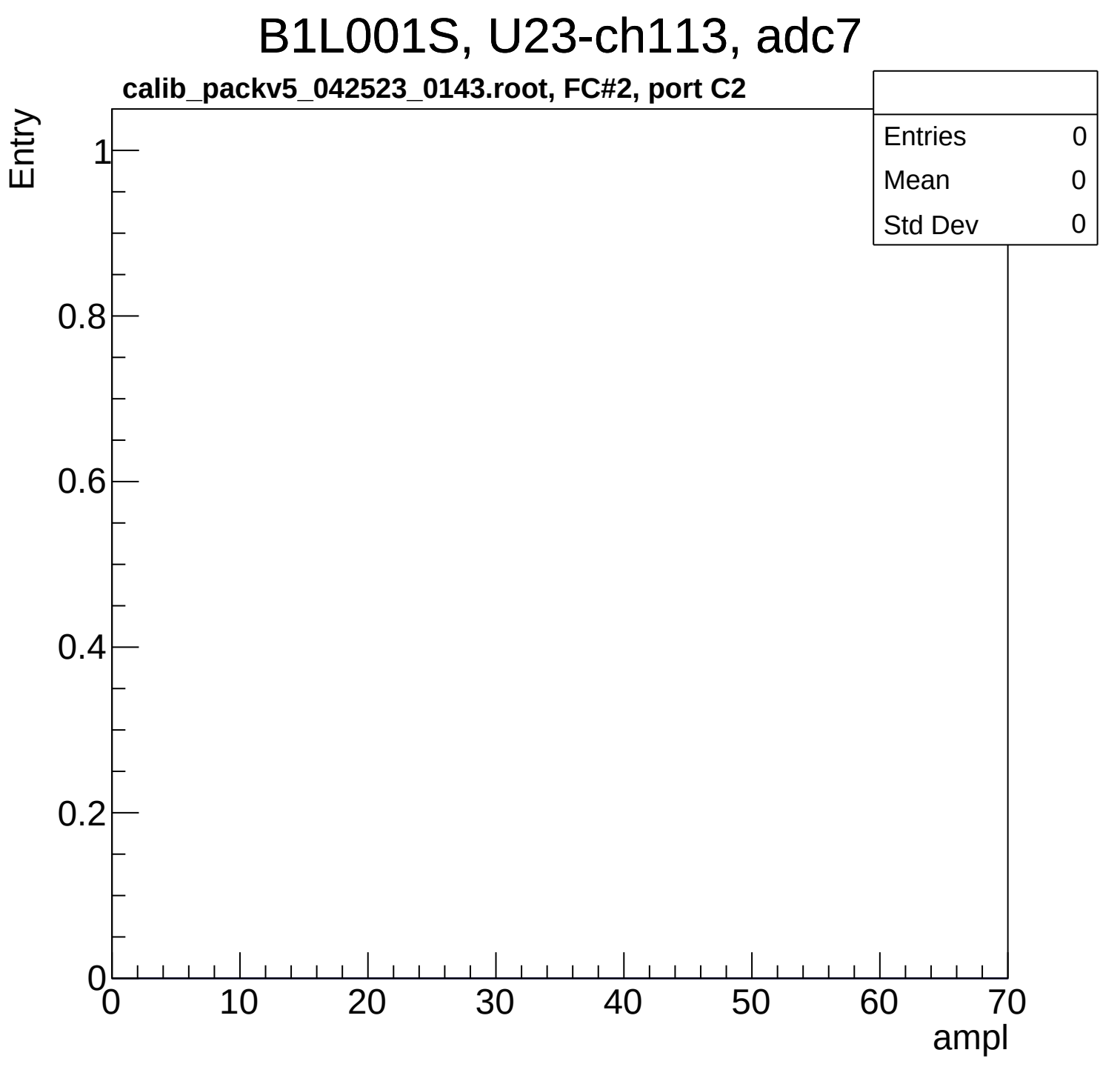
B1L001S, U23-ch113, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
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10
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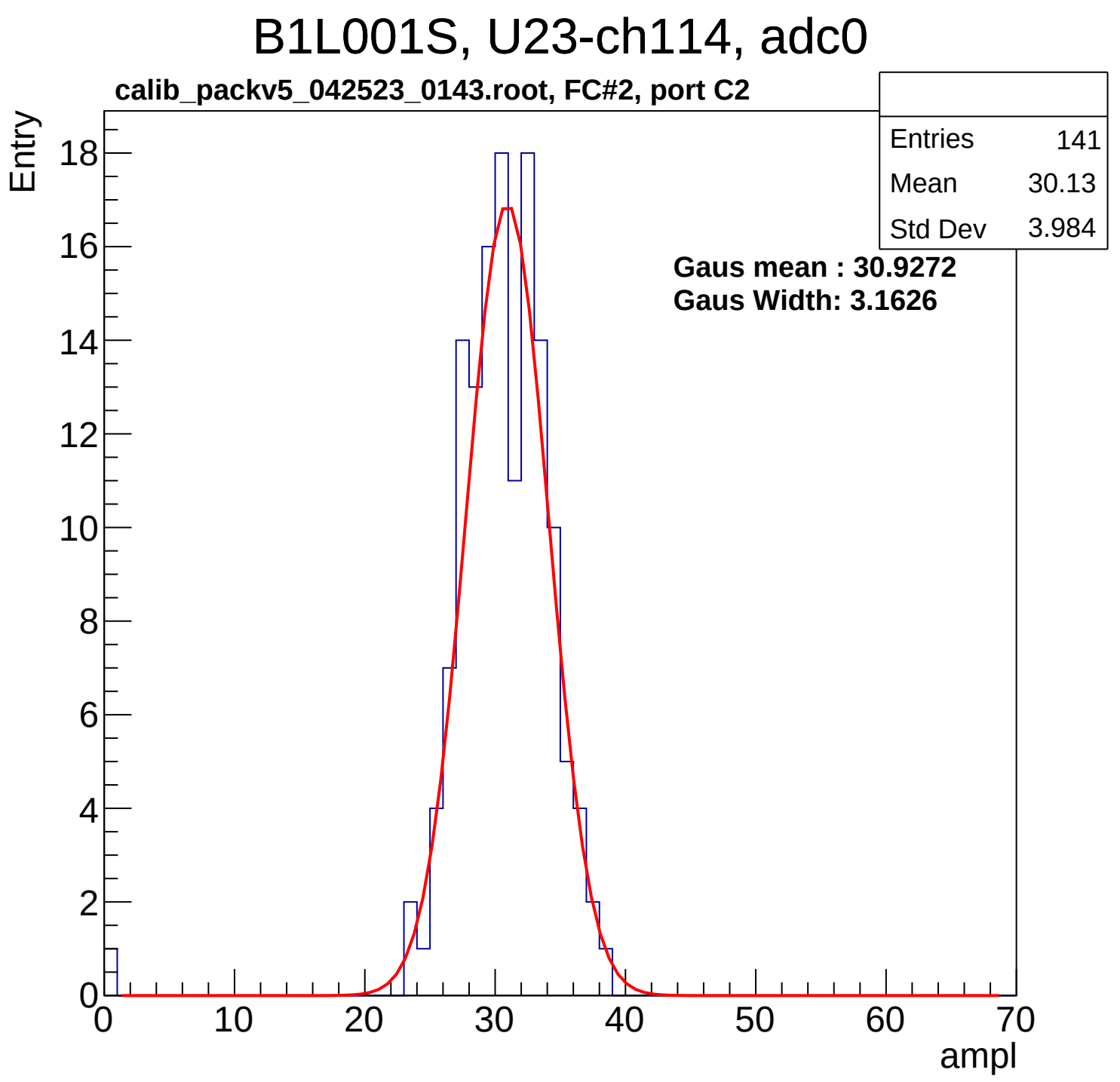
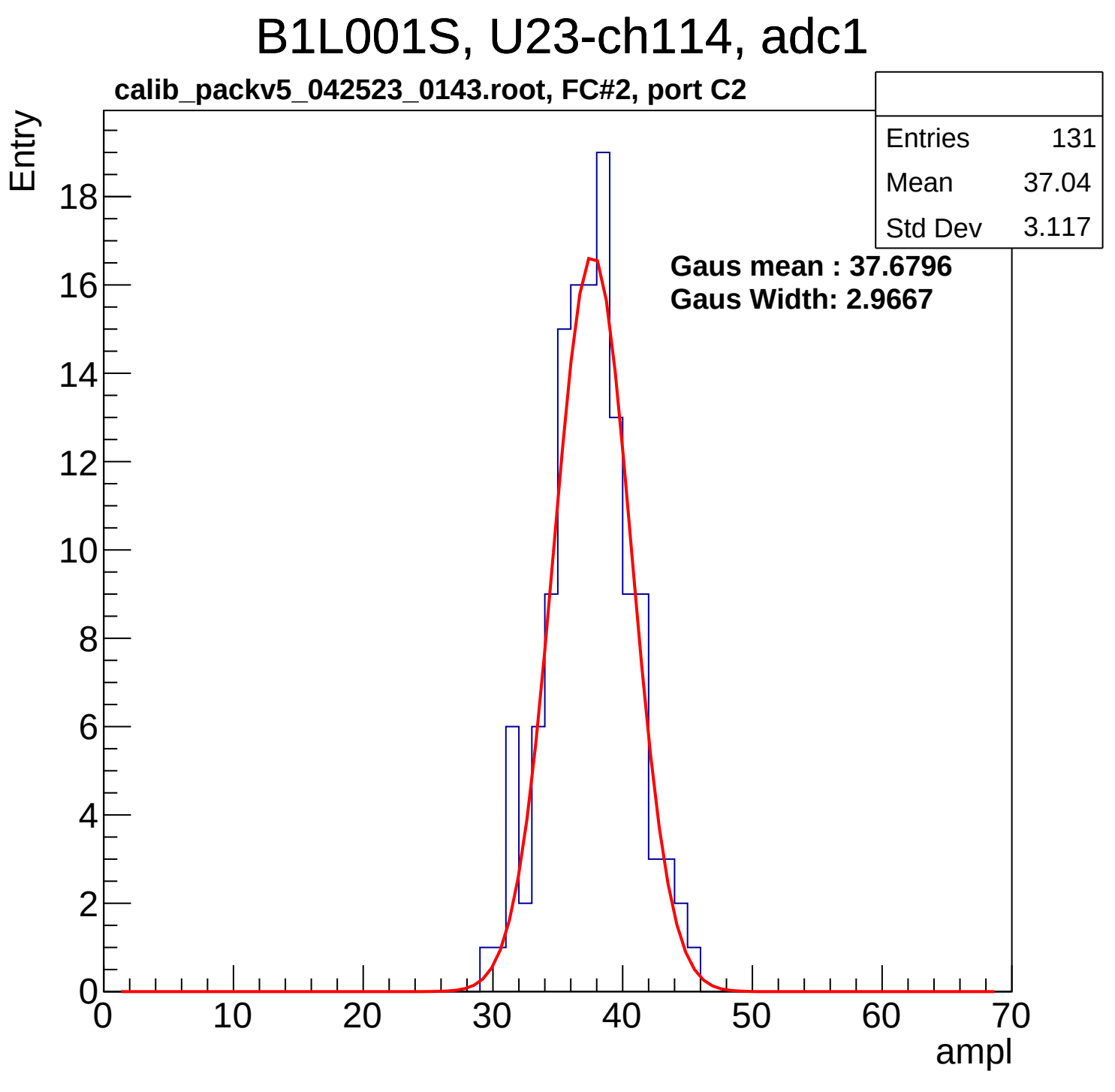
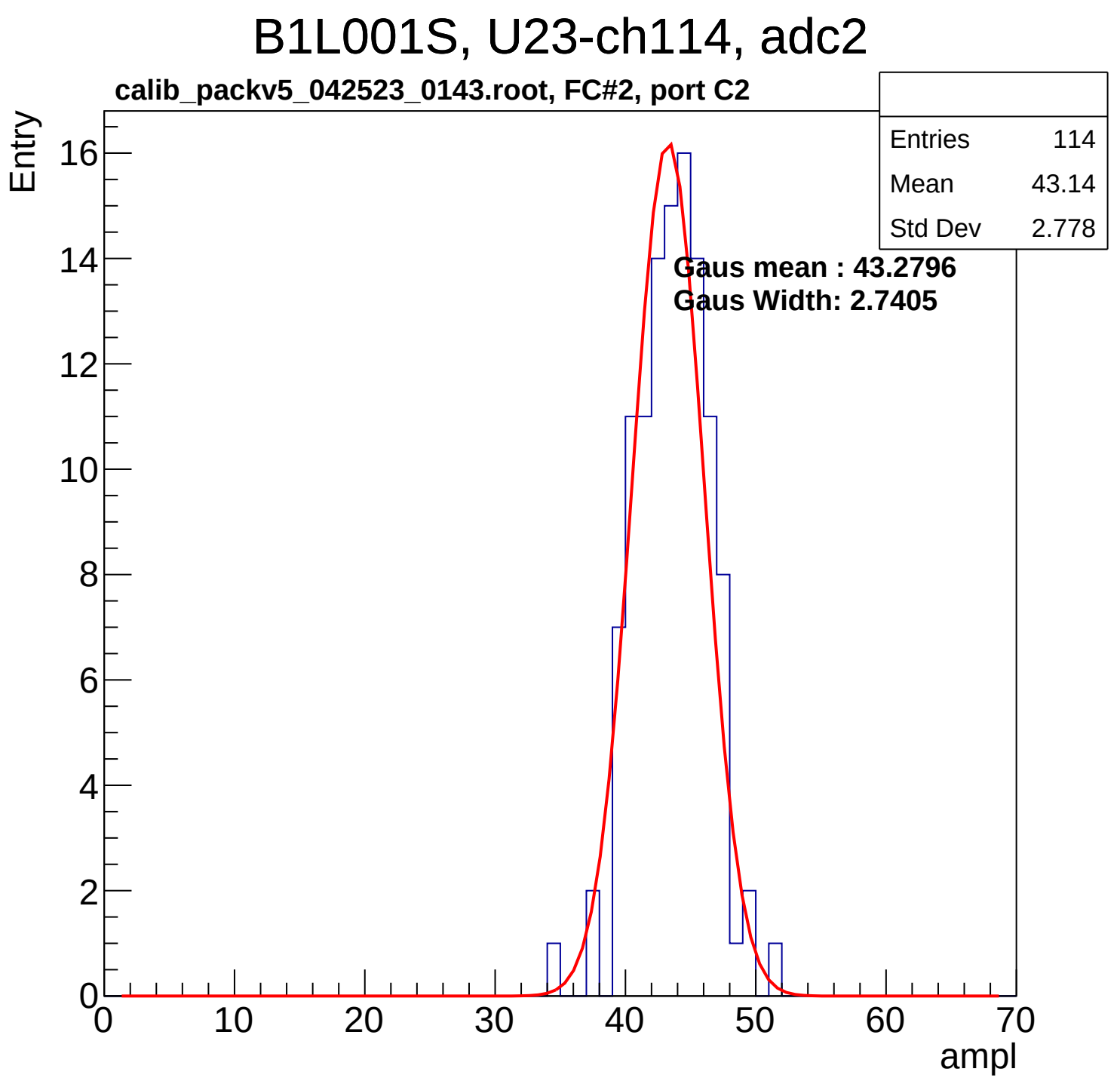
60
70
ampl


B1L001S, U23-ch114, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 141
Mean 30.13
Std Dev 3.984
Gaus mean : 30.9272
Gaus Width: 3.1626
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch114, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 37.04
Std Dev 3.117
Gaus mean : 37.6796
Gaus Width: 2.9667
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch114, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 43.14
Std Dev 2.778
Gaus mean : 43.2796
Gaus Width: 2.7405
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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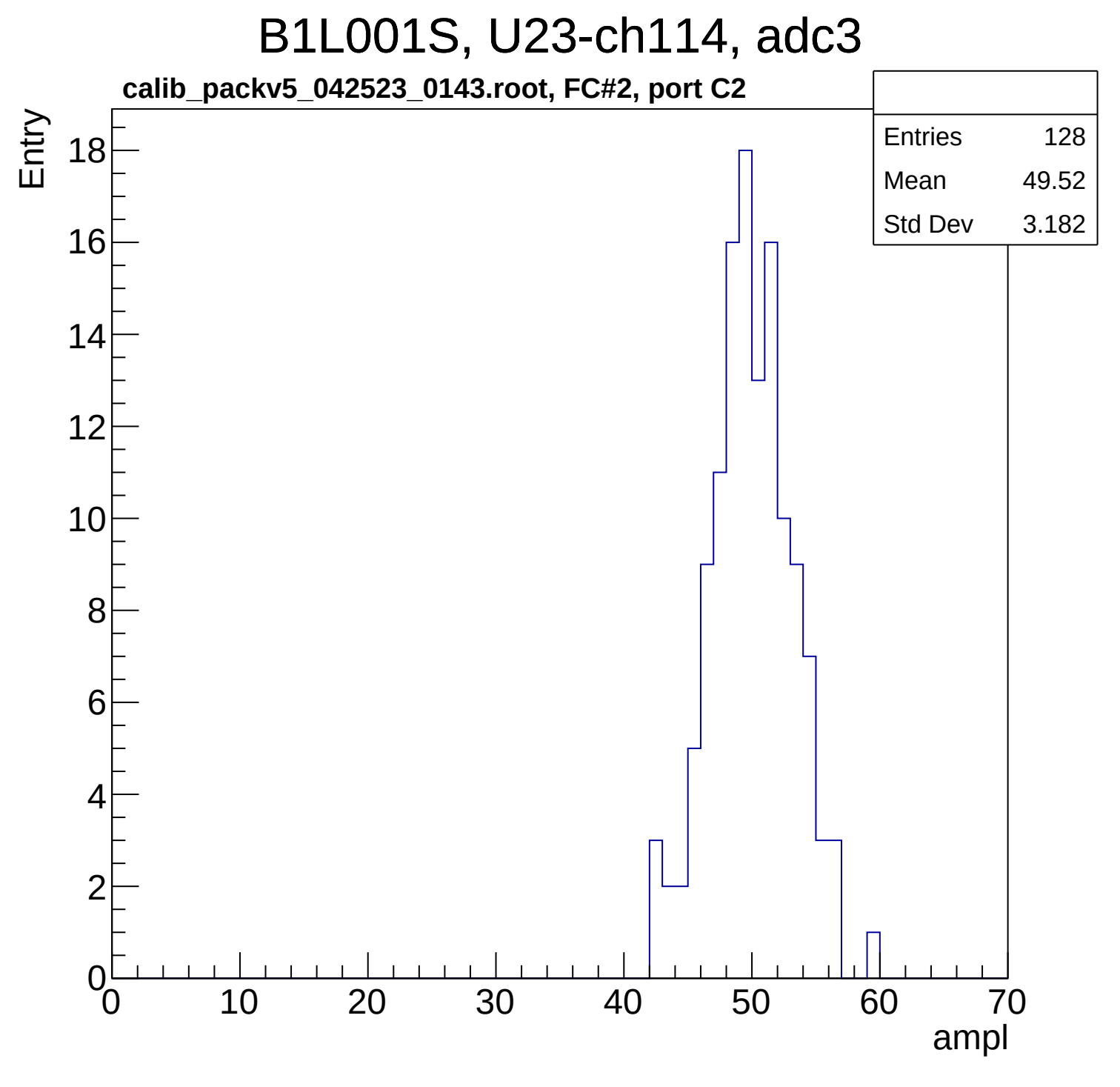
B1L001S, U23-ch114, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 49.52
Std Dev 3.182
Entry
ampl
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4
6
8
10
12
14
16
18
0
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40
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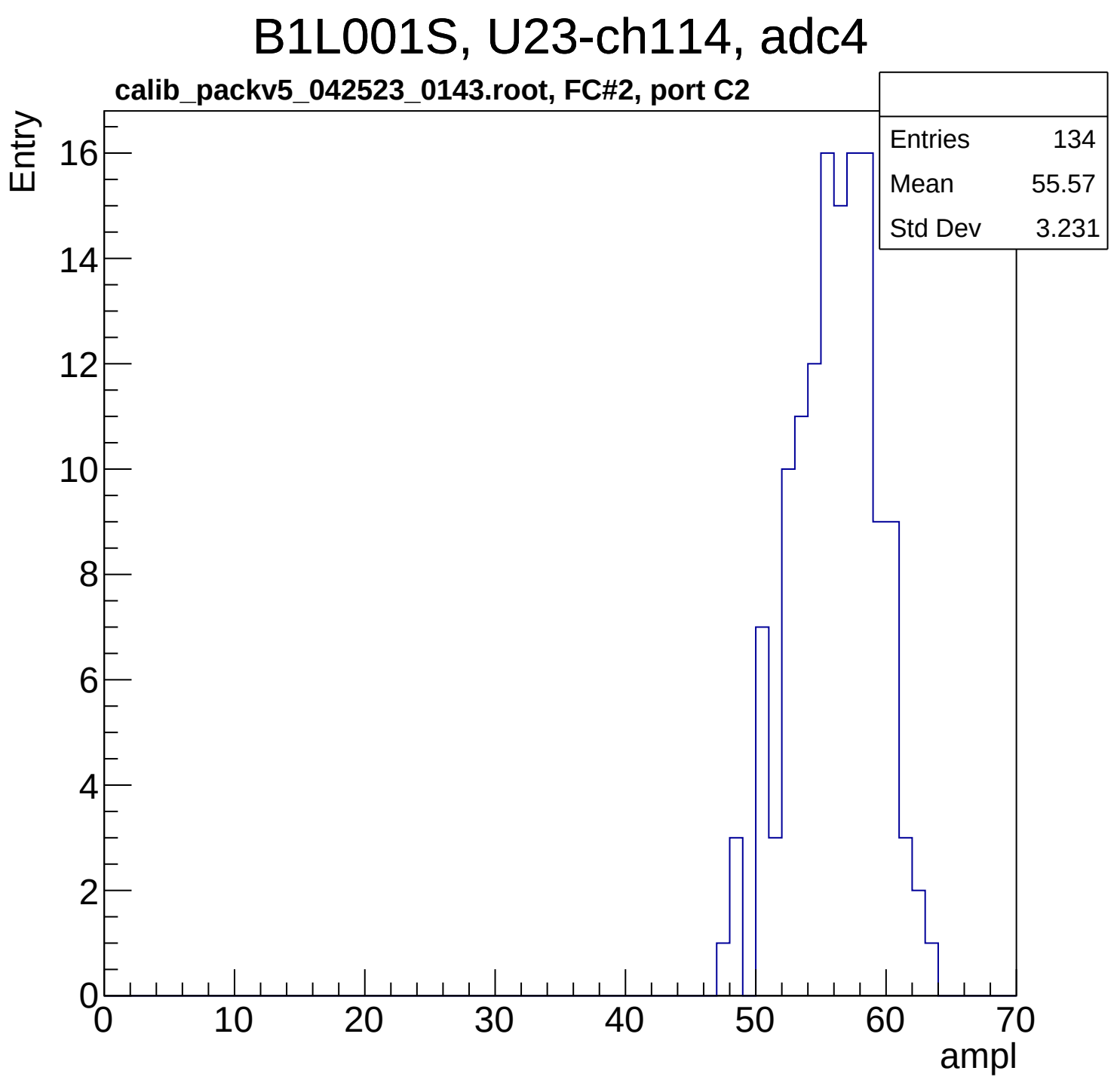
B1L001S, U23-ch114, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 55.57
Std Dev 3.231
Entry
ampl
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2
4
6
8
10
12
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16
0
10
20
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40
50
60
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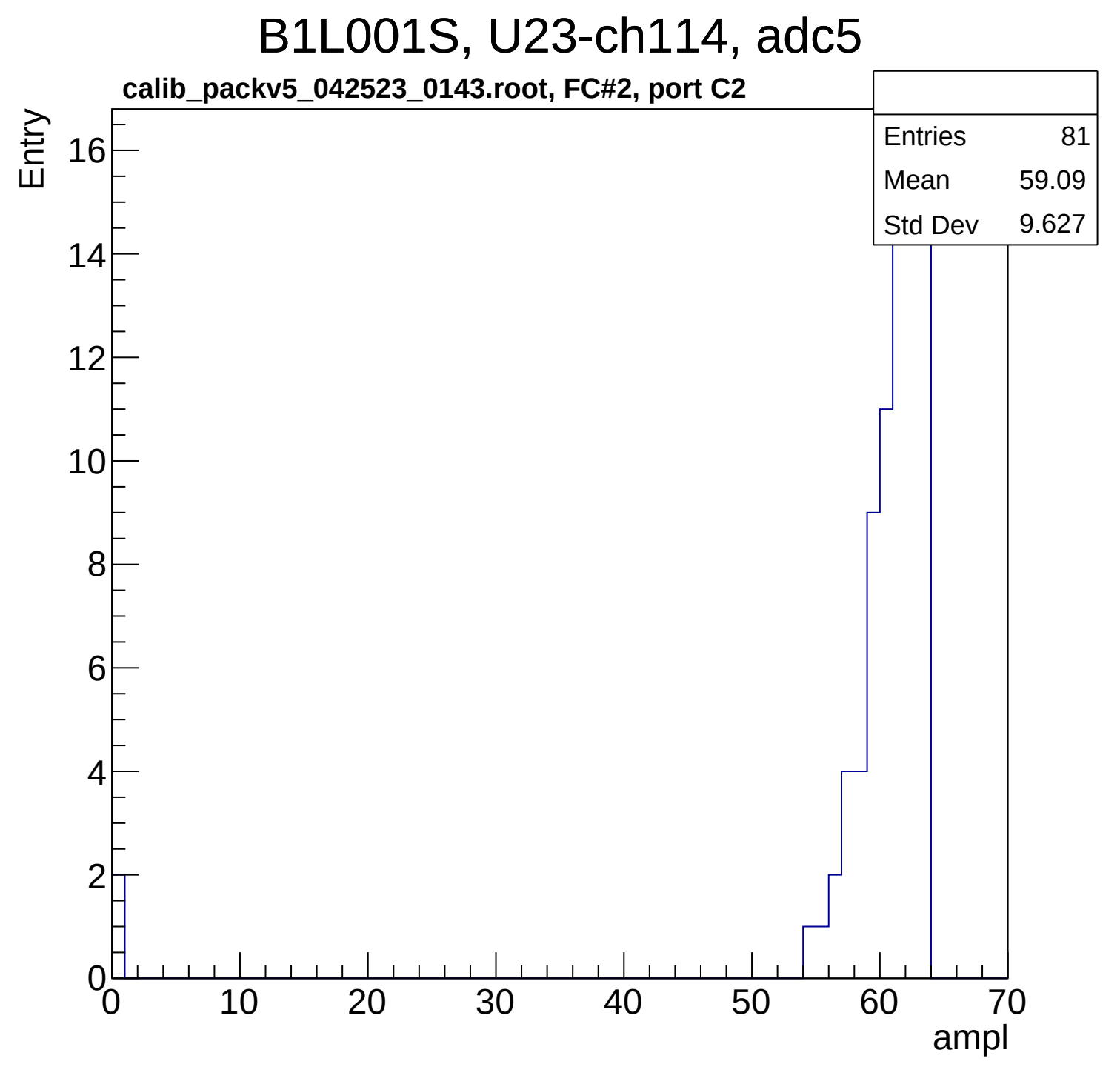
B1L001S, U23-ch114, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 59.09
Std Dev 9.627
Entry
0
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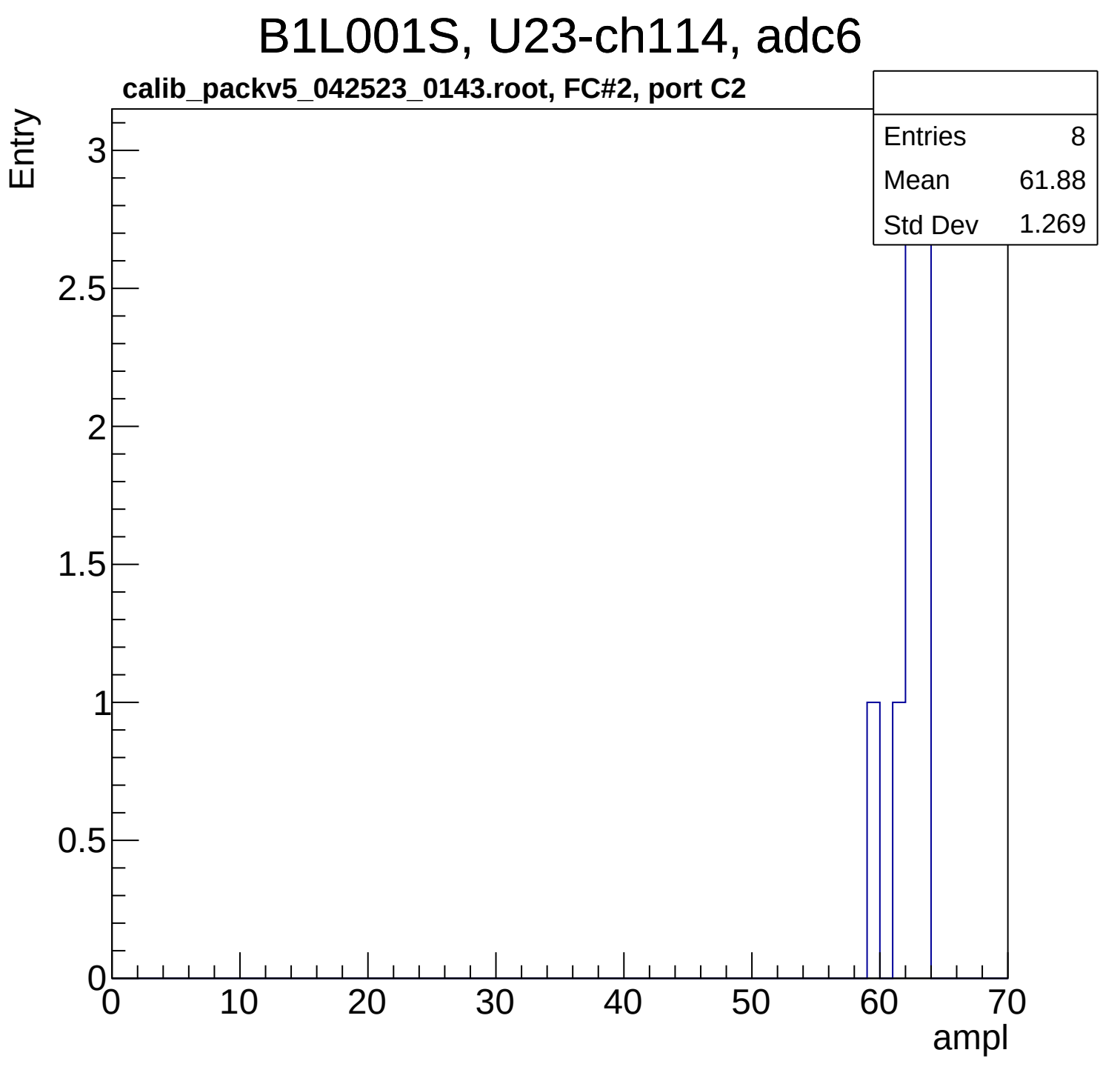
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch114, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 8
Mean 61.88
Std Dev 1.269
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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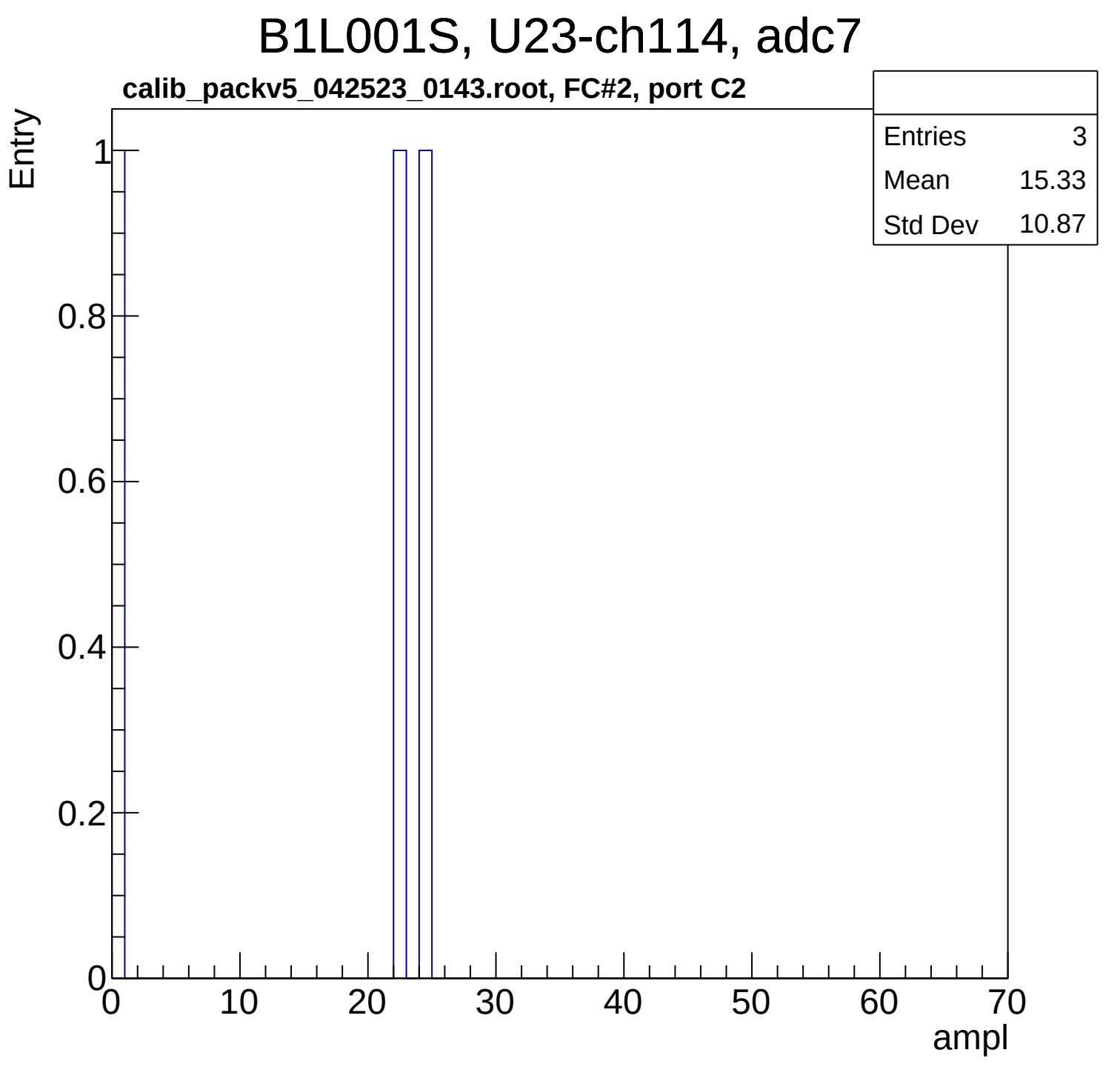
B1L001S, U23-ch114, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 15.33
Std Dev 10.87
Entry
ampl
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0
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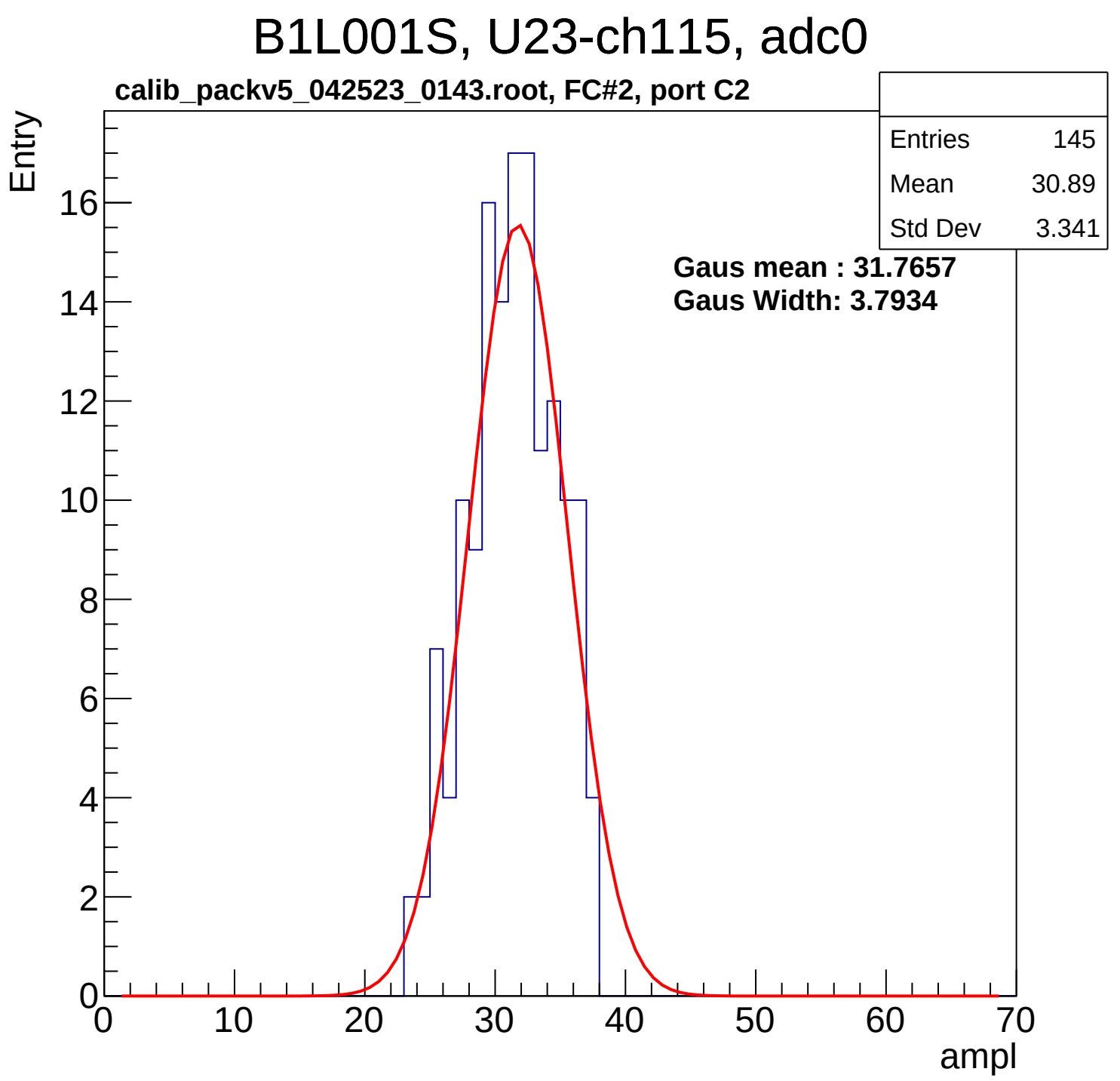
B1L001S, U23-ch115, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 30.89
Std Dev 3.341
Gaus mean : 31.7657
Gaus Width: 3.7934
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
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60
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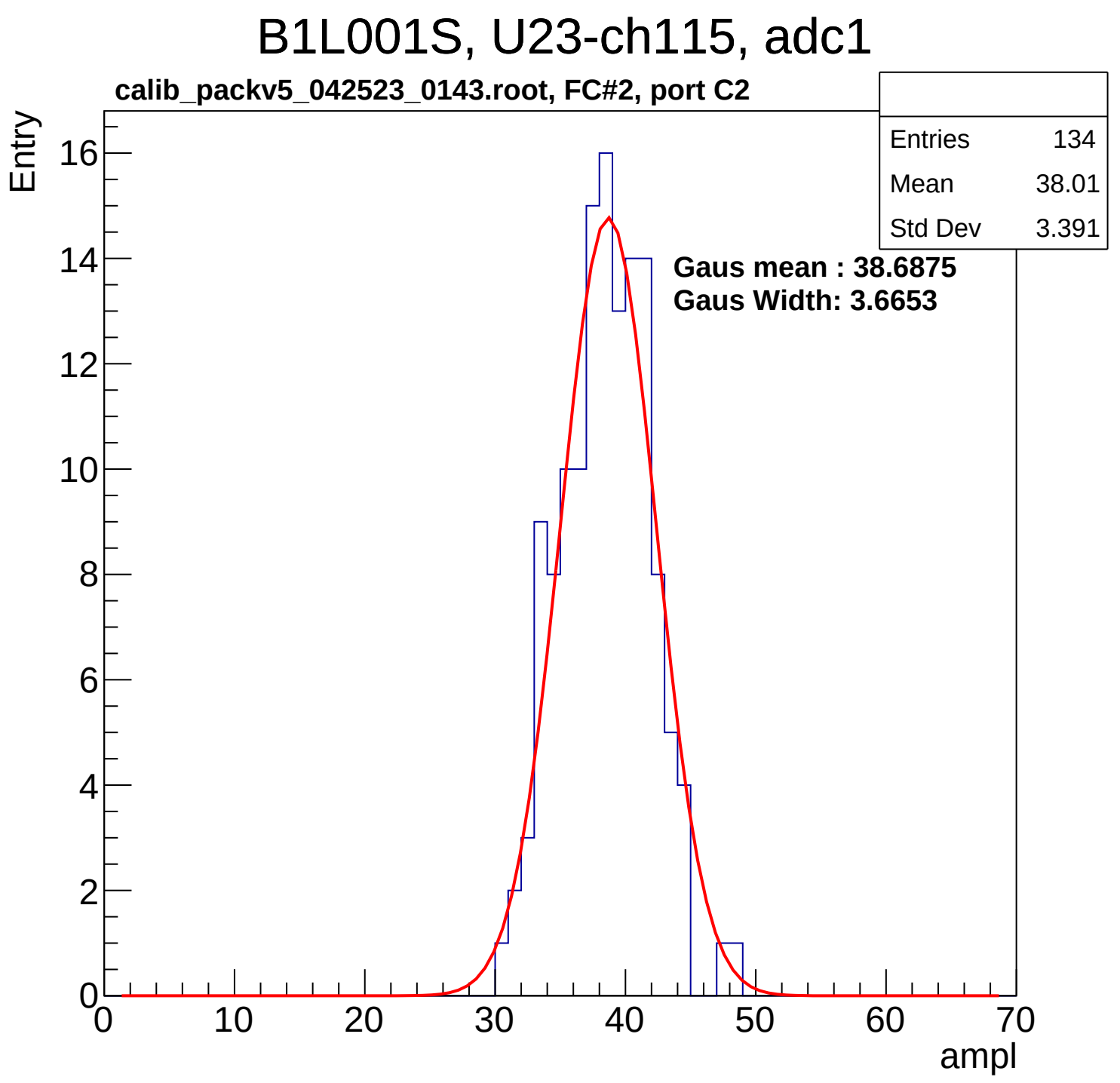
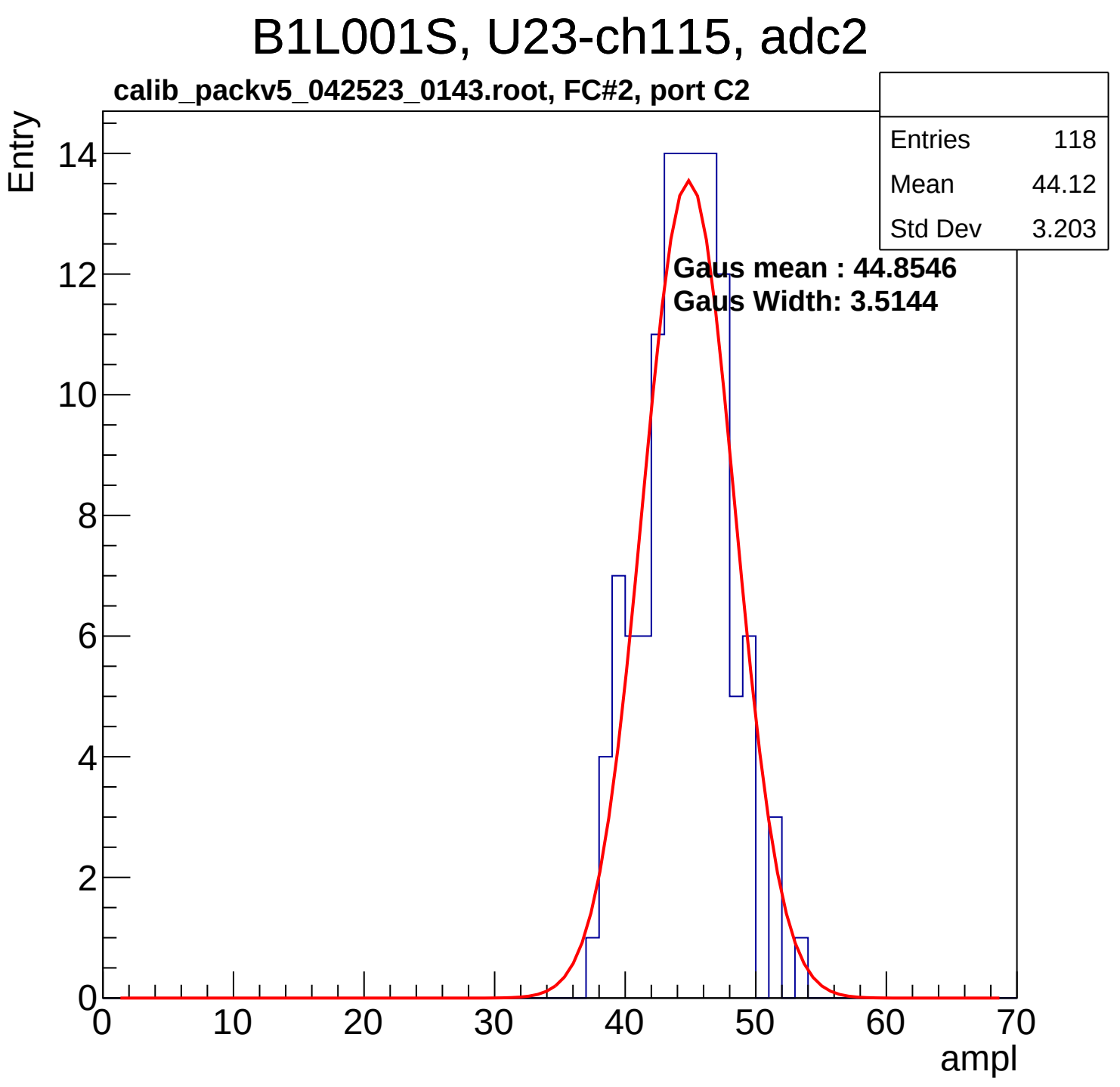

B1L001S, U23-ch115, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 134
Mean 38.01
Std Dev 3.391
Gaus mean : 38.6875
Gaus Width: 3.6653
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch115, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 44.12
Std Dev 3.203
Gaus mean : 44.8546
Gaus Width: 3.5144
Entry
ampl
0
2
4
6
8
10
12
14
0
10
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60
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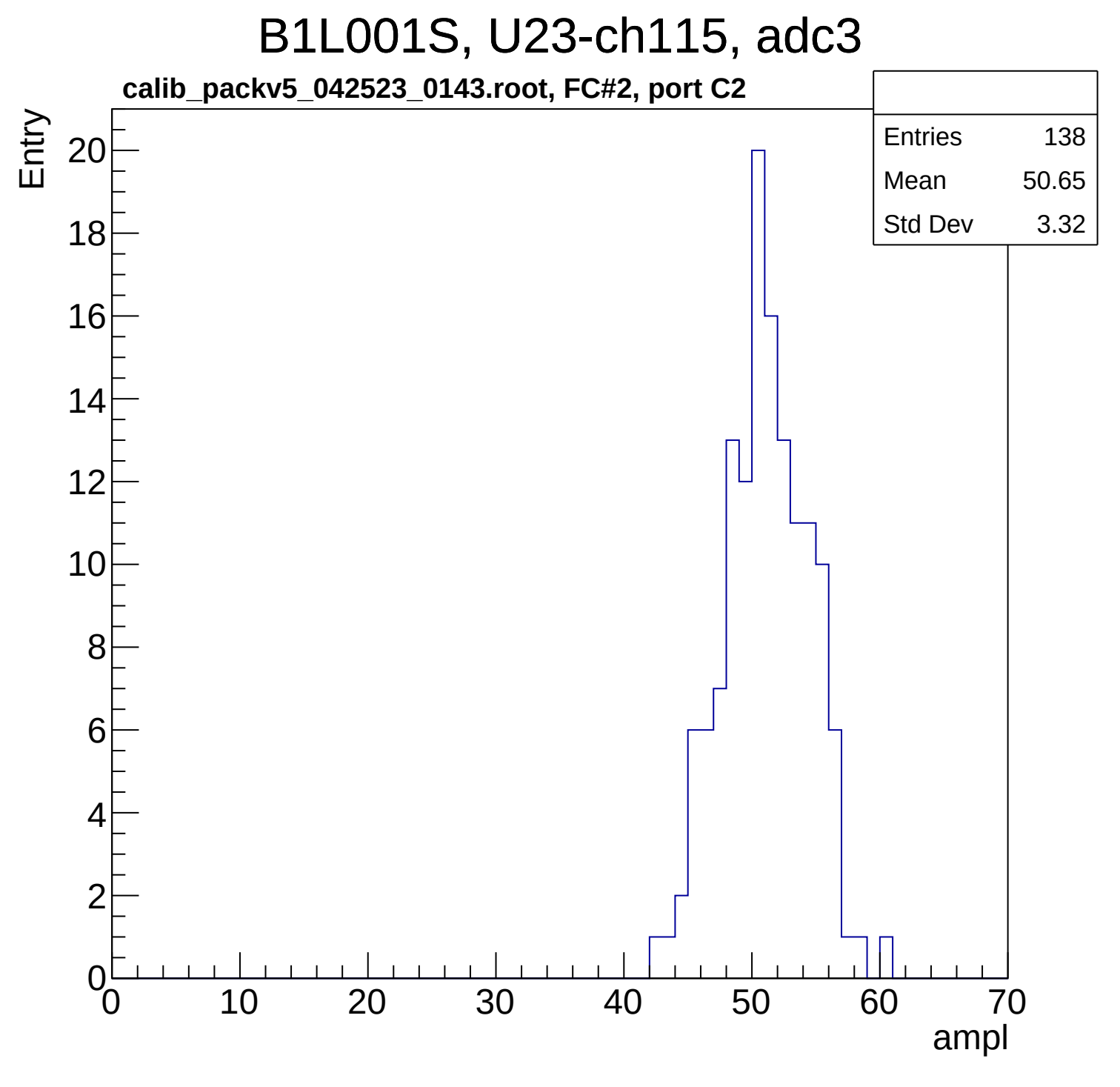
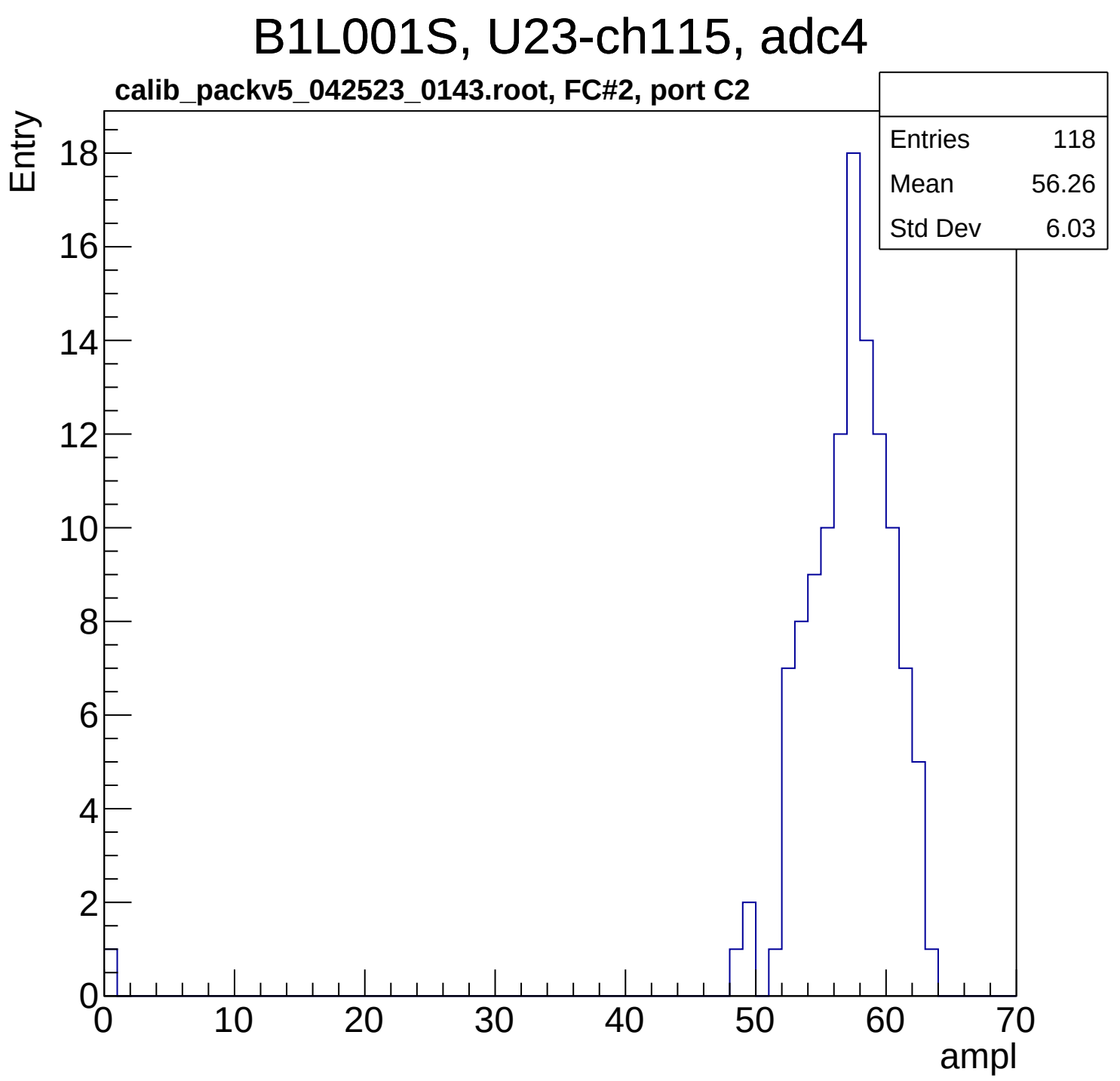

B1L001S, U23-ch115, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 50.65
Std Dev 3.32
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch115, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 56.26
Std Dev 6.03
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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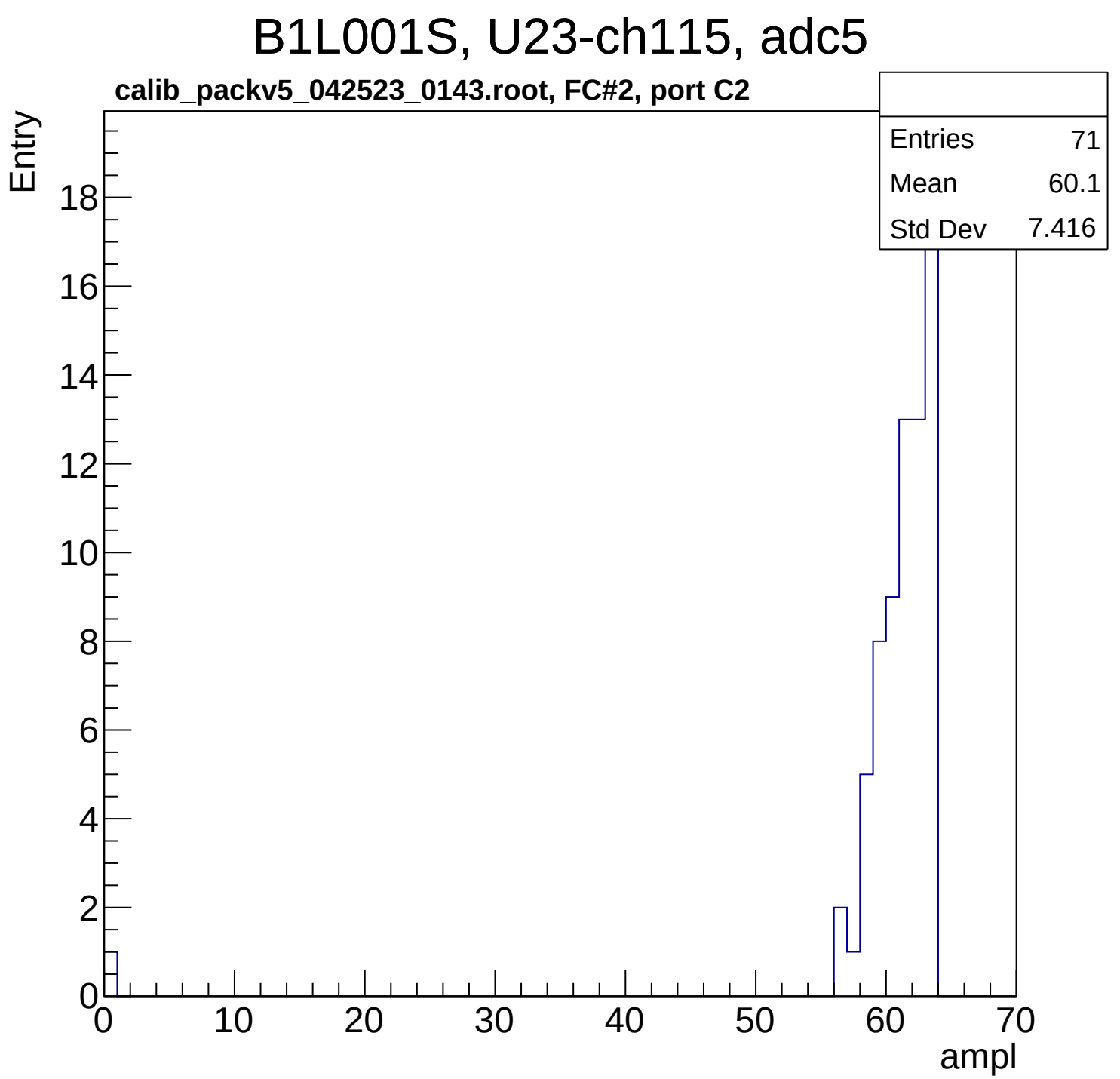
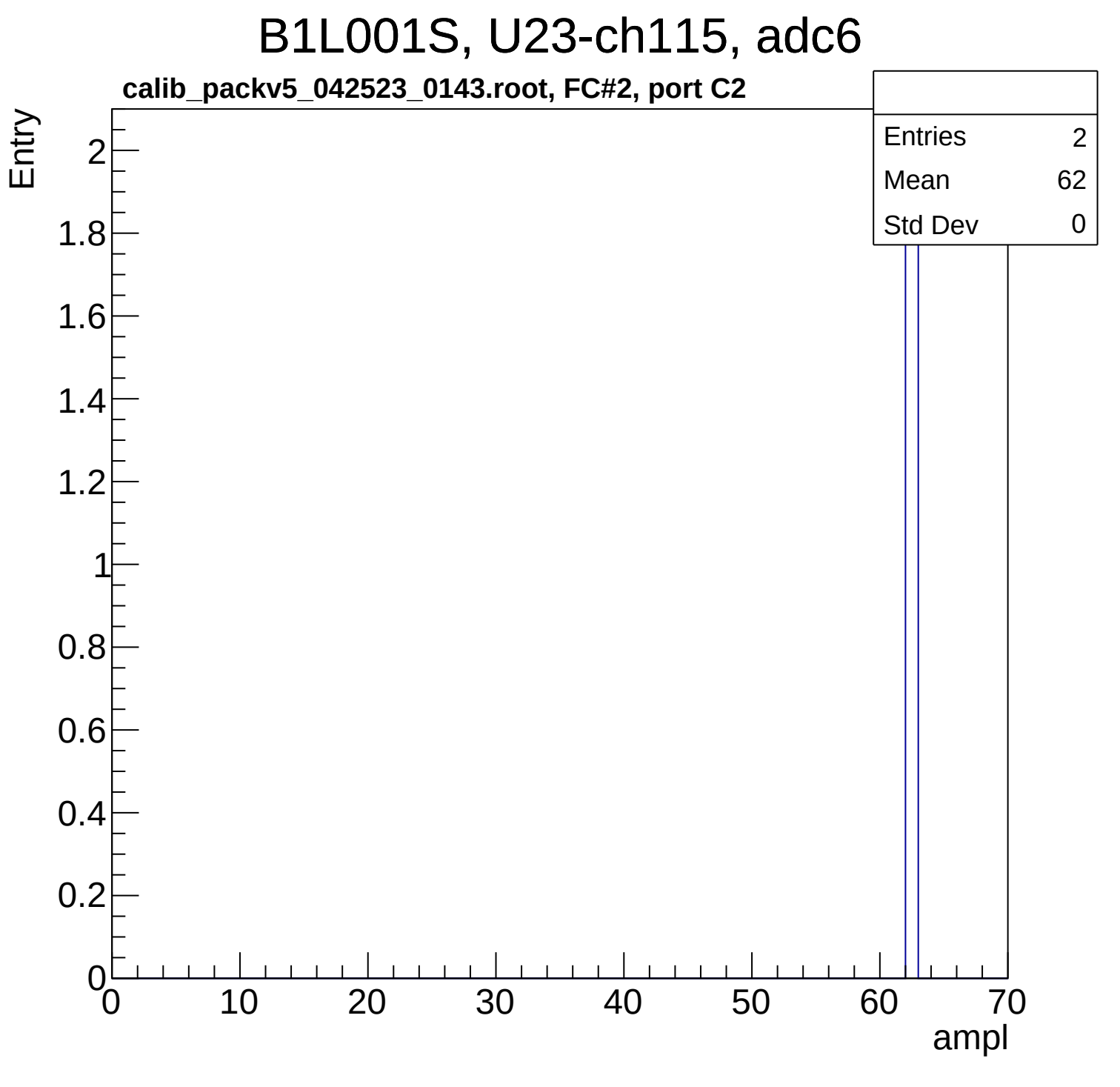
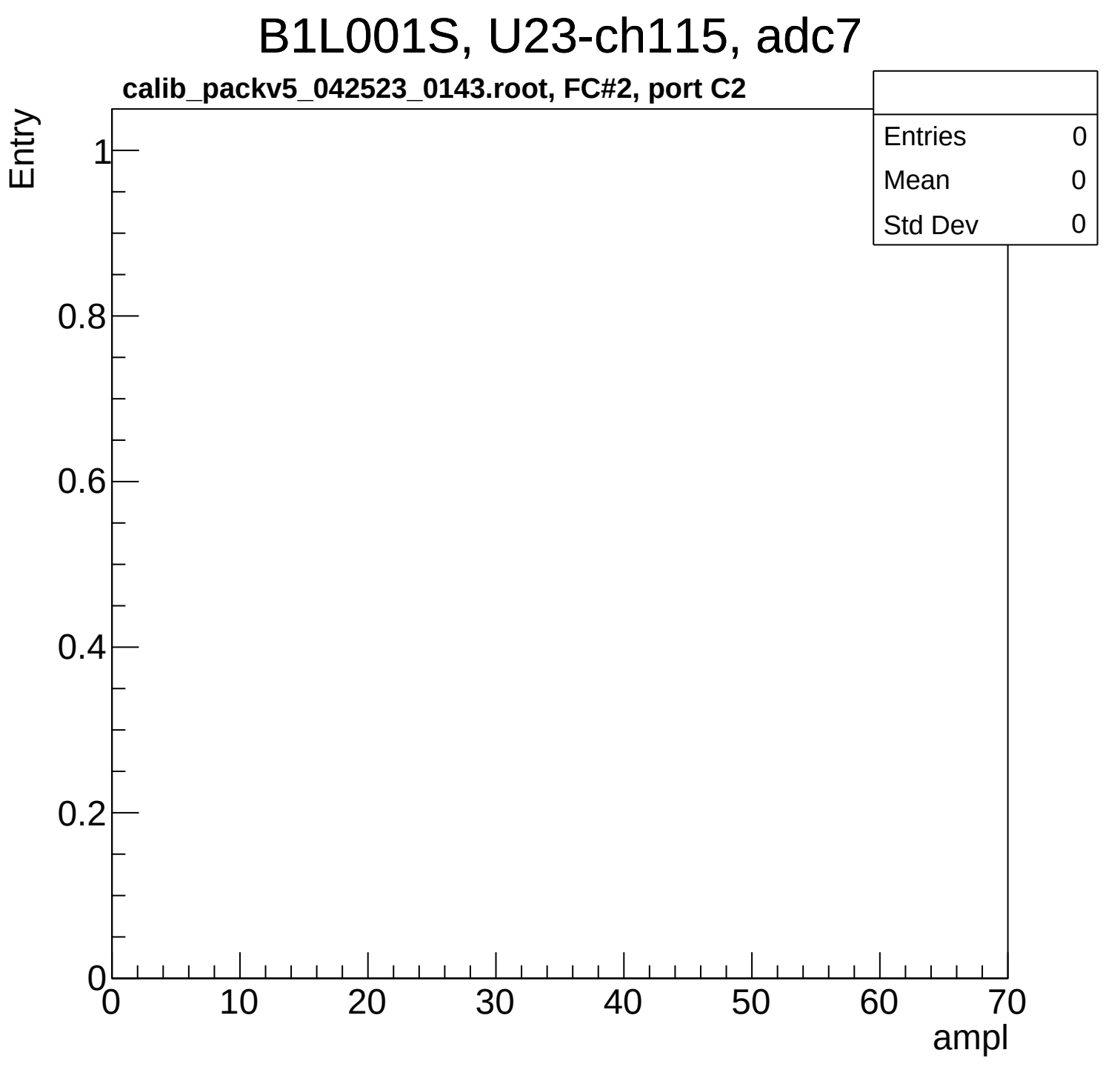
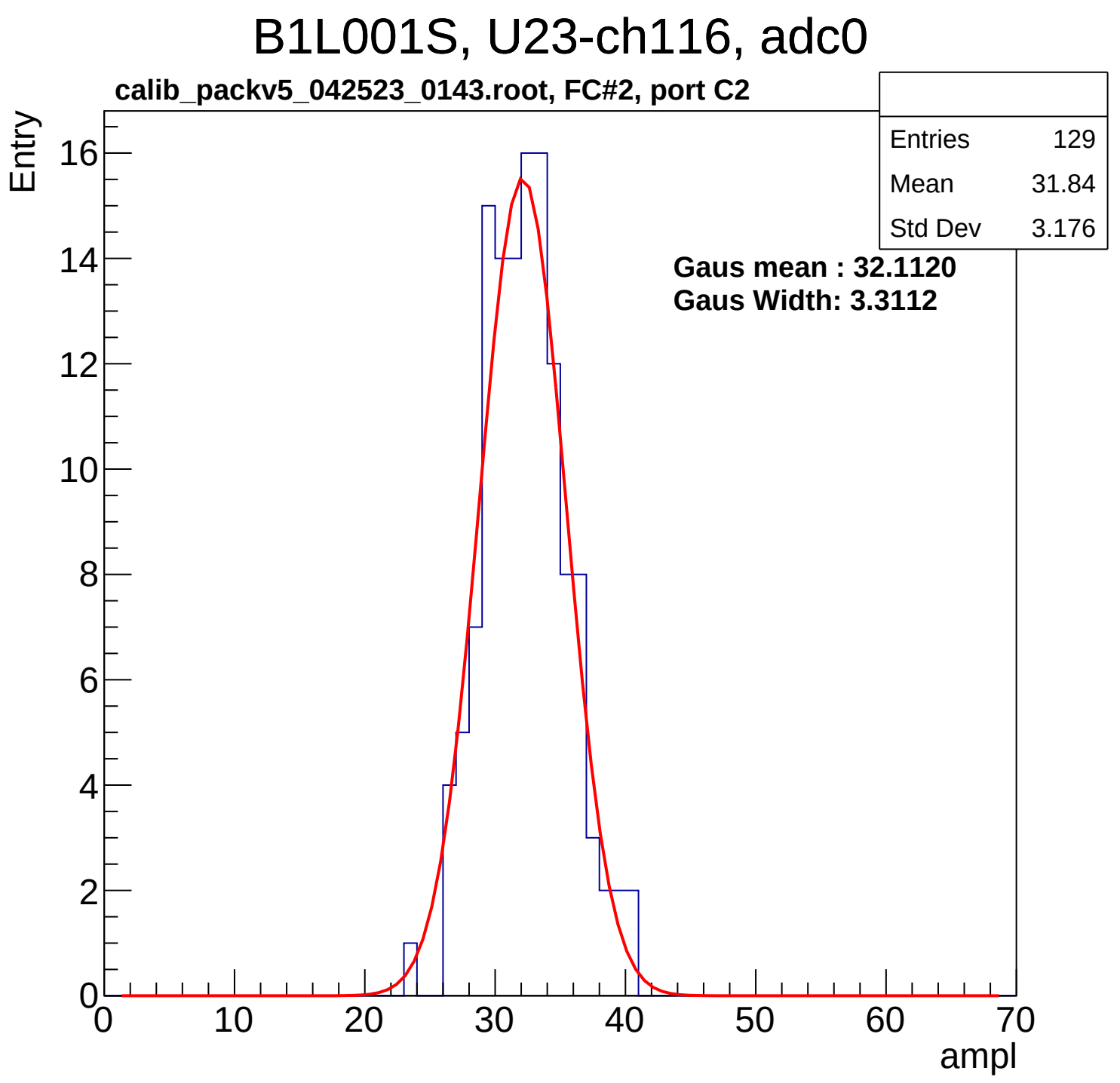
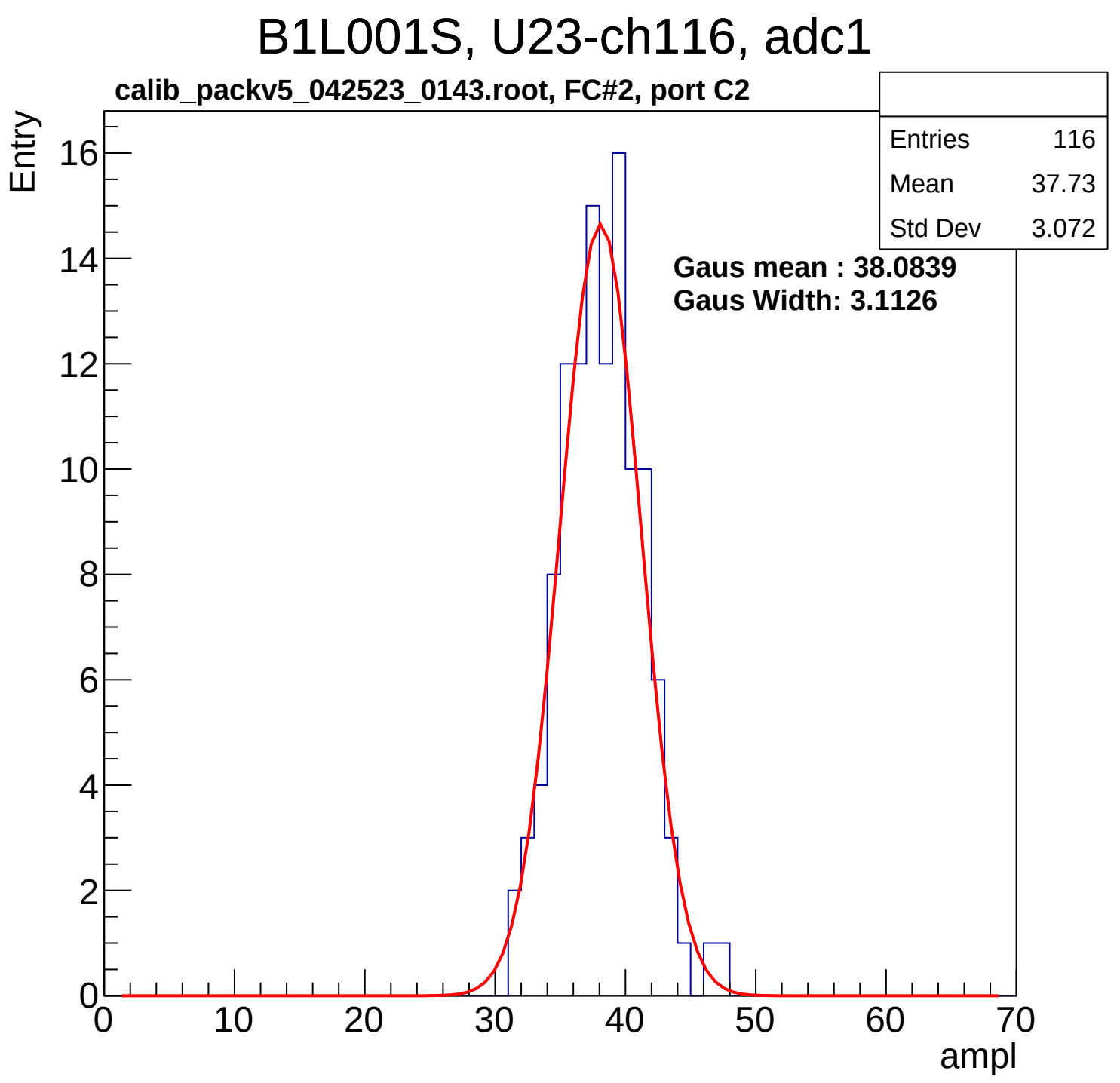
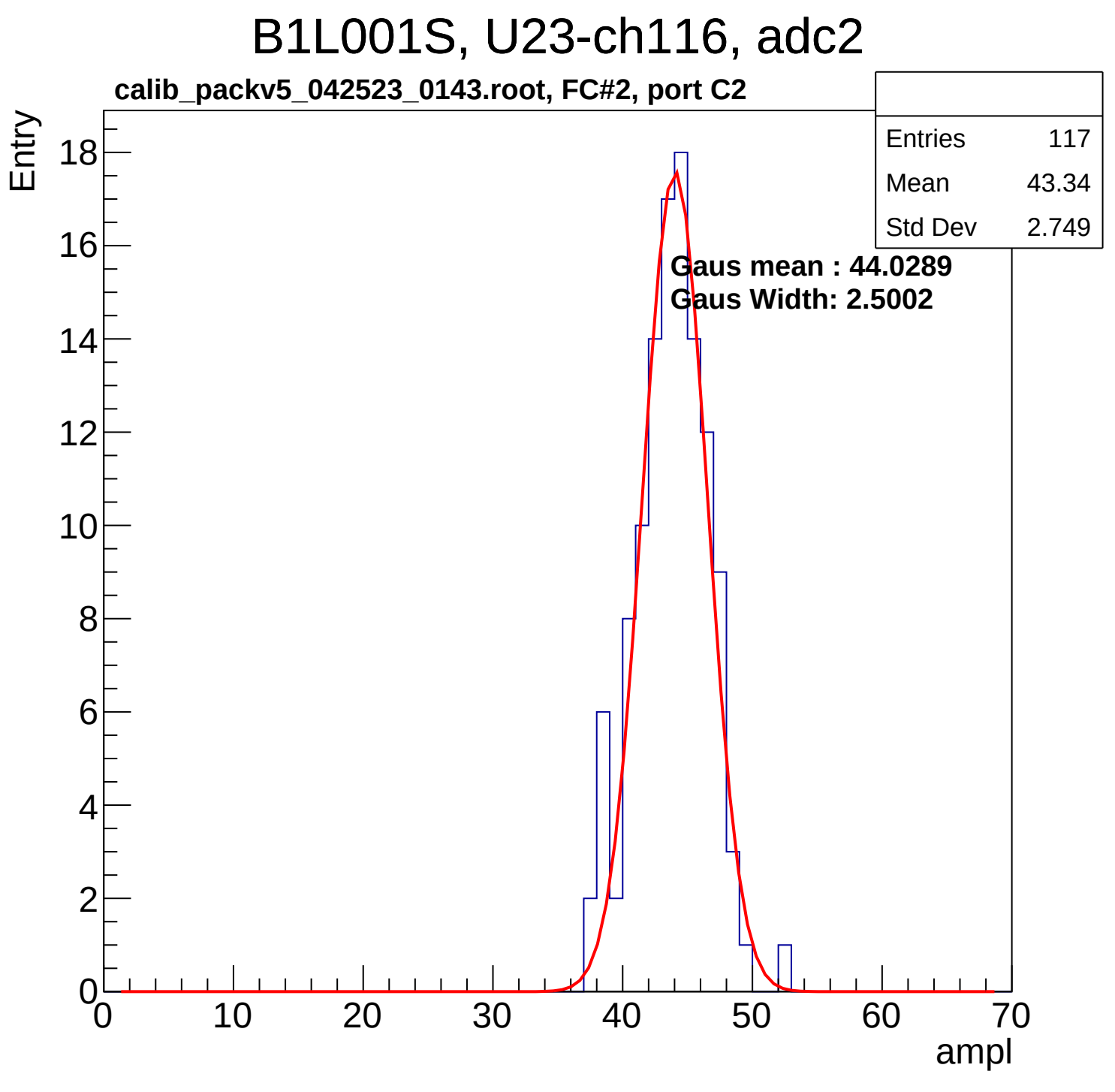

B1L001S, U23-ch115, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 71
Mean 60.1
Std Dev 7.416
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch115, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch115, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch116, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 129
Mean 31.84
Std Dev 3.176
Gaus mean : 32.1120
Gaus Width: 3.3112
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch116, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 116
Mean 37.73
Std Dev 3.072
Gaus mean : 38.0839
Gaus Width: 3.1126
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch116, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 43.34
Std Dev 2.749
Gaus mean : 44.0289
Gaus Width: 2.5002
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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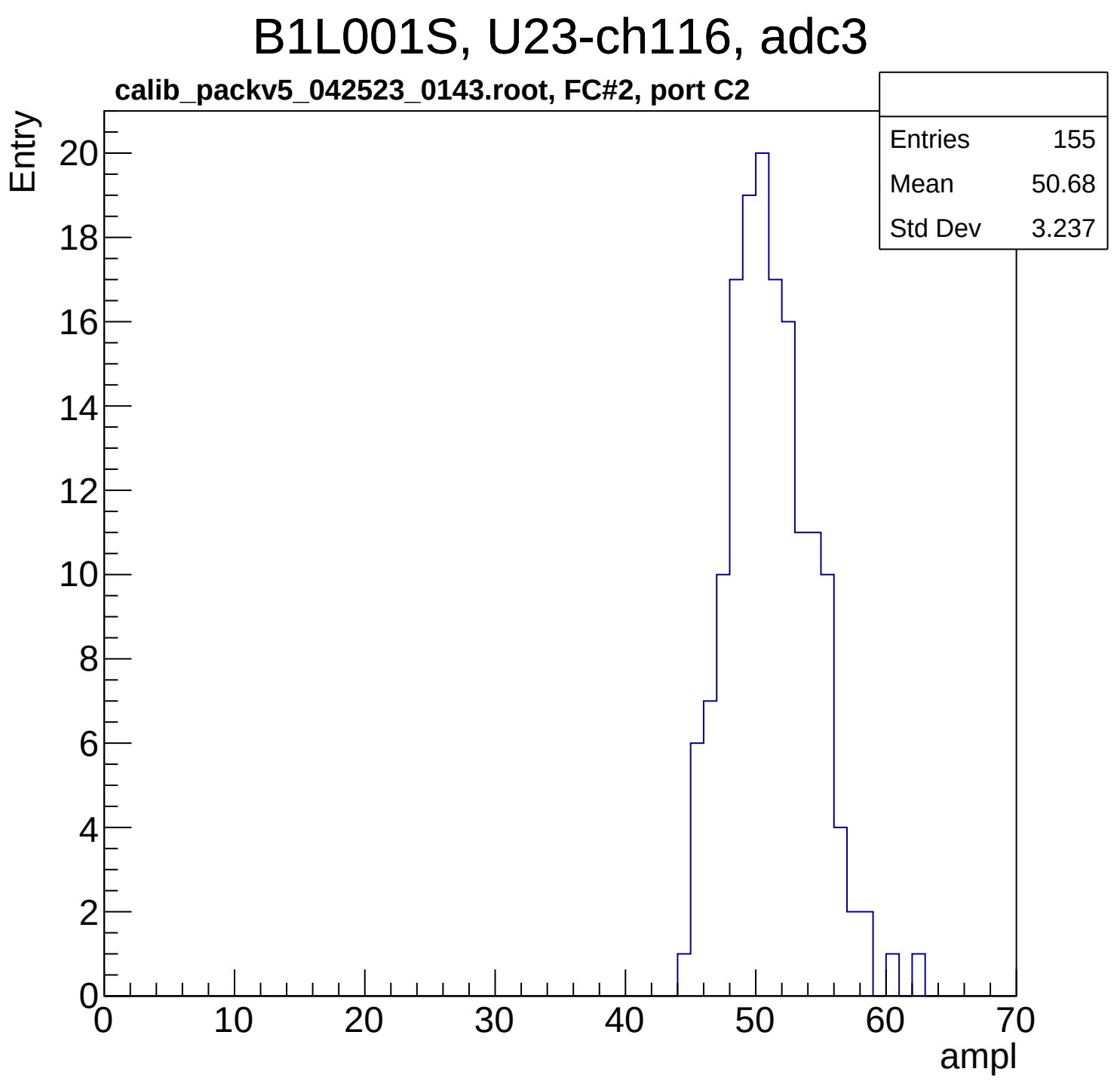
B1L001S, U23-ch116, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 50.68
Std Dev 3.237
Entry
ampl
0
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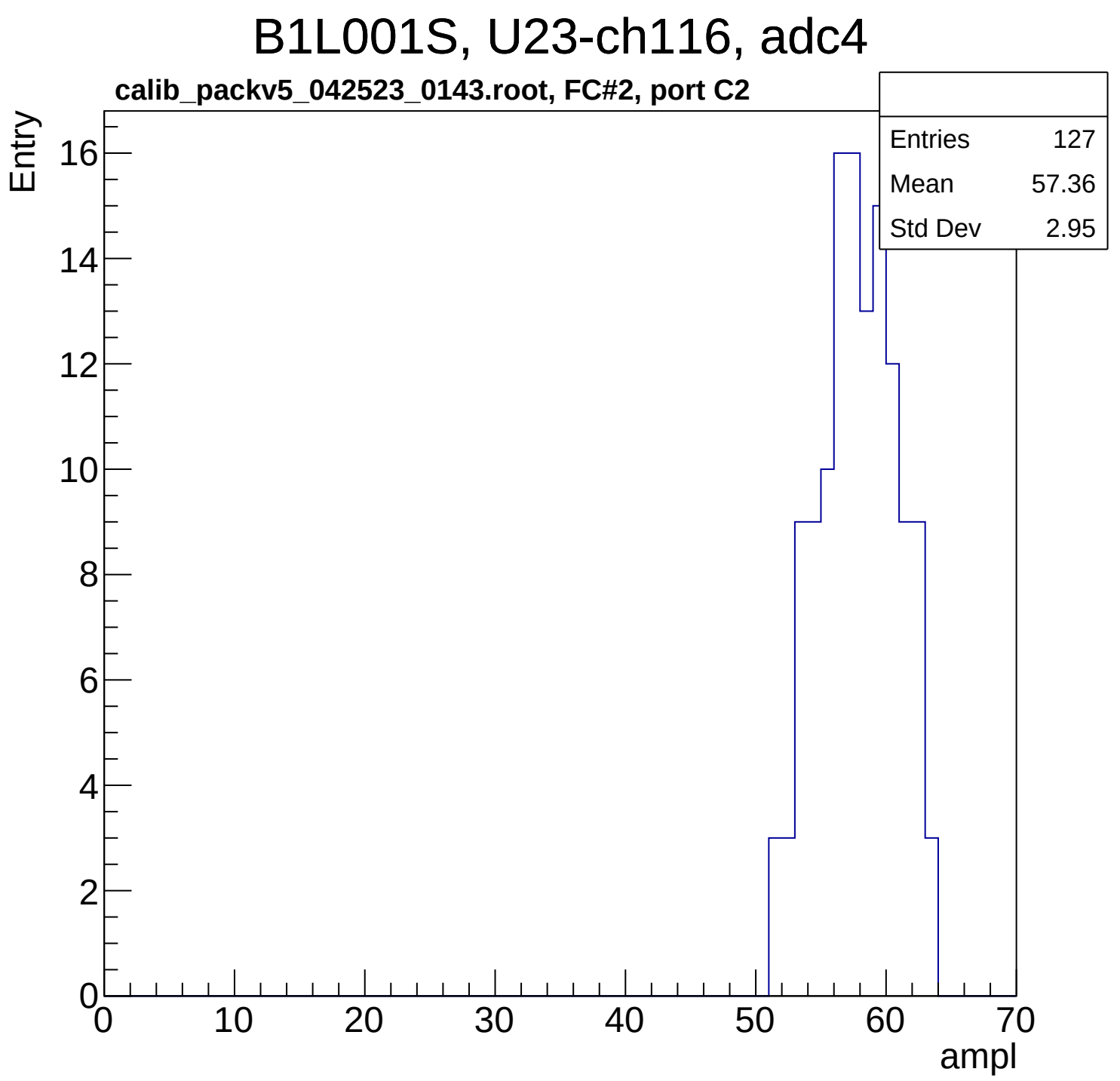
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
70


B1L001S, U23-ch116, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 57.36
Std Dev 2.95
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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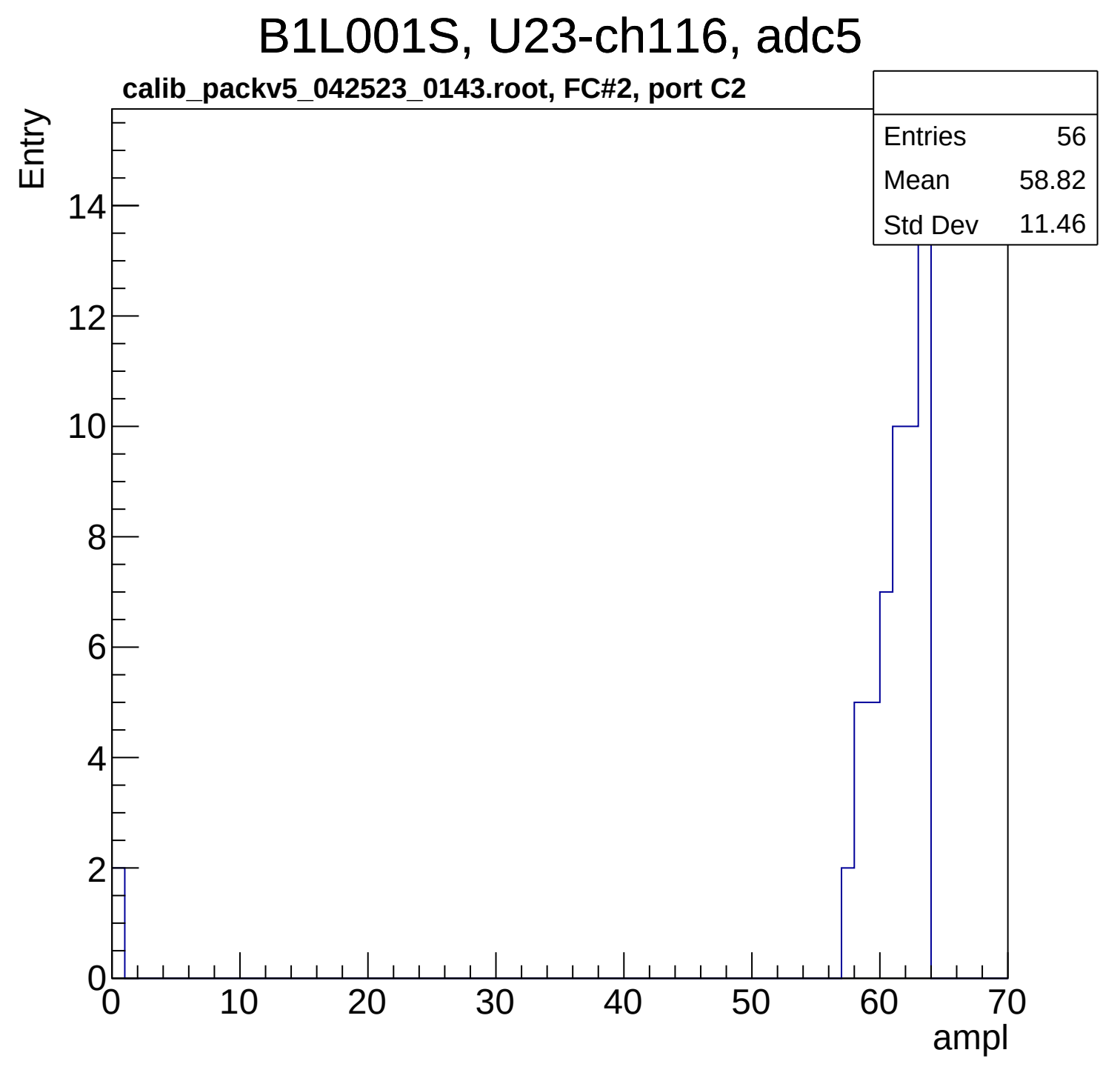
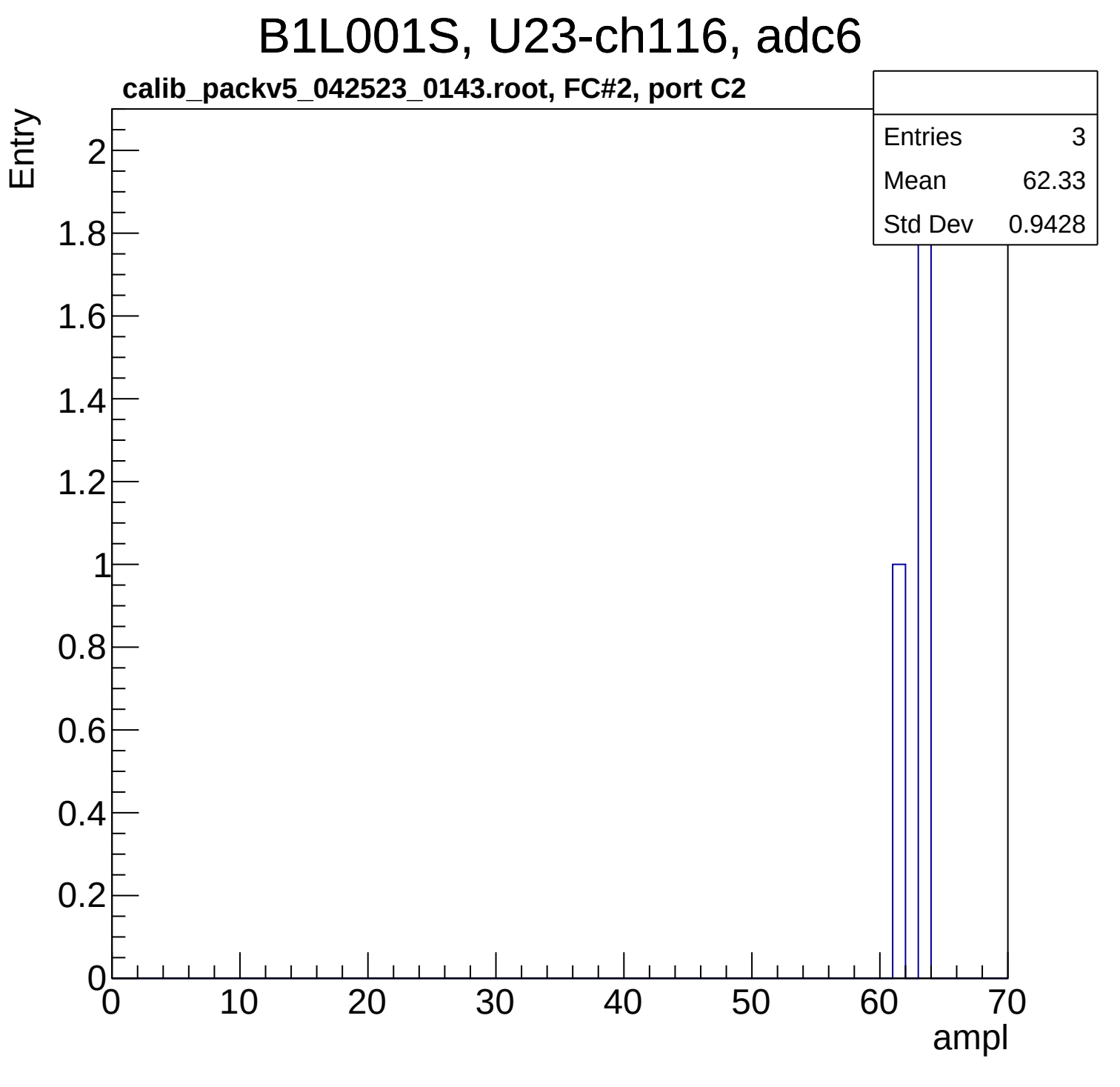
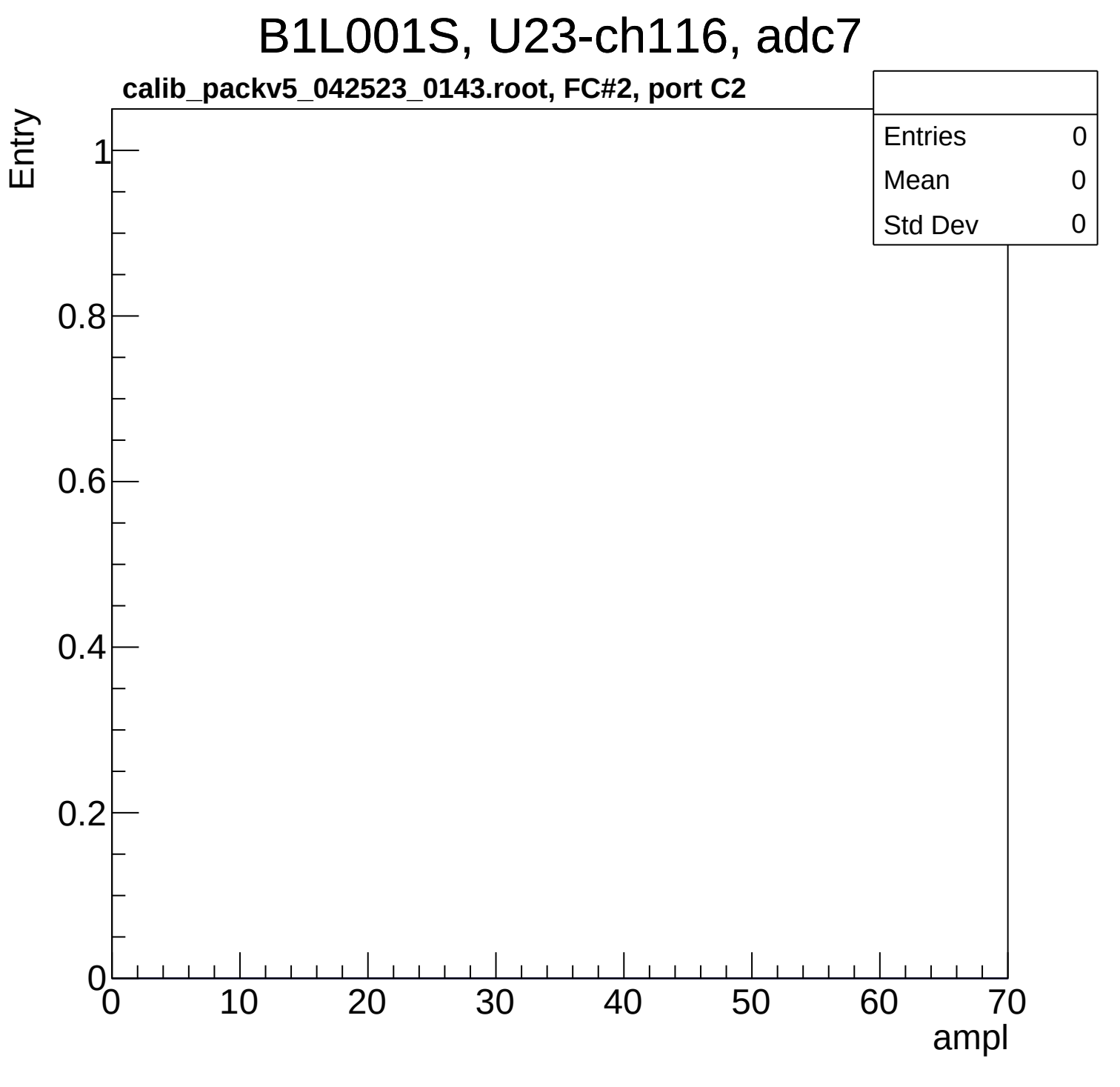
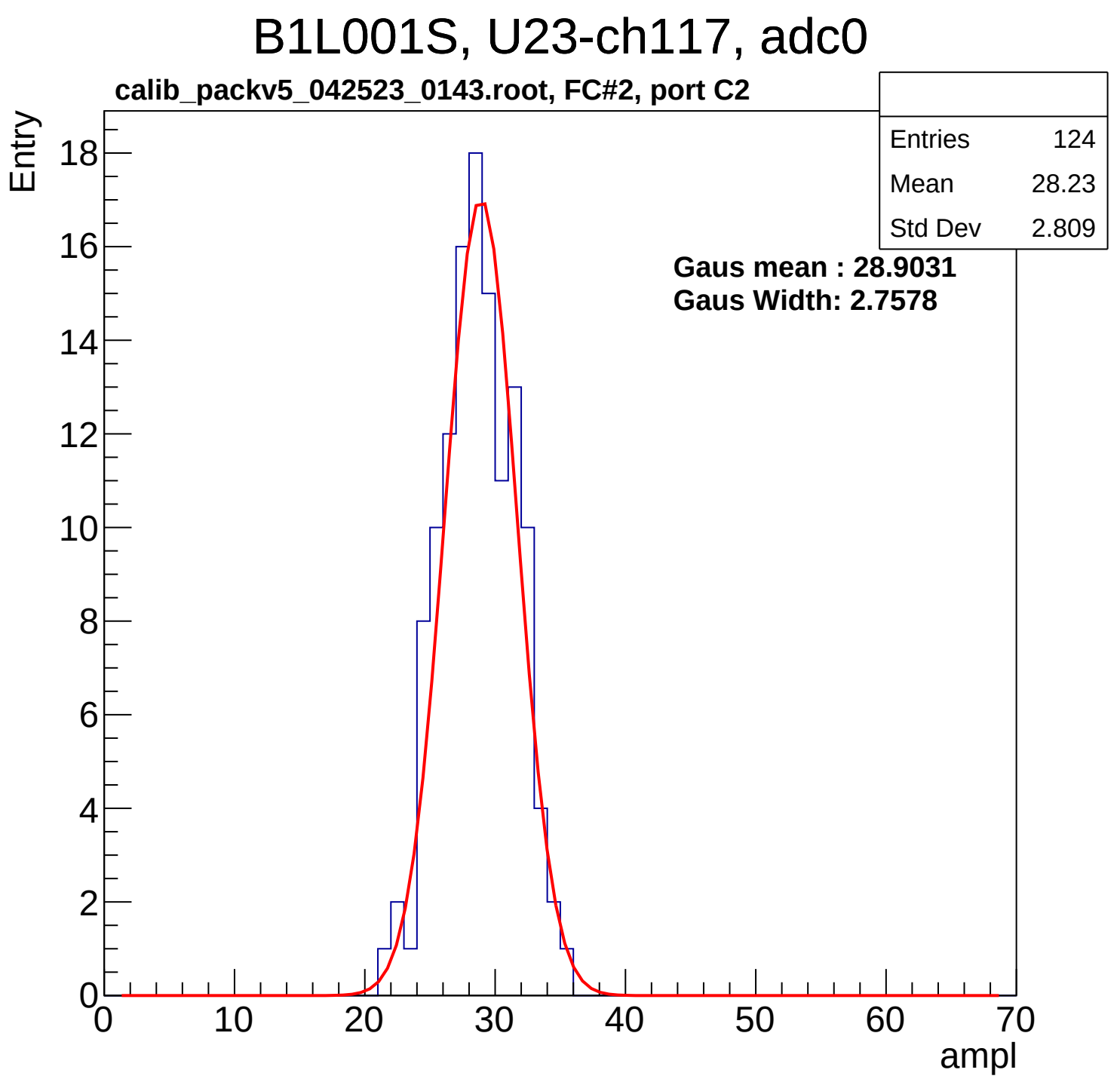

B1L001S, U23-ch116, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 56
Mean 58.82
Std Dev 11.46
Entry
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch116, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch116, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch117, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 124
Mean 28.23
Std Dev 2.809
Gaus mean : 28.9031
Gaus Width: 2.7578
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
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40
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60
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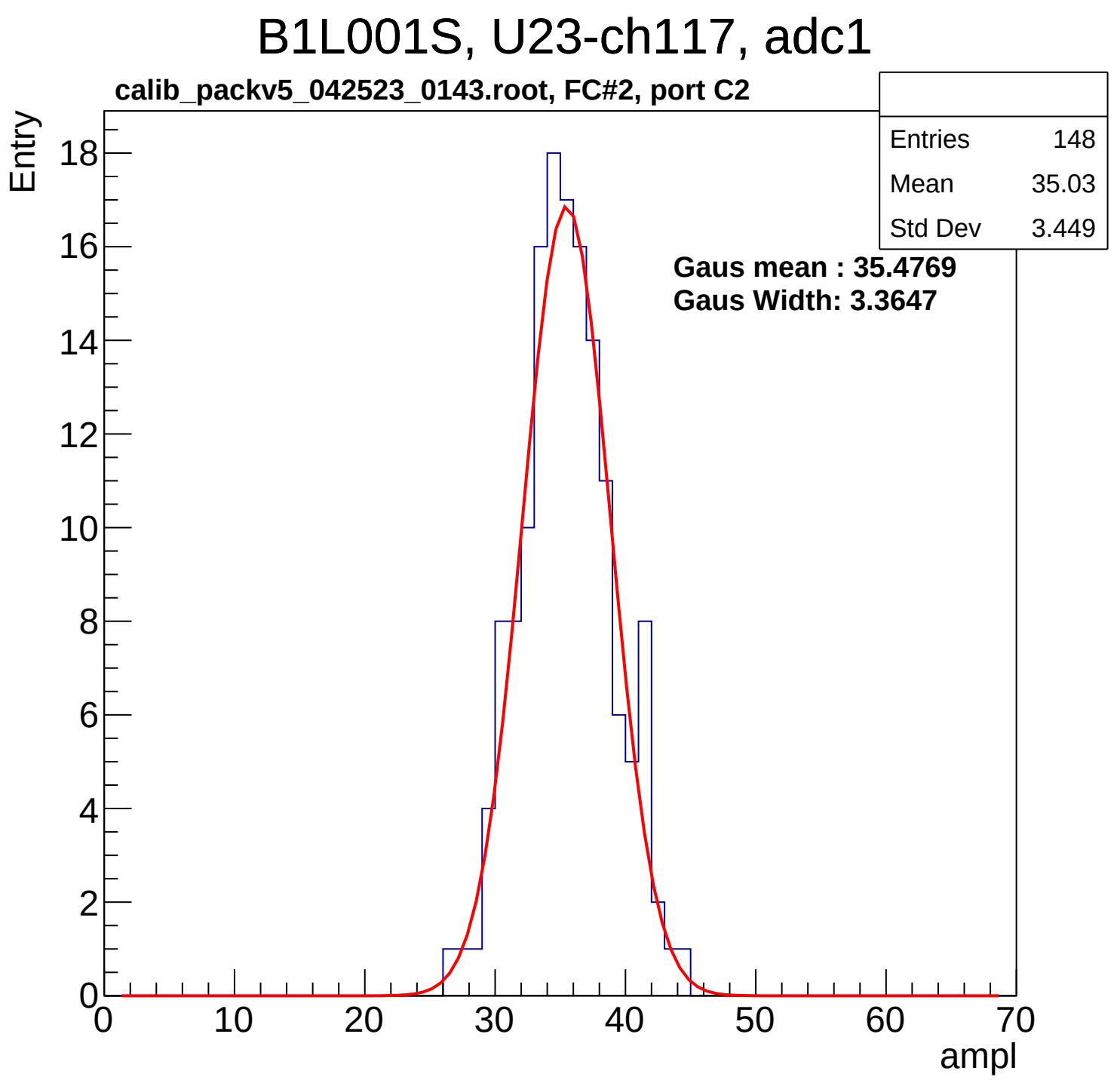
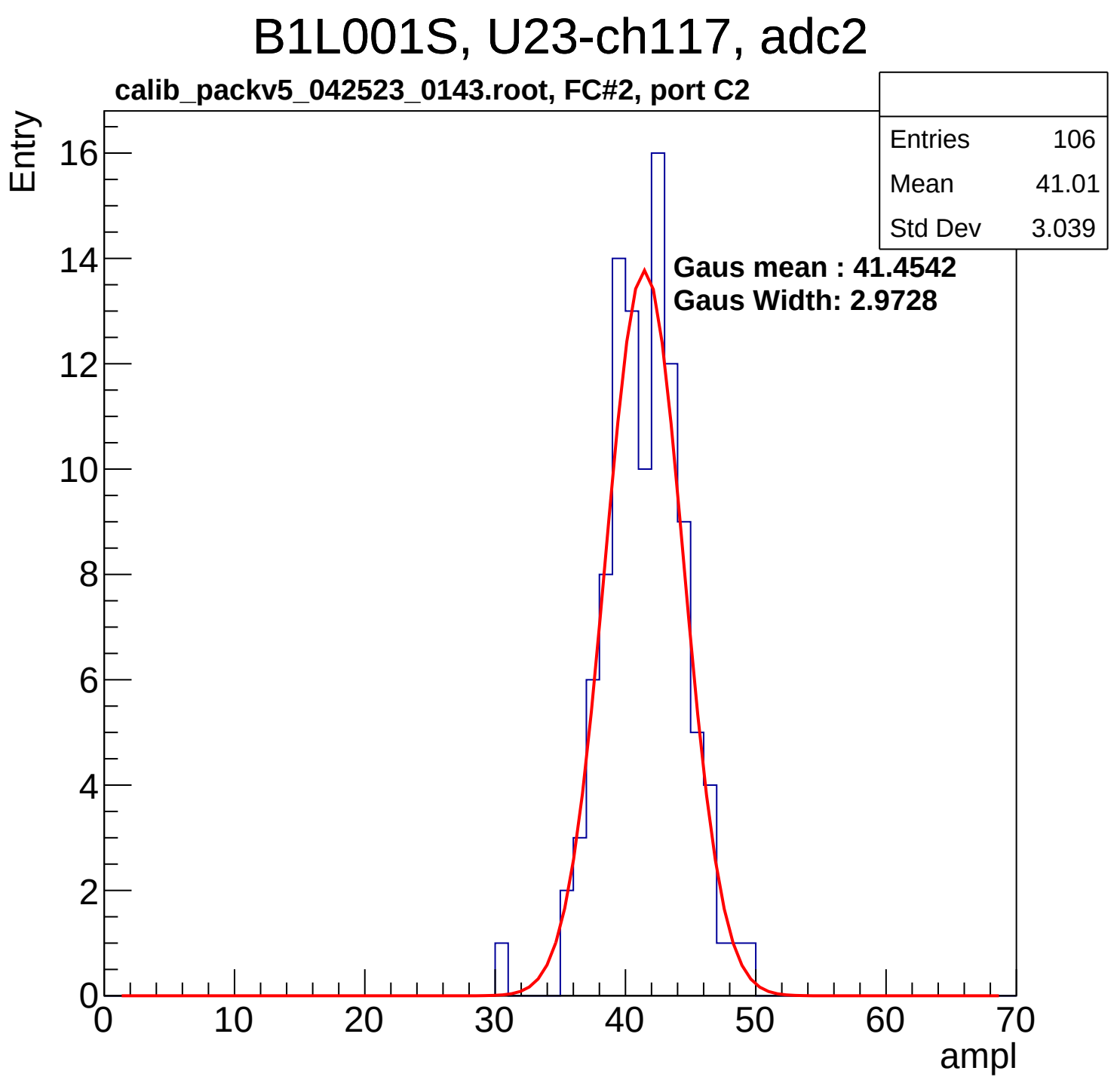

B1L001S, U23-ch117, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 35.03
Std Dev 3.449
Gaus mean : 35.4769
Gaus Width: 3.3647
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch117, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 41.01
Std Dev 3.039
Gaus mean : 41.4542
Gaus Width: 2.9728
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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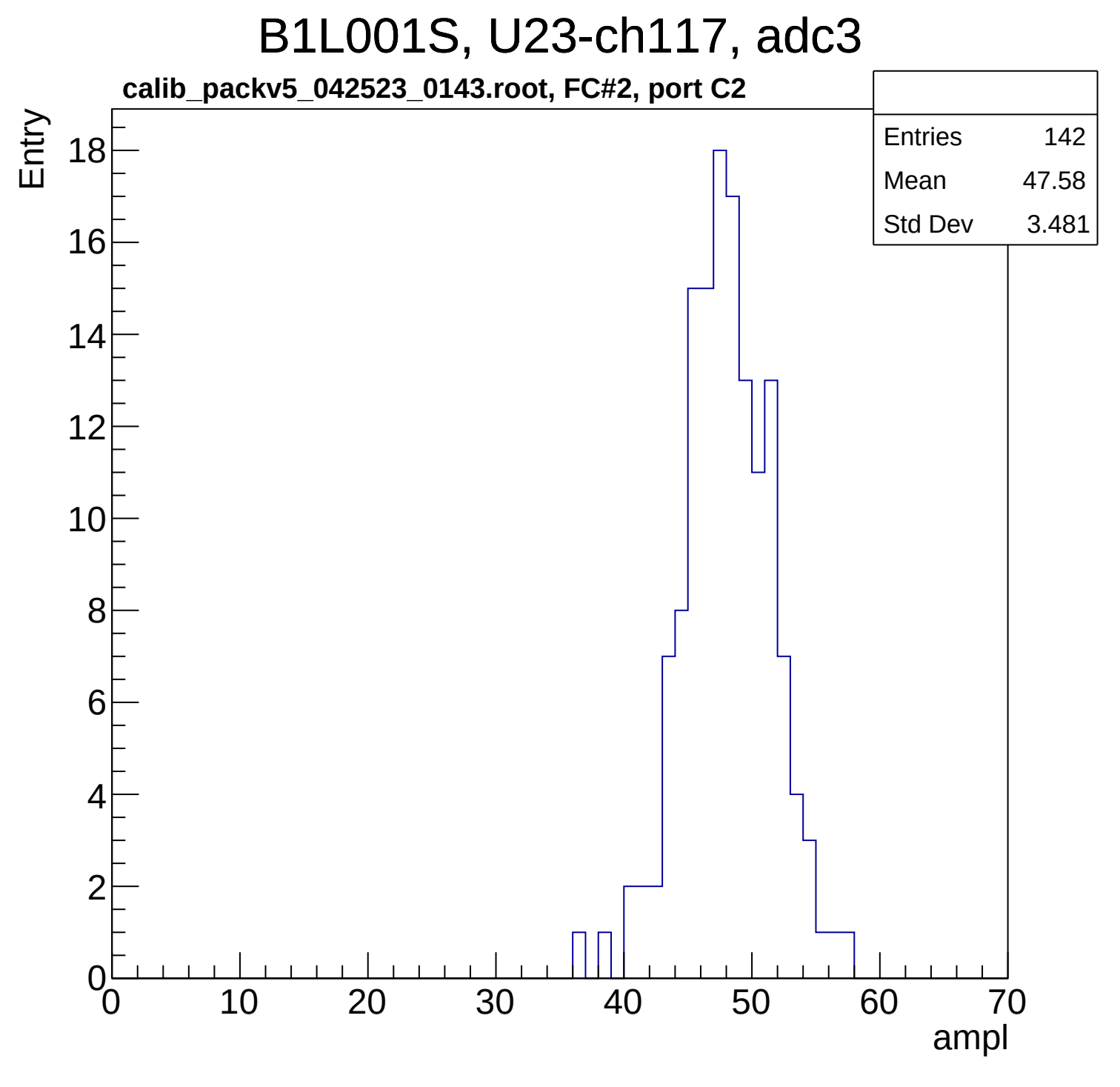
B1L001S, U23-ch117, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 47.58
Std Dev 3.481
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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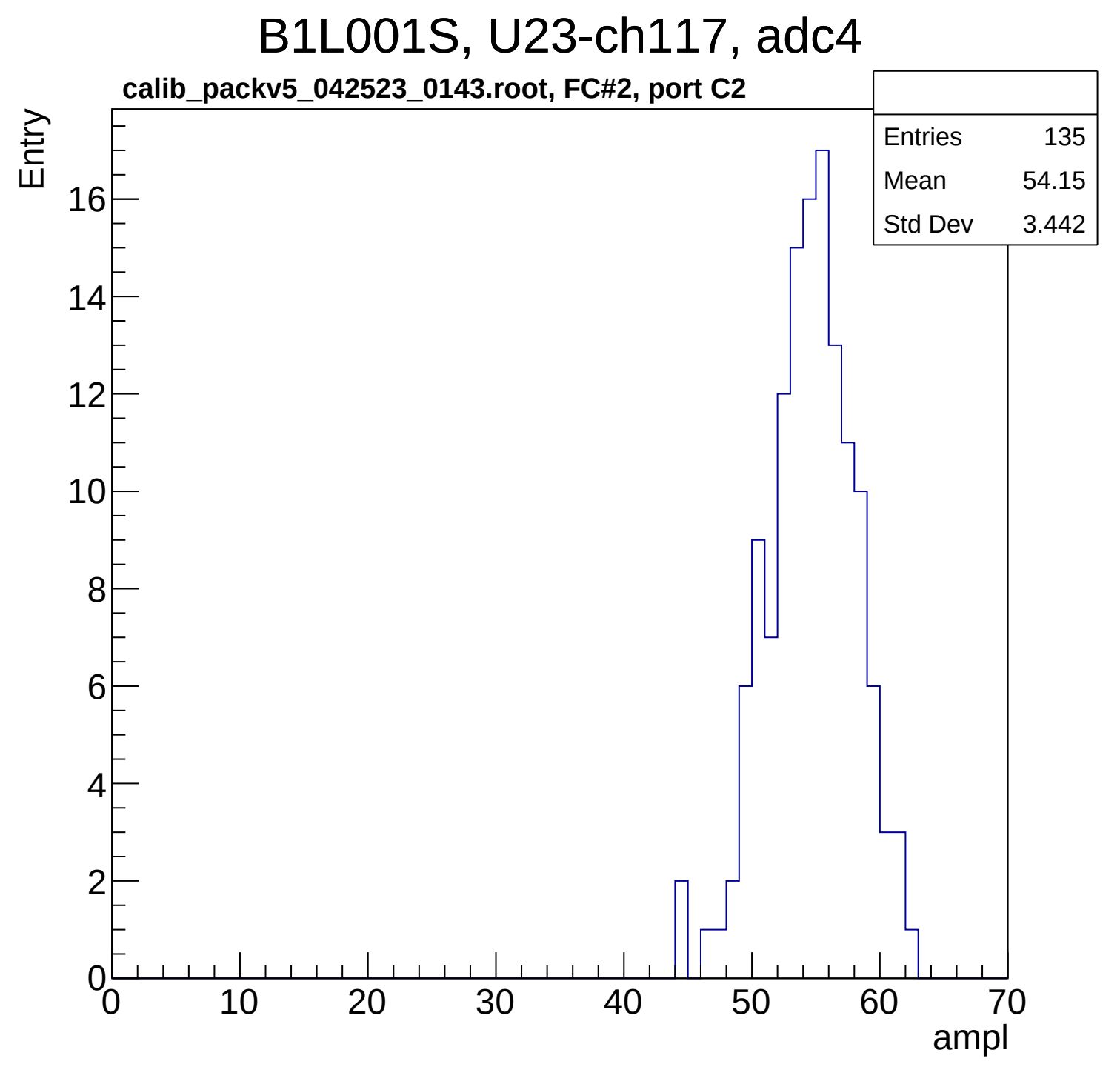
B1L001S, U23-ch117, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 135
Mean 54.15
Std Dev 3.442
Entry
ampl
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2
4
6
8
10
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14
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0
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40
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60
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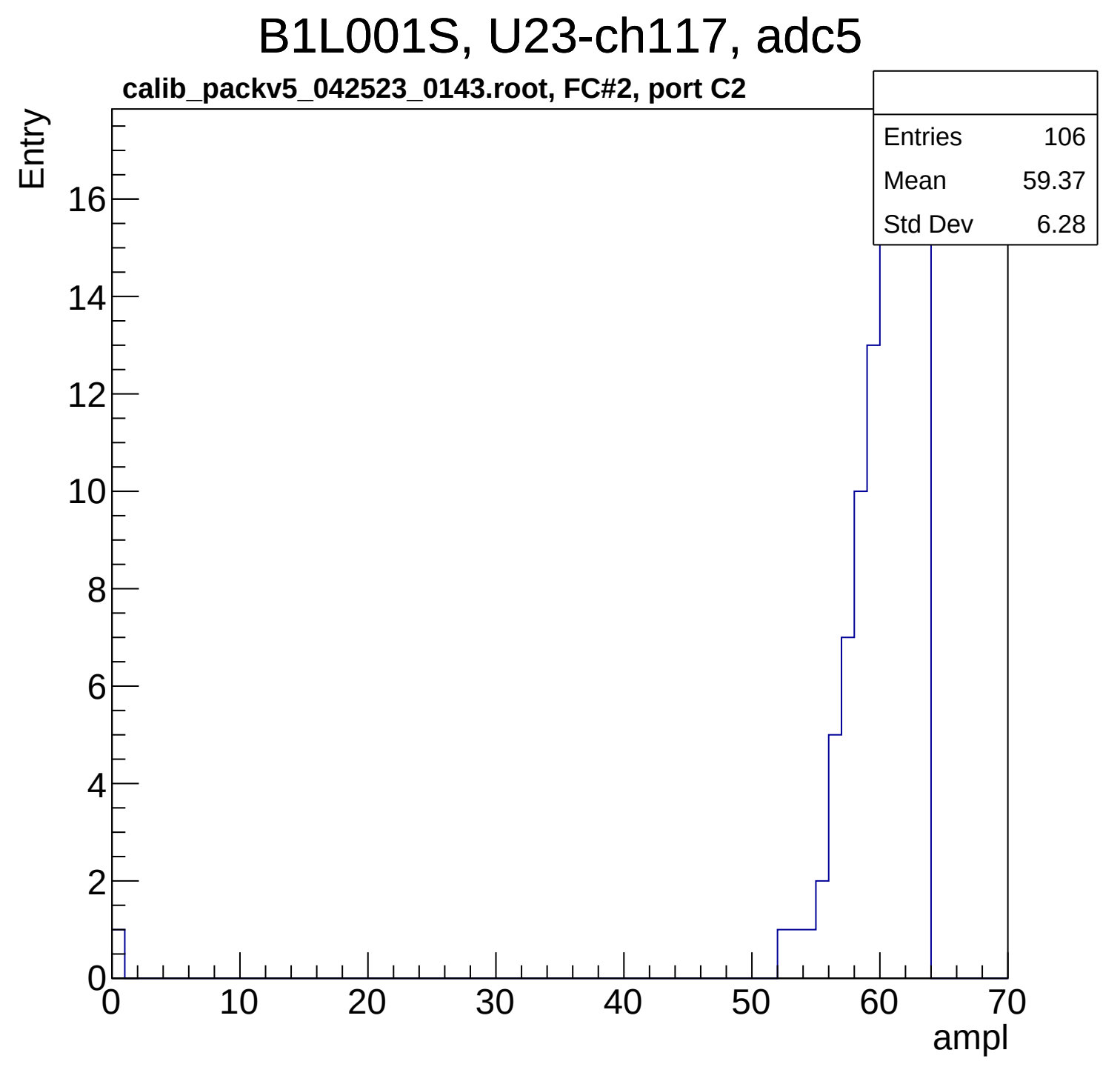
B1L001S, U23-ch117, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 106
Mean 59.37
Std Dev 6.28
Entry
ampl
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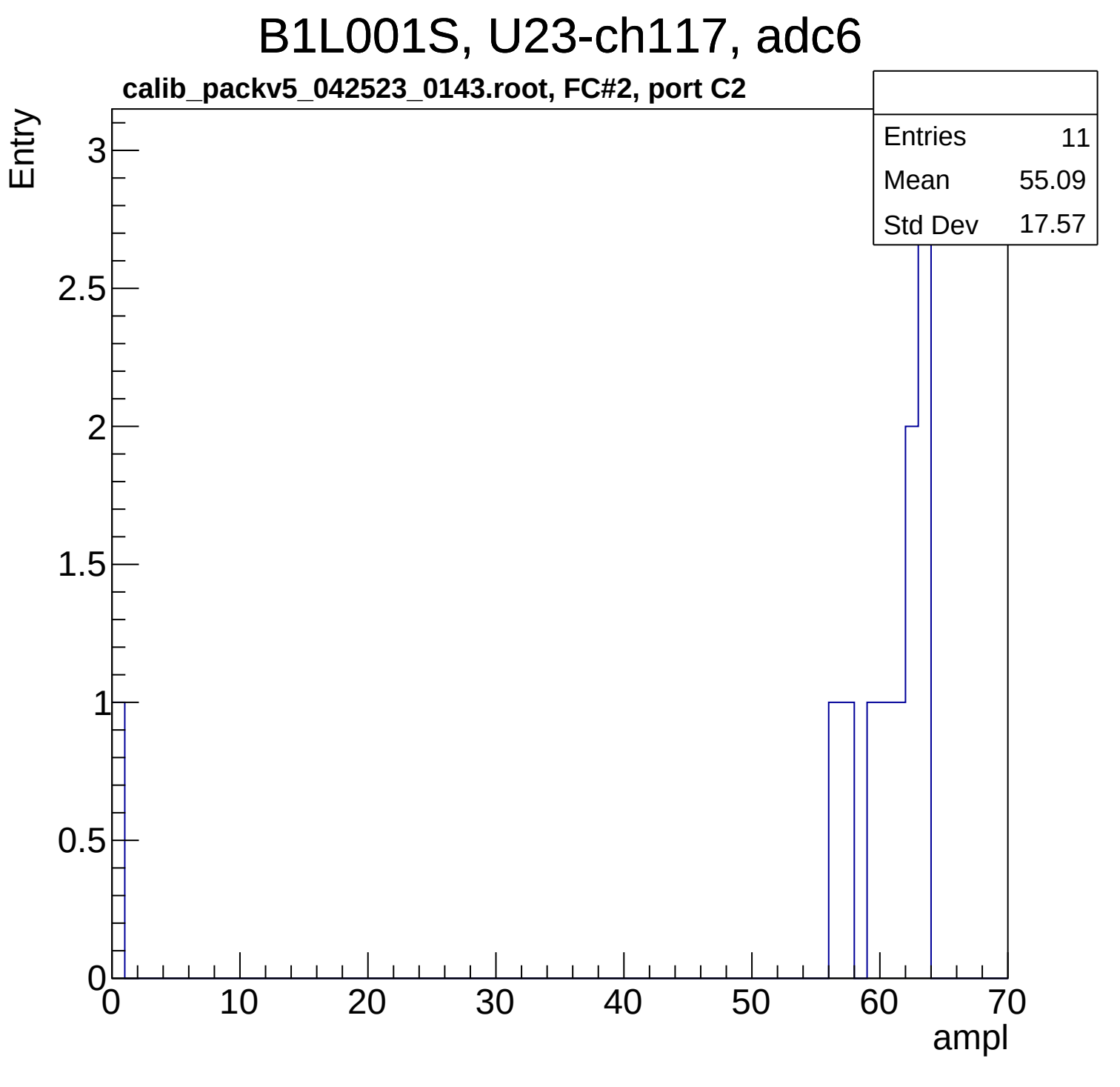
B1L001S, U23-ch117, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 11
Mean 55.09
Std Dev 17.57
Entry
ampl
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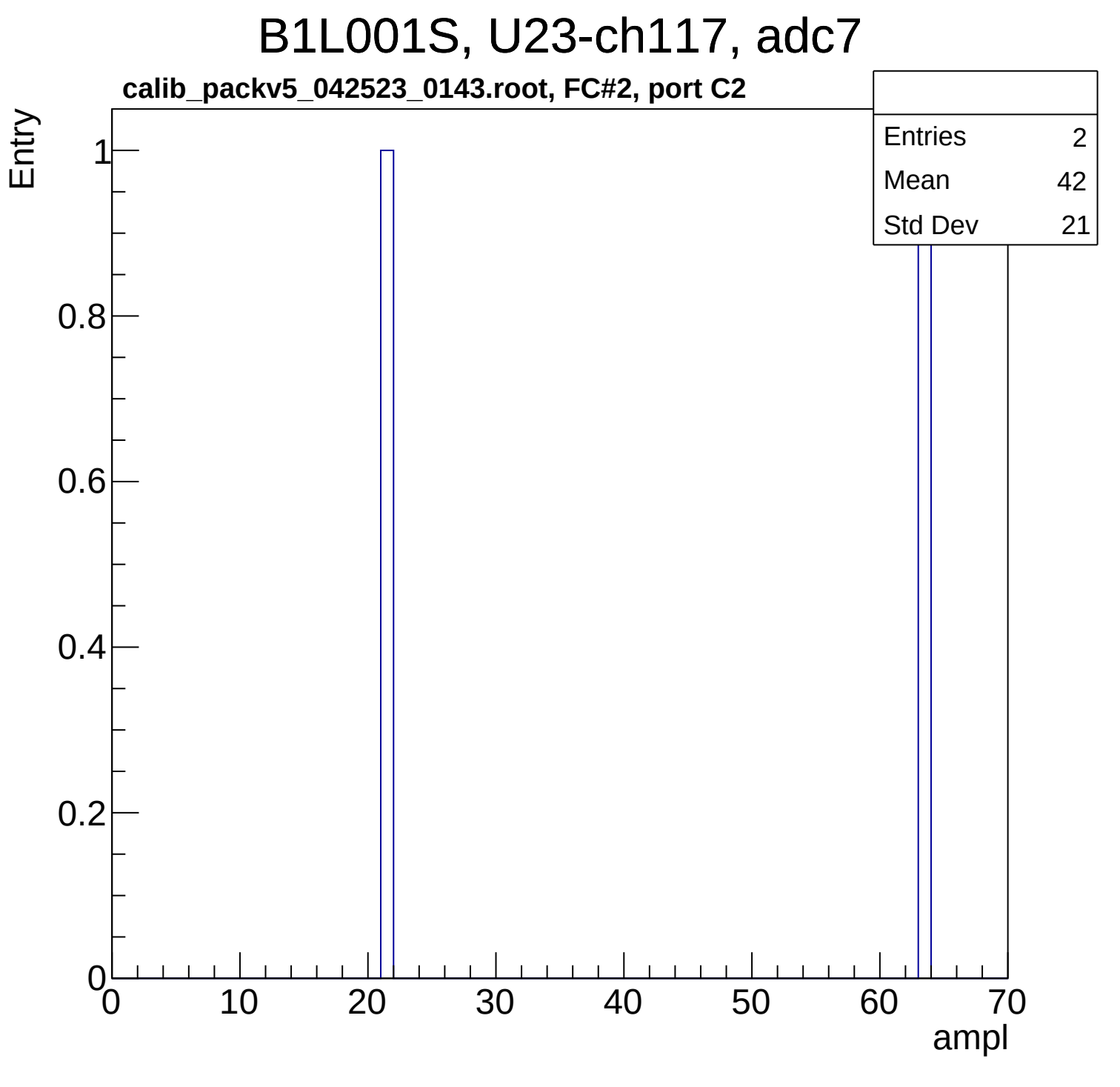
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L001S, U23-ch117, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 42
Std Dev 21
Entry
ampl
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0.2
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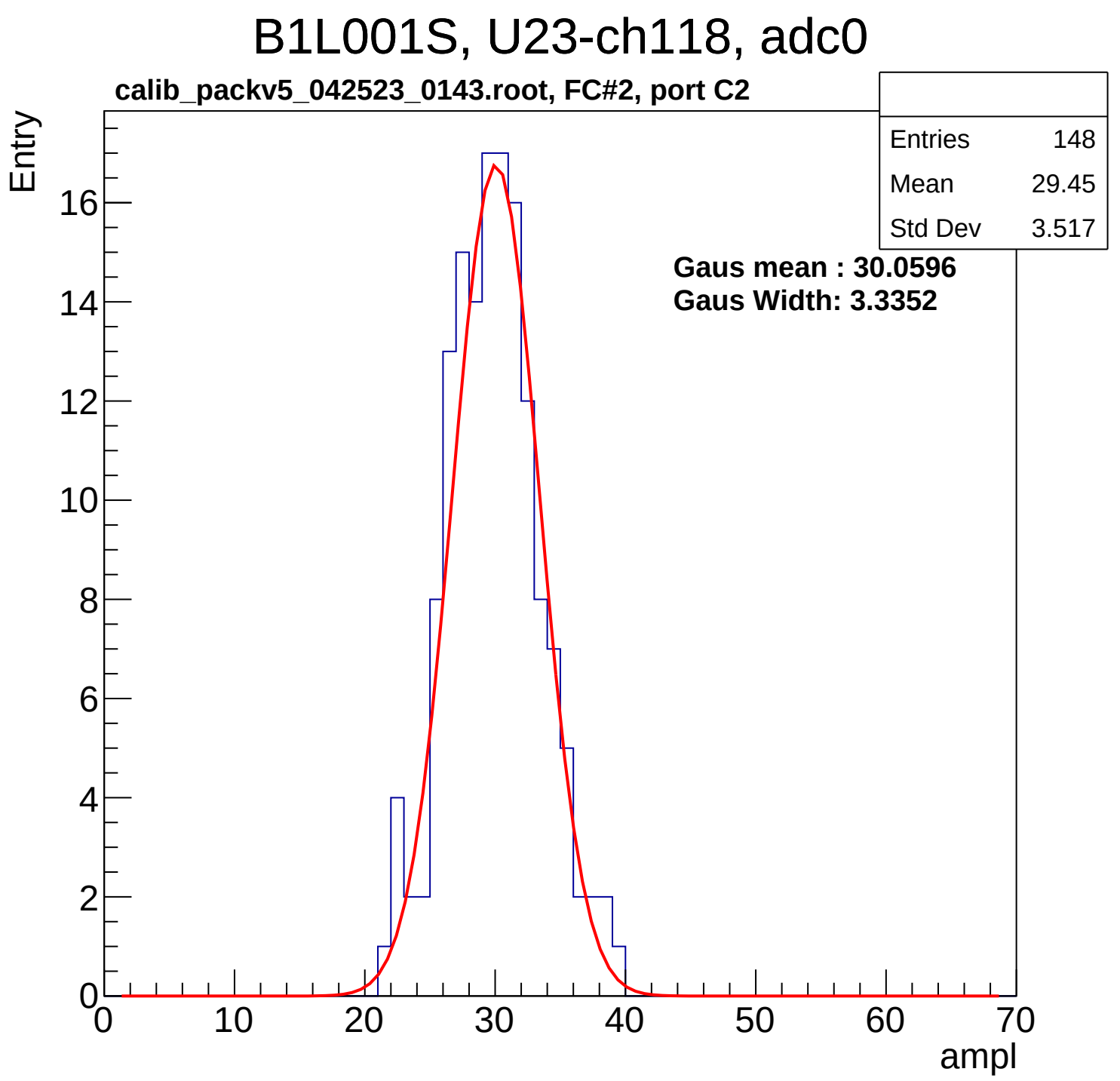
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L001S, U23-ch118, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 29.45
Std Dev 3.517
Gaus mean : 30.0596
Gaus Width: 3.3352
Entry
ampl
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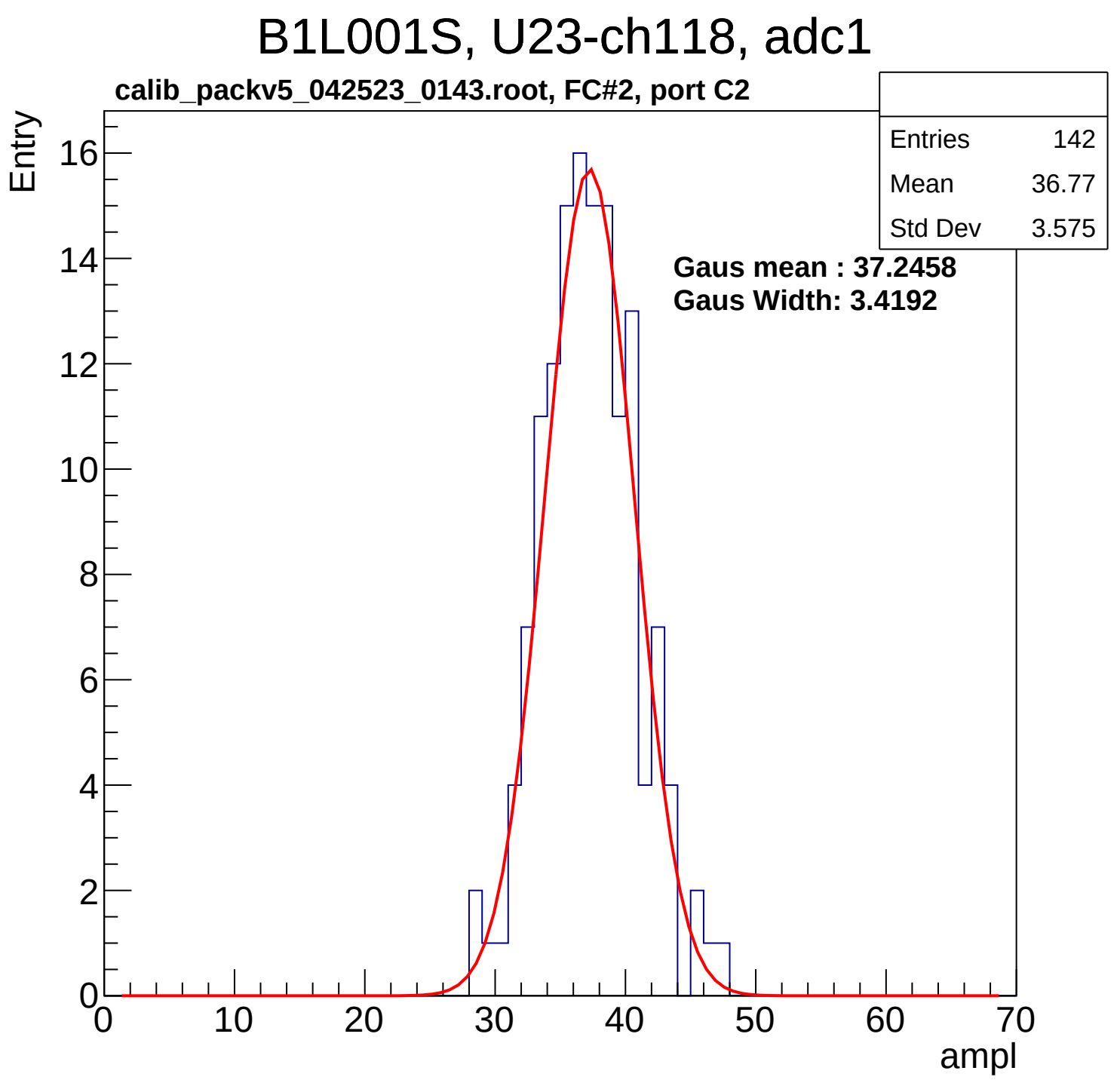
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch118, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 36.77
Std Dev 3.575
Gaus mean : 37.2458
Gaus Width: 3.4192
Entry
ampl
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2
4
6
8
10
12
14
16
0
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20
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60
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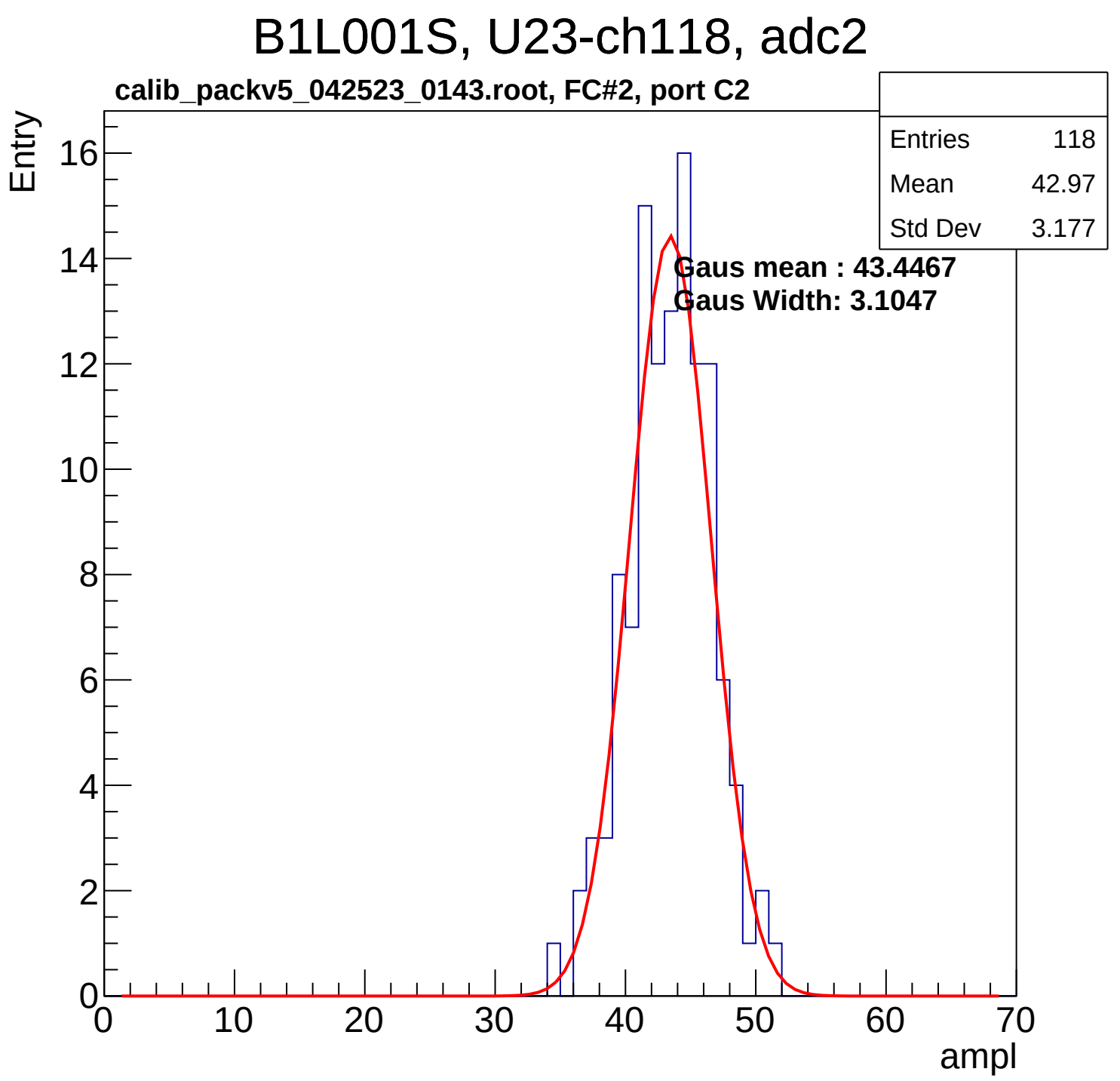
B1L001S, U23-ch118, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 118
Mean 42.97
Std Dev 3.177
Gaus mean : 43.4467
Gaus Width: 3.1047
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
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60
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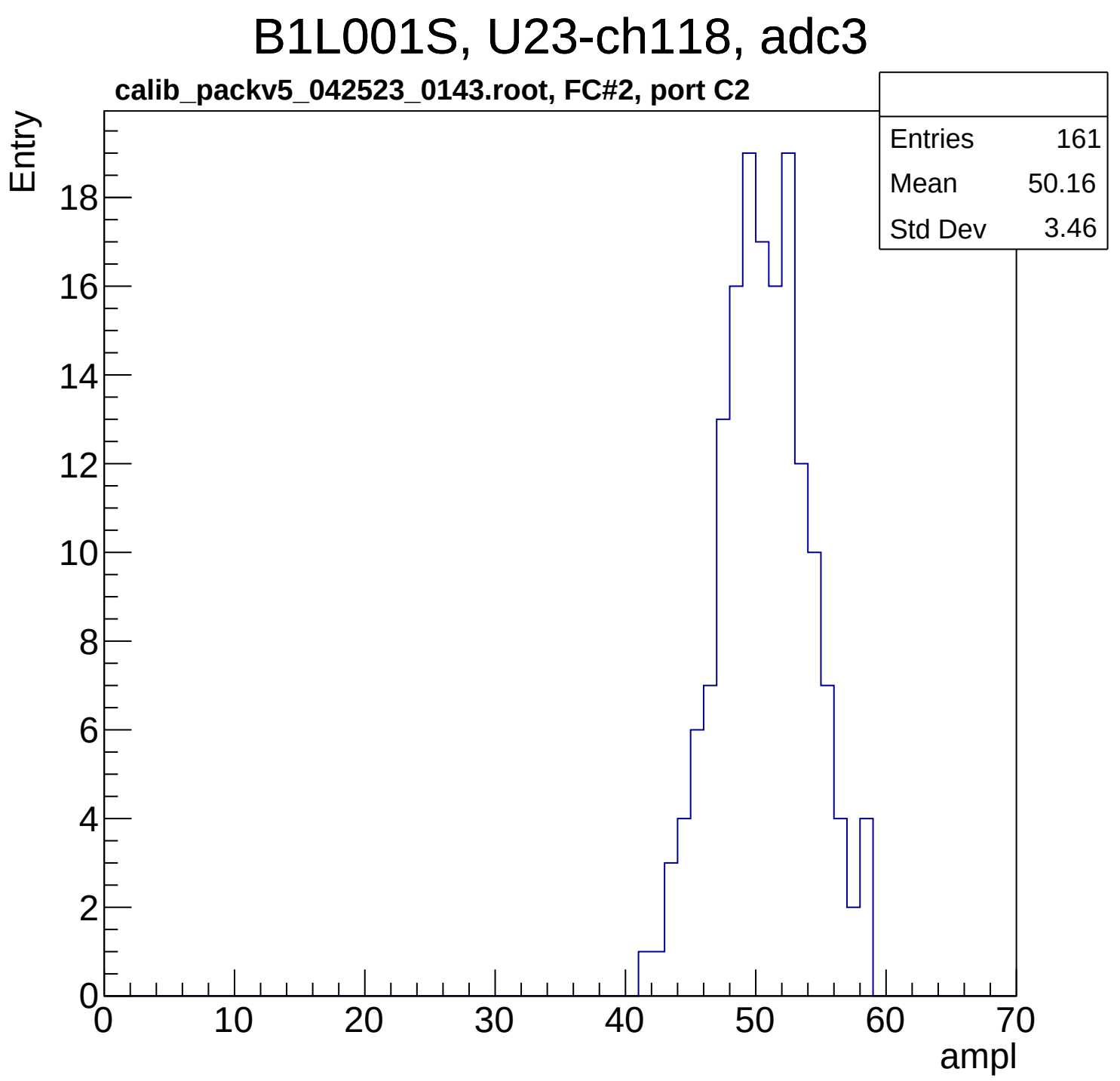
B1L001S, U23-ch118, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 161
Mean 50.16
Std Dev 3.46
Entry
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6
8
10
12
14
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10
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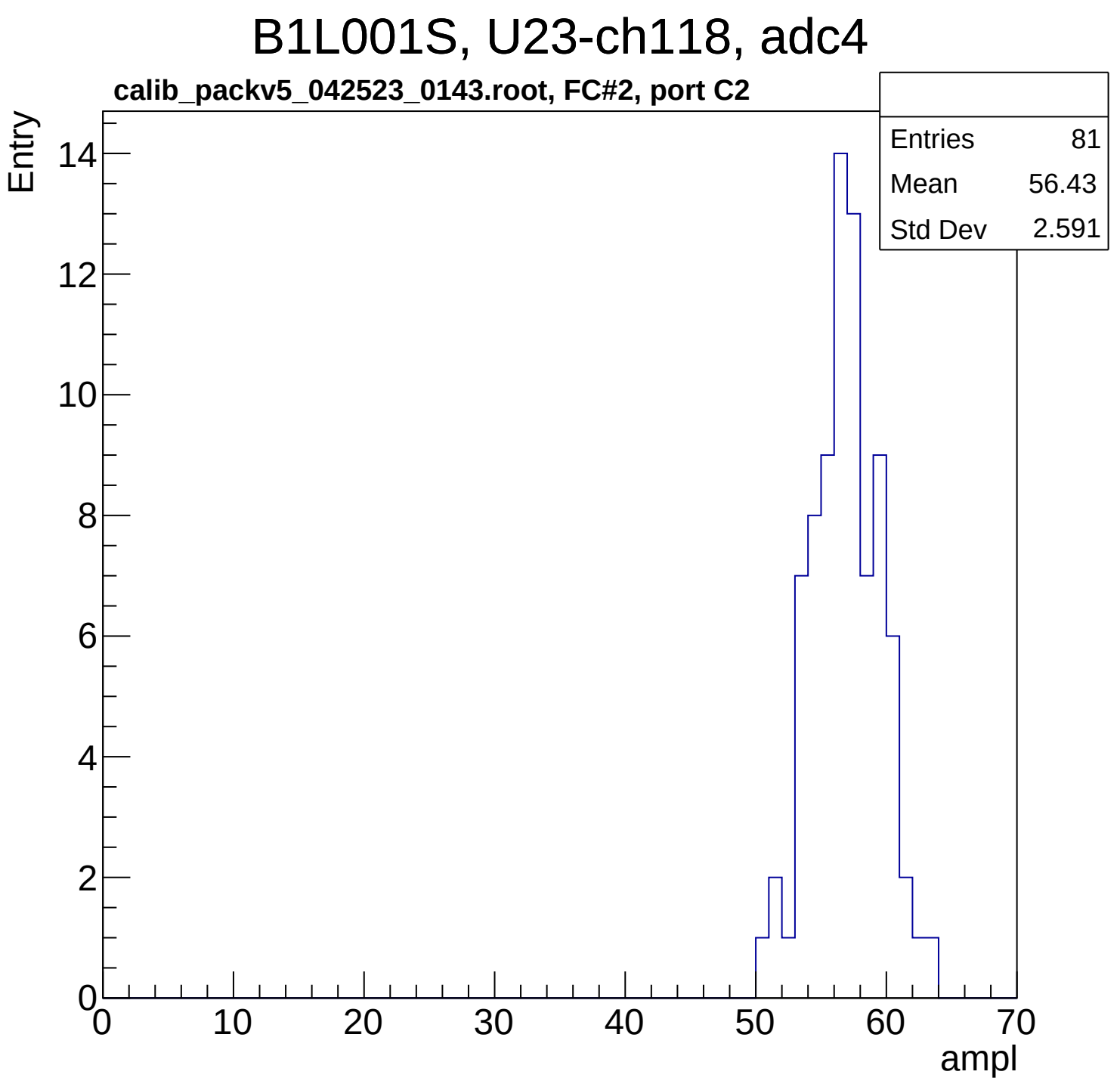
70
ampl


B1L001S, U23-ch118, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 56.43
Std Dev 2.591
Entry
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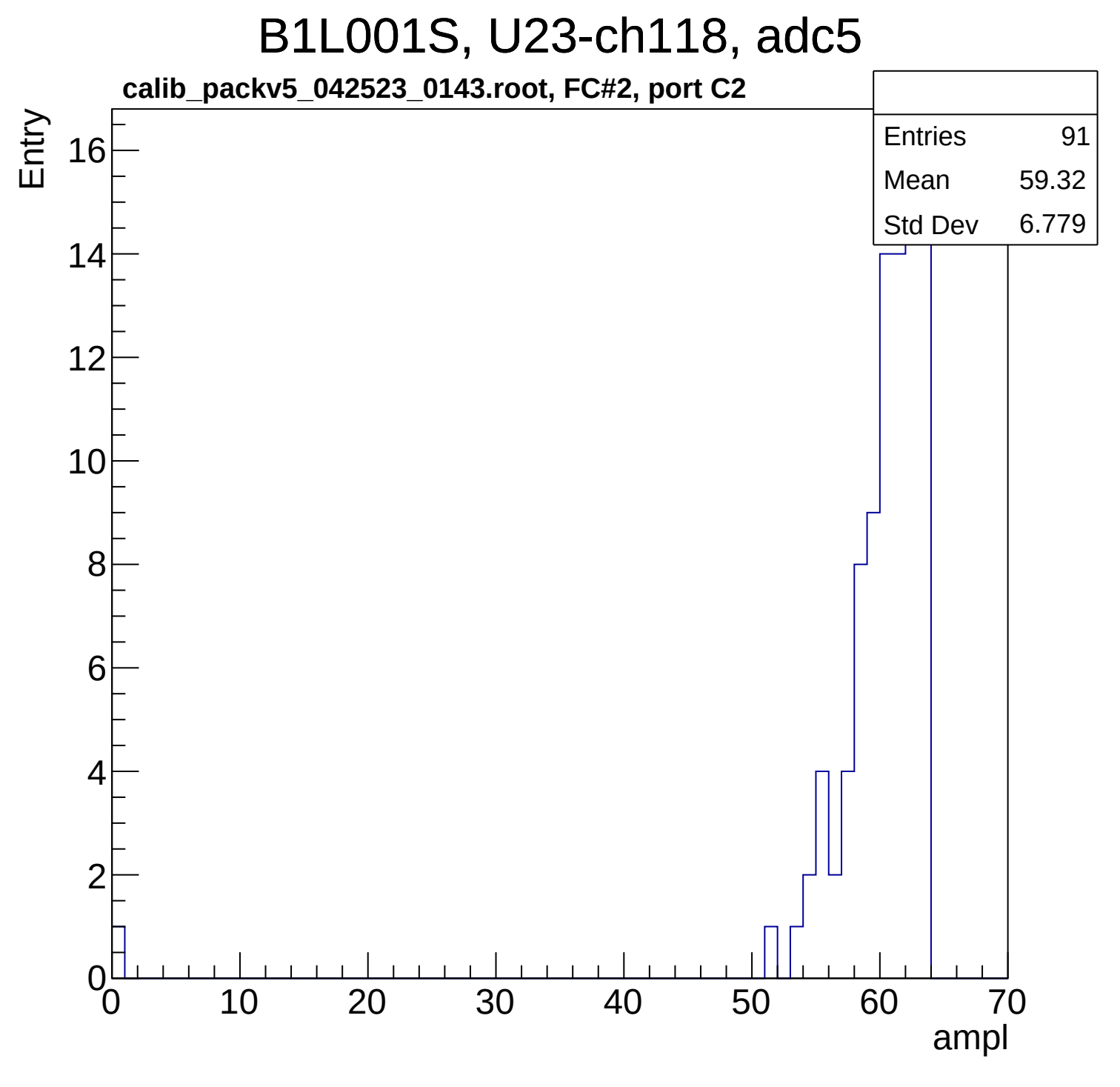
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch118, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 59.32
Std Dev 6.779
Entry
ampl
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2
4
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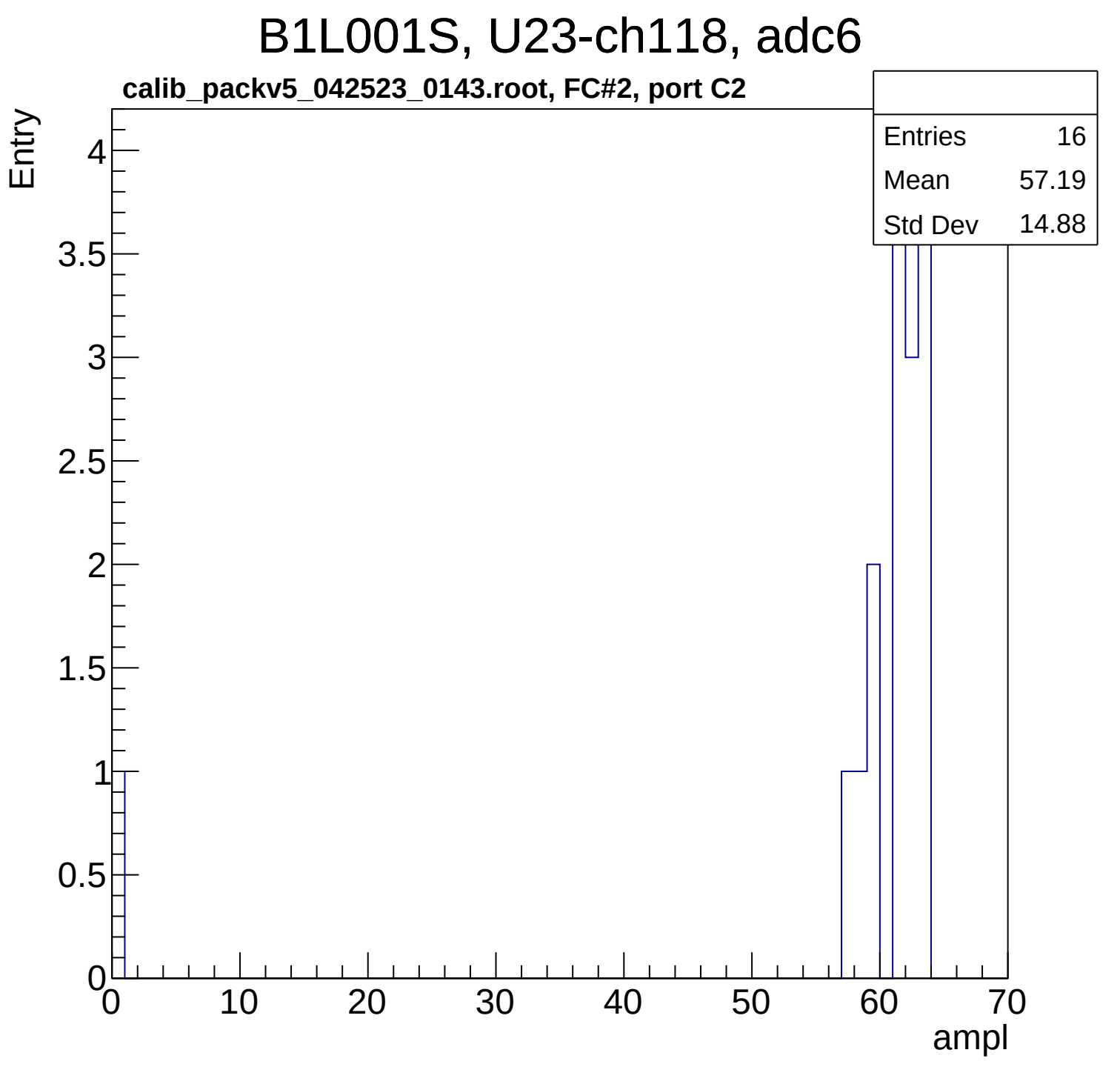
B1L001S, U23-ch118, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 16
Mean 57.19
Std Dev 14.88
Entry
ampl
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1.5
2
2.5
3
3.5
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0
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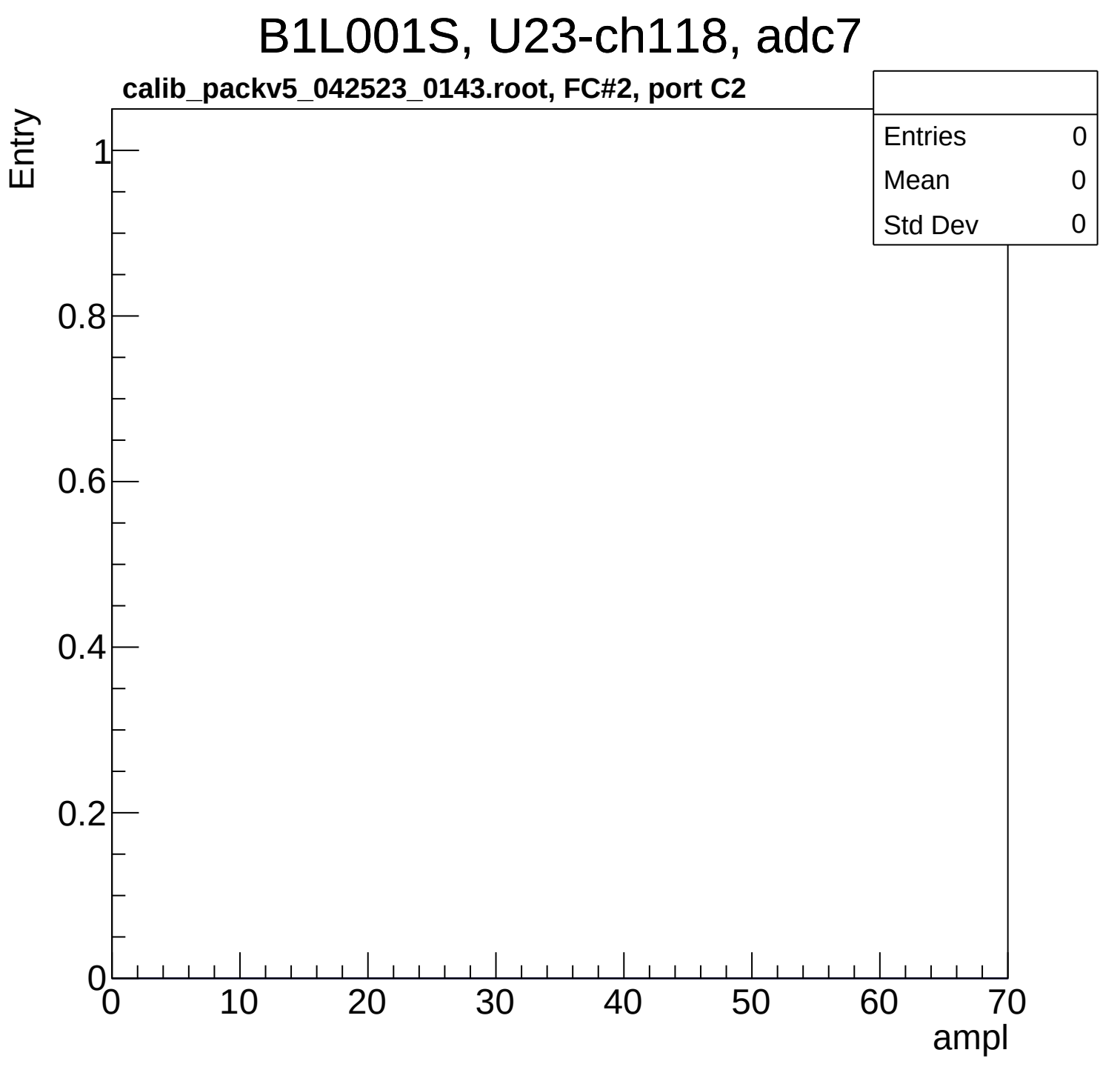
60
70


B1L001S, U23-ch118, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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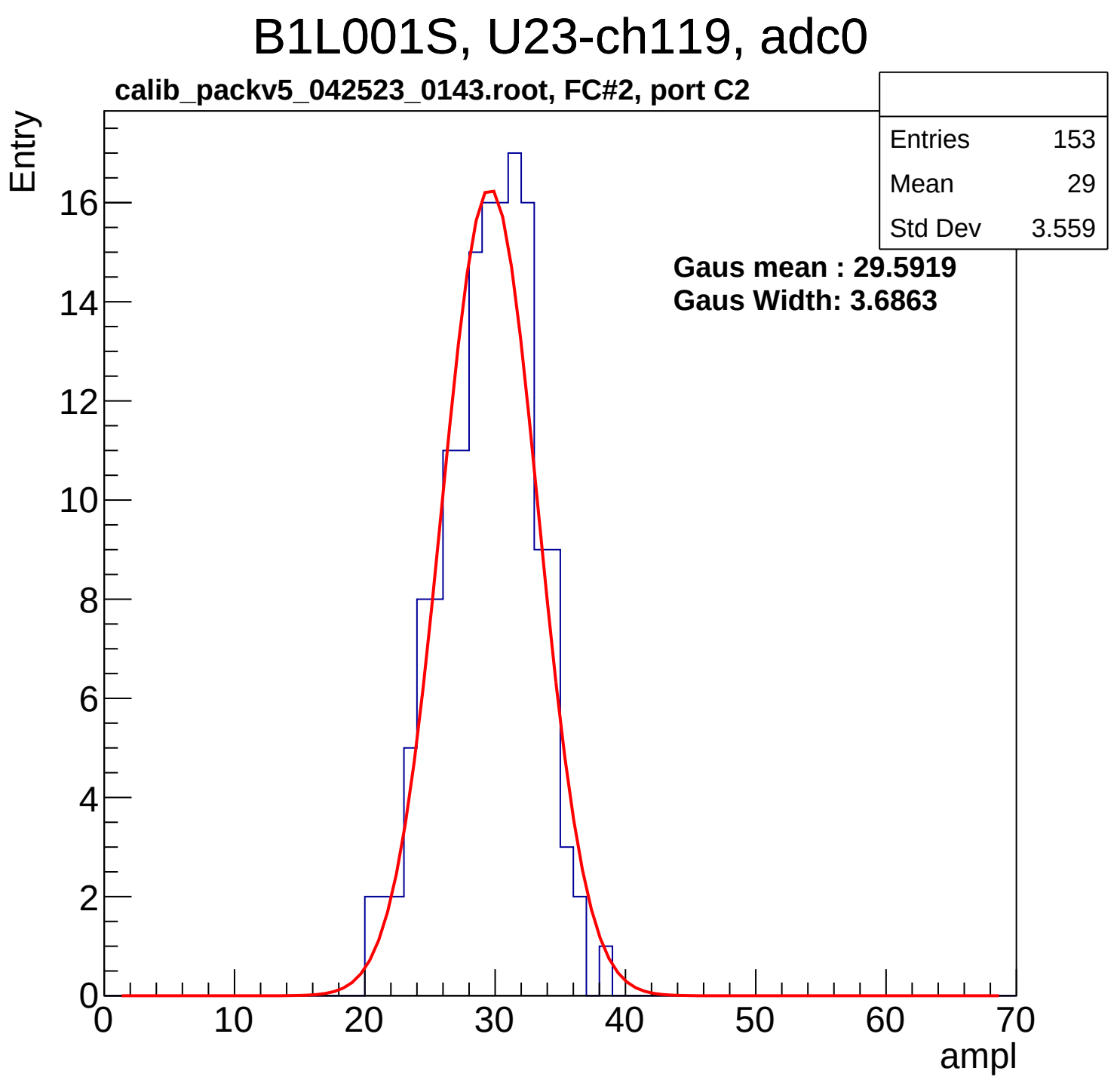
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch119, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 29
Std Dev 3.559
Gaus mean : 29.5919
Gaus Width: 3.6863
Entry
ampl
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2
4
6
8
10
12
14
16
0
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60
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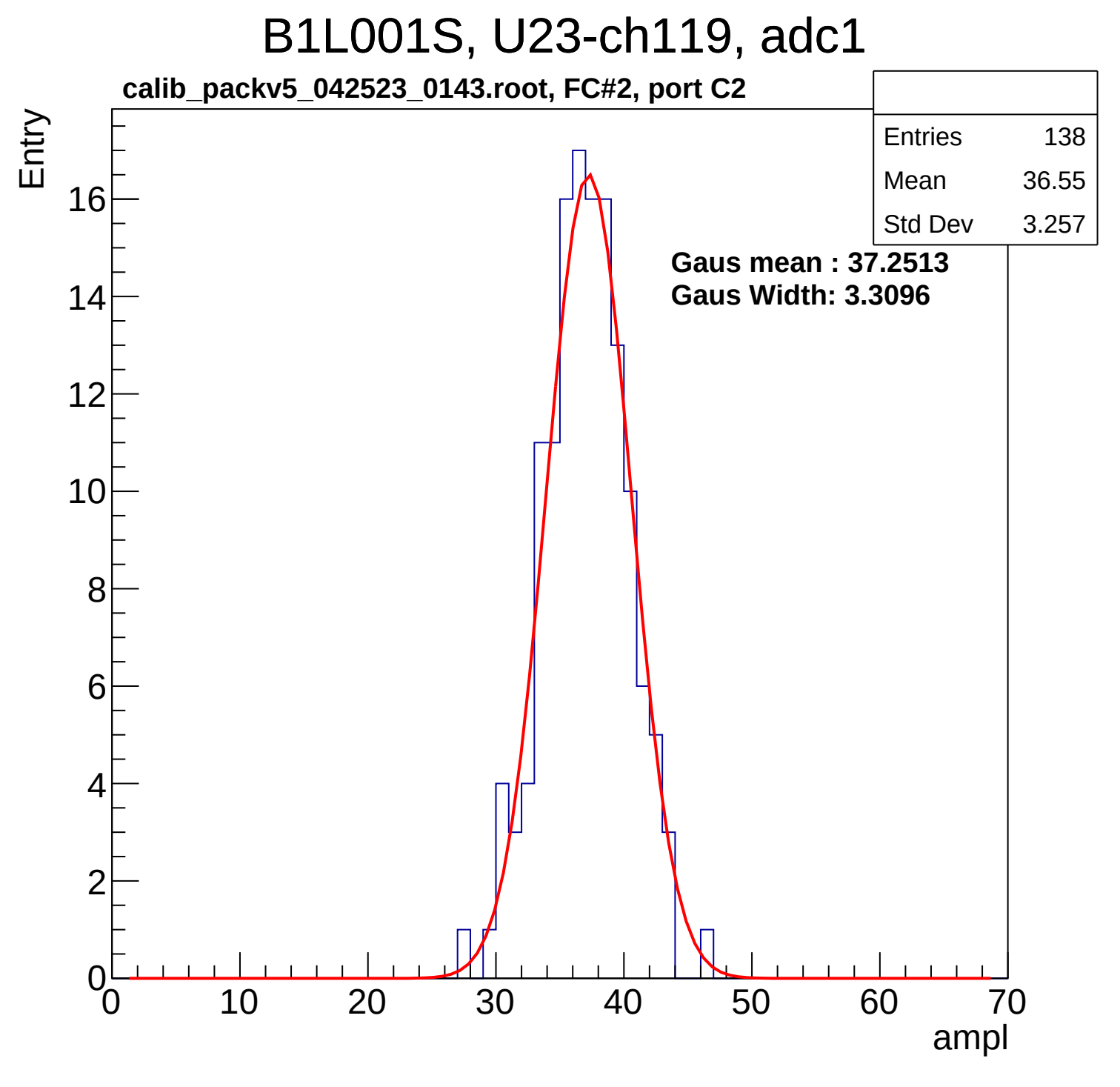
B1L001S, U23-ch119, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 138
Mean 36.55
Std Dev 3.257
Gaus mean : 37.2513
Gaus Width: 3.3096
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
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40
50
60
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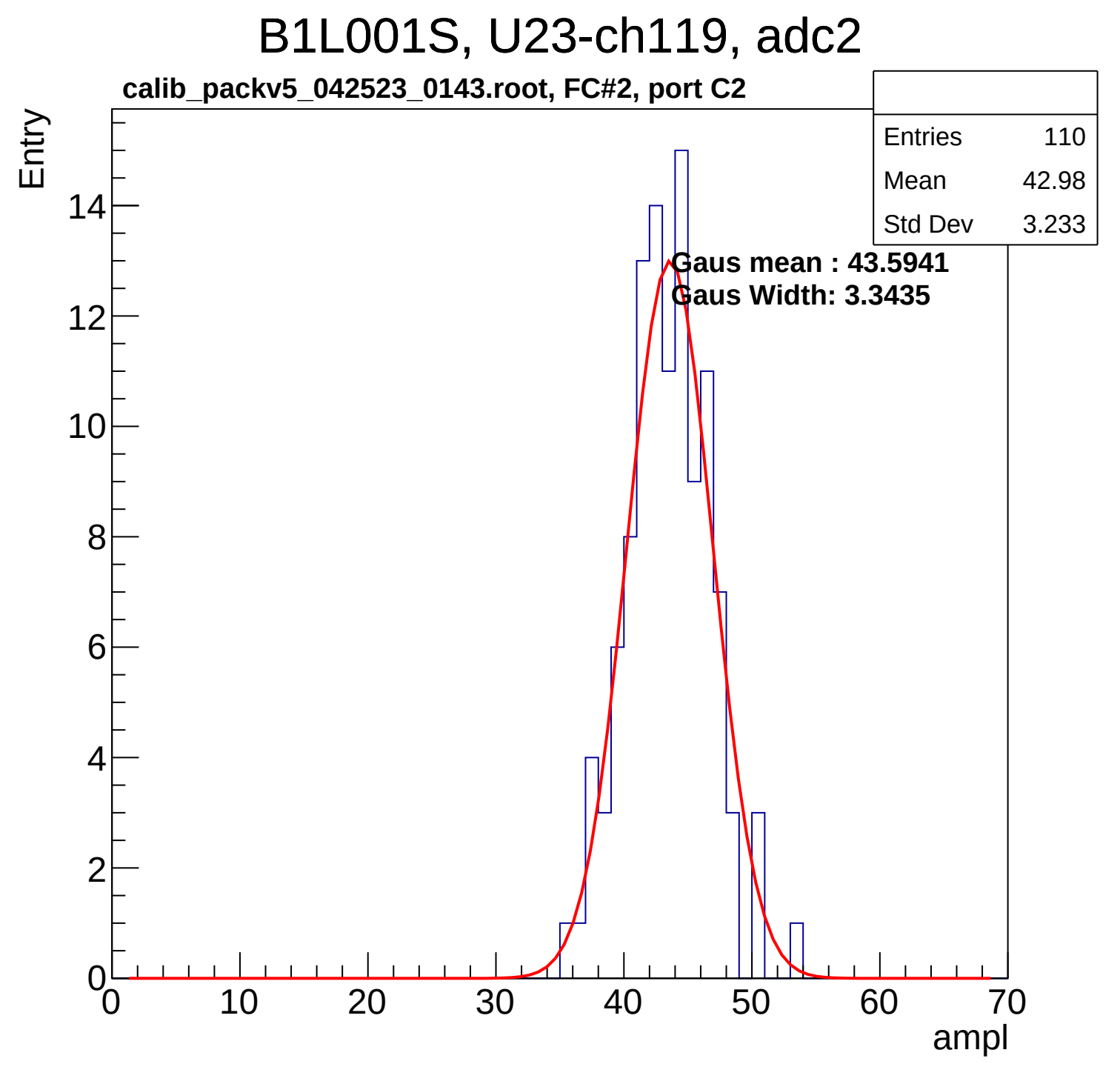
B1L001S, U23-ch119, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 110
Mean 42.98
Std Dev 3.233
Gaus mean : 43.5941
Gaus Width: 3.3435
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
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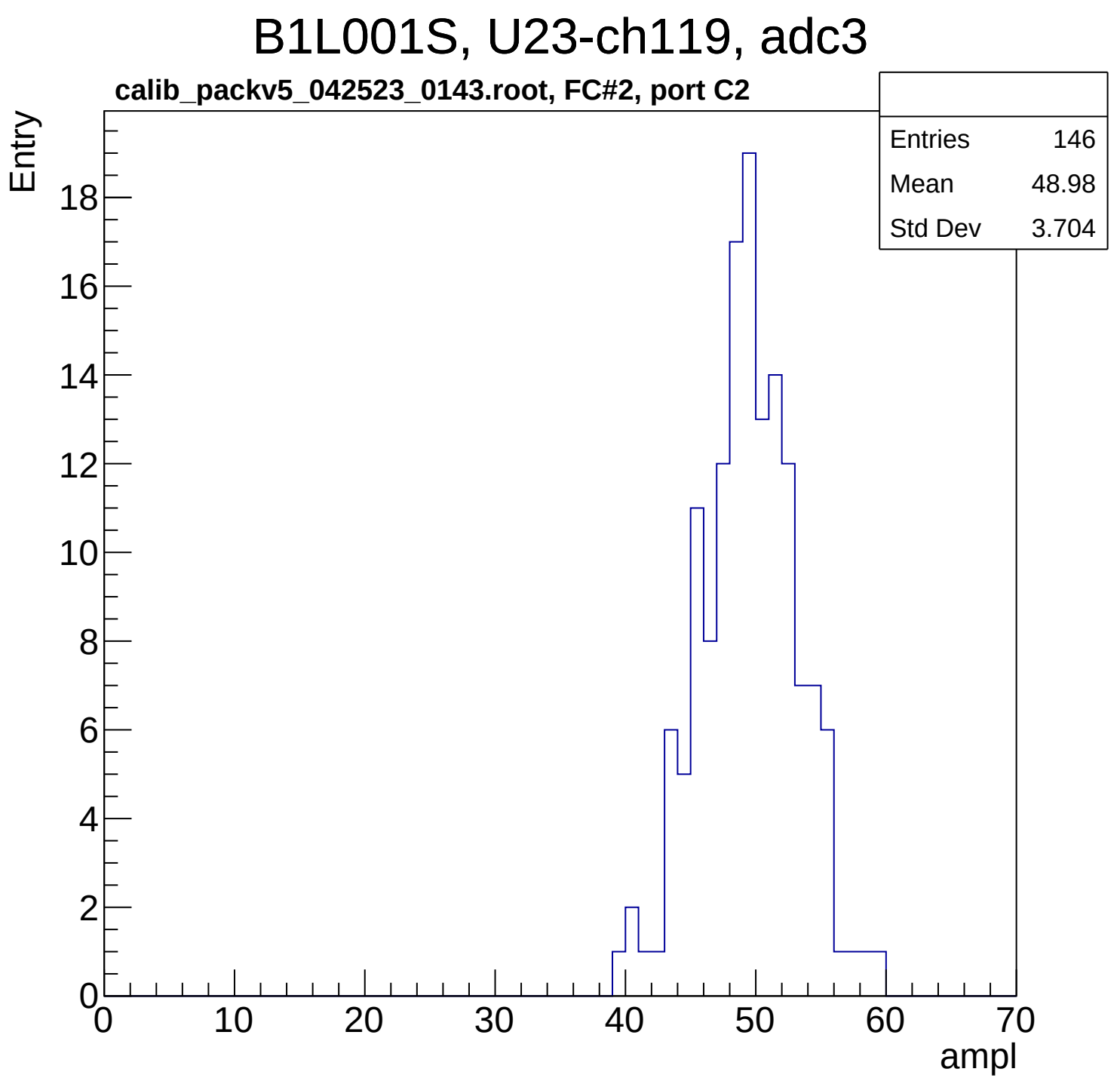
B1L001S, U23-ch119, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 48.98
Std Dev 3.704
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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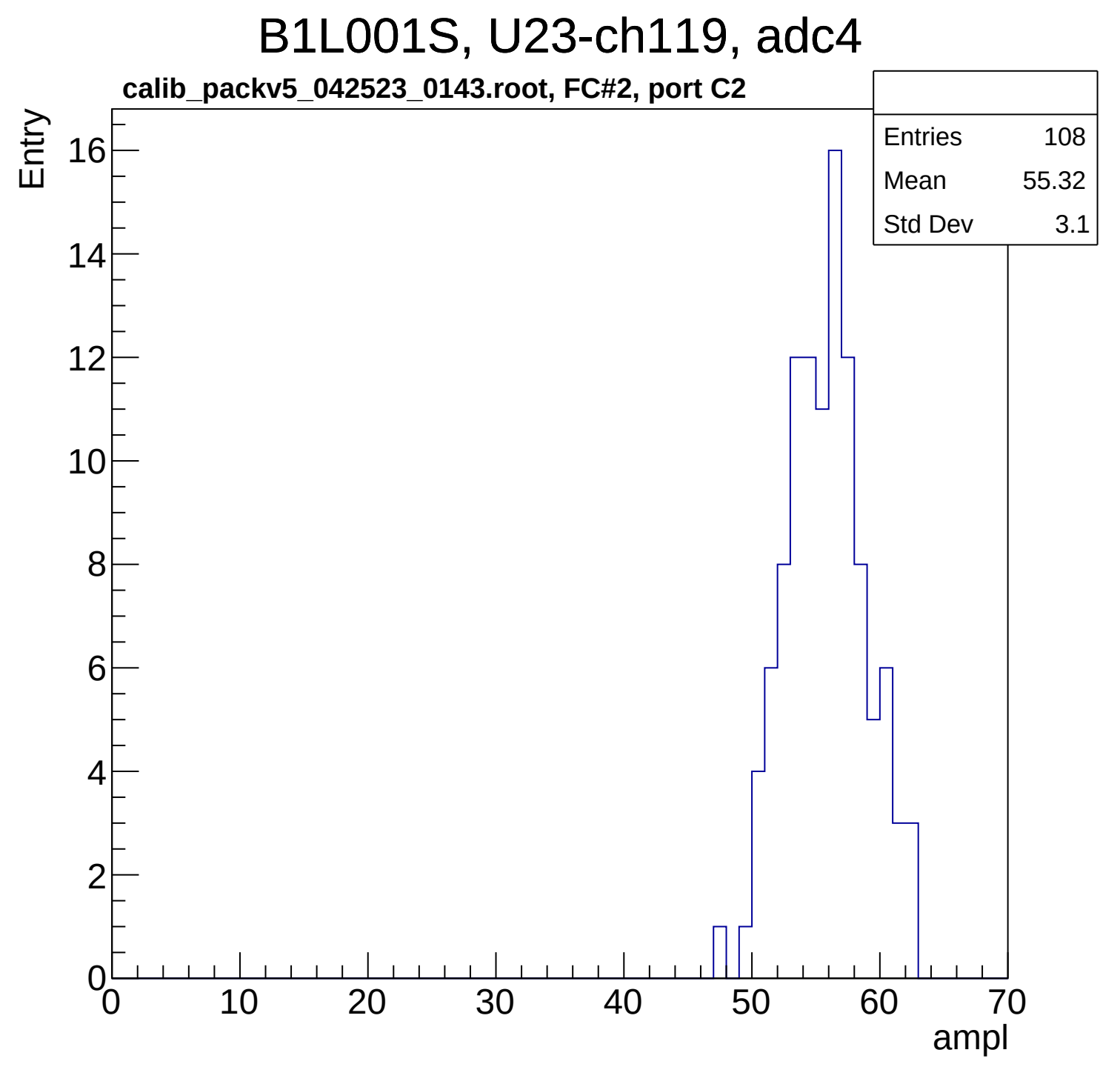
B1L001S, U23-ch119, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 55.32
Std Dev 3.1
Entry
ampl
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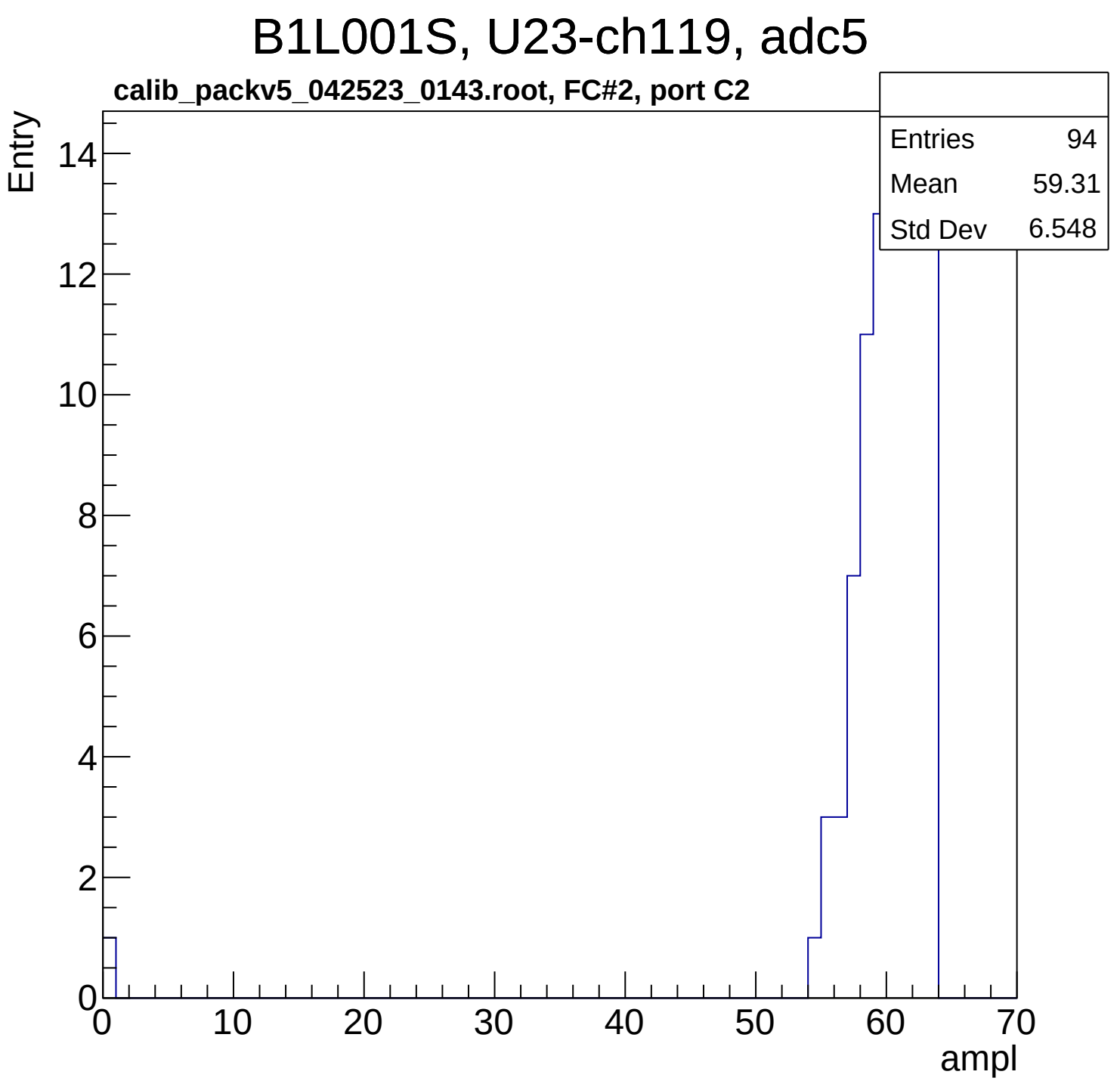
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch119, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 94
Mean 59.31
Std Dev 6.548
Entry
ampl
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2
4
6
8
10
12
14
0
10
20
30
40
50
60
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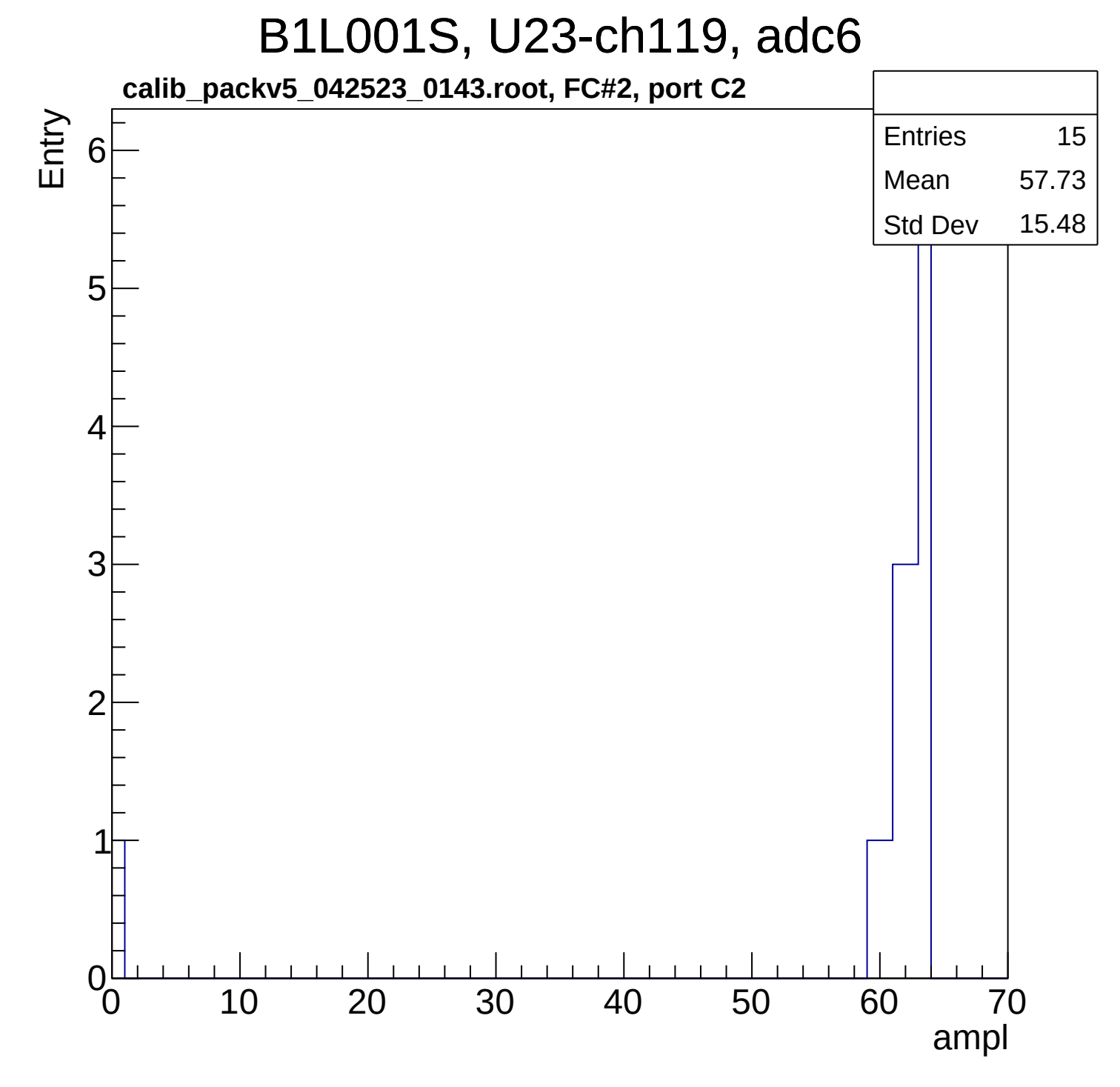
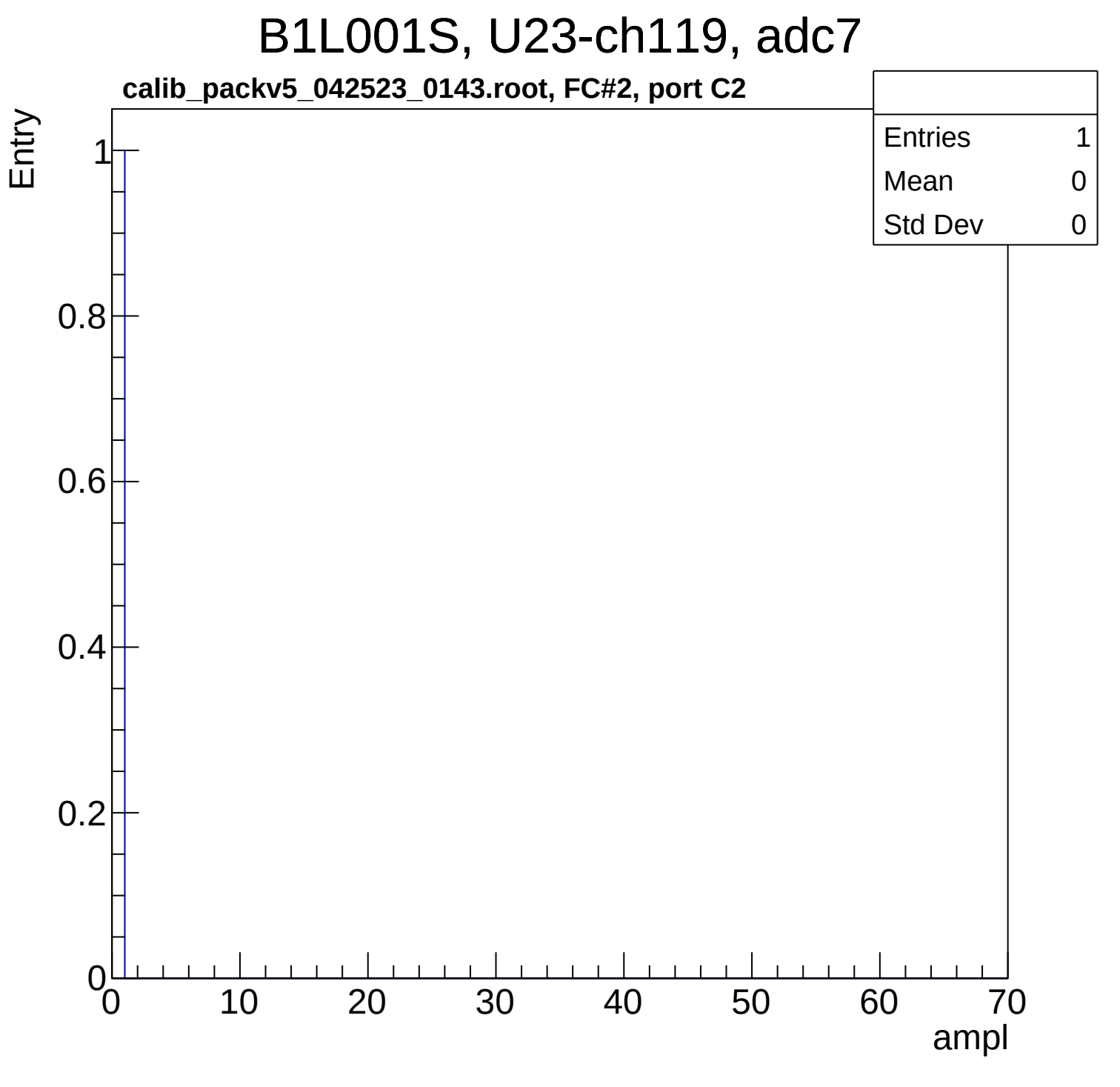

B1L001S, U23-ch119, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 15
Mean 57.73
Std Dev 15.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch119, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 0
Std Dev 0
Entry
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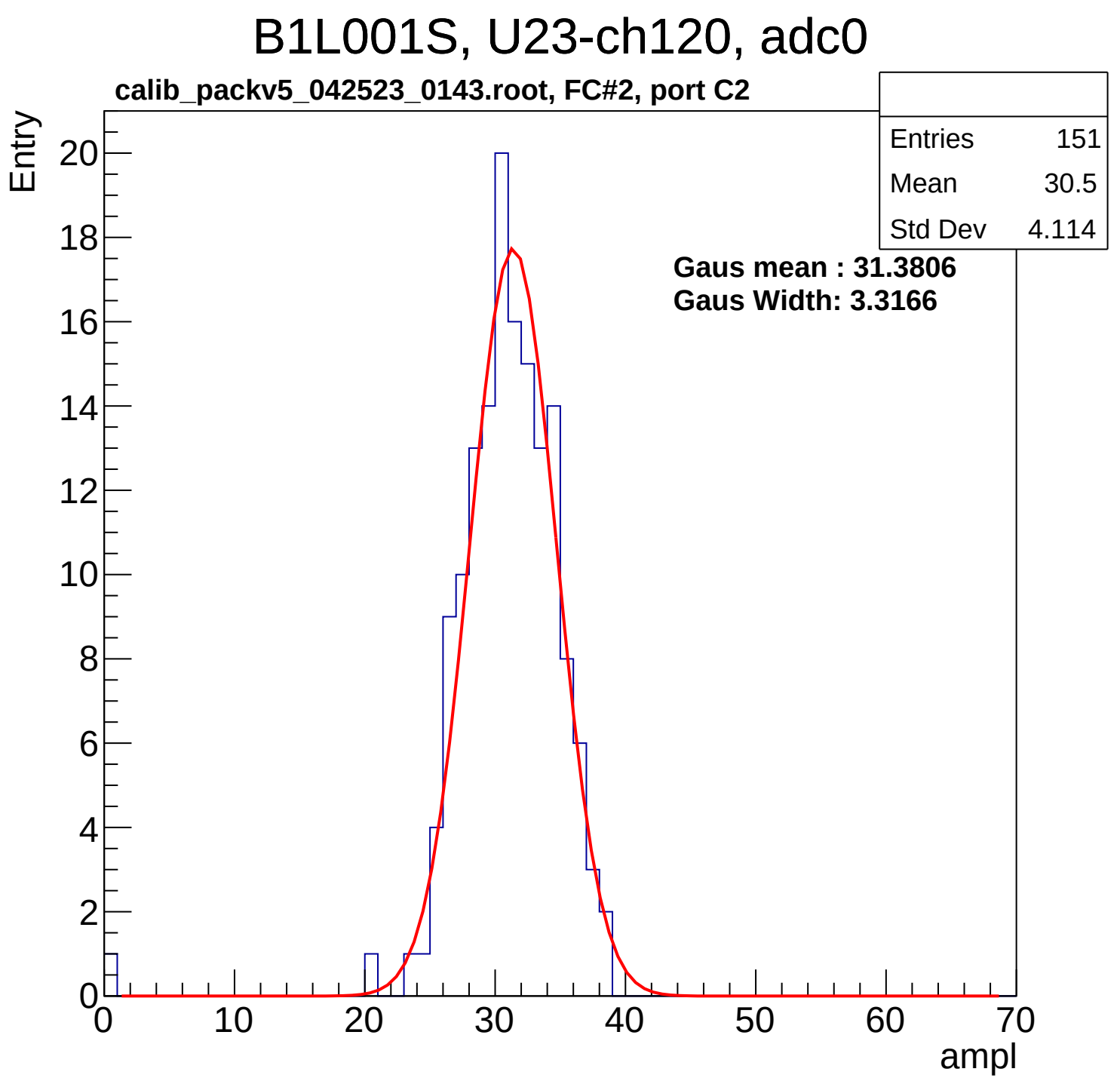
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch120, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 30.5
Std Dev 4.114
Gaus mean : 31.3806
Gaus Width: 3.3166
Entry
ampl
0
2
4
6
8
10
12
14
16
18
20
0
10
20
30
40
50
60
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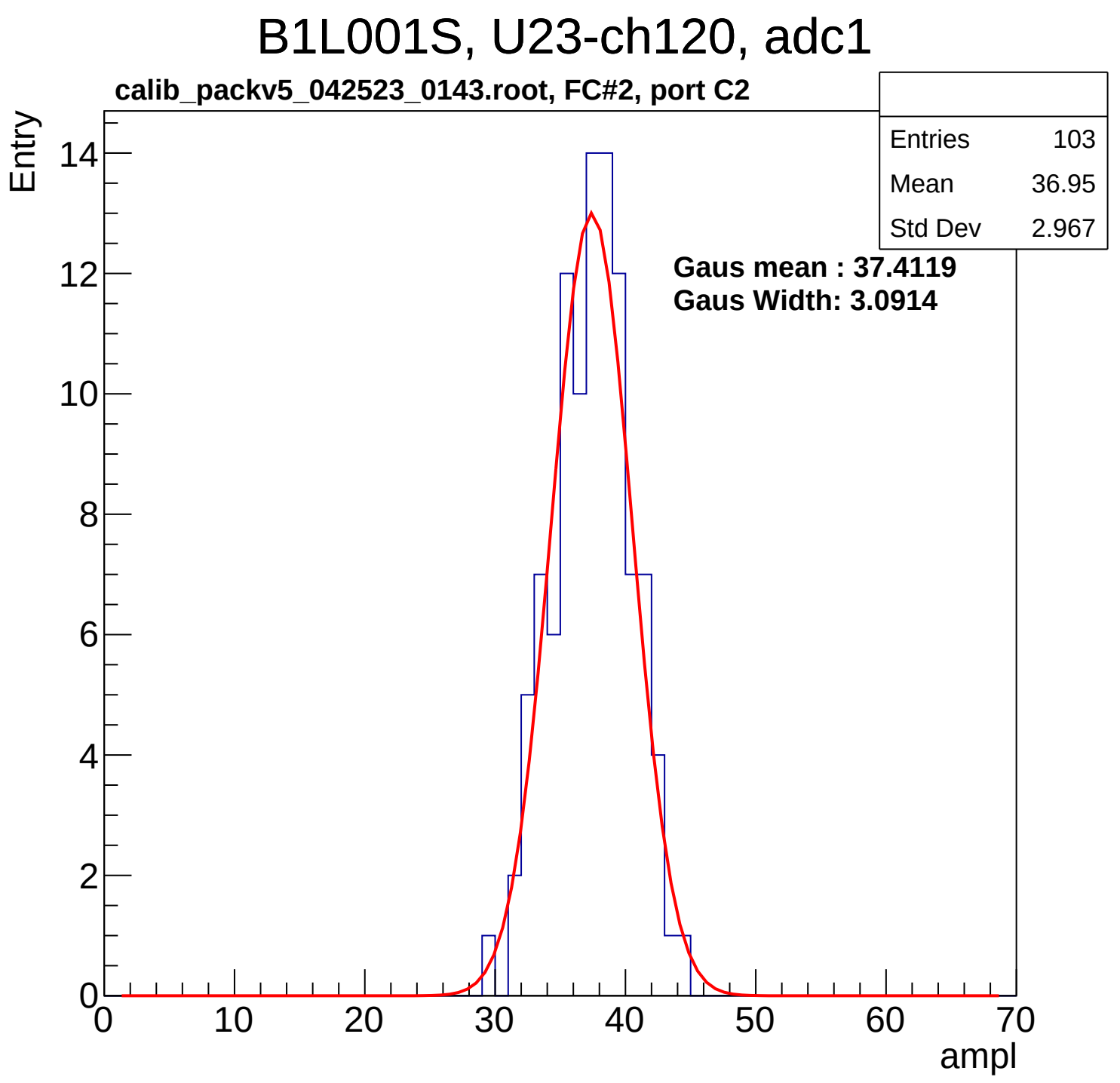
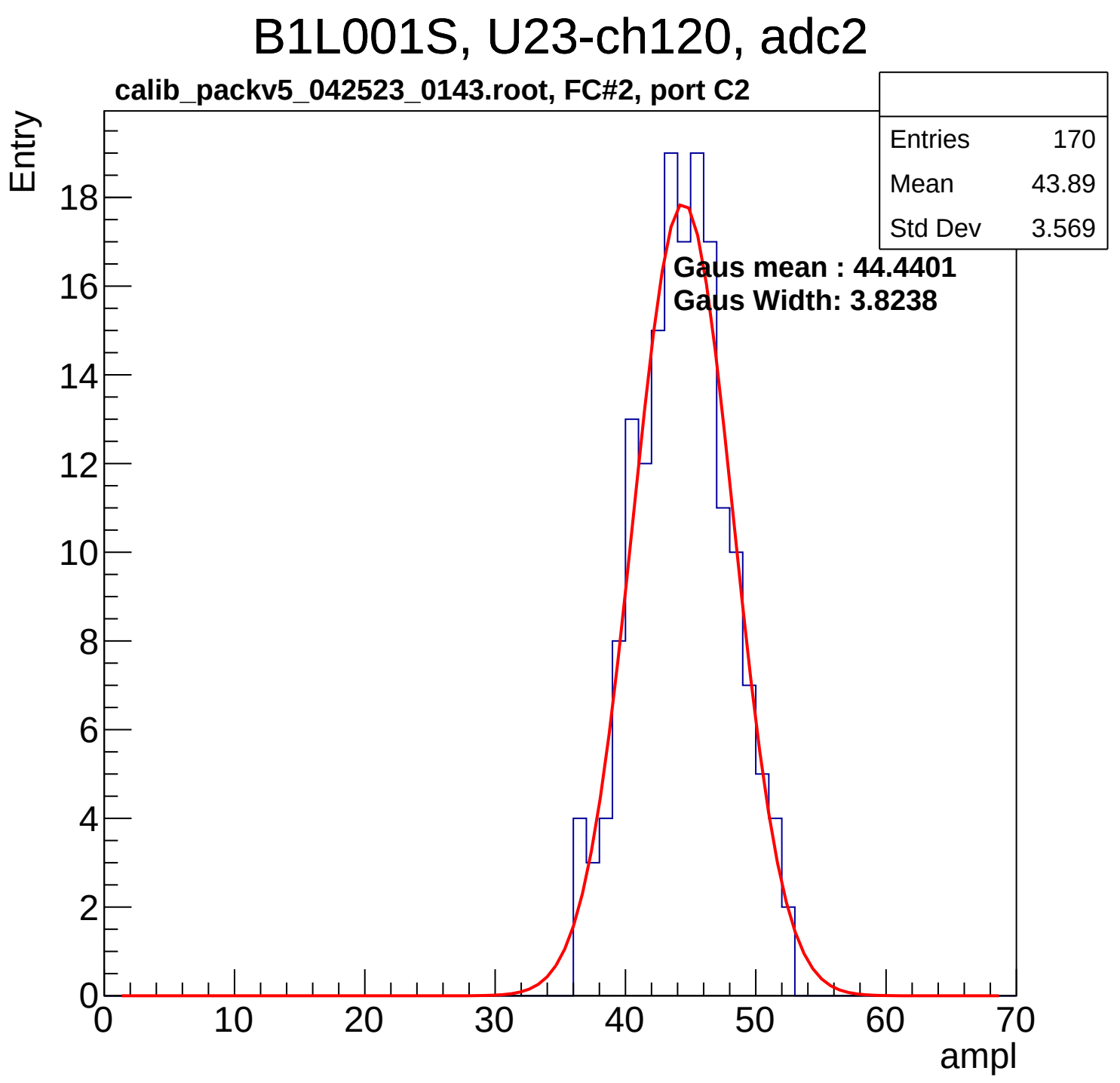

B1L001S, U23-ch120, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 36.95
Std Dev 2.967
Gaus mean : 37.4119
Gaus Width: 3.0914
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70


B1L001S, U23-ch120, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 170
Mean 43.89
Std Dev 3.569
Gaus mean : 44.4401
Gaus Width: 3.8238
Entry
ampl
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4
6
8
10
12
14
16
18
0
10
20
30
40
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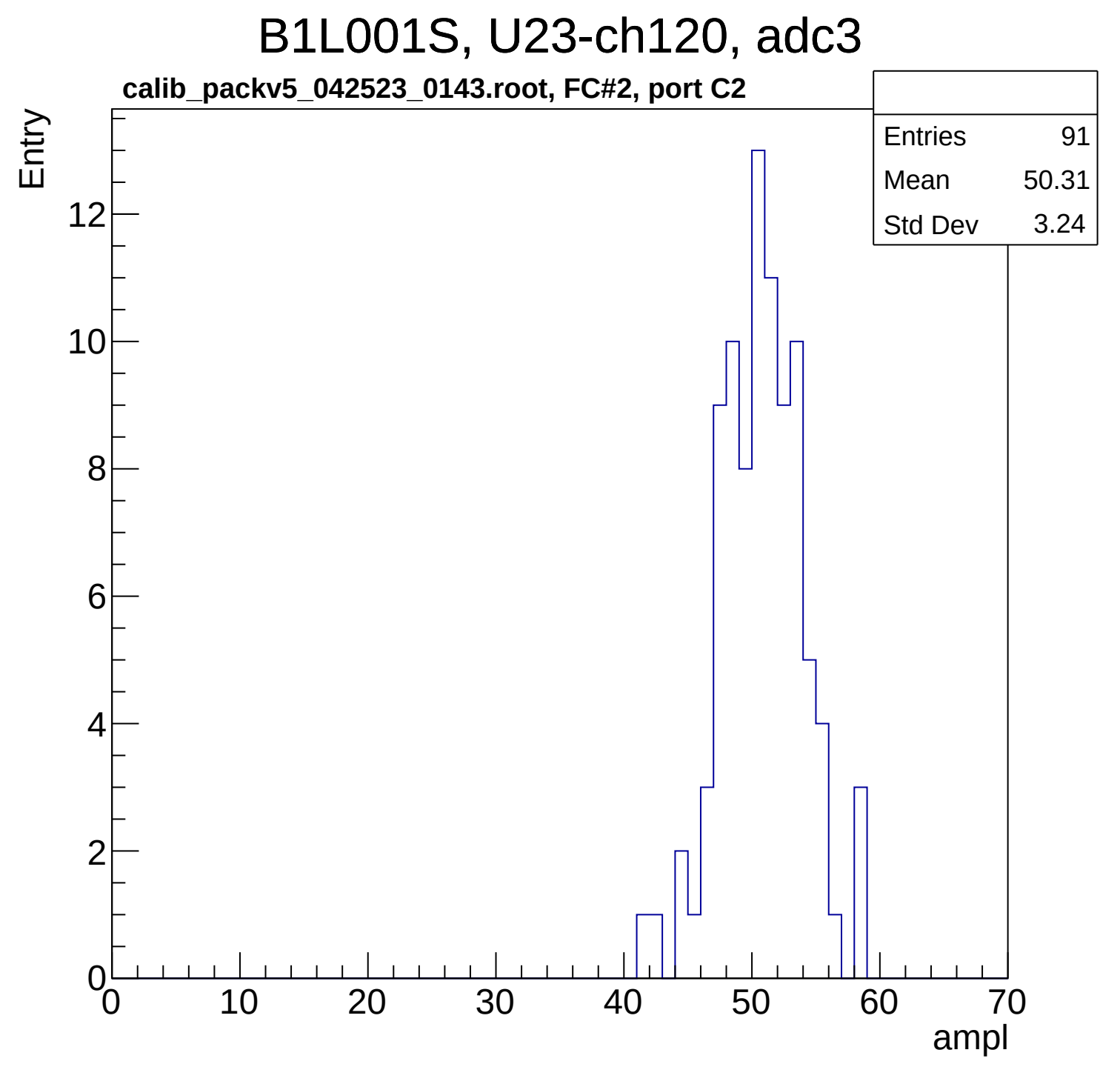
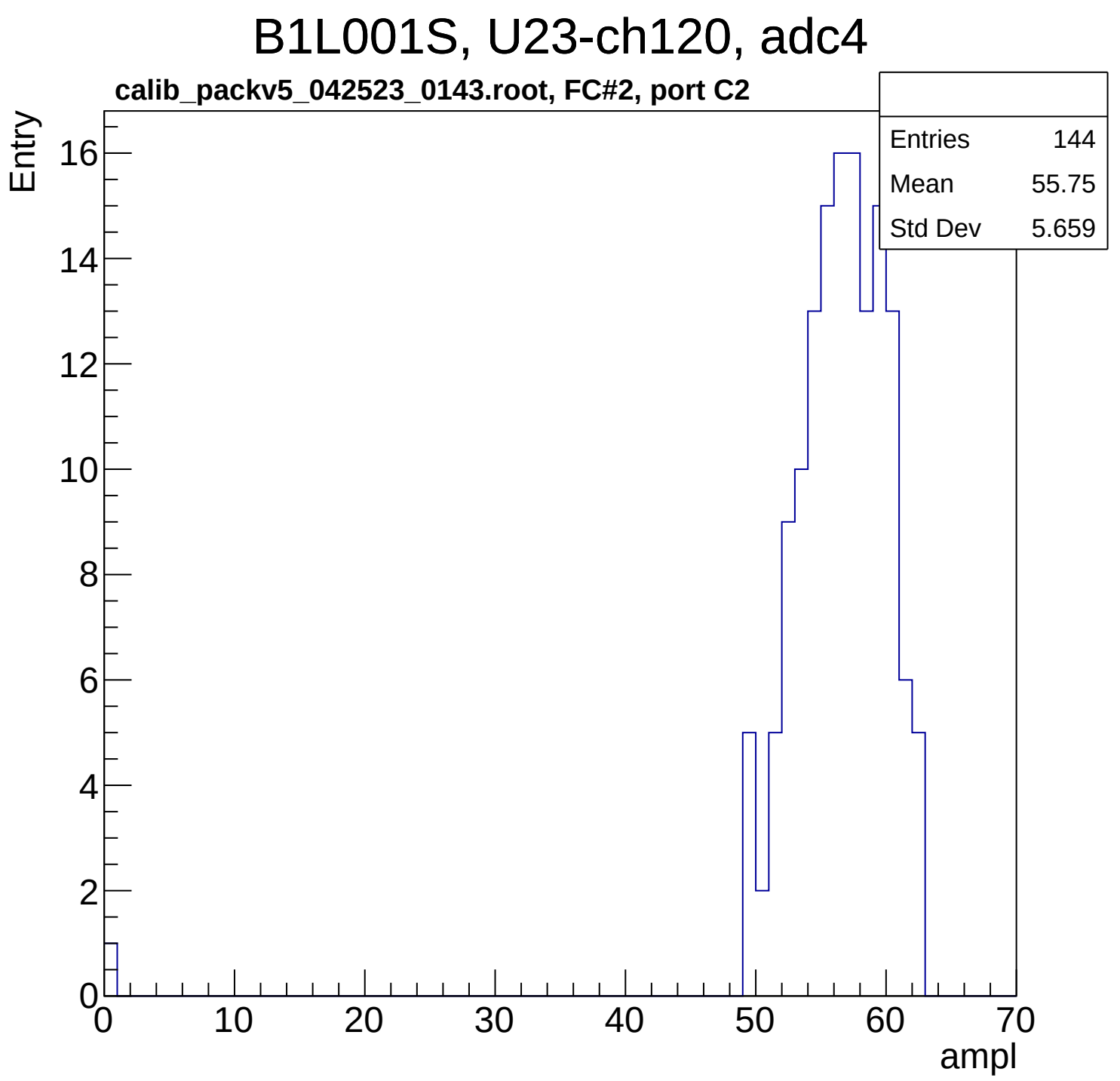

B1L001S, U23-ch120, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 91
Mean 50.31
Std Dev 3.24
Entry
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch120, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 144
Mean 55.75
Std Dev 5.659
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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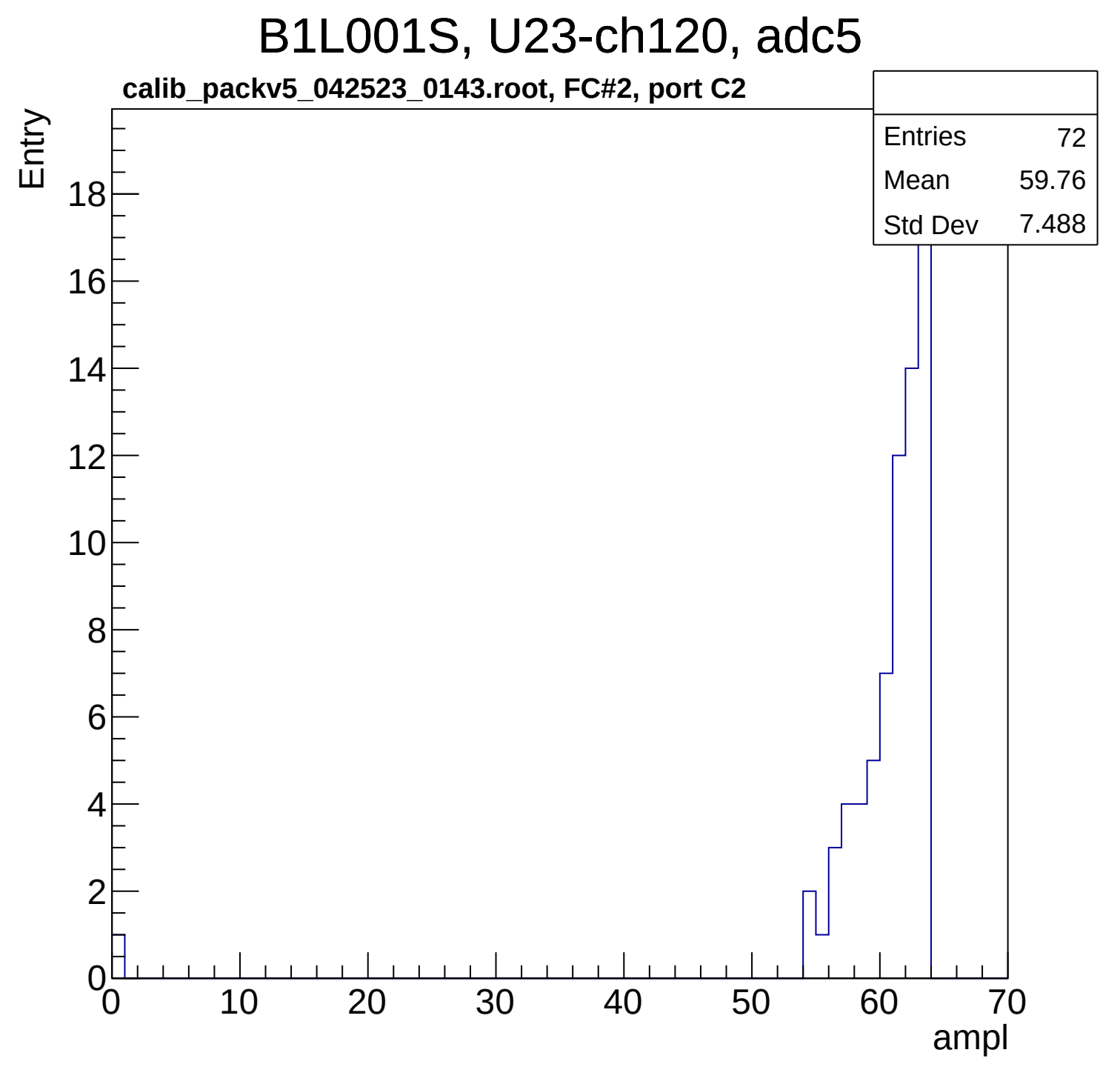
B1L001S, U23-ch120, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 72
Mean 59.76
Std Dev 7.488
Entry
ampl
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4
6
8
10
12
14
16
18
0
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20
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40
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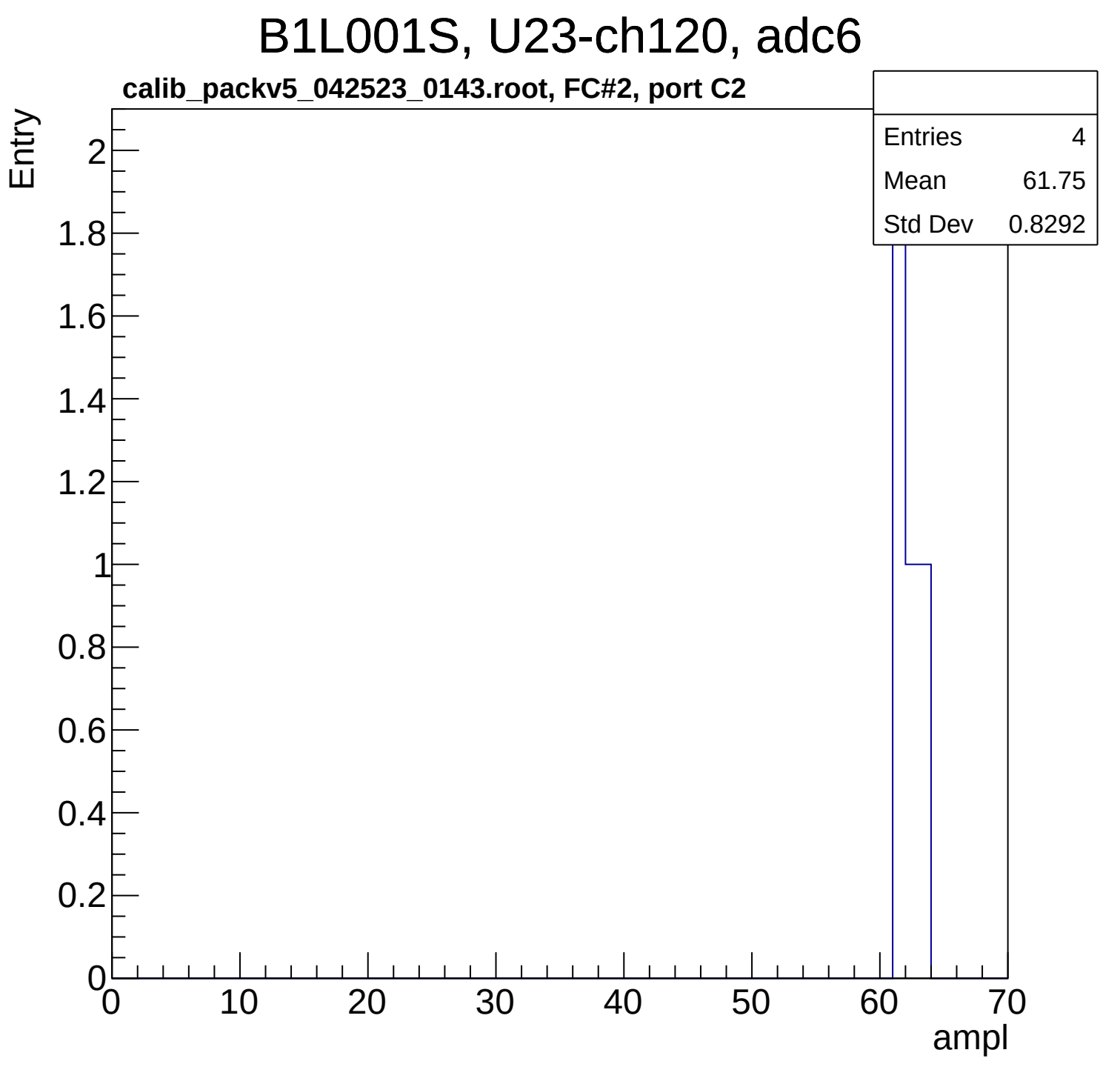
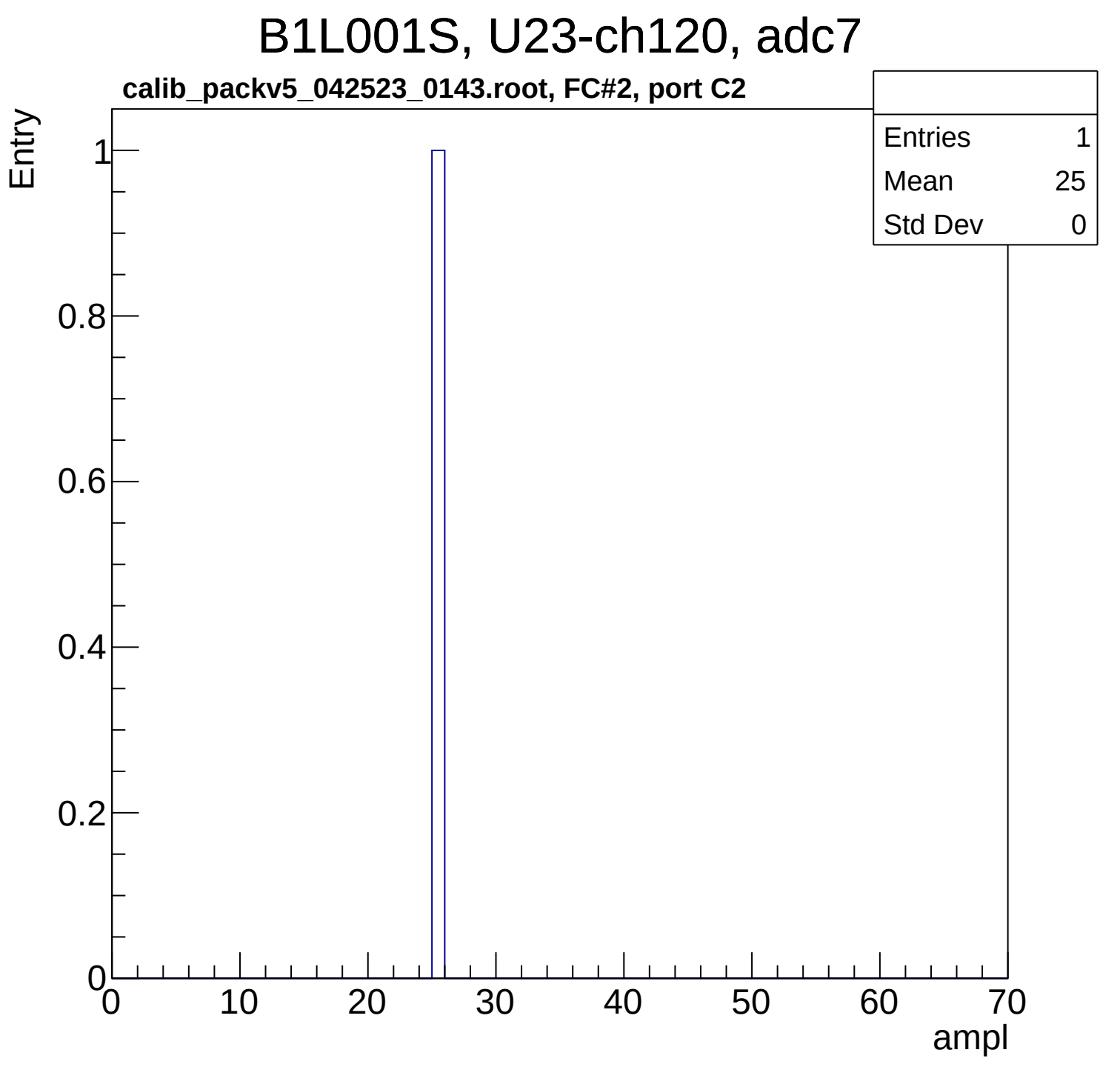

B1L001S, U23-ch120, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 4
Mean 61.75
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch120, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 25
Std Dev 0
Entry
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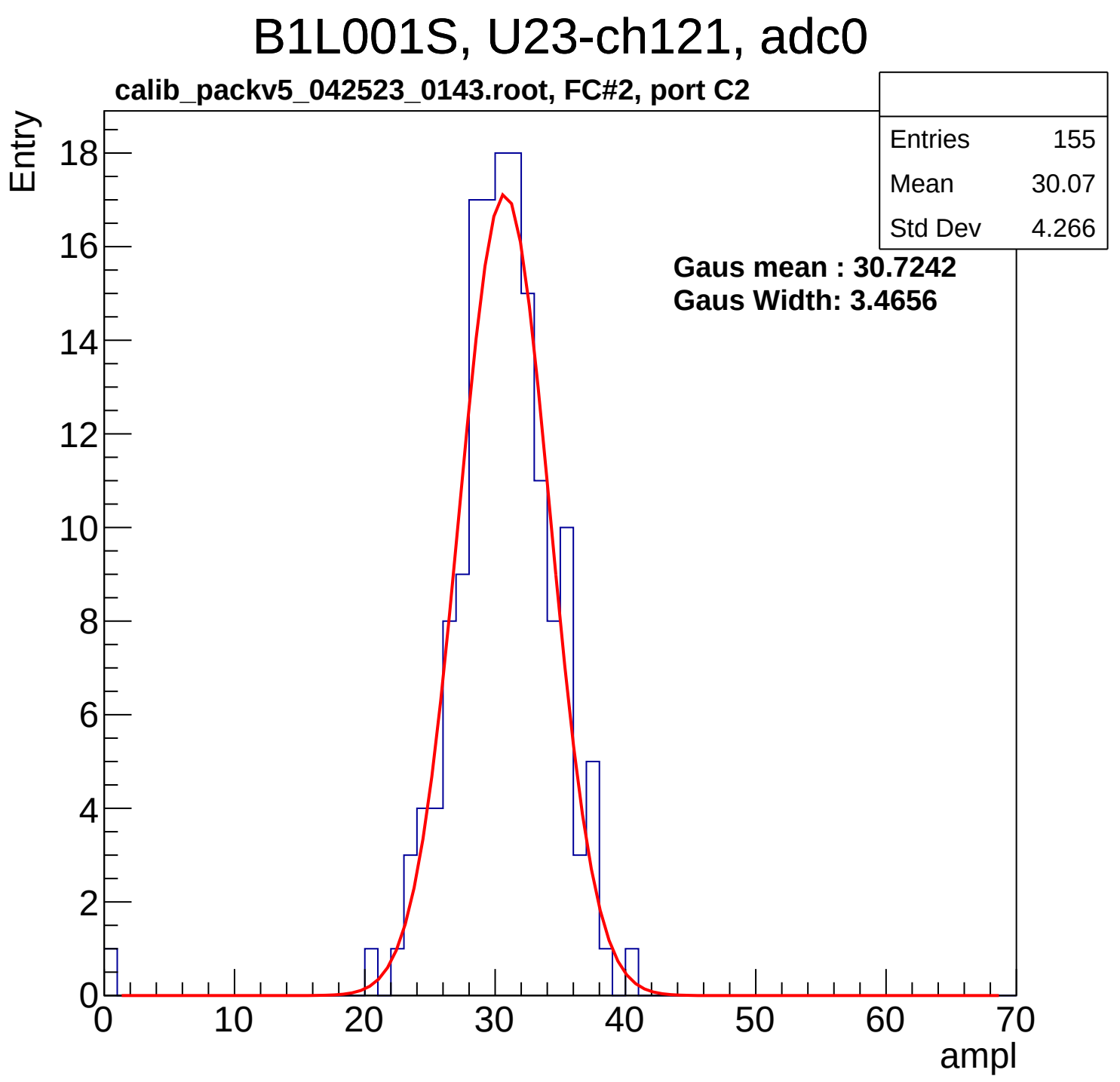
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch121, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 30.07
Std Dev 4.266
Gaus mean : 30.7242
Gaus Width: 3.4656
Entry
ampl
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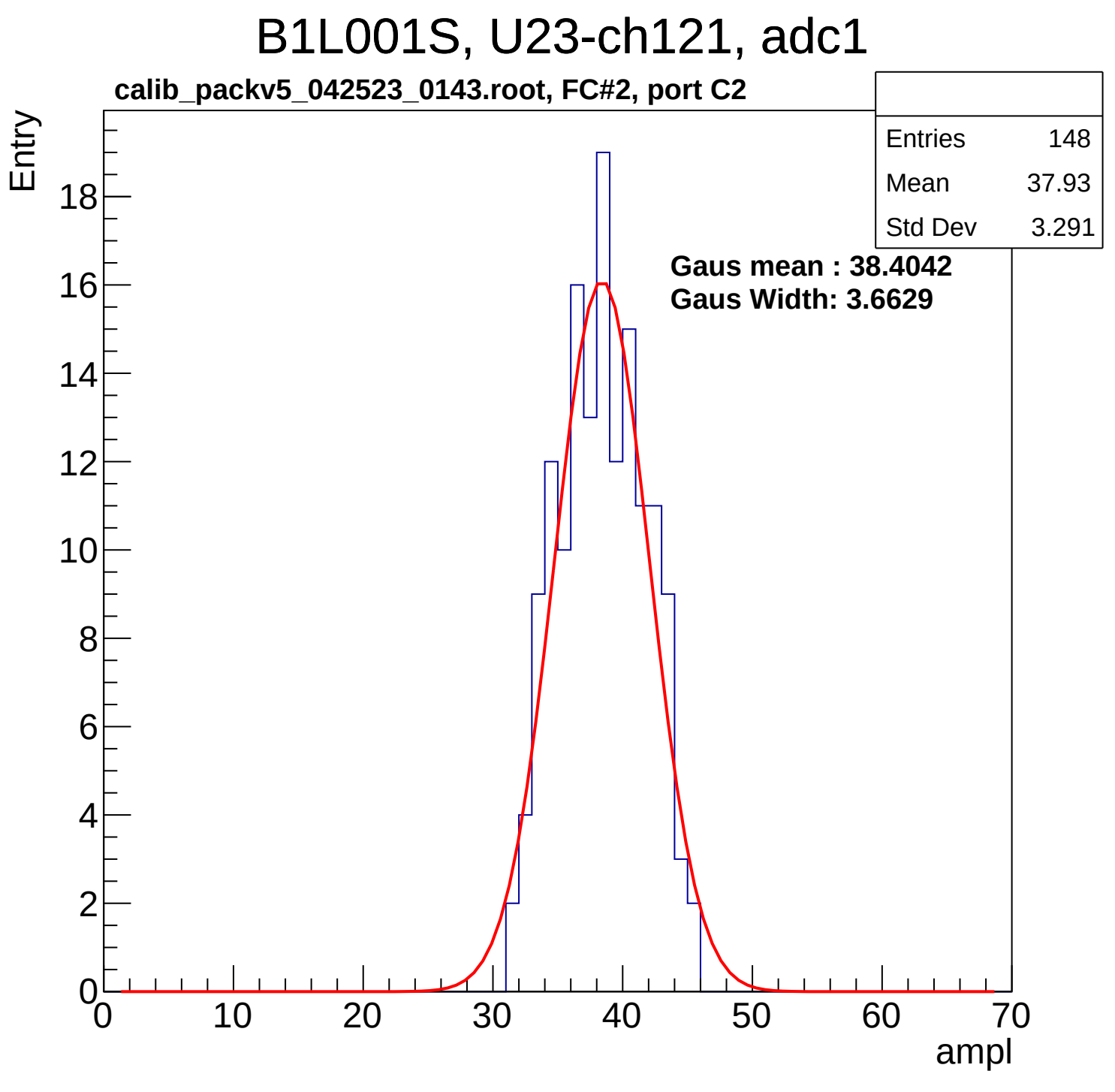
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch121, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 148
Mean 37.93
Std Dev 3.291
Gaus mean : 38.4042
Gaus Width: 3.6629
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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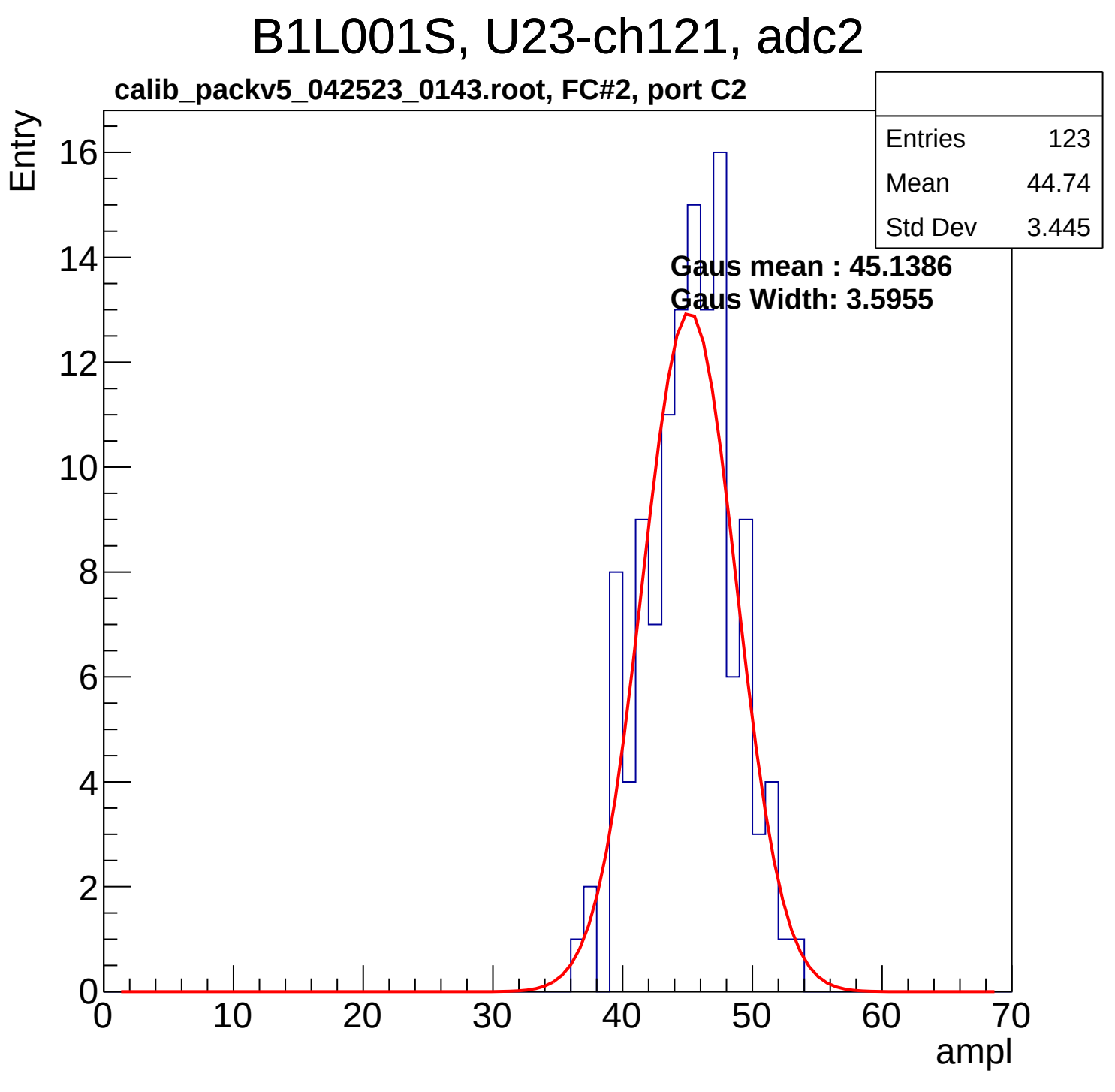
60
70


B1L001S, U23-ch121, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 123
Mean 44.74
Std Dev 3.445
Gaus mean : 45.1386
Gaus Width: 3.5955
Entry
ampl
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2
4
6
8
10
12
14
16
0
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60
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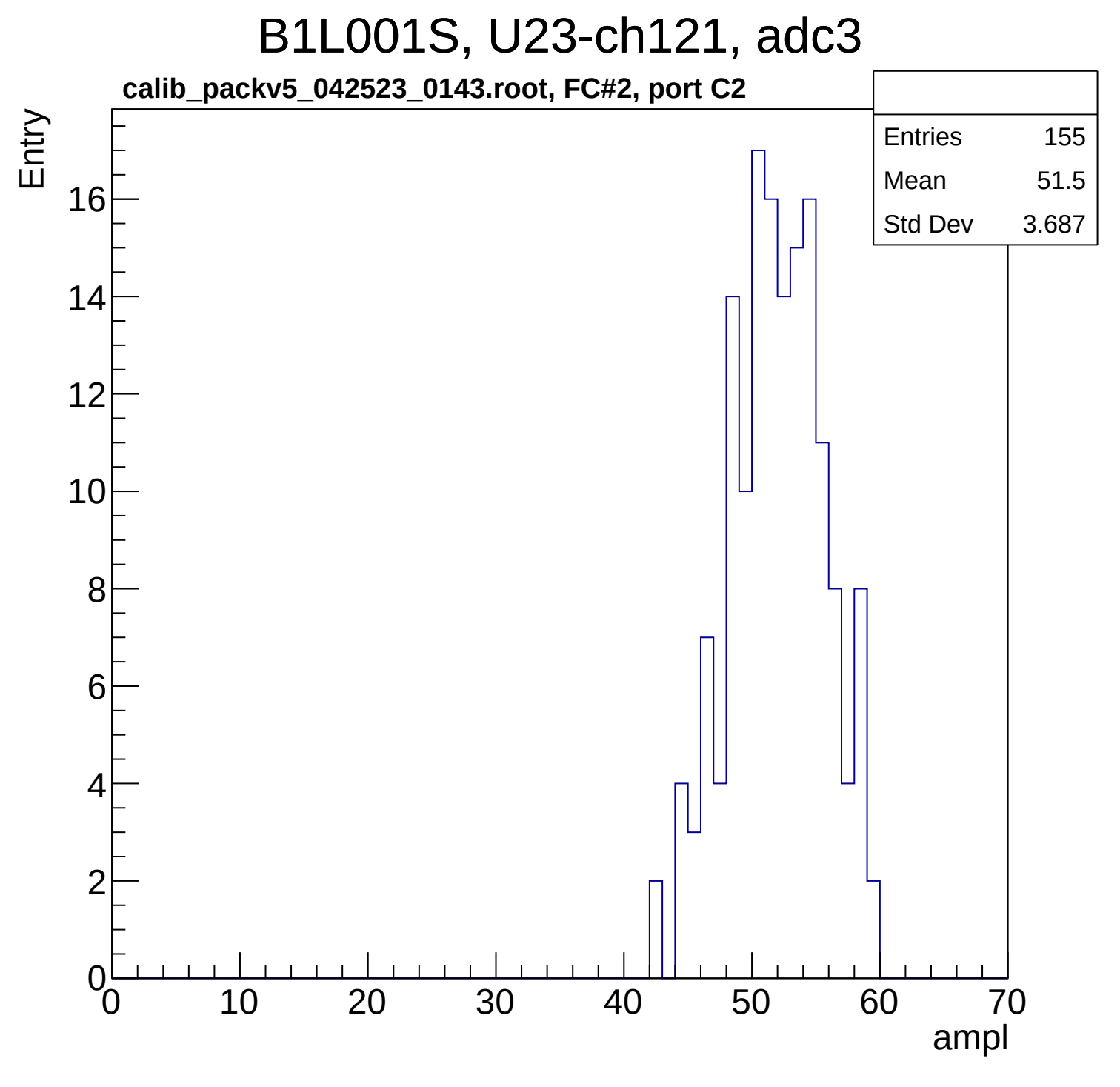
B1L001S, U23-ch121, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 155
Mean 51.5
Std Dev 3.687
Entry
ampl
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10
12
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0
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60
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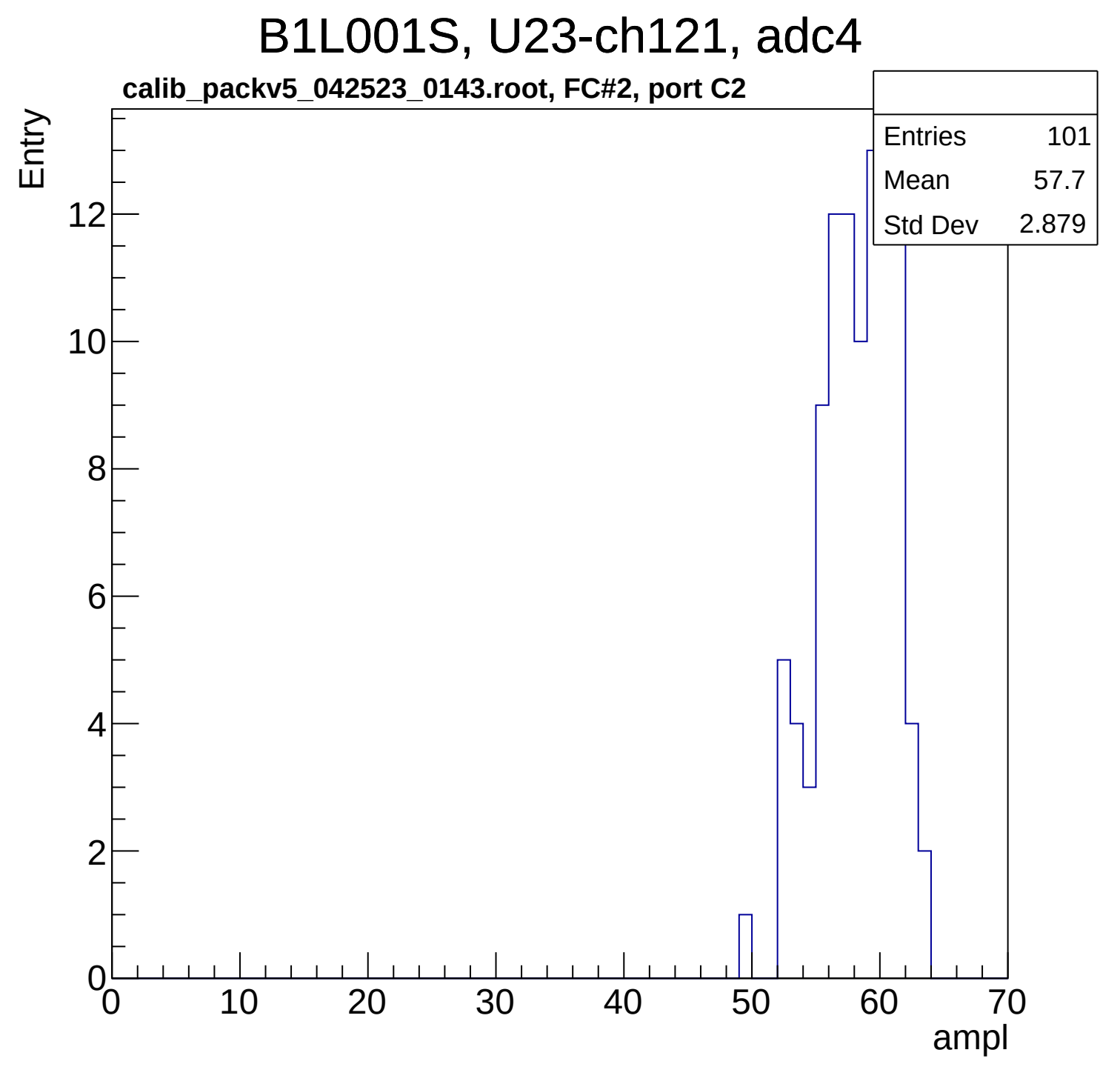
B1L001S, U23-ch121, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 101
Mean 57.7
Std Dev 2.879
Entry
ampl
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0
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40
50
60
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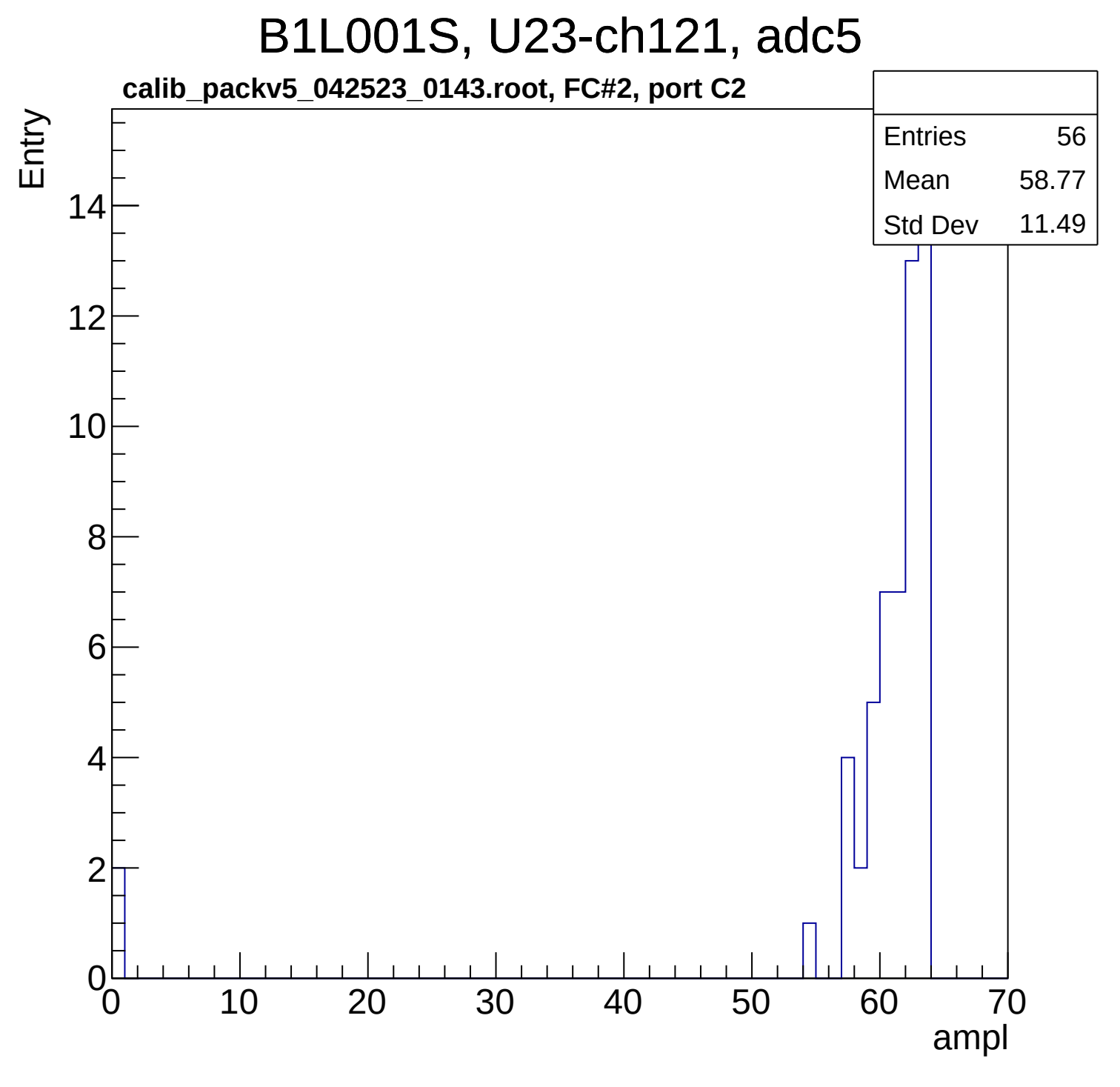
B1L001S, U23-ch121, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 56
Mean 58.77
Std Dev 11.49
Entry
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10
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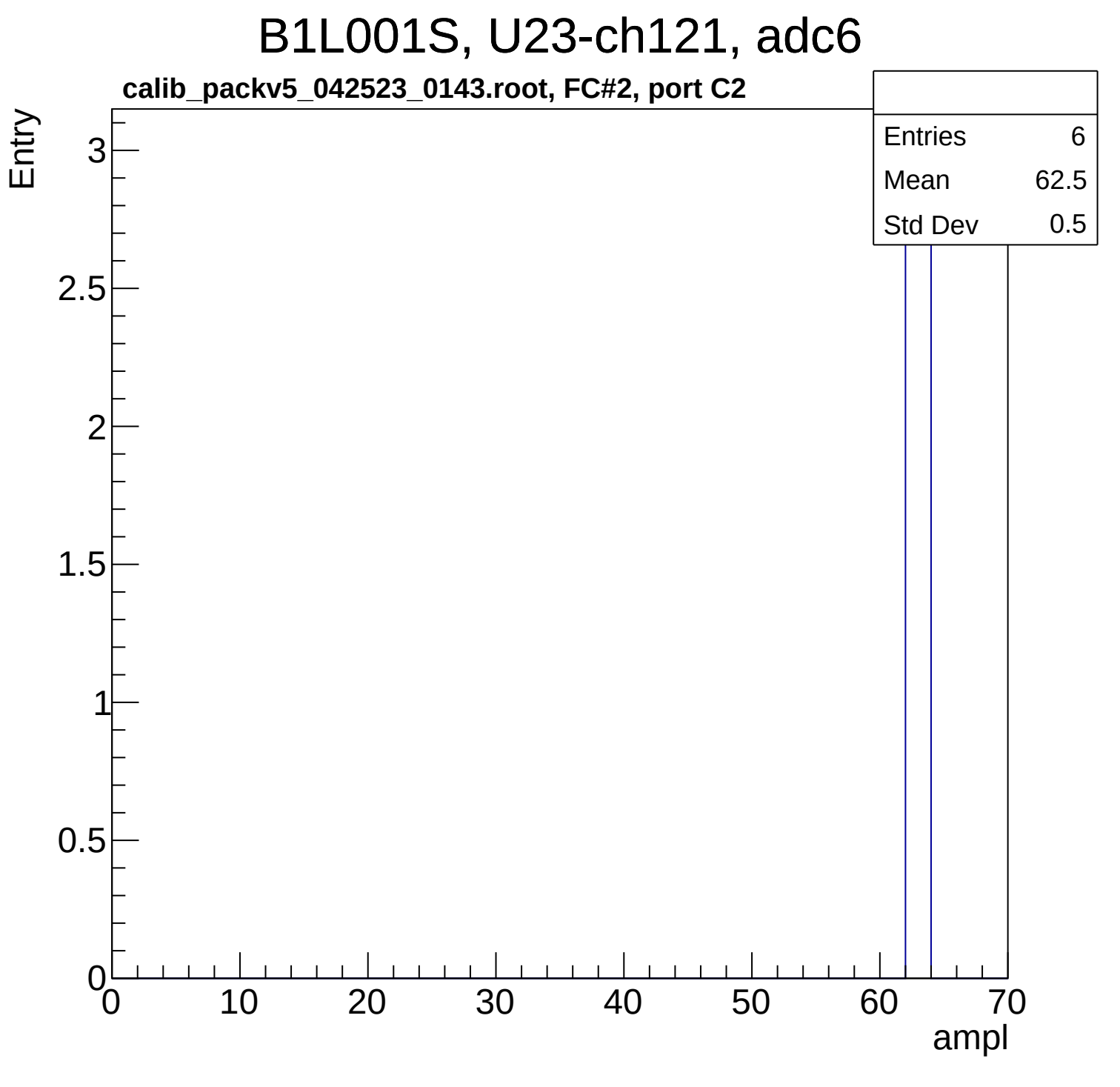
70
ampl


B1L001S, U23-ch121, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
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1
1.5
2
2.5
3
0
10
20
30
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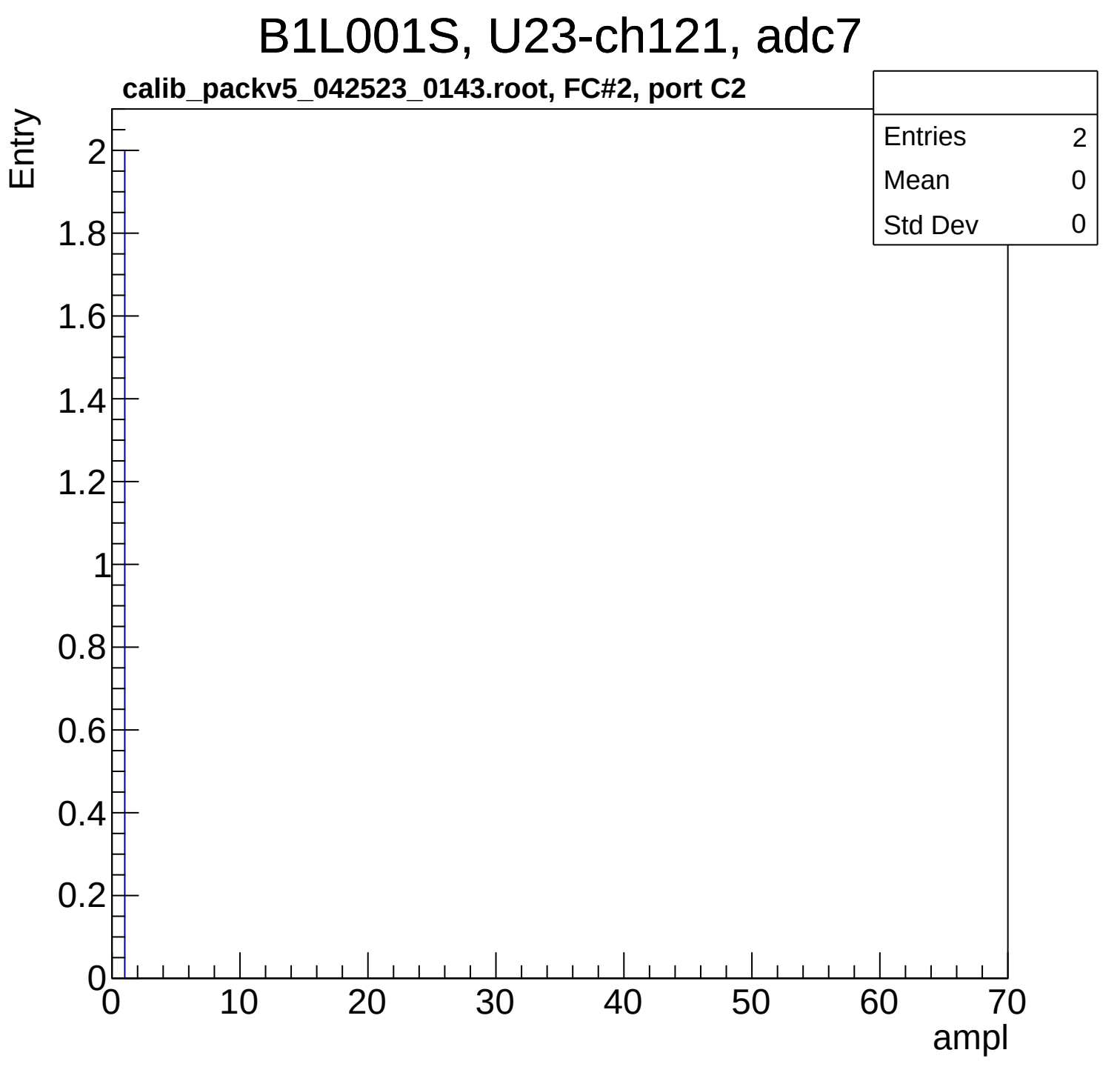
50
60
70
ampl


B1L001S, U23-ch121, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
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10
20
30
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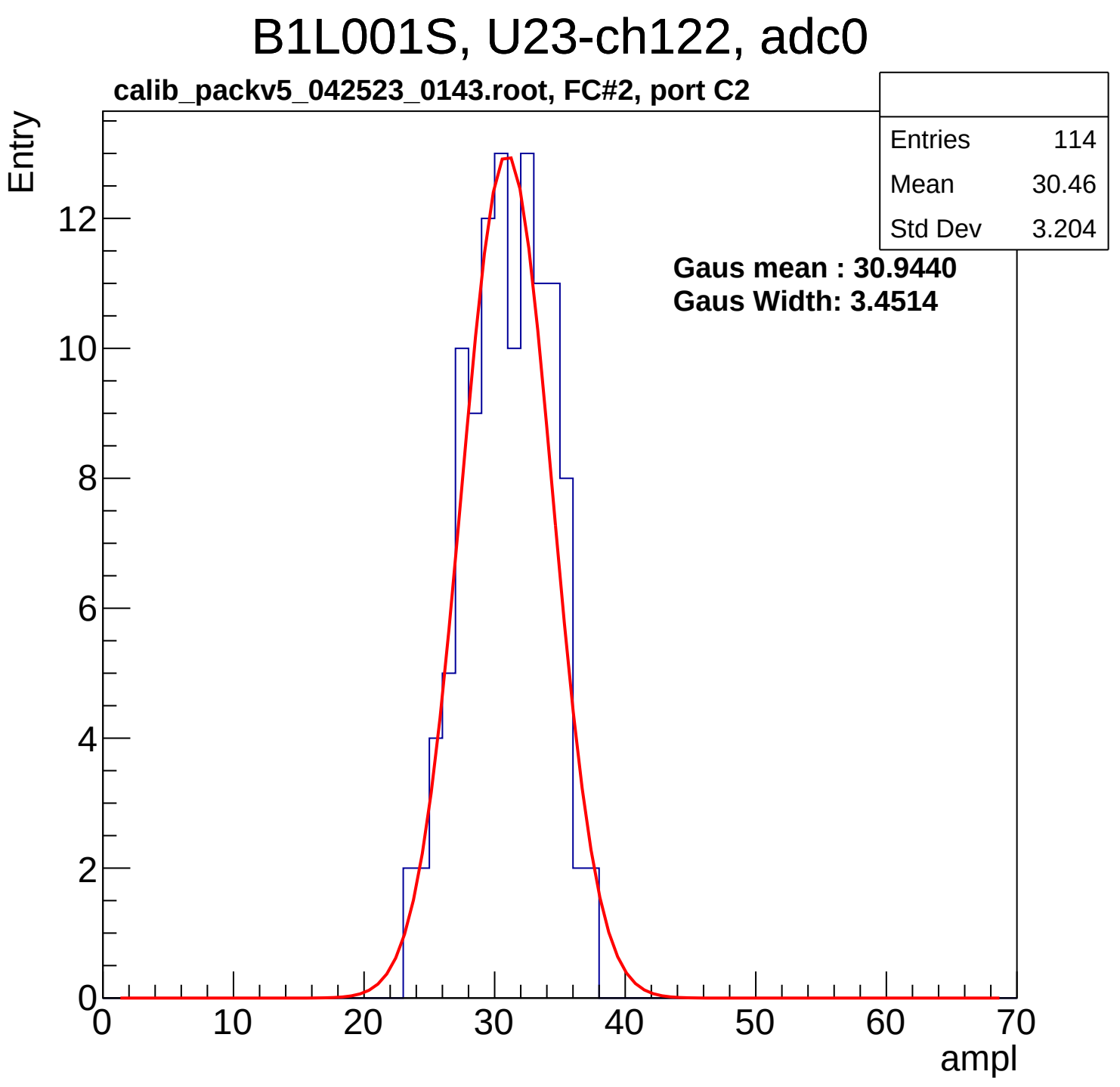
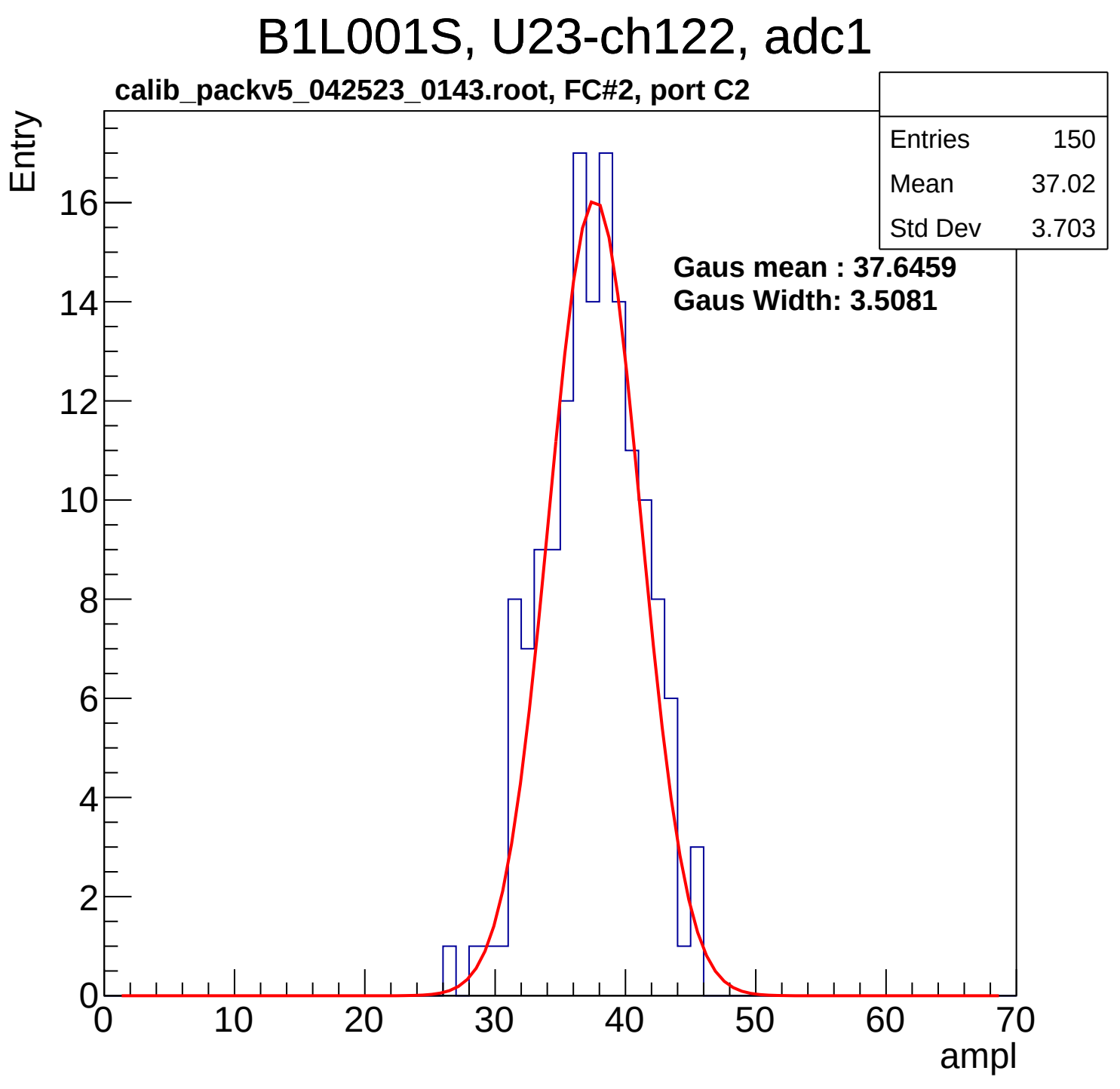
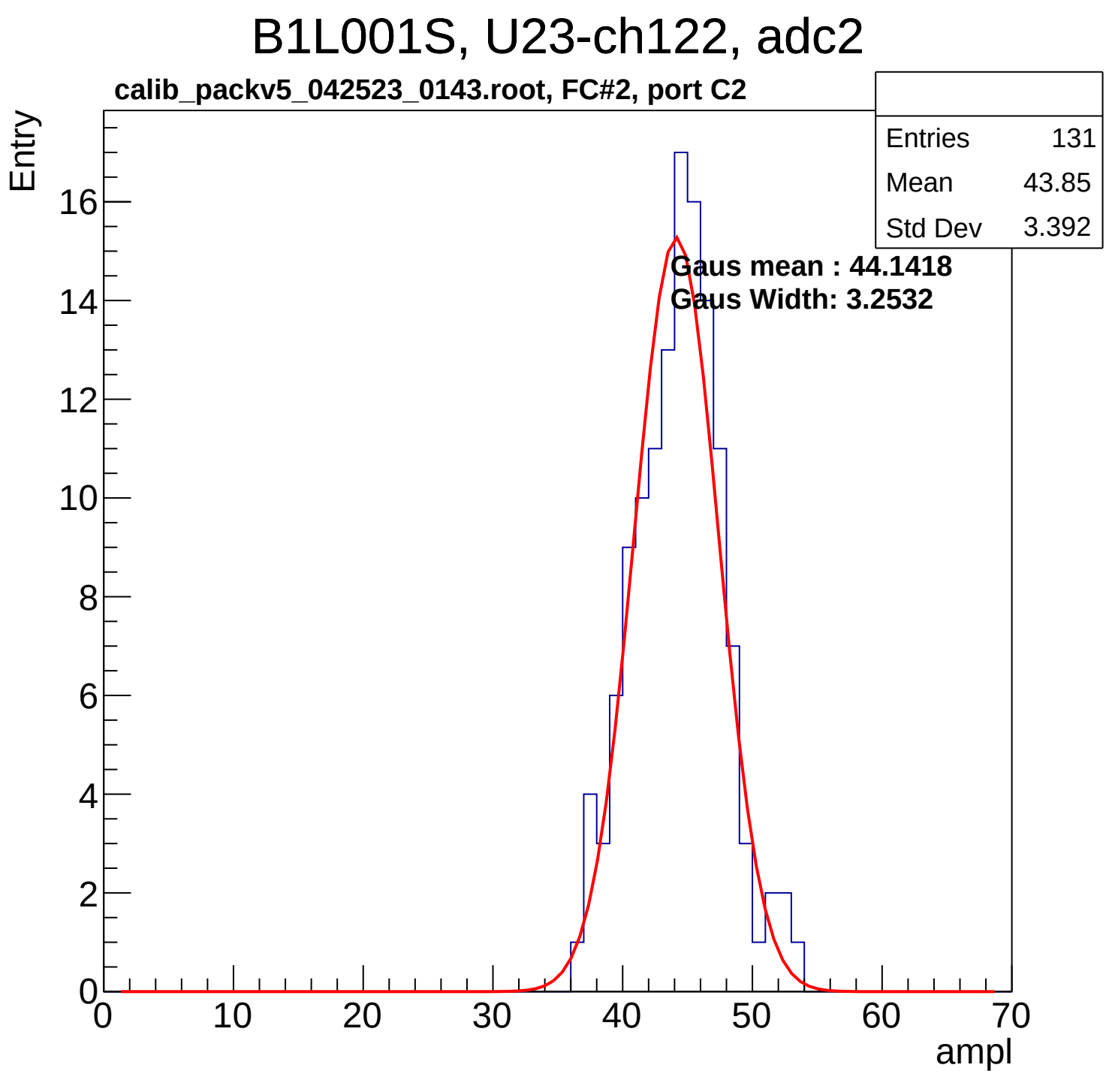
50
60
70
ampl


B1L001S, U23-ch122, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 114
Mean 30.46
Std Dev 3.204
Gaus mean : 30.9440
Gaus Width: 3.4514
Entry
ampl
0
2
4
6
8
10
12
0
10
20
30
40
50
60
70


B1L001S, U23-ch122, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 150
Mean 37.02
Std Dev 3.703
Gaus mean : 37.6459
Gaus Width: 3.5081
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch122, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 131
Mean 43.85
Std Dev 3.392
Gaus mean : 44.1418
Gaus Width: 3.2532
Entry
ampl
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4
6
8
10
12
14
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0
10
20
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60
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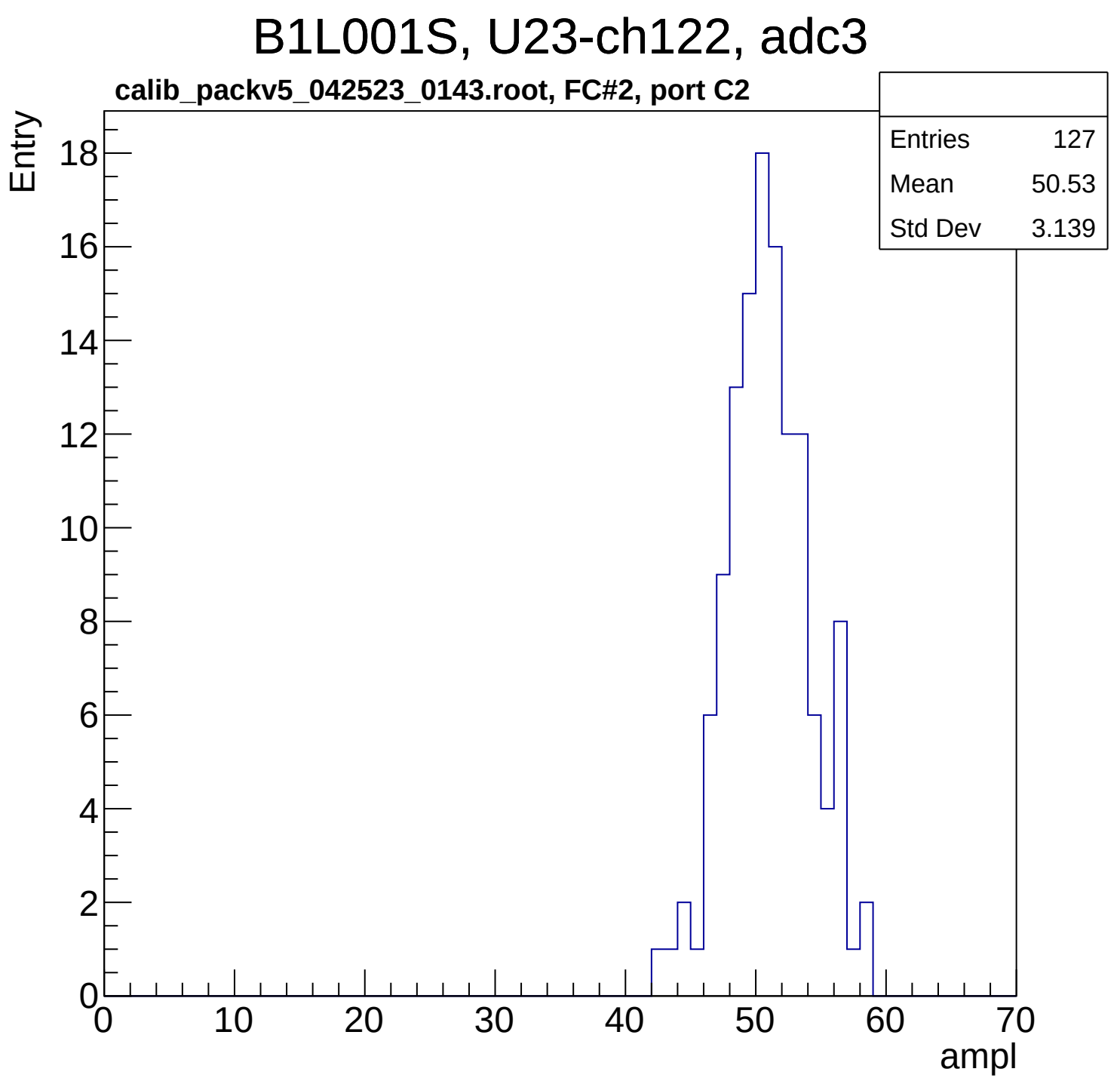
B1L001S, U23-ch122, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 127
Mean 50.53
Std Dev 3.139
Entry
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10
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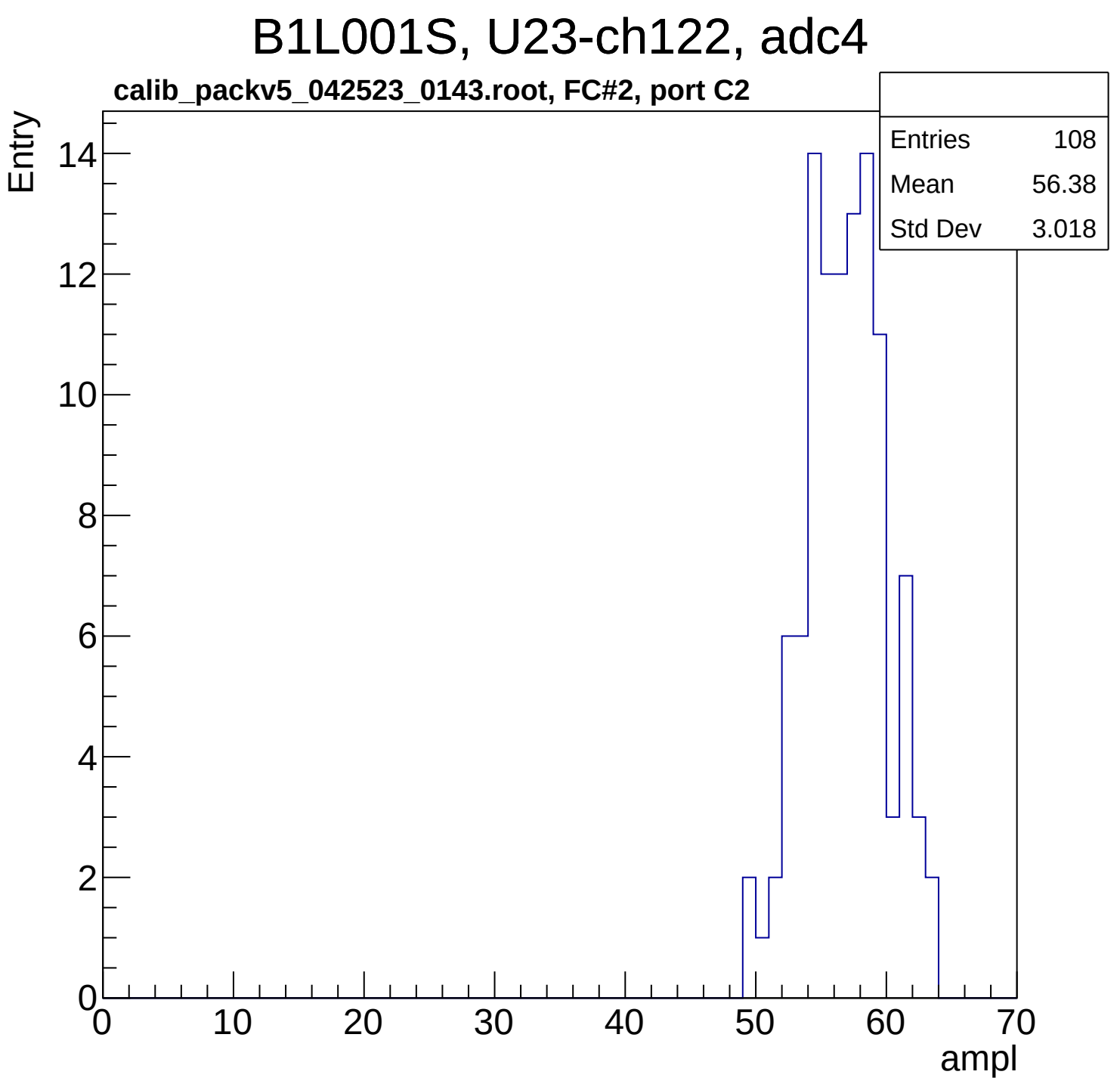
ampl


B1L001S, U23-ch122, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 108
Mean 56.38
Std Dev 3.018
Entry
ampl
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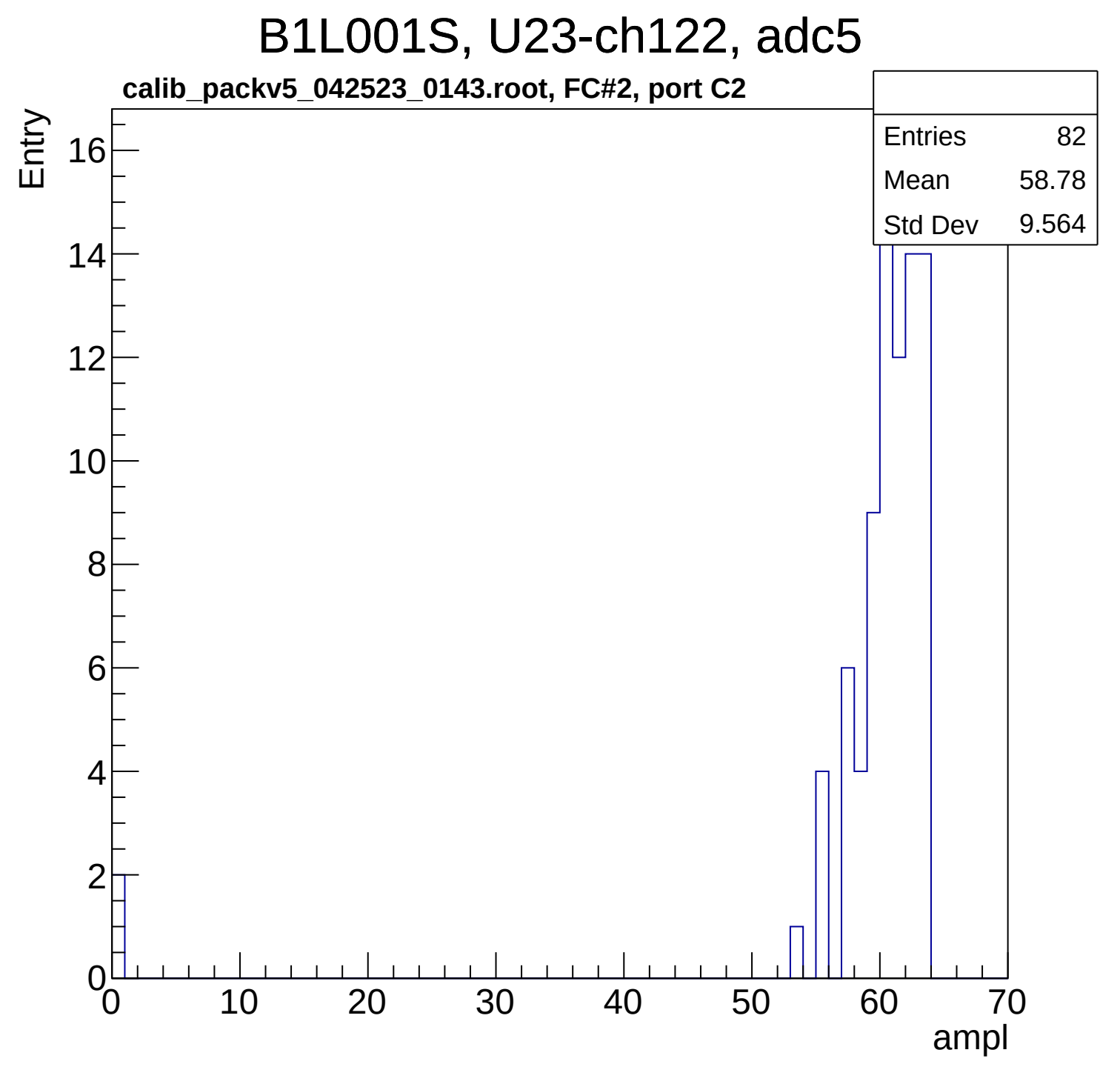
B1L001S, U23-ch122, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 82
Mean 58.78
Std Dev 9.564
Entry
ampl
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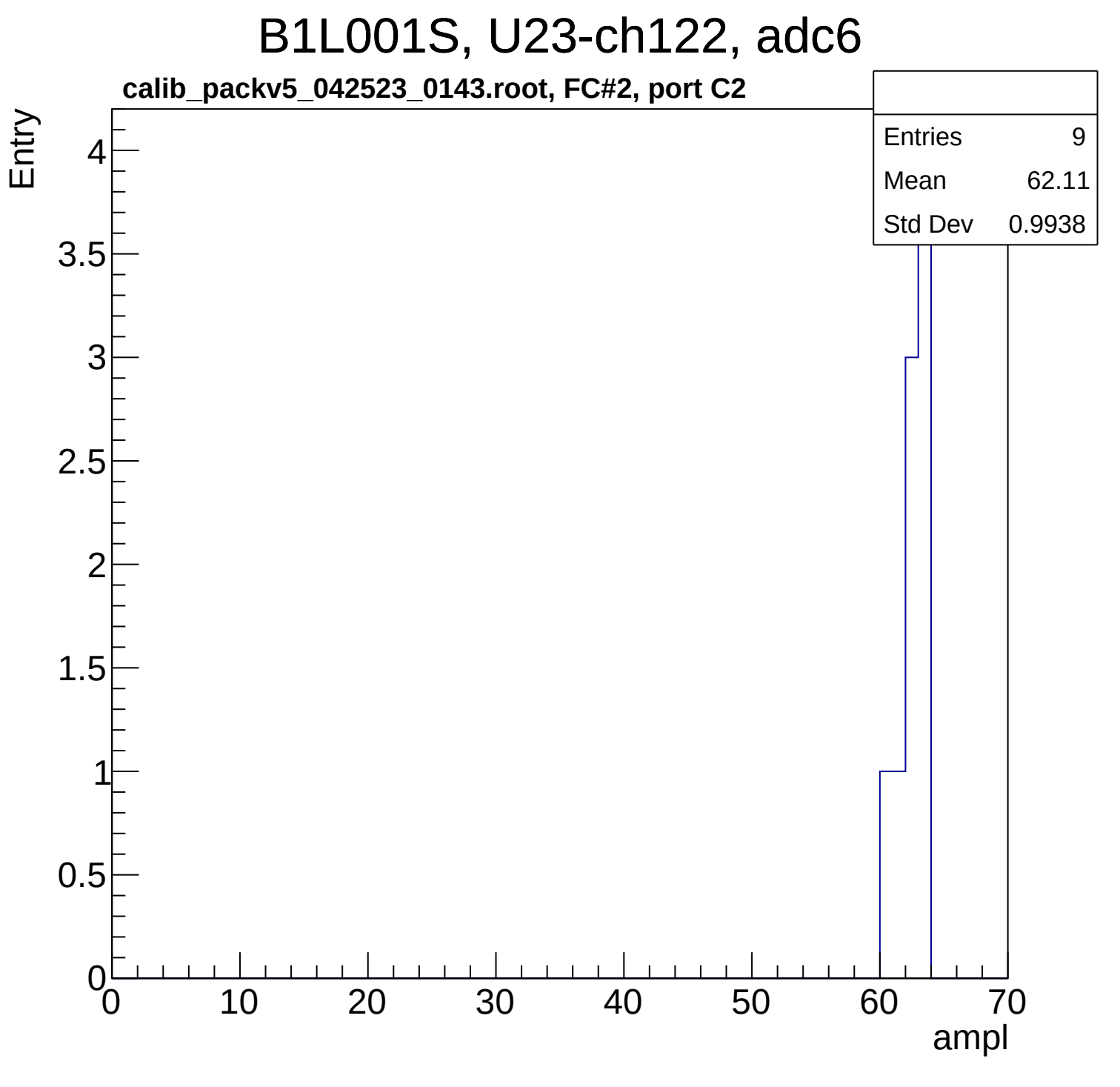
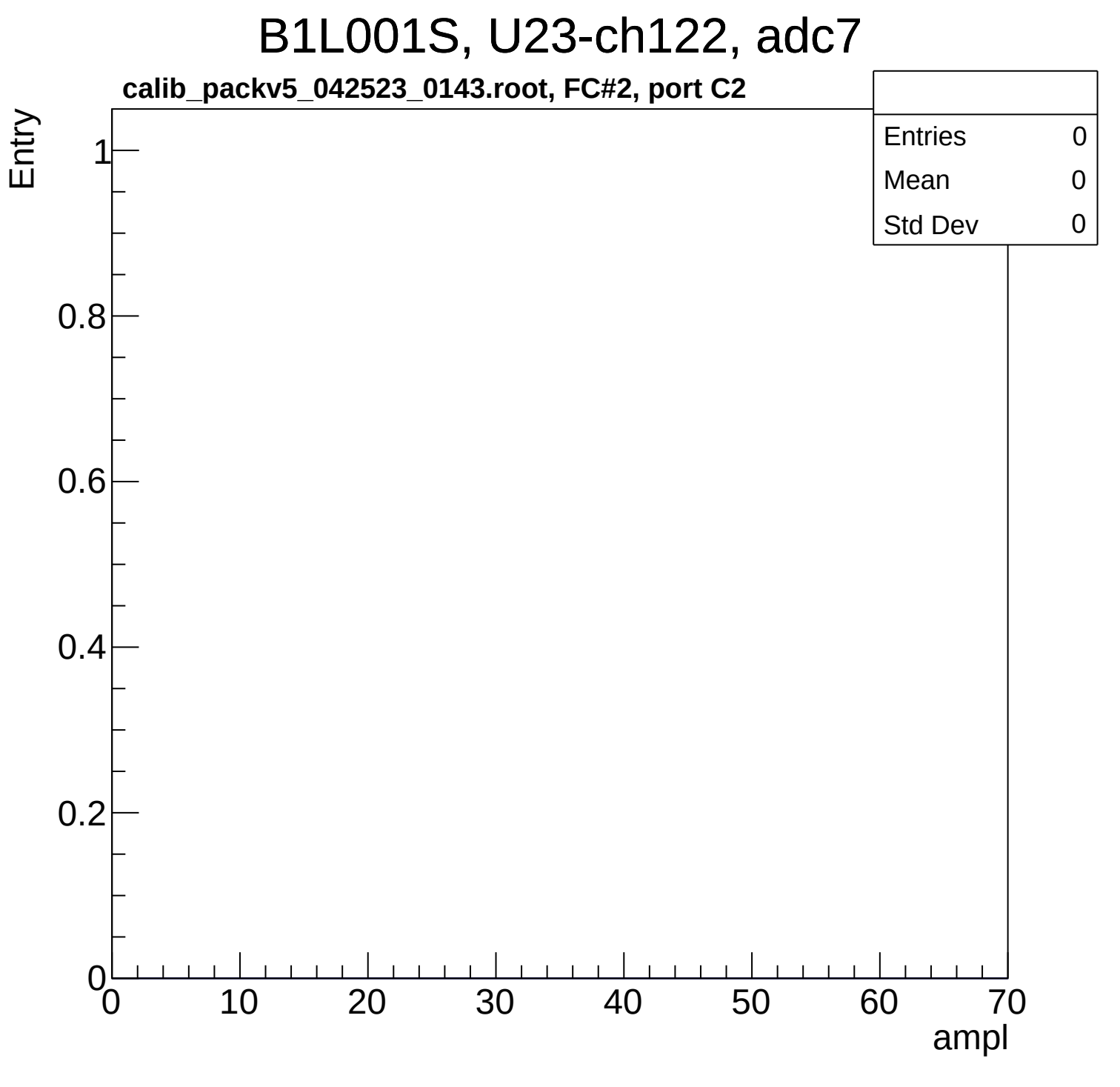
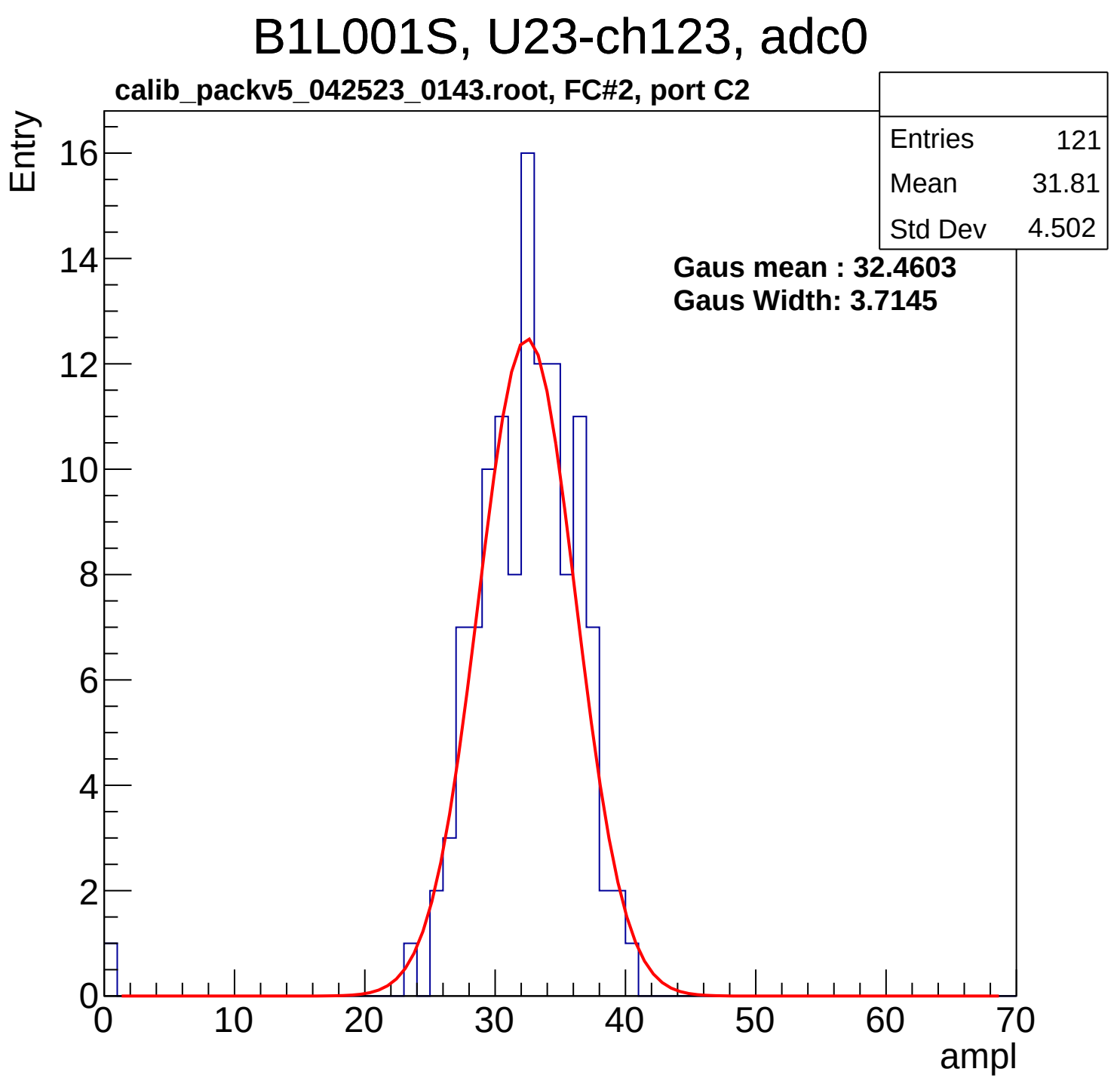

B1L001S, U23-ch122, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L001S, U23-ch122, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch123, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 121
Mean 31.81
Std Dev 4.502
Gaus mean : 32.4603
Gaus Width: 3.7145
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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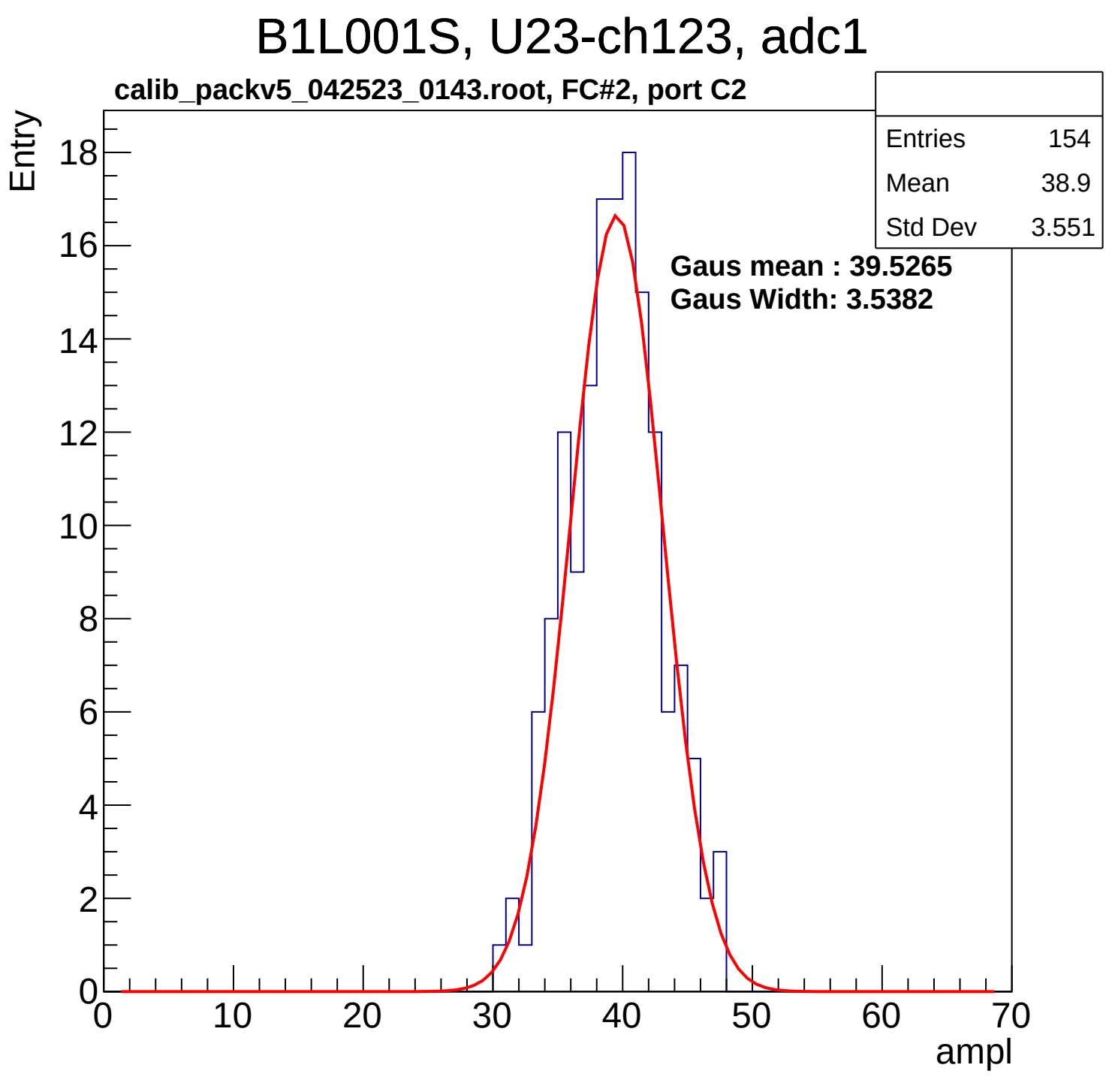
B1L001S, U23-ch123, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 154
Mean 38.9
Std Dev 3.551
Gaus mean : 39.5265
Gaus Width: 3.5382
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
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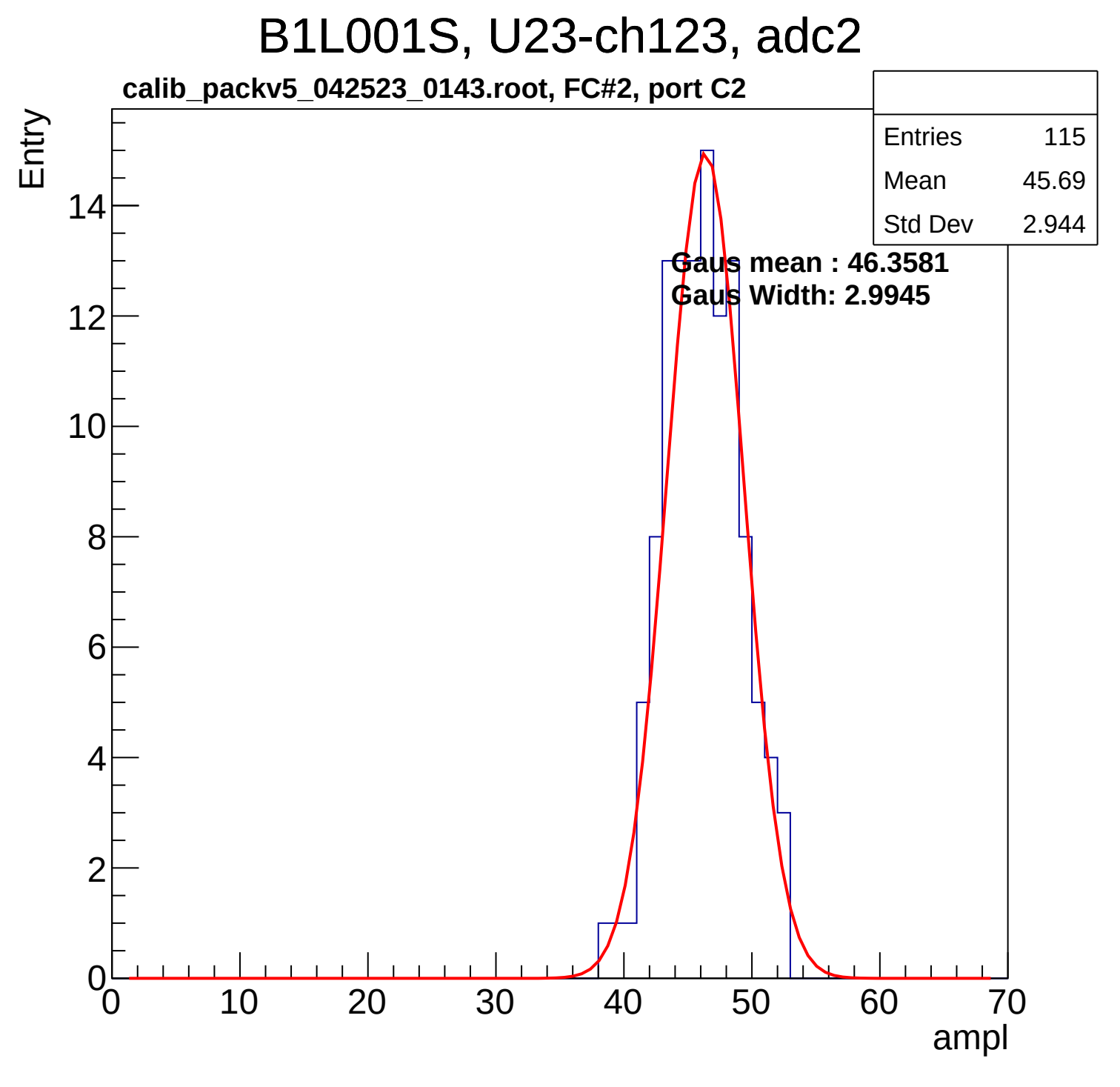
B1L001S, U23-ch123, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 115
Mean 45.69
Std Dev 2.944
Gaus mean : 46.3581
Gaus Width: 2.9945
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
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60
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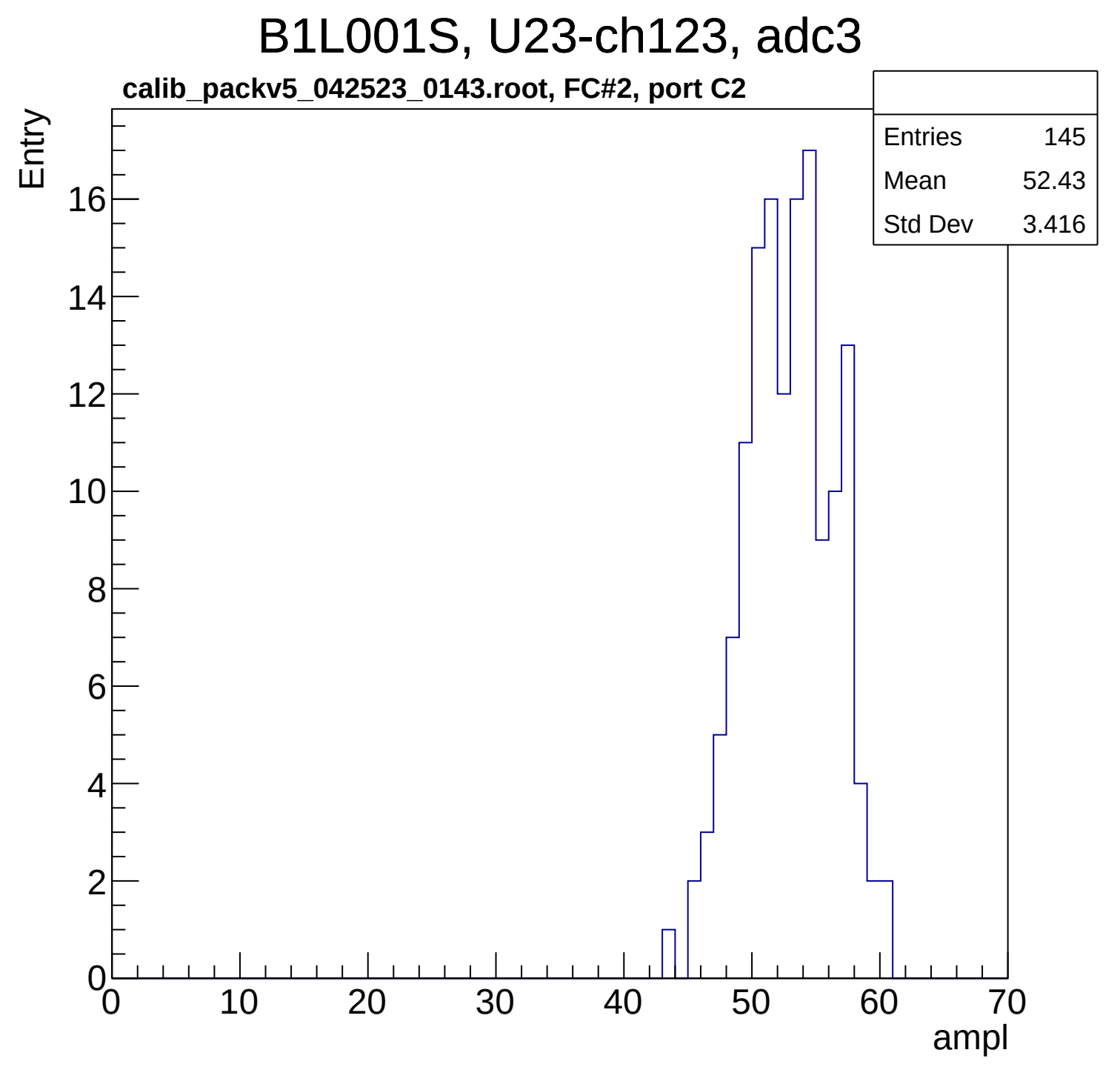
B1L001S, U23-ch123, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 145
Mean 52.43
Std Dev 3.416
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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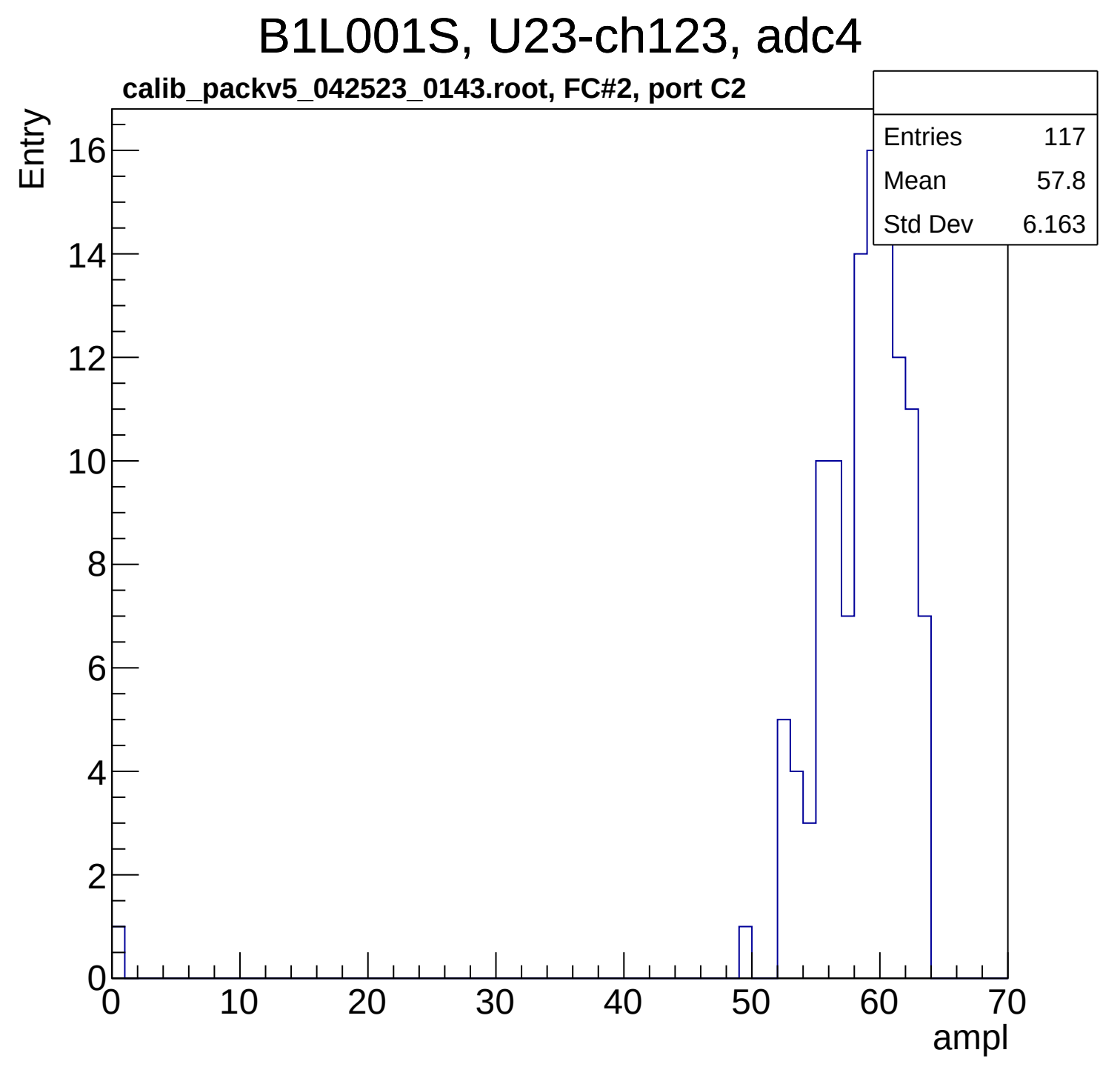
B1L001S, U23-ch123, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 57.8
Std Dev 6.163
Entry
ampl
0
2
4
6
8
10
12
14
16
0
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20
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40
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60
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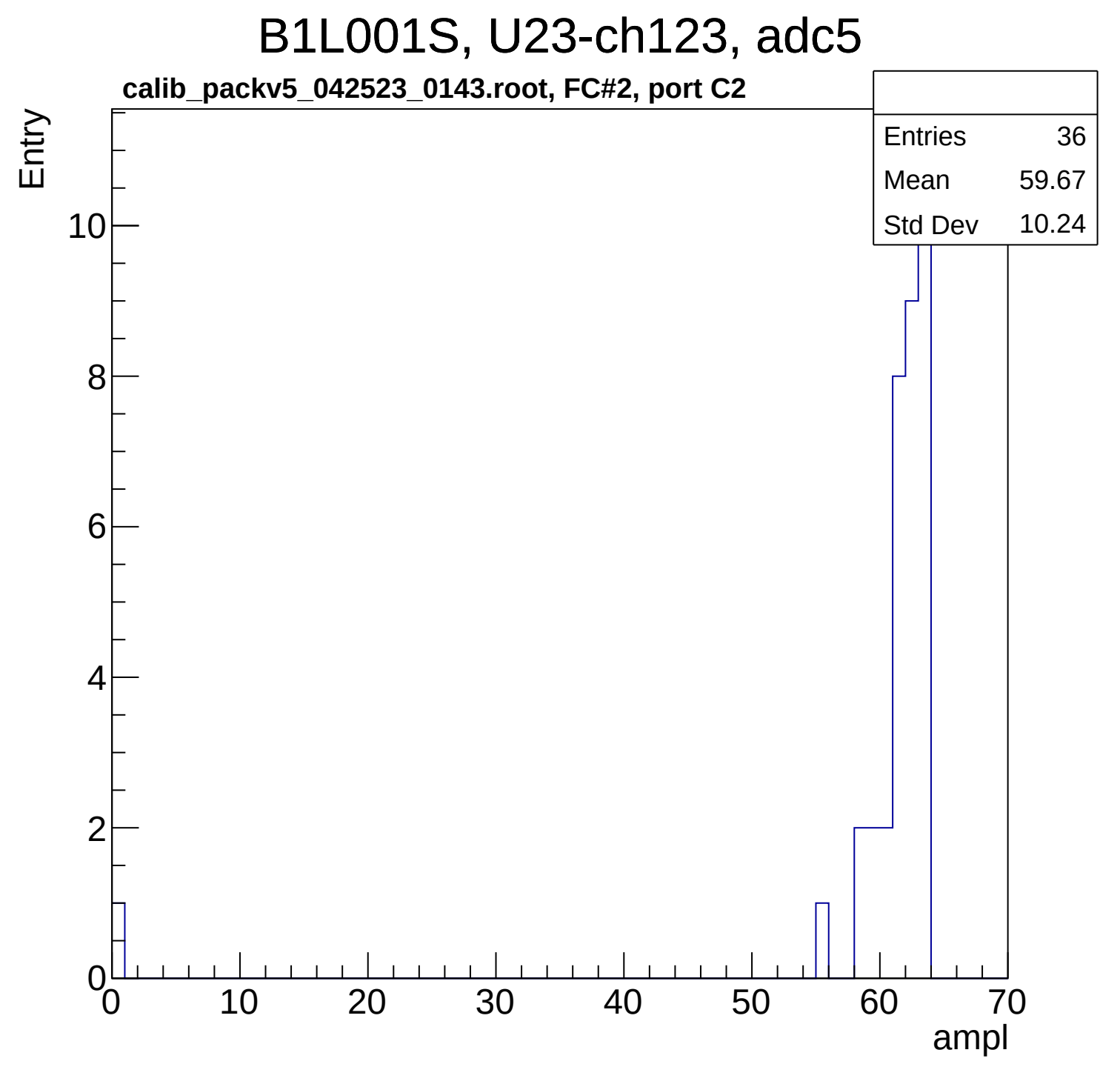
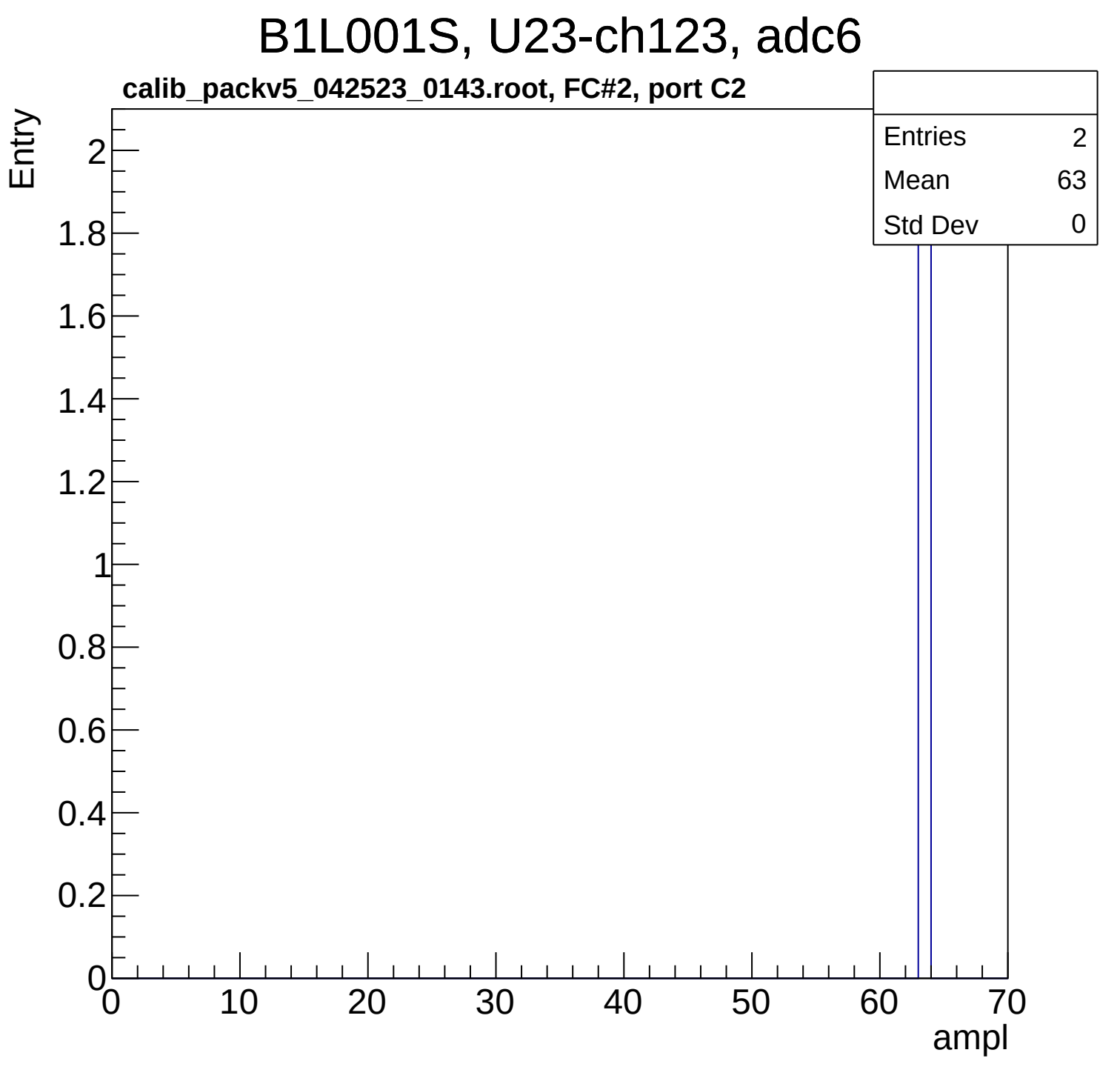
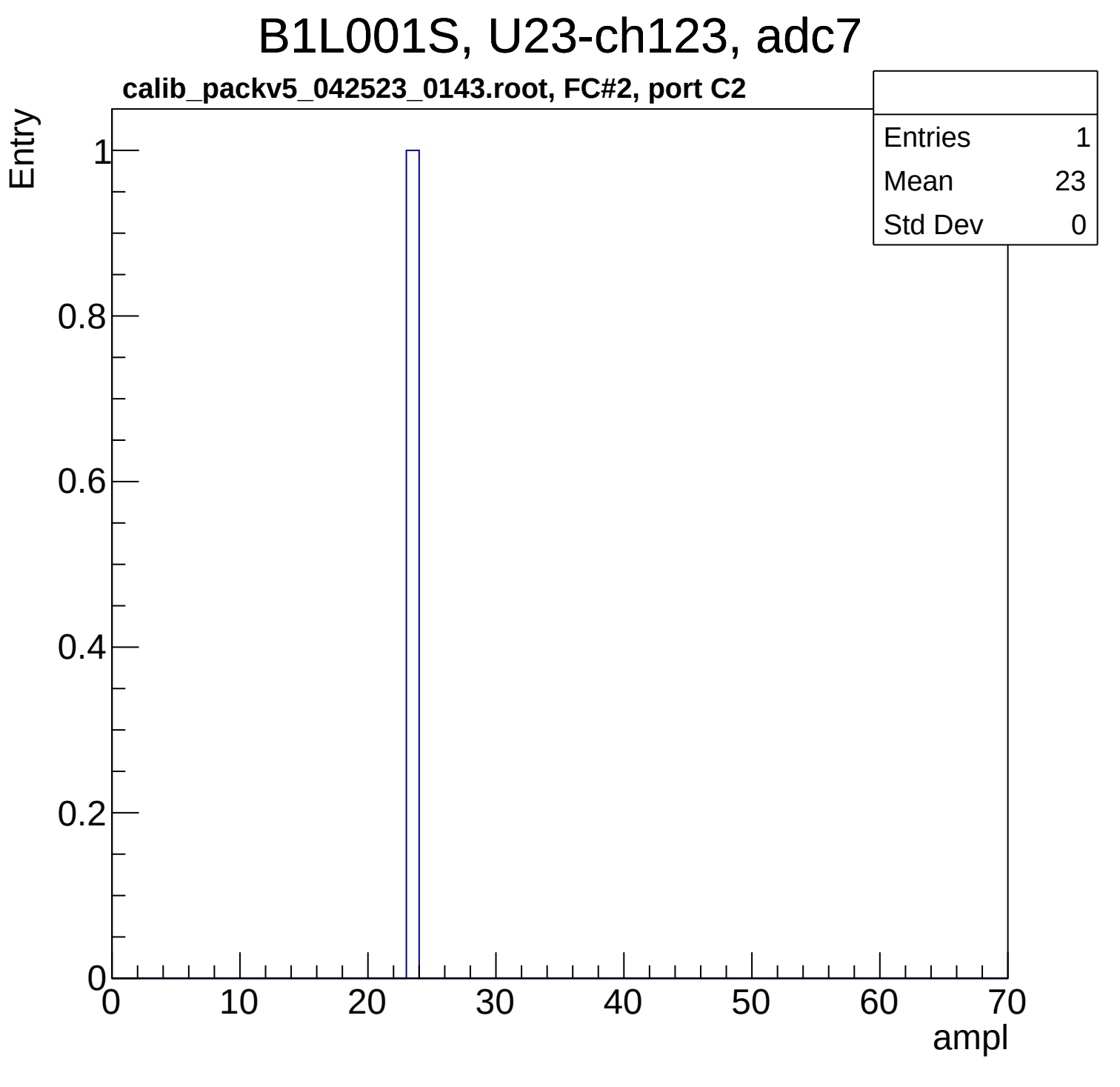

B1L001S, U23-ch123, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 36
Mean 59.67
Std Dev 10.24
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch123, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L001S, U23-ch123, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 1
Mean 23
Std Dev 0
Entry
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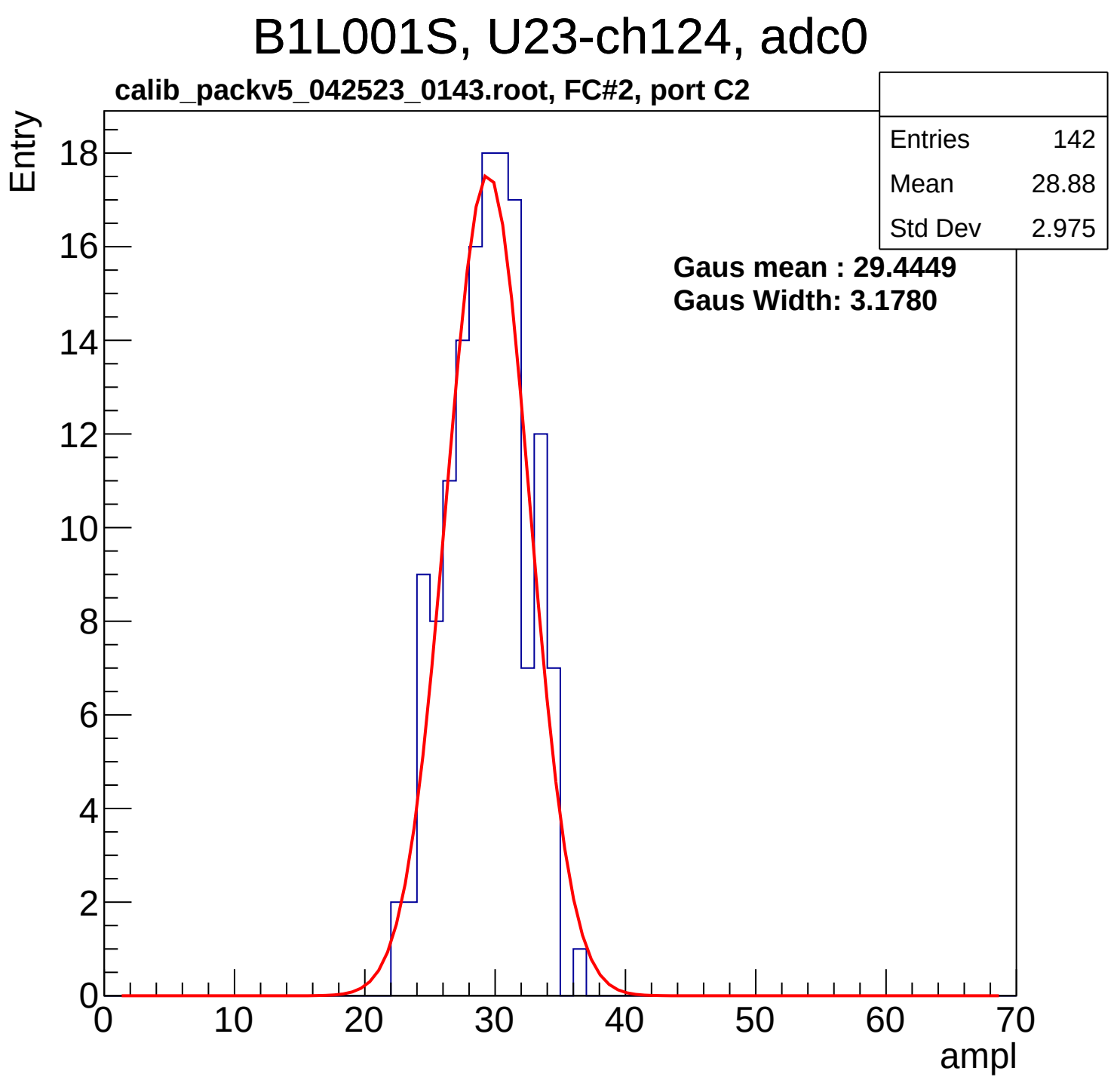
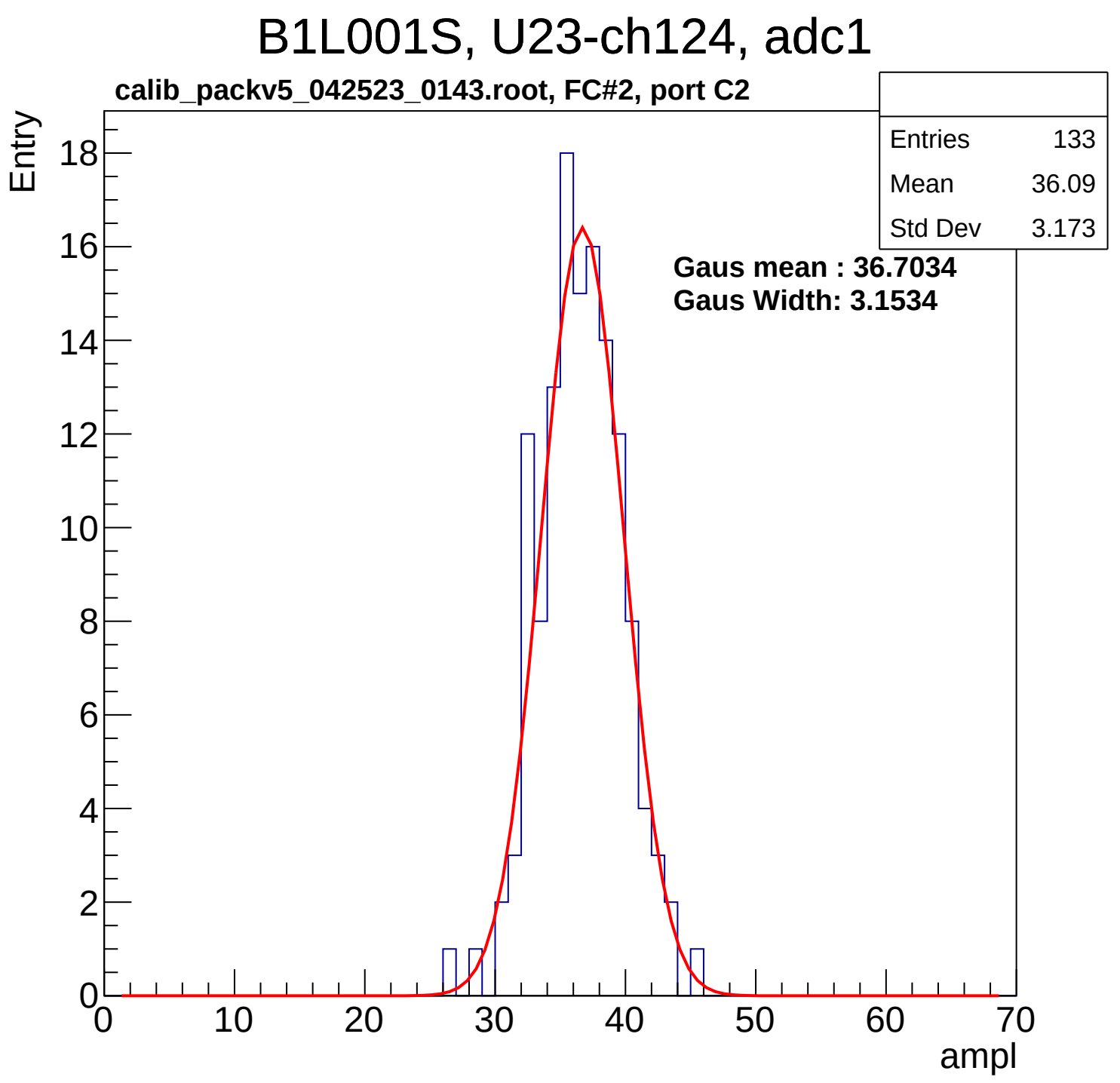
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch124, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 142
Mean 28.88
Std Dev 2.975
Gaus mean : 29.4449
Gaus Width: 3.1780
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch124, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 133
Mean 36.09
Std Dev 3.173
Gaus mean : 36.7034
Gaus Width: 3.1534
Entry
ampl
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2
4
6
8
10
12
14
16
18
0
10
20
30
40
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60
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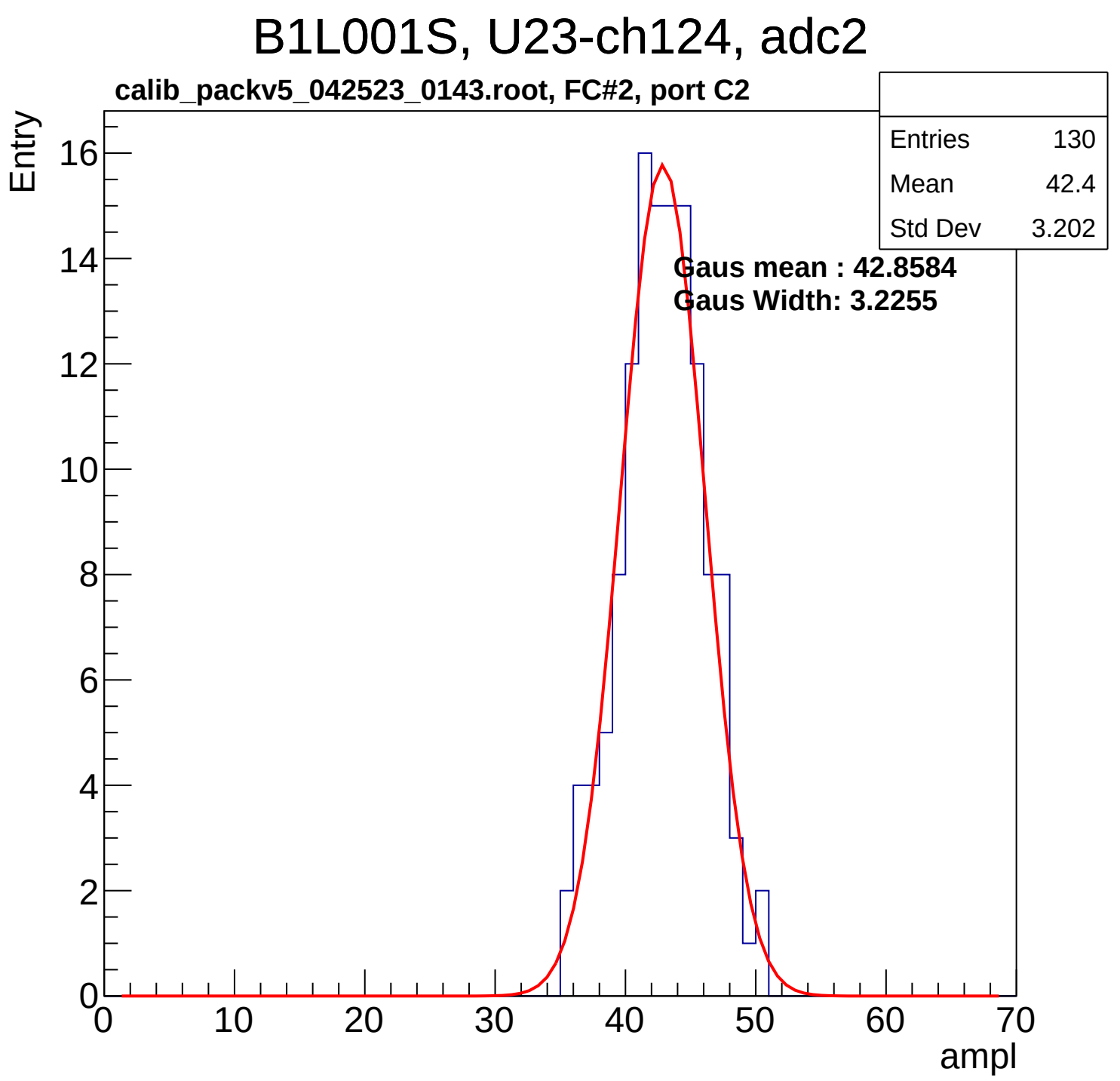
B1L001S, U23-ch124, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 42.4
Std Dev 3.202
Gaus mean : 42.8584
Gaus Width: 3.2255
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
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40
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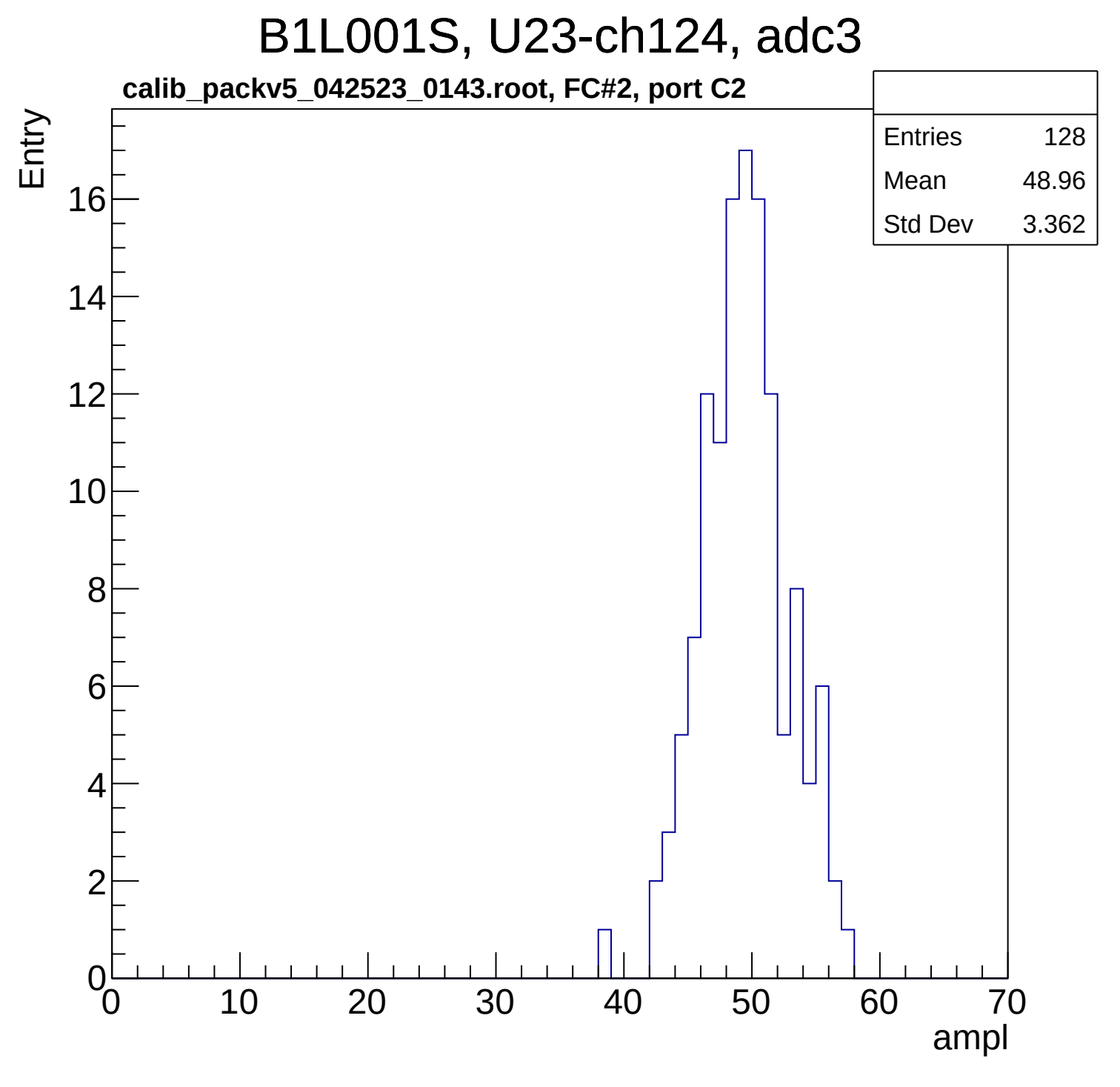
B1L001S, U23-ch124, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 128
Mean 48.96
Std Dev 3.362
Entry
ampl
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2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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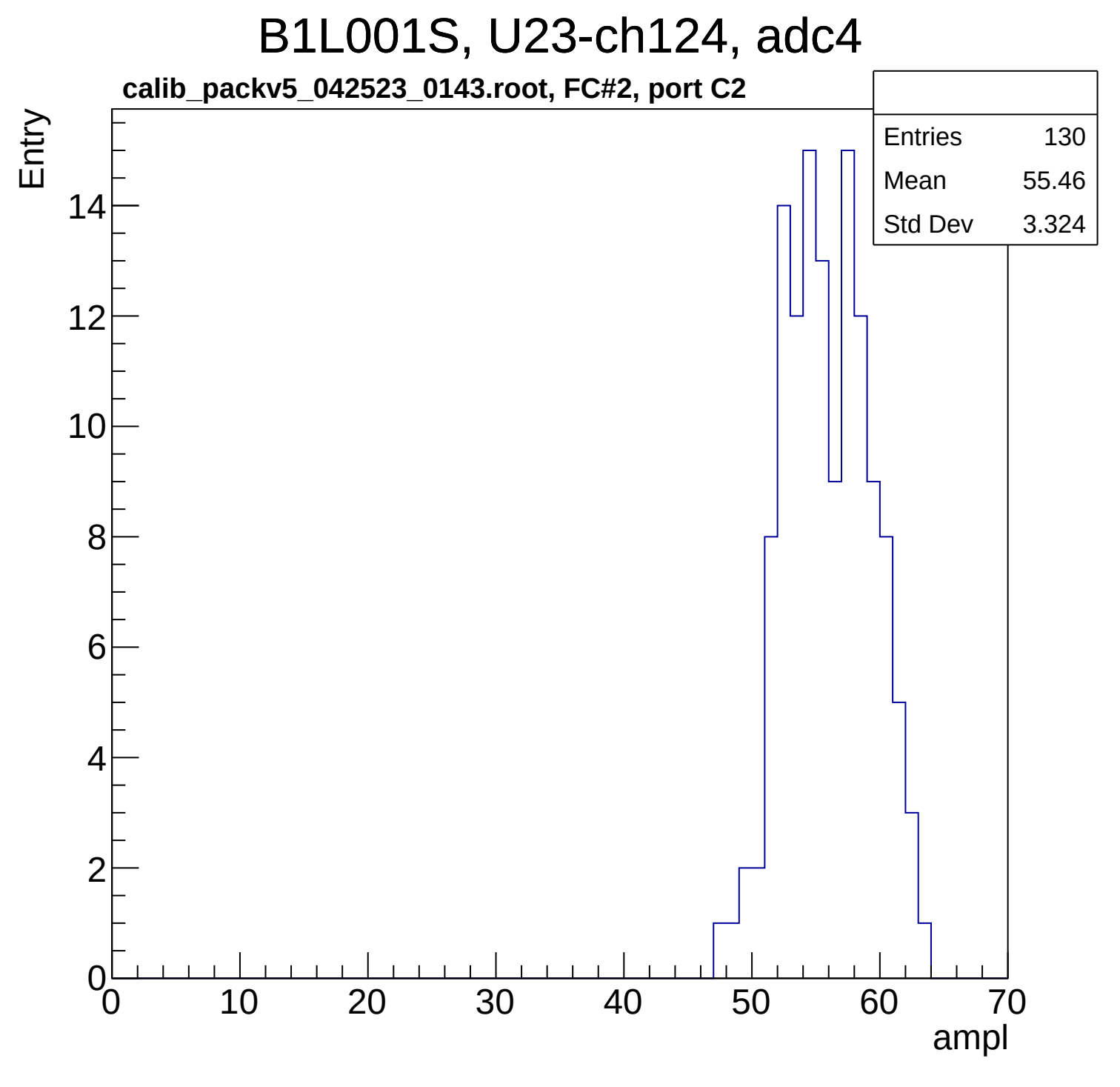
B1L001S, U23-ch124, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 130
Mean 55.46
Std Dev 3.324
Entry
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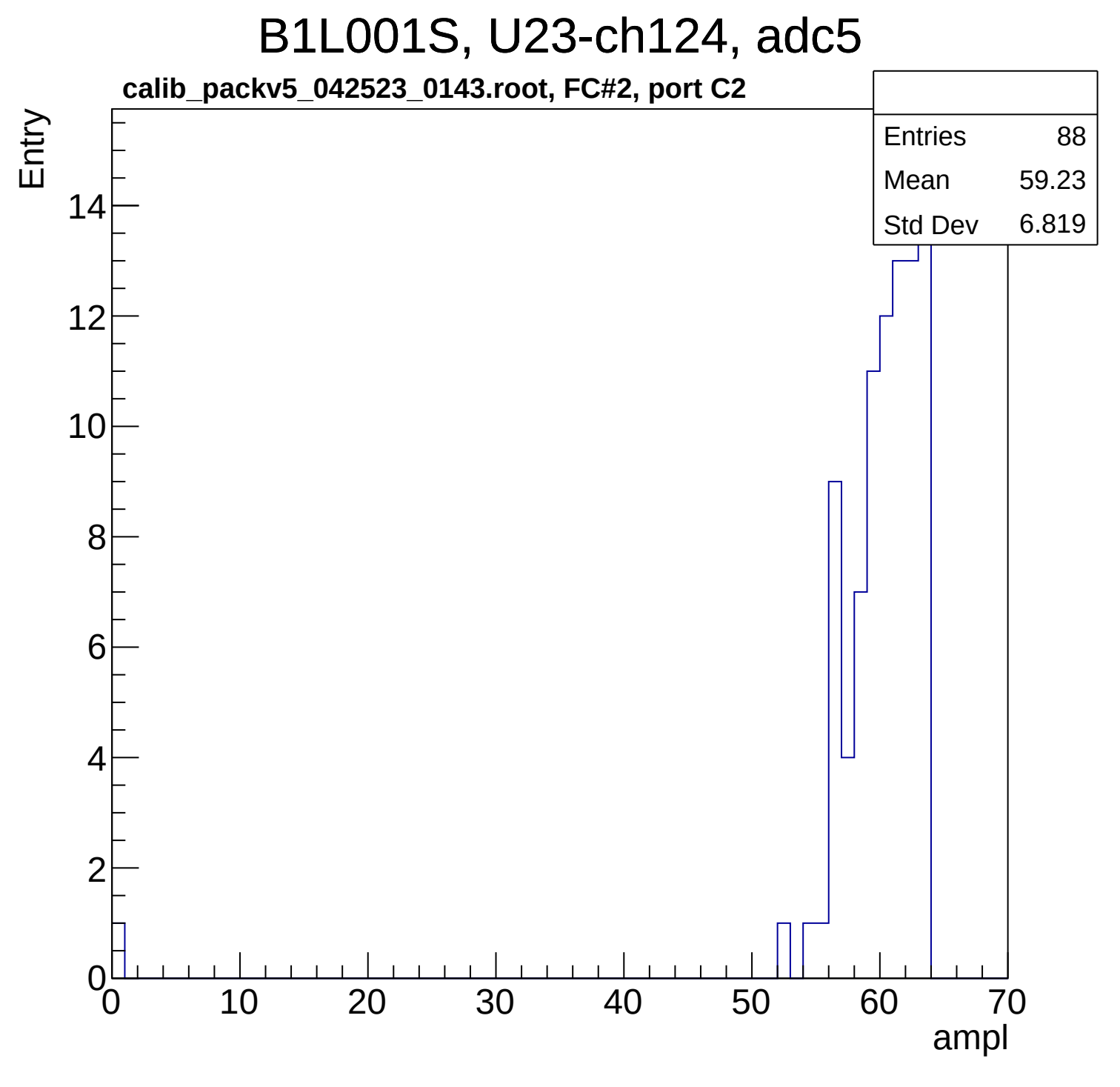
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch124, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 88
Mean 59.23
Std Dev 6.819
Entry
ampl
0
2
4
6
8
10
12
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0
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20
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40
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60
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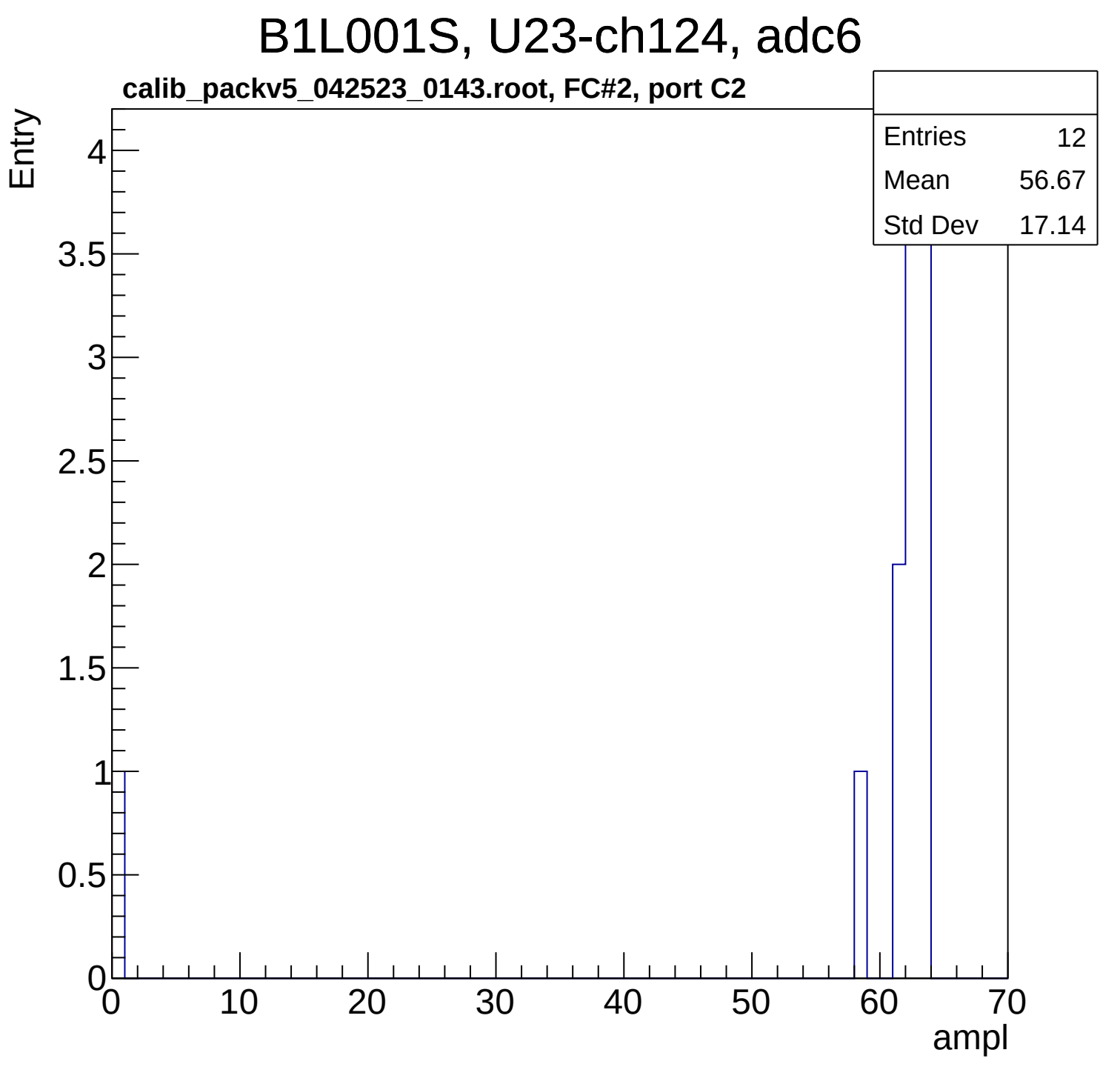
B1L001S, U23-ch124, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 56.67
Std Dev 17.14
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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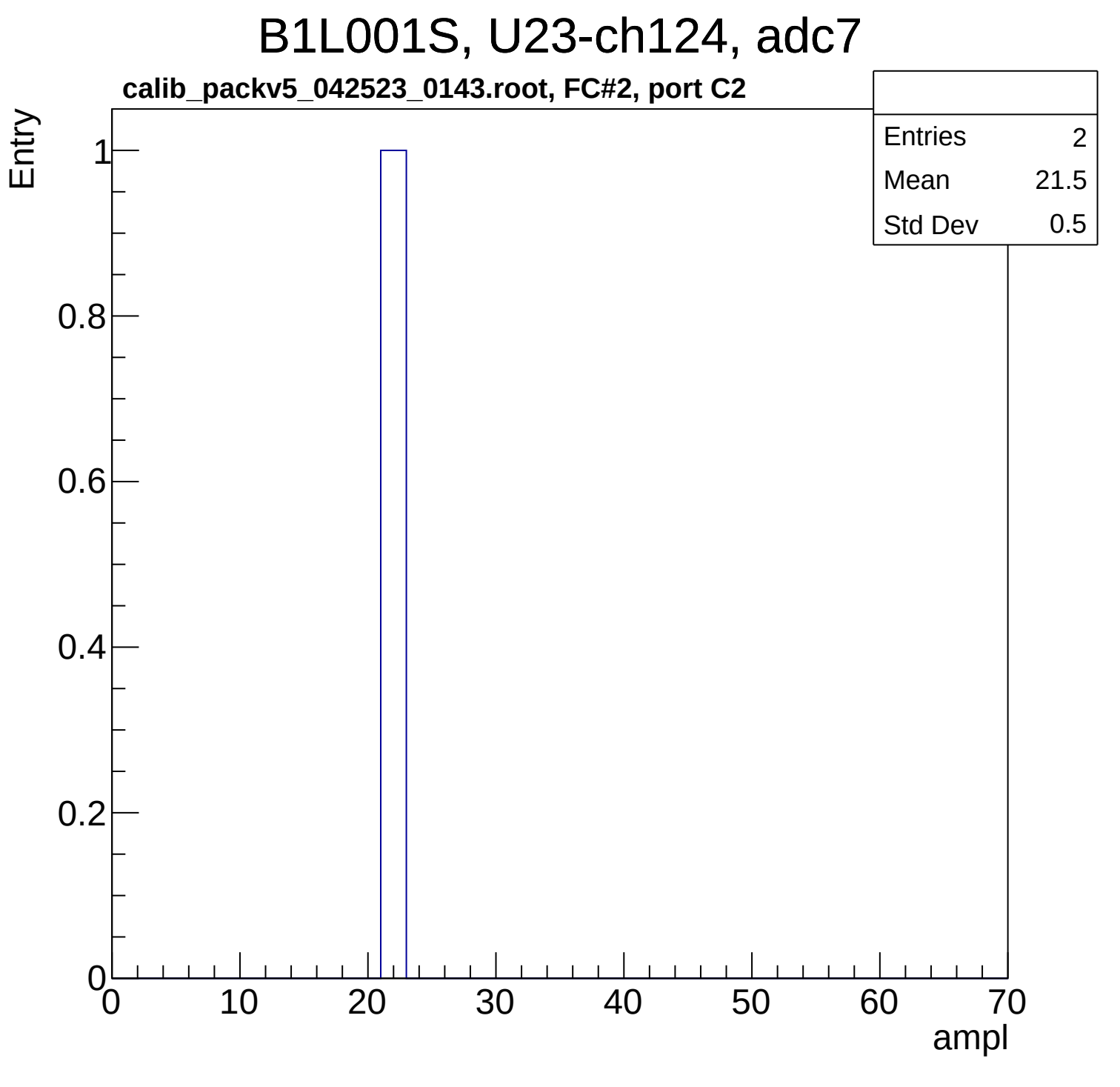
B1L001S, U23-ch124, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 21.5
Std Dev 0.5
Entry
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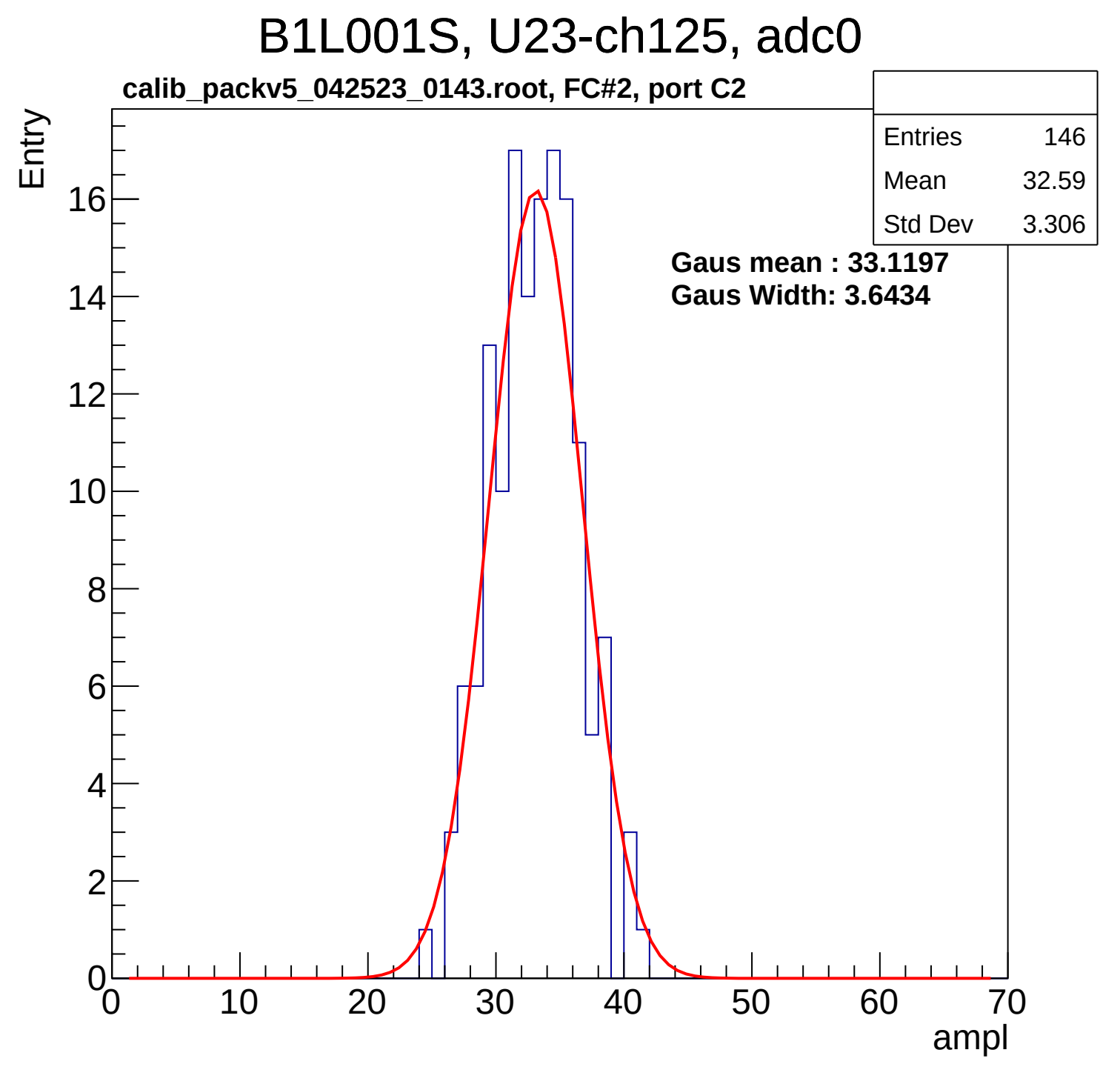
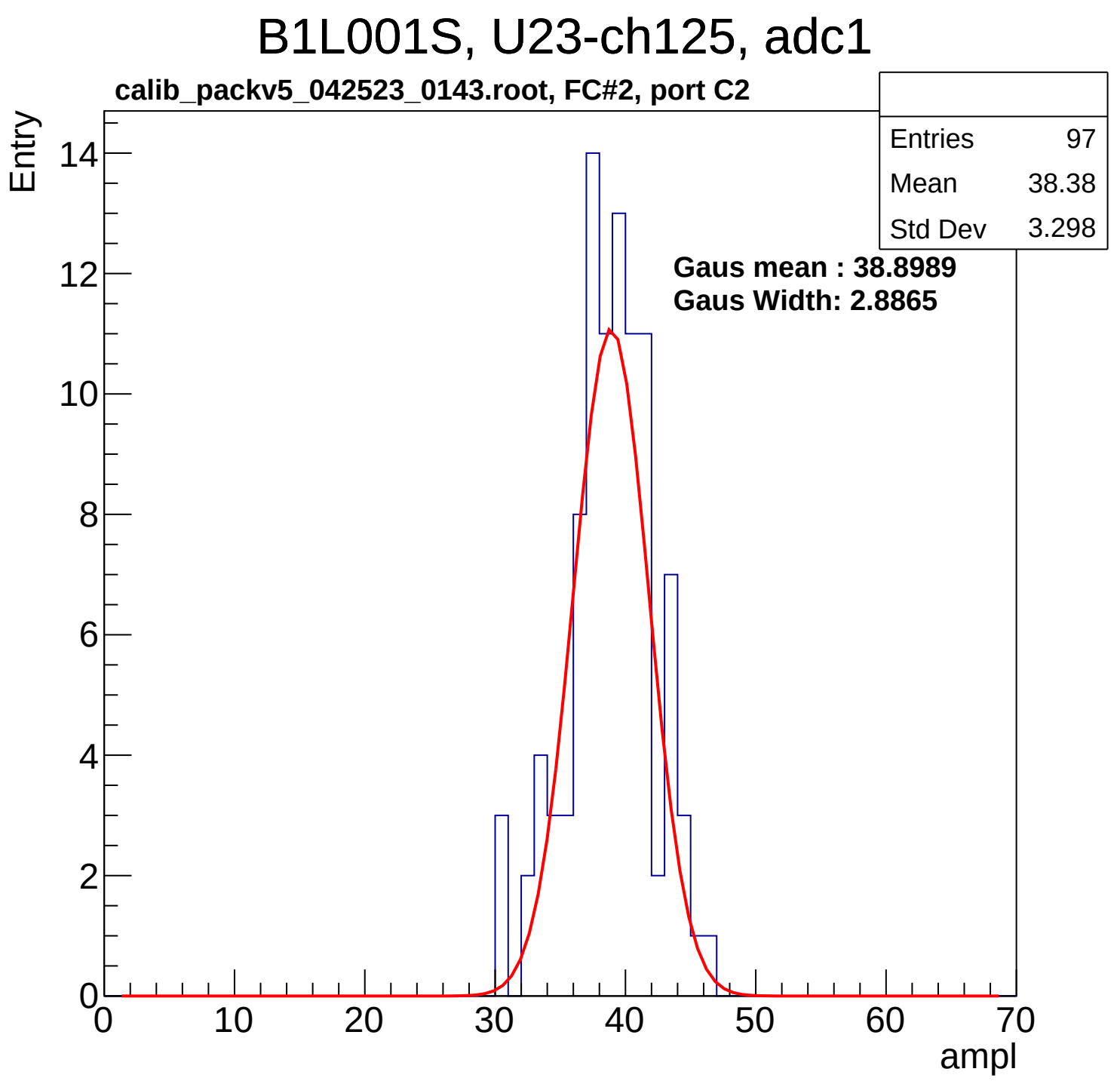
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch125, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 146
Mean 32.59
Std Dev 3.306
Gaus mean : 33.1197
Gaus Width: 3.6434
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch125, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 97
Mean 38.38
Std Dev 3.298
Gaus mean : 38.8989
Gaus Width: 2.8865
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
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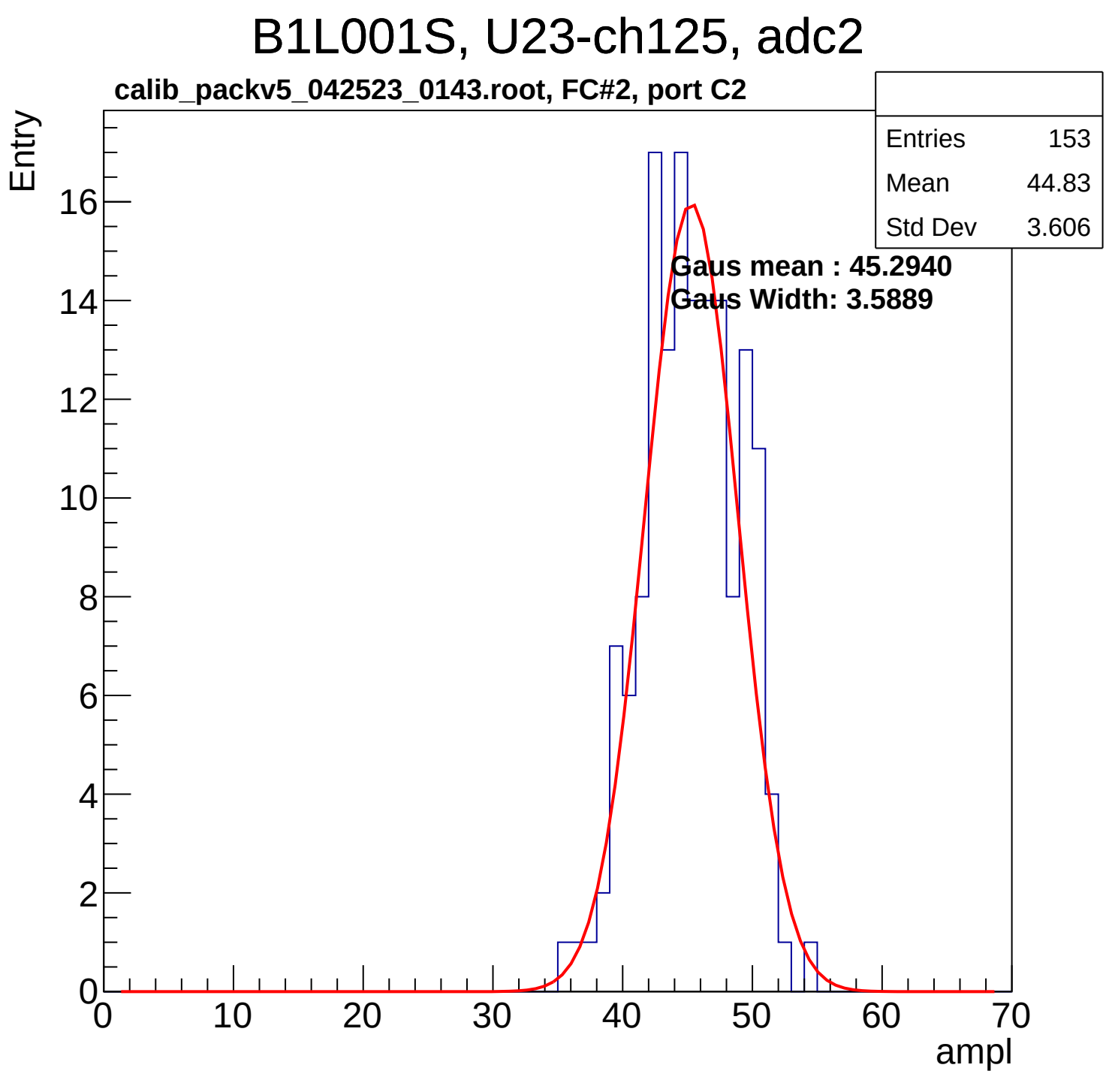
40
50
60
70


B1L001S, U23-ch125, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 153
Mean 44.83
Std Dev 3.606
Gaus mean : 45.2940
Gaus Width: 3.5889
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
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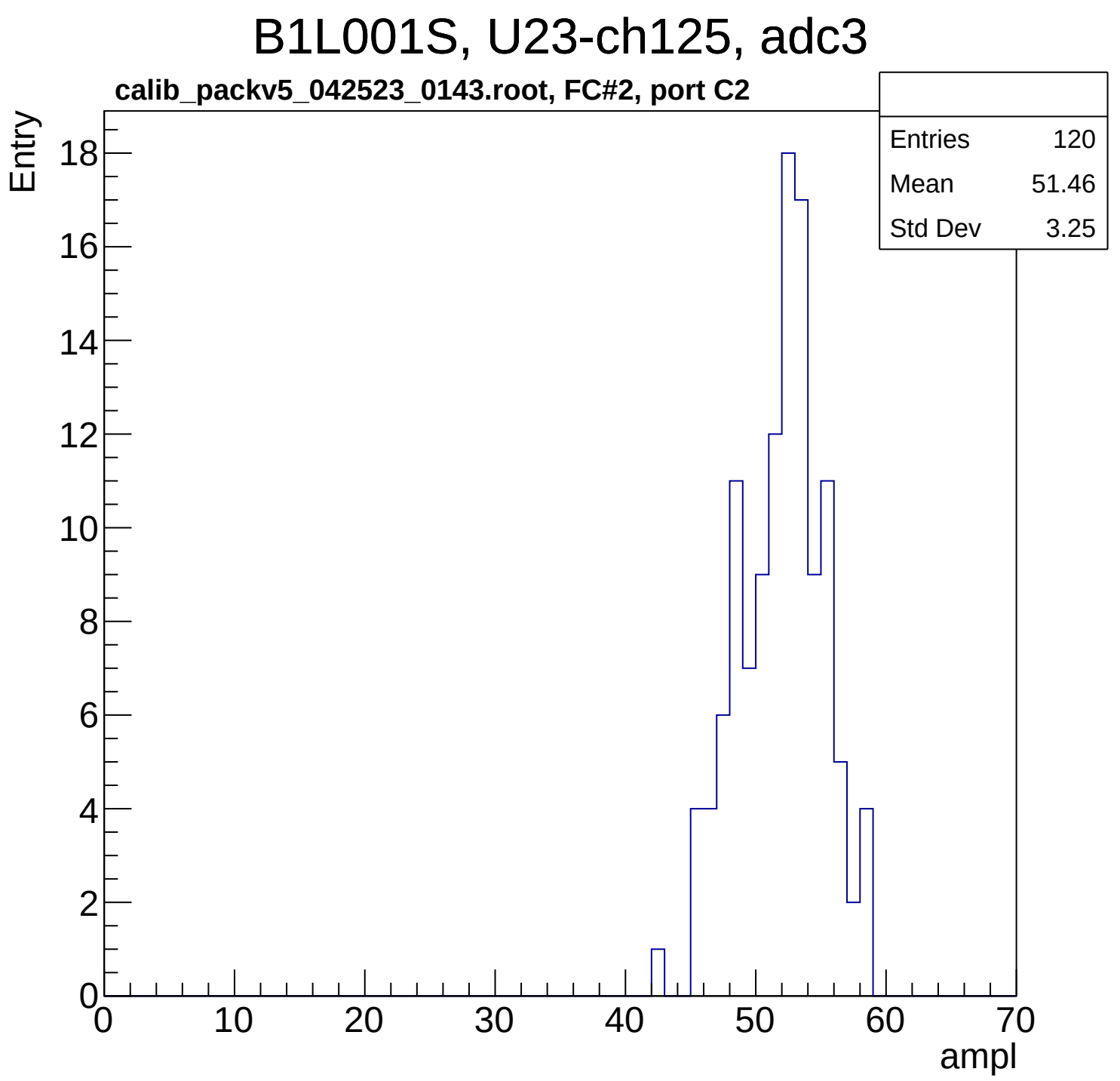
B1L001S, U23-ch125, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 51.46
Std Dev 3.25
Entry
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8
10
12
14
16
18
0
10
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30
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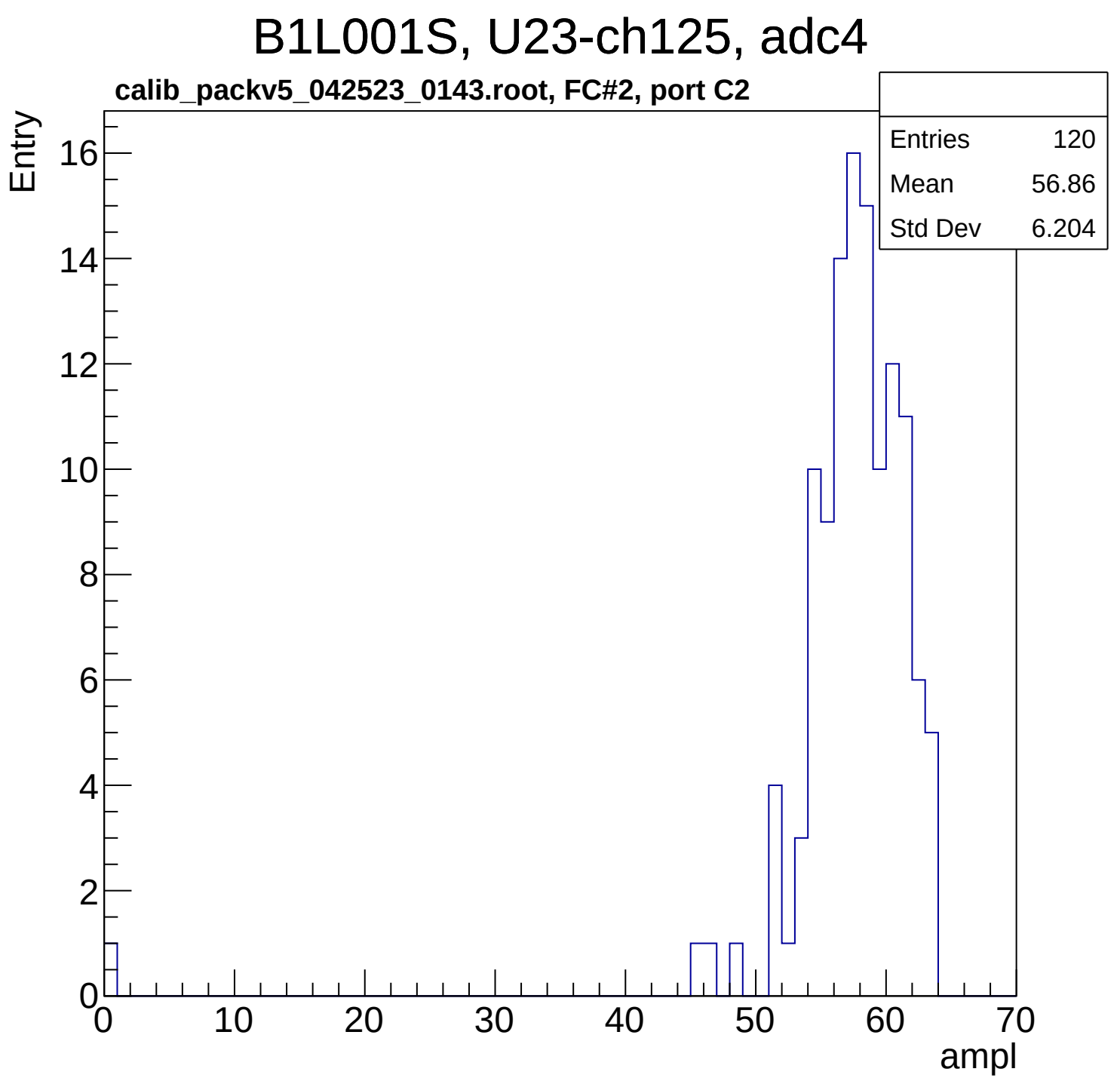
50
60
70
ampl


B1L001S, U23-ch125, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 120
Mean 56.86
Std Dev 6.204
Entry
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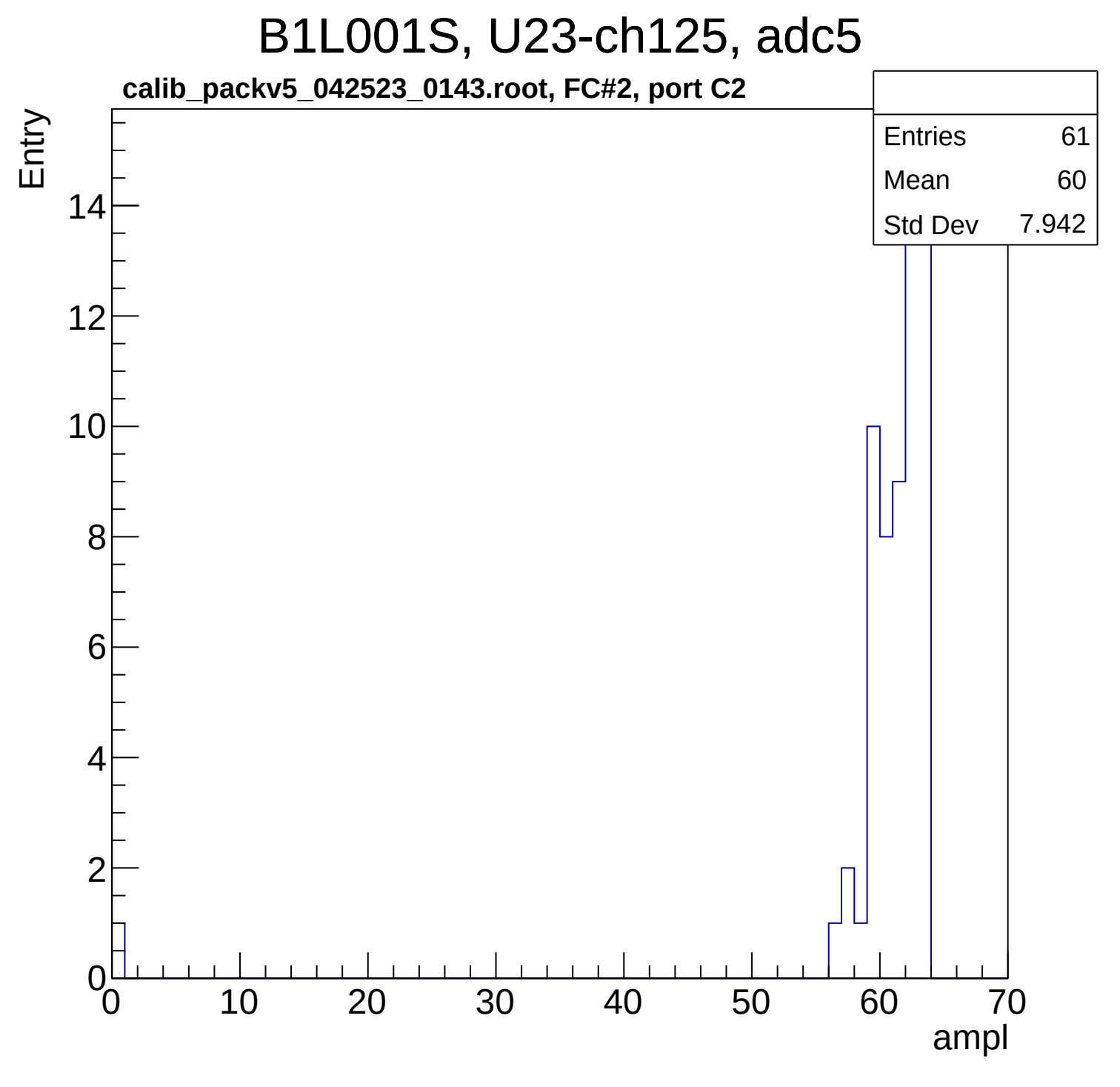
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch125, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 61
Mean 60
Std Dev 7.942
Entry
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2
4
6
8
10
12
14
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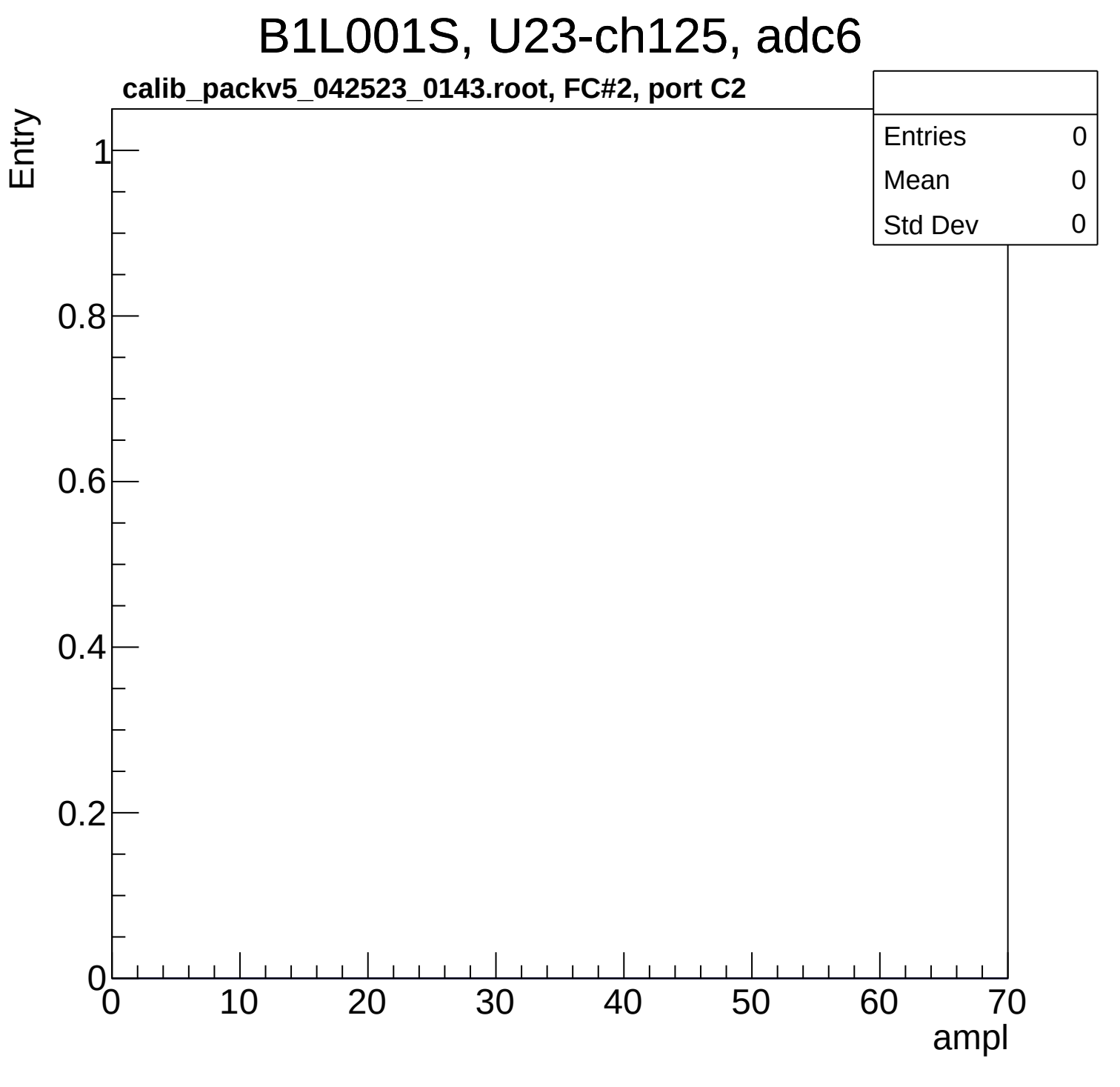
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch125, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
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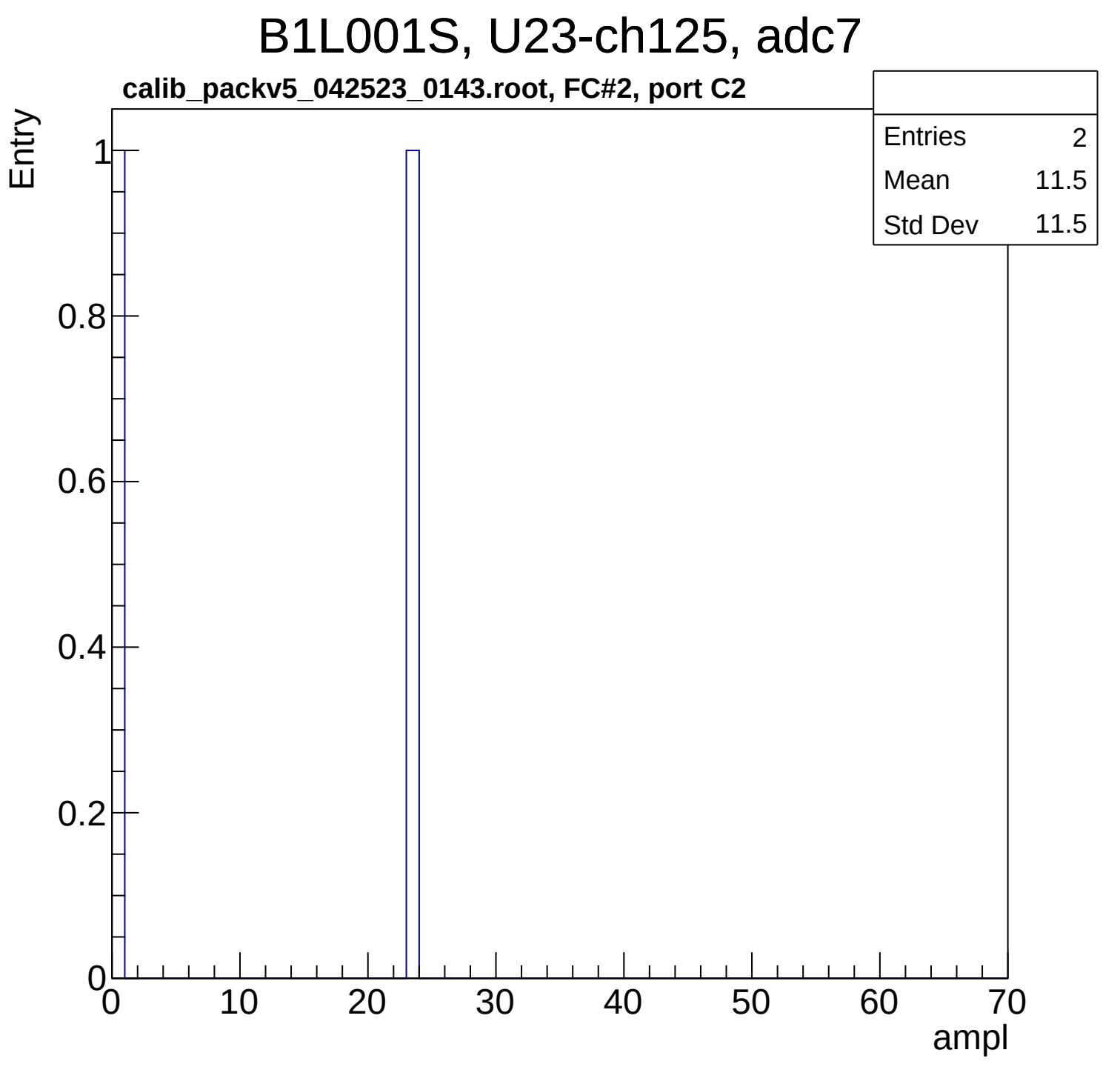
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch125, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
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0.2
0.4
0.6
0.8
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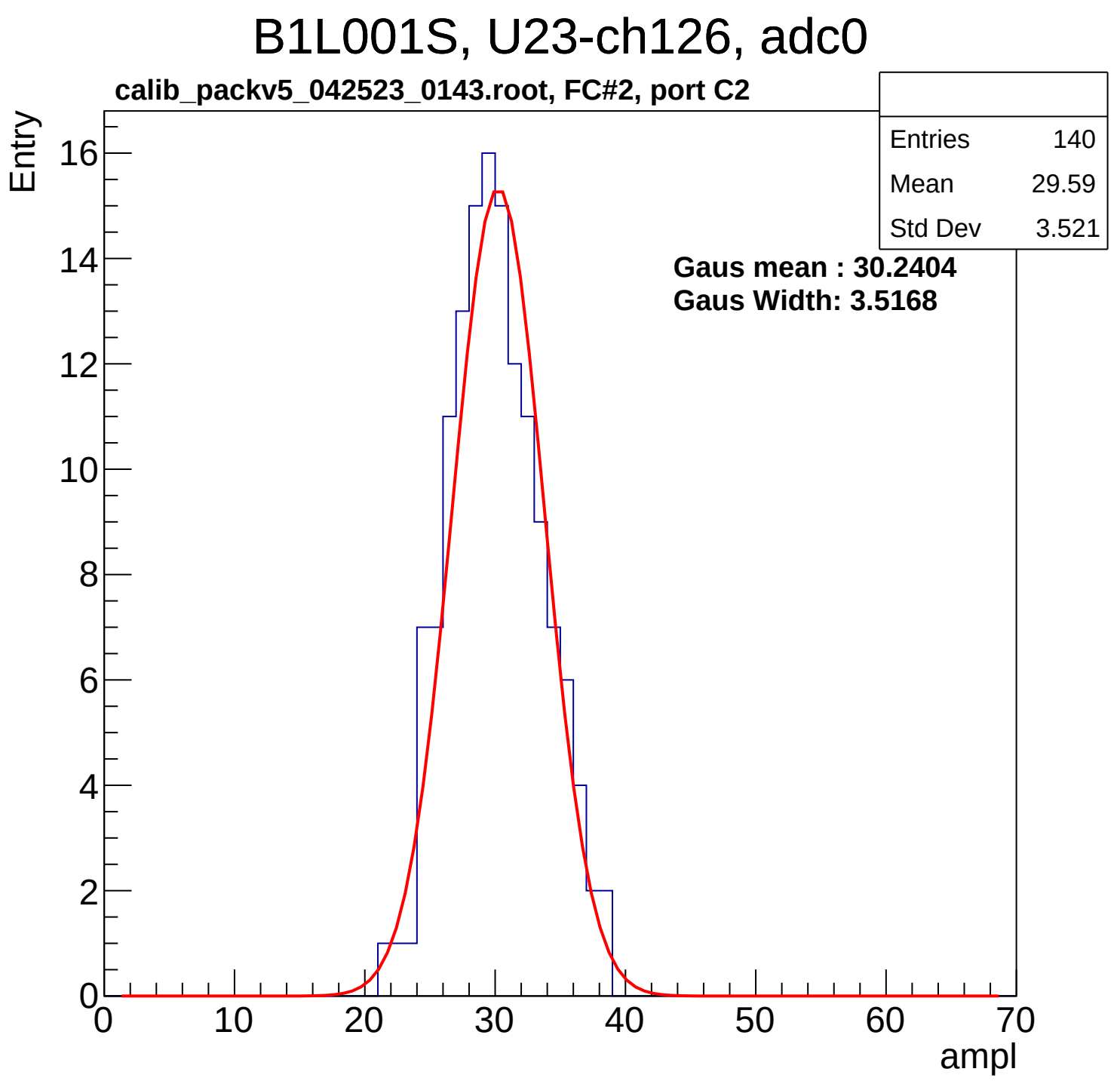
B1L001S, U23-ch126, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 140
Mean 29.59
Std Dev 3.521
Gaus mean : 30.2404
Gaus Width: 3.5168
Entry
ampl
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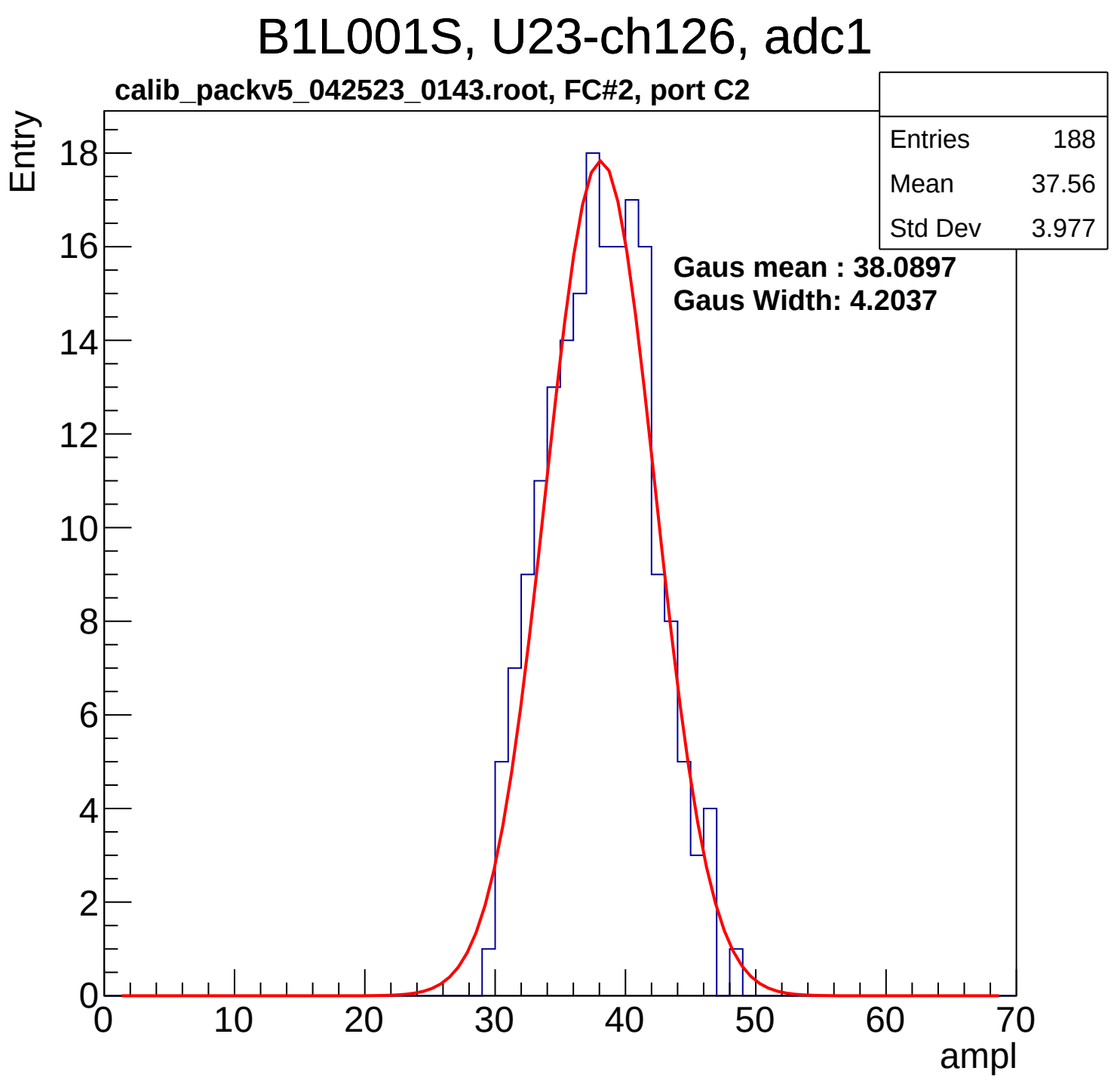
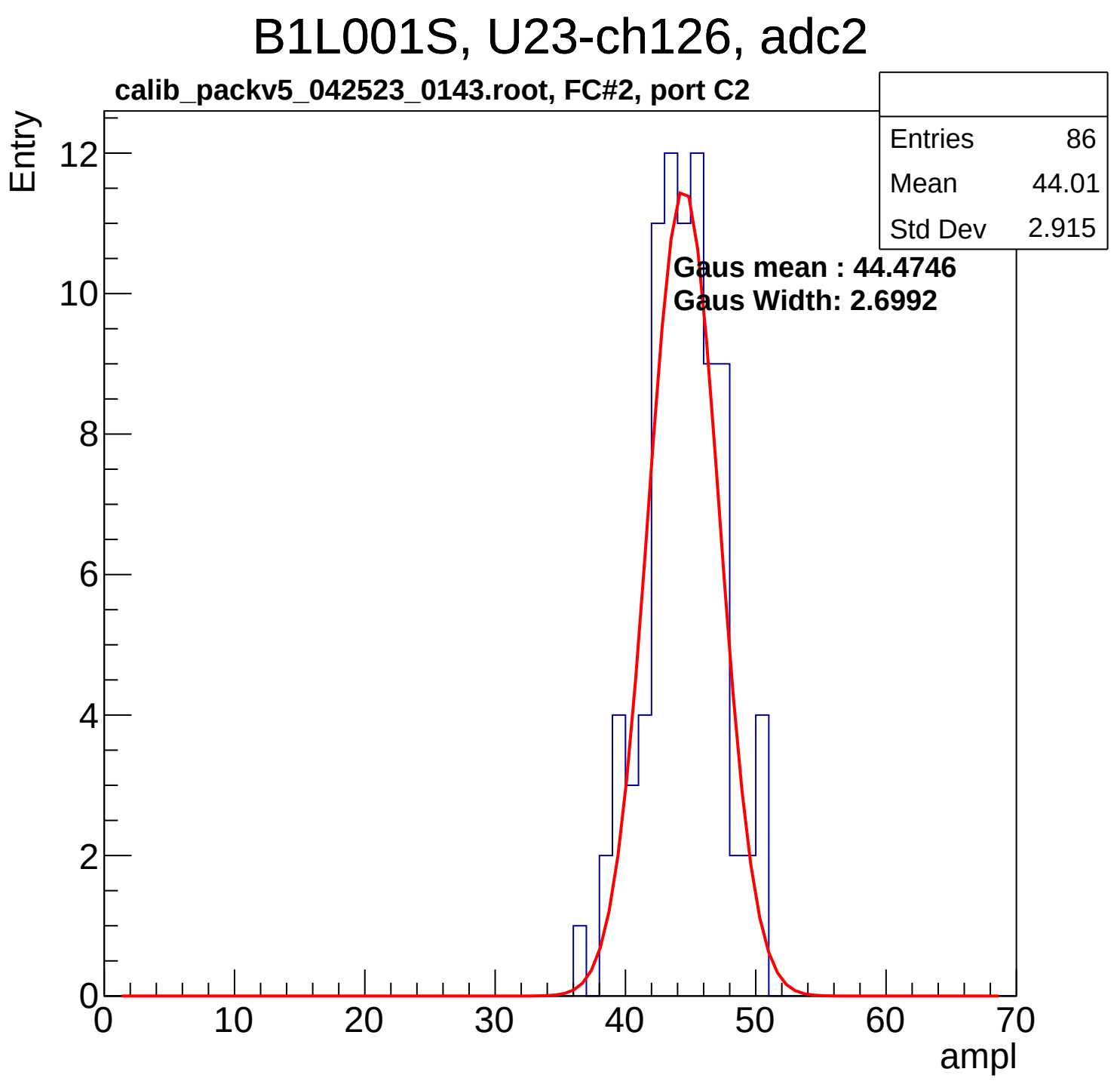
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70


B1L001S, U23-ch126, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 188
Mean 37.56
Std Dev 3.977
Gaus mean : 38.0897
Gaus Width: 4.2037
Entry
ampl
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70


B1L001S, U23-ch126, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 86
Mean 44.01
Std Dev 2.915
Gaus mean : 44.4746
Gaus Width: 2.6992
Entry
ampl
0
2
4
6
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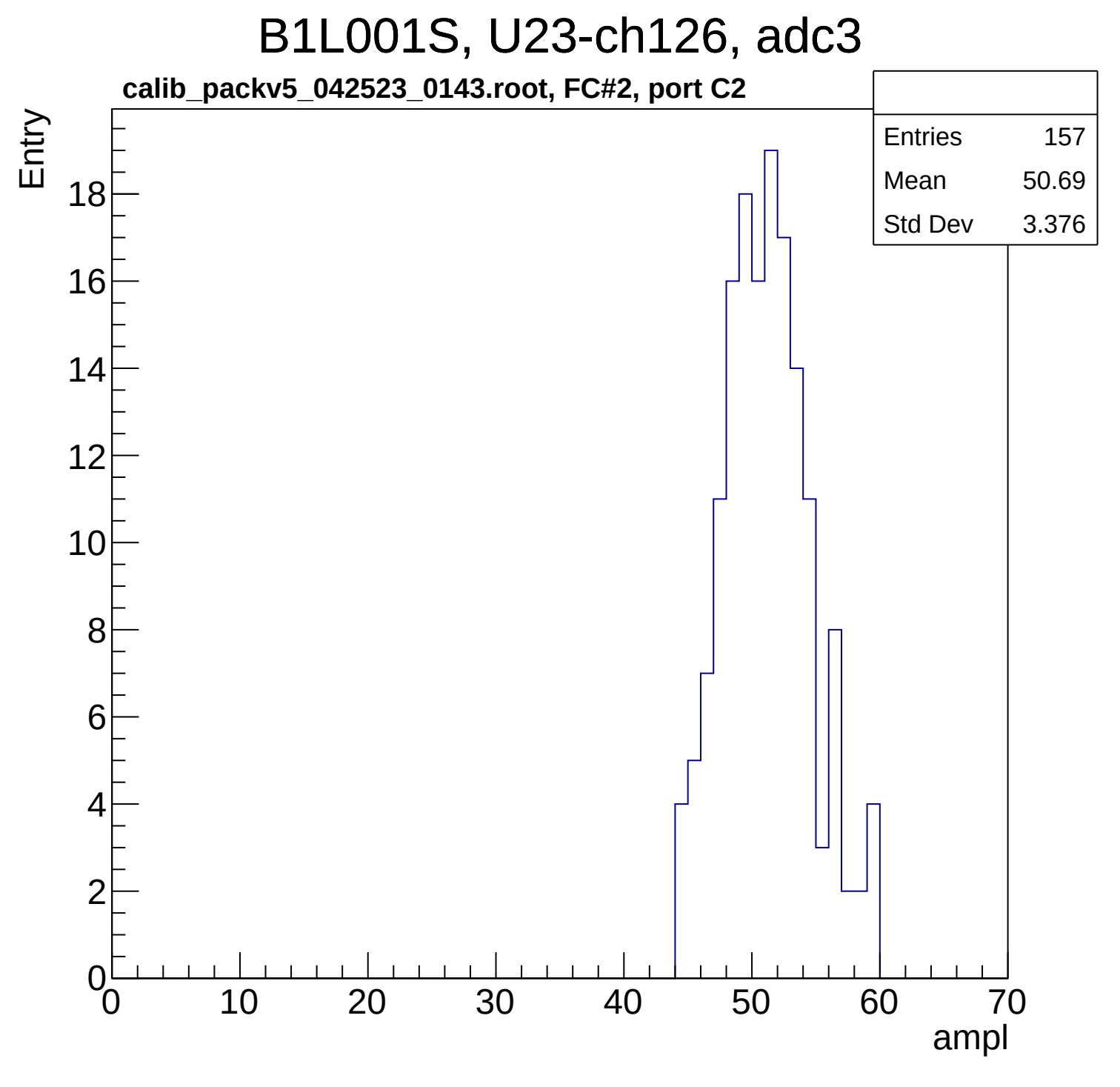
B1L001S, U23-ch126, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 157
Mean 50.69
Std Dev 3.376
Entry
ampl
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8
10
12
14
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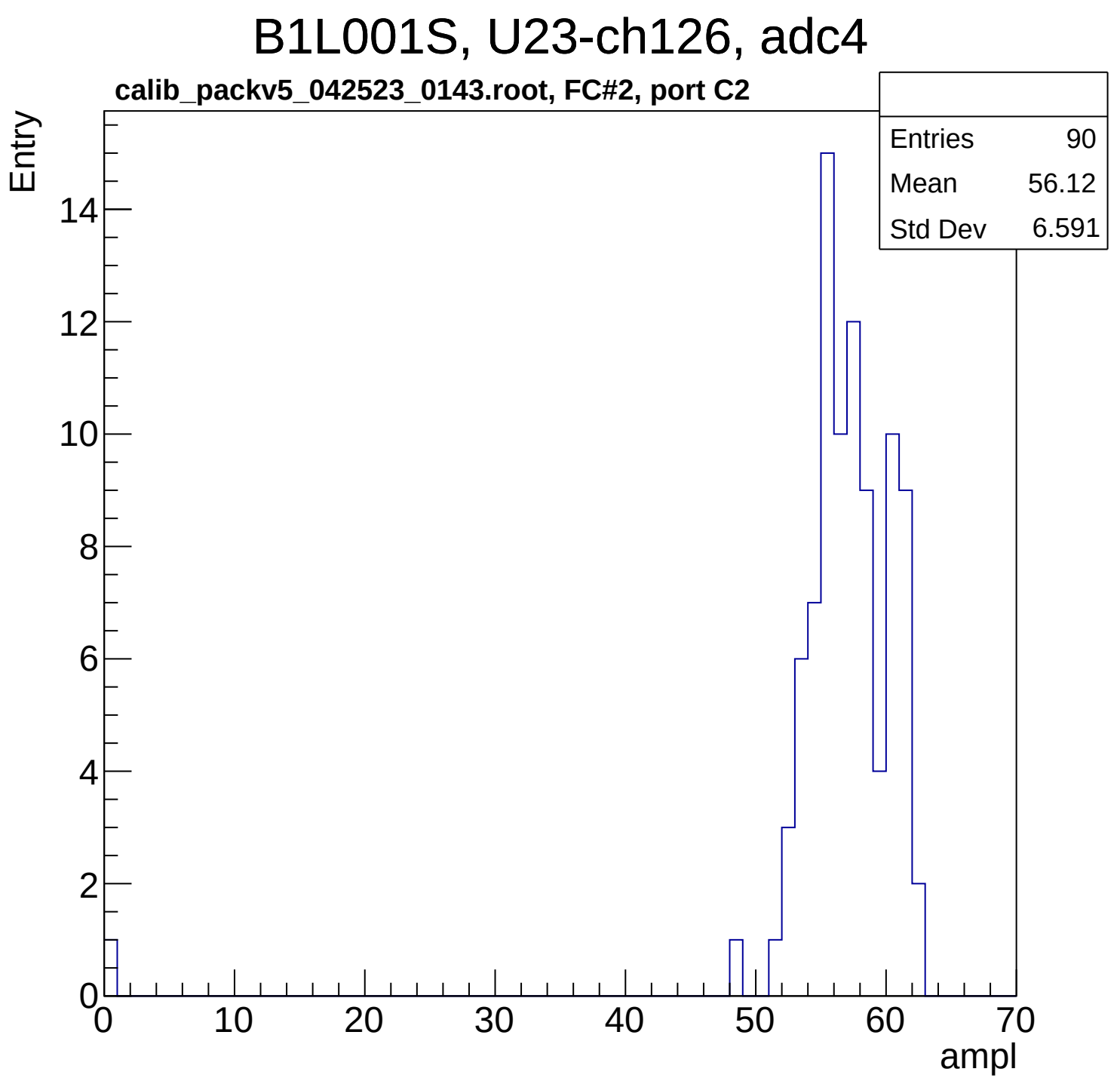
B1L001S, U23-ch126, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 90
Mean 56.12
Std Dev 6.591
Entry
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4
6
8
10
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10
20
30
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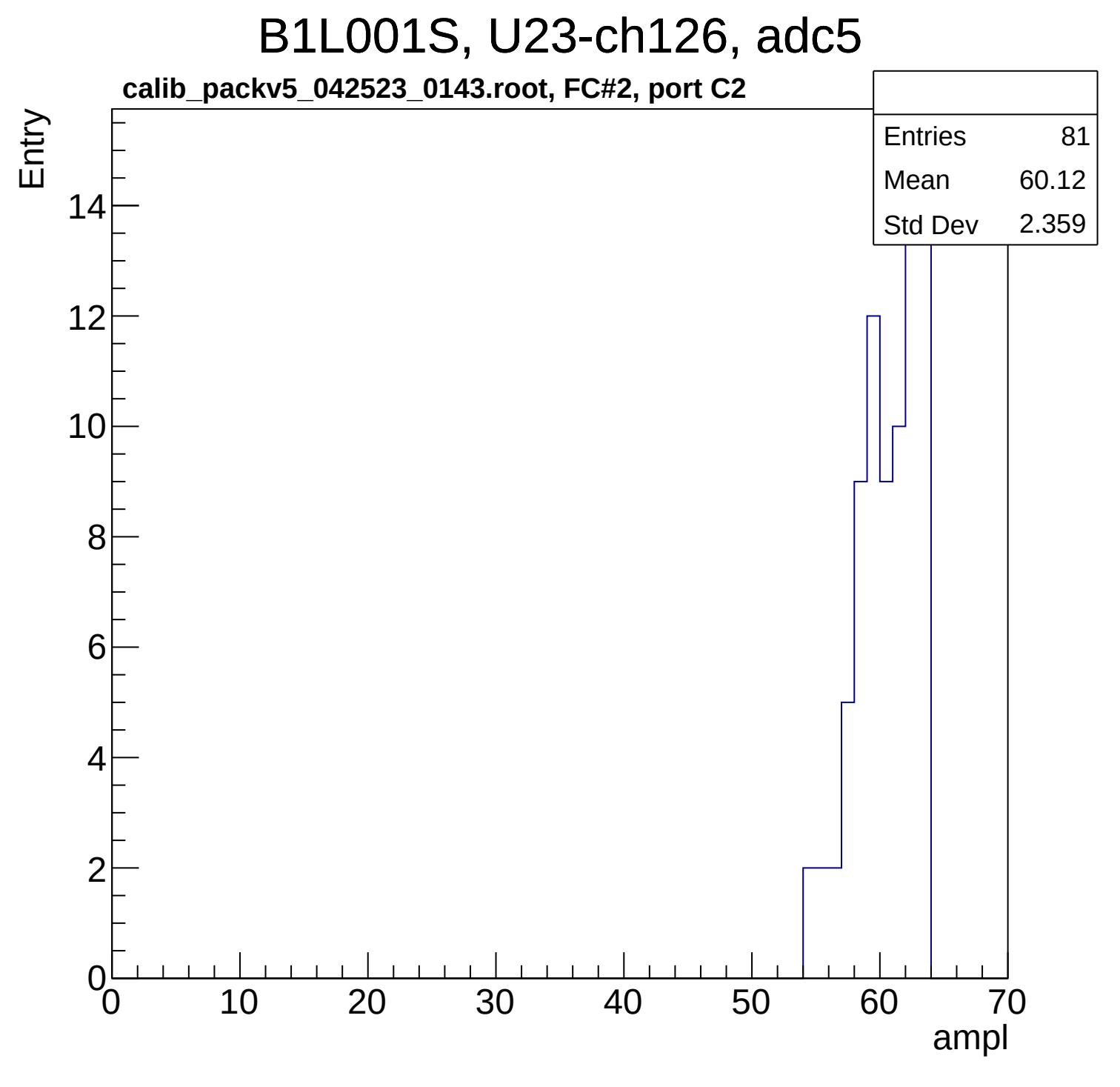
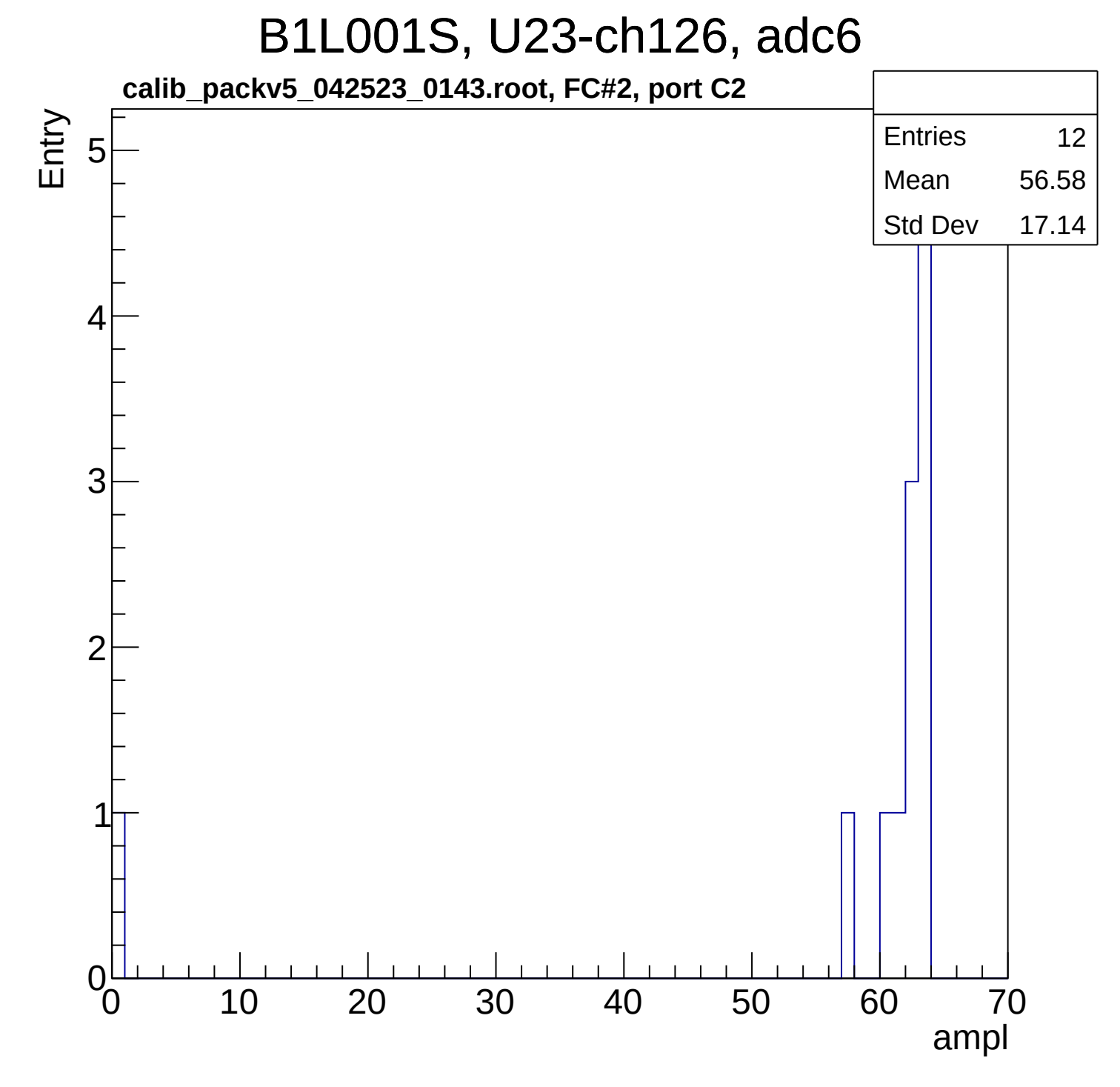
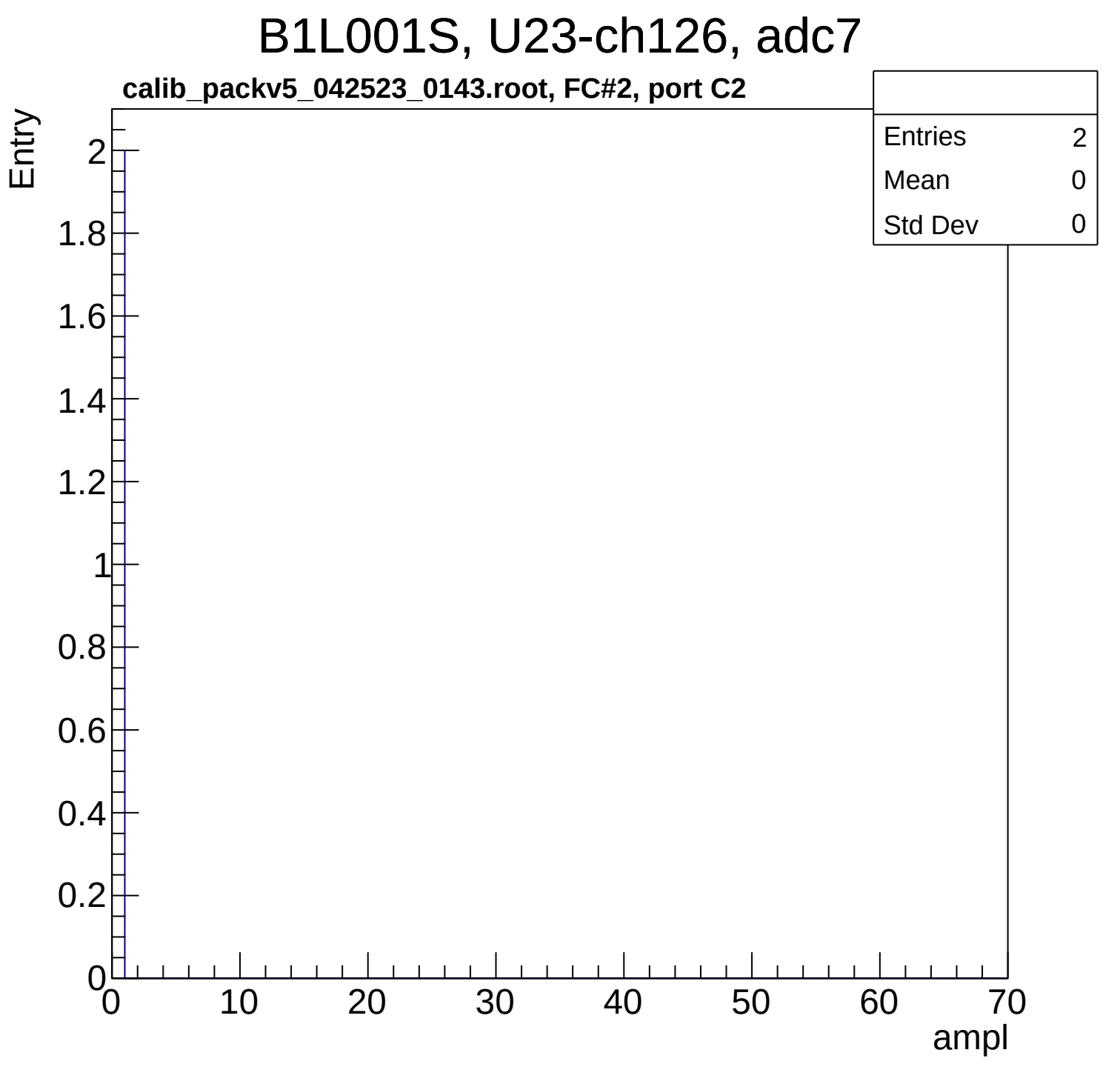
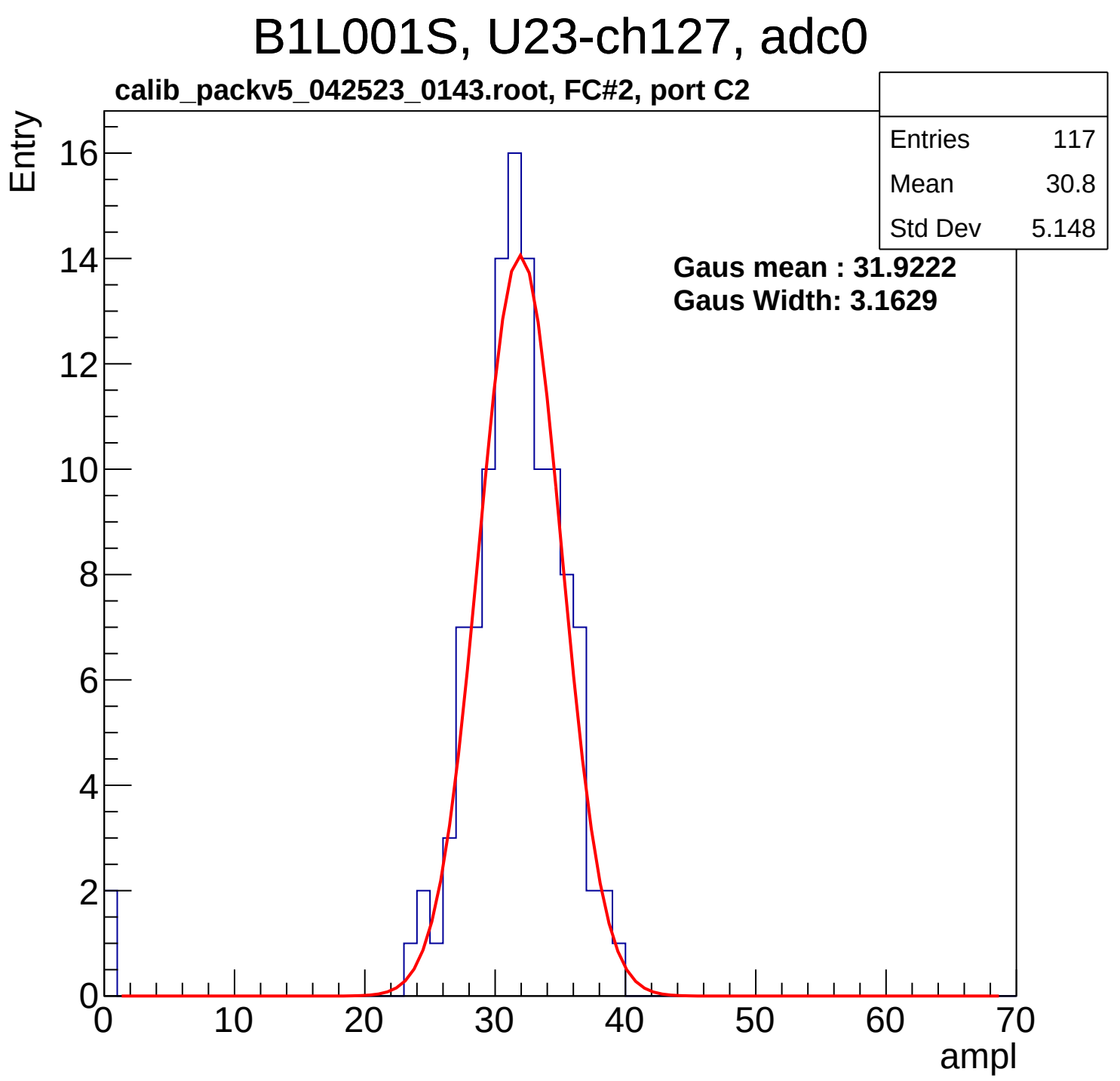
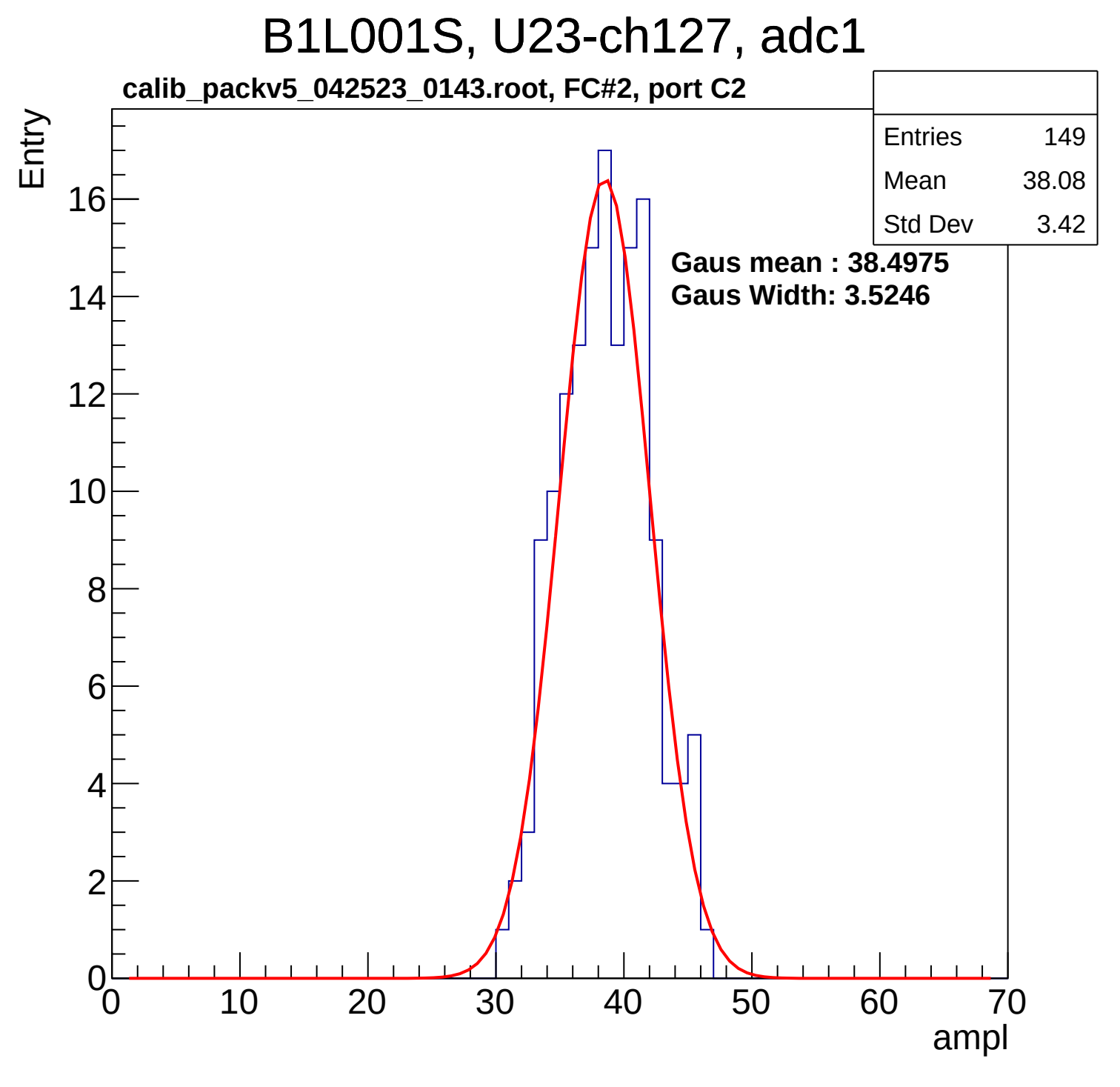
50
60
70
ampl

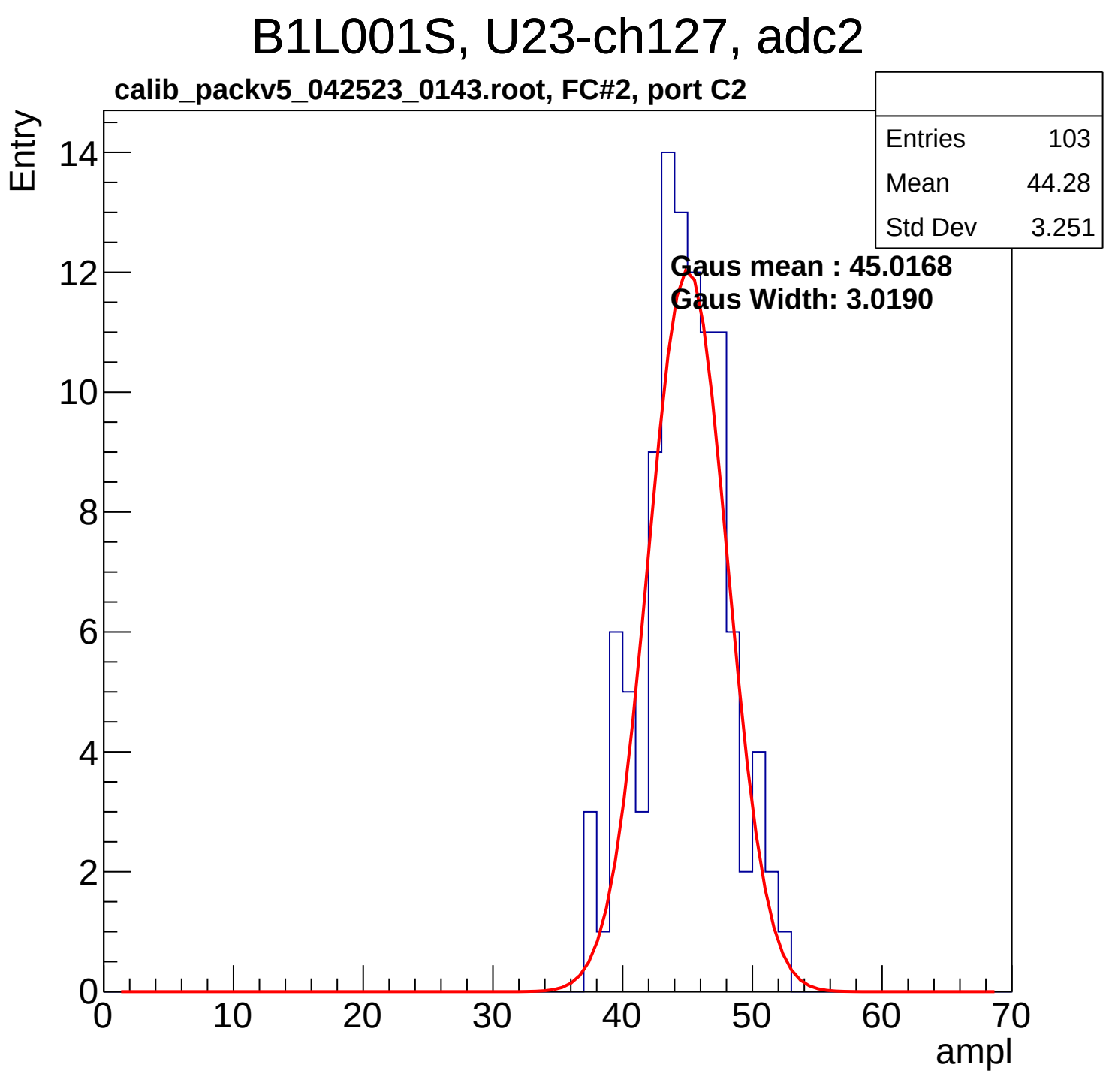

B1L001S, U23-ch126, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 81
Mean 60.12
Std Dev 2.359
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

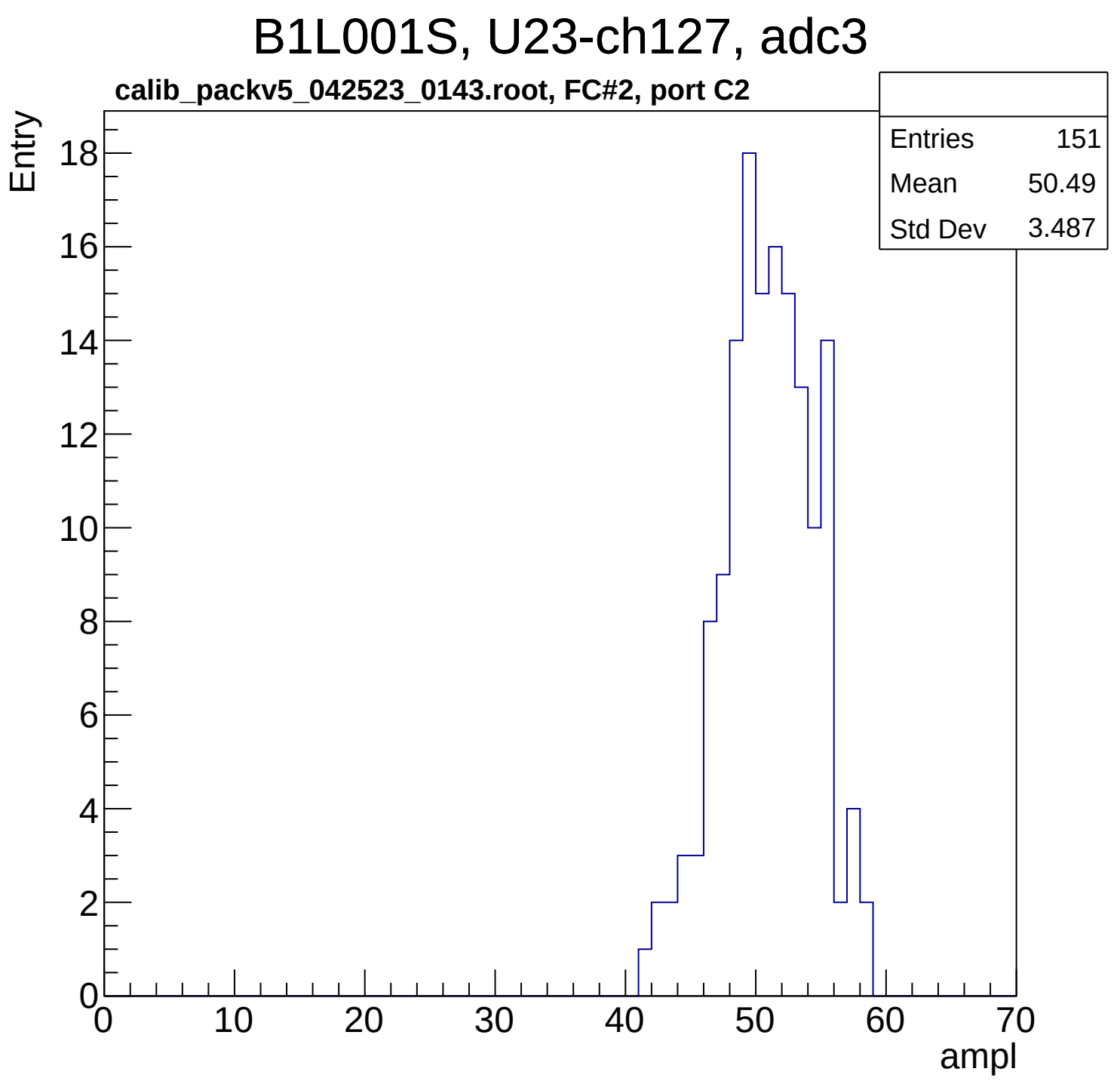

B1L001S, U23-ch126, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 12
Mean 56.58
Std Dev 17.14
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

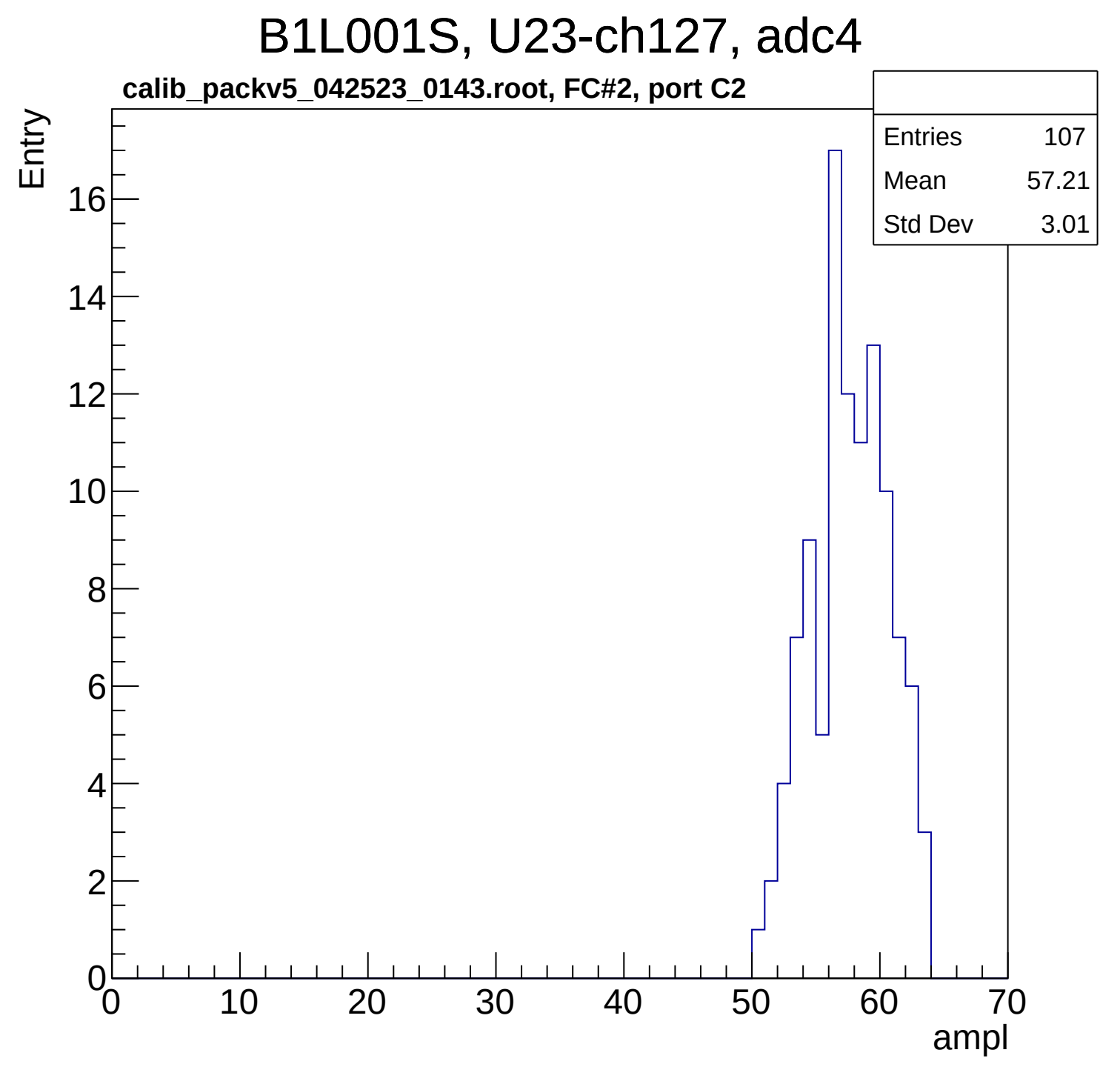

B1L001S, U23-ch126, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

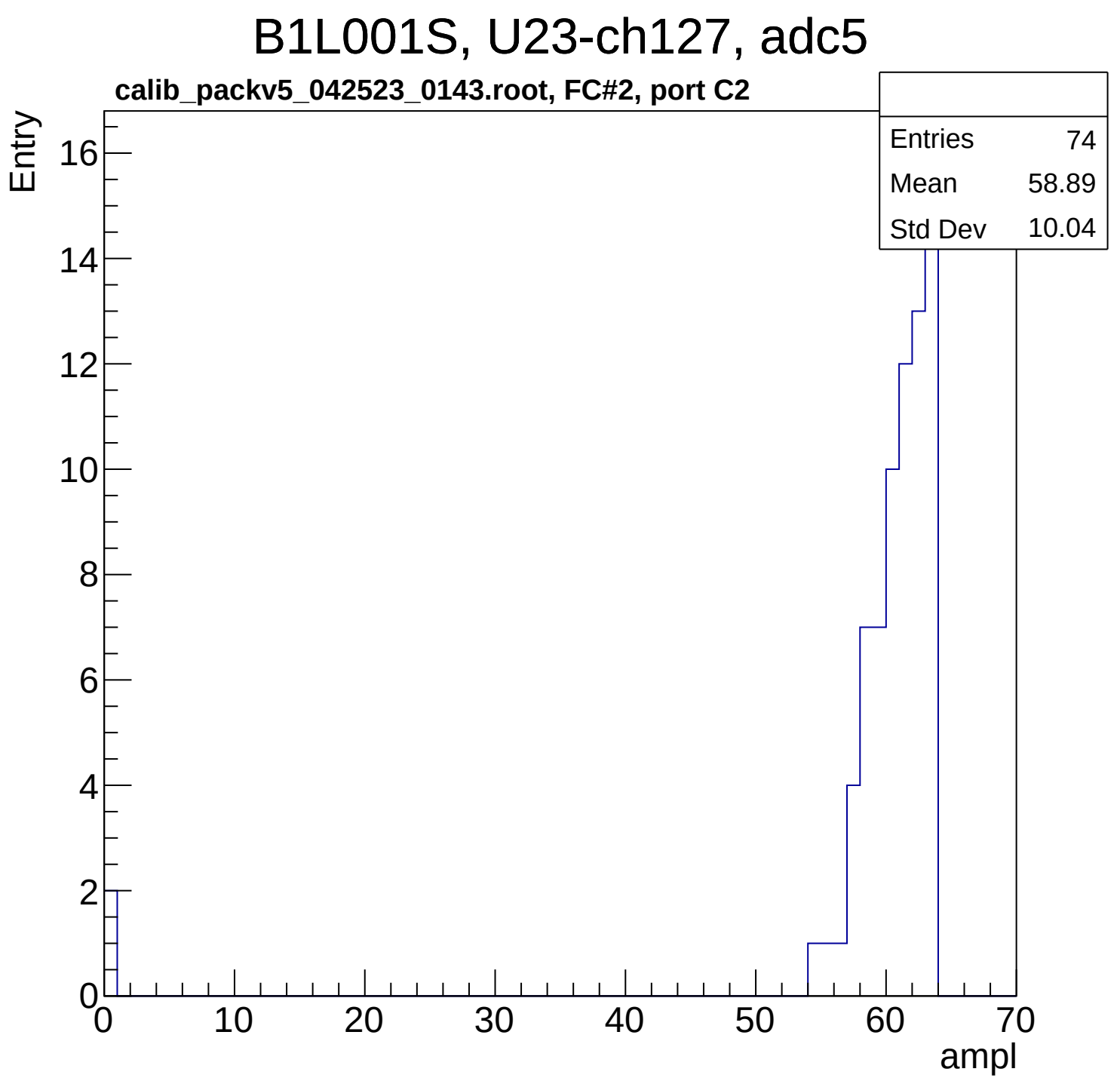

B1L001S, U23-ch127, adc0
calib_packv5_042523_0143.root, FC#2, port C2
Entries 117
Mean 30.8
Std Dev 5.148
Gaus mean : 31.9222
Gaus Width: 3.1629
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

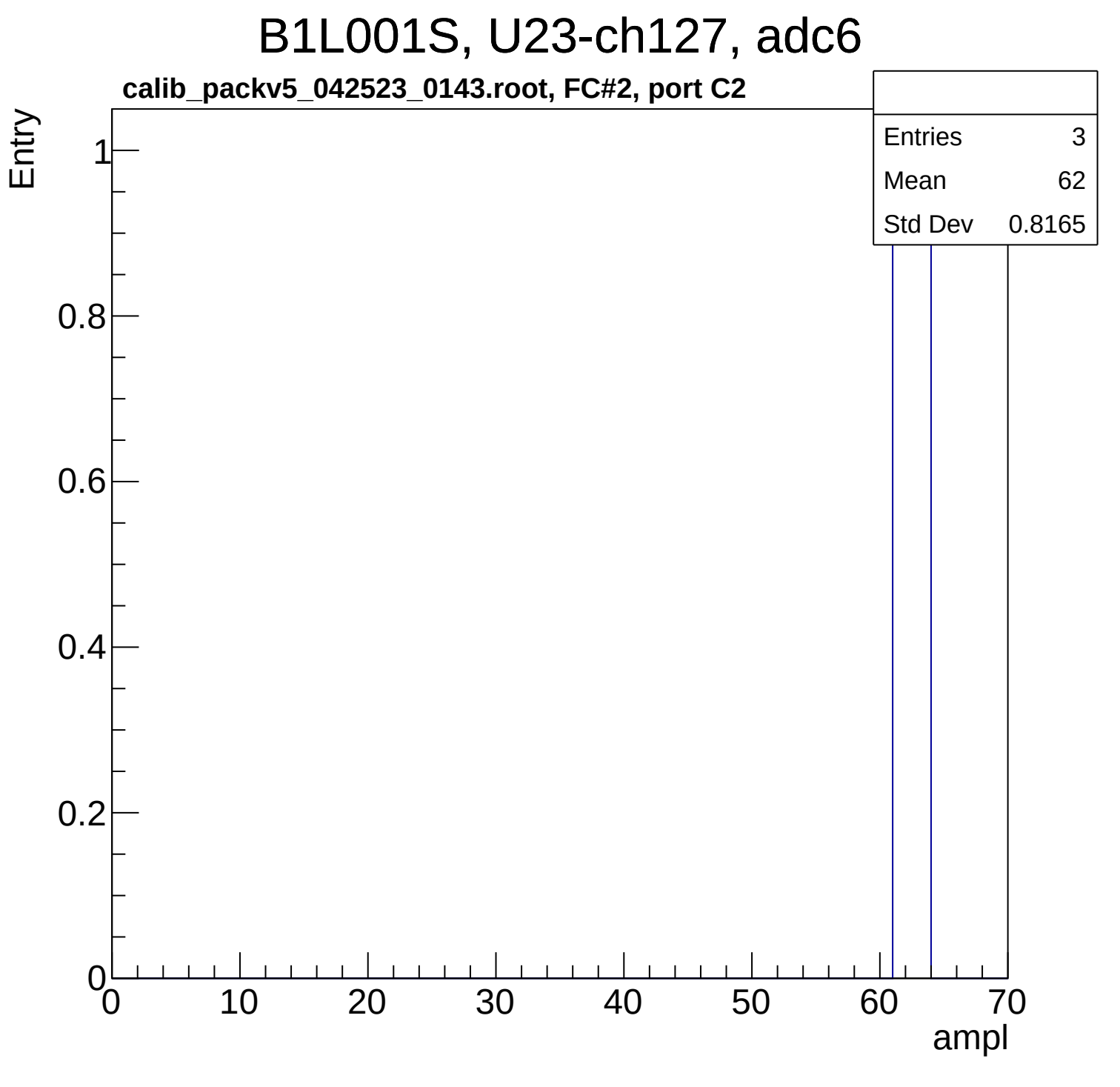

B1L001S, U23-ch127, adc1
calib_packv5_042523_0143.root, FC#2, port C2
Entries 149
Mean 38.08
Std Dev 3.42
Gaus mean : 38.4975
Gaus Width: 3.5246
Entry
ampl
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70

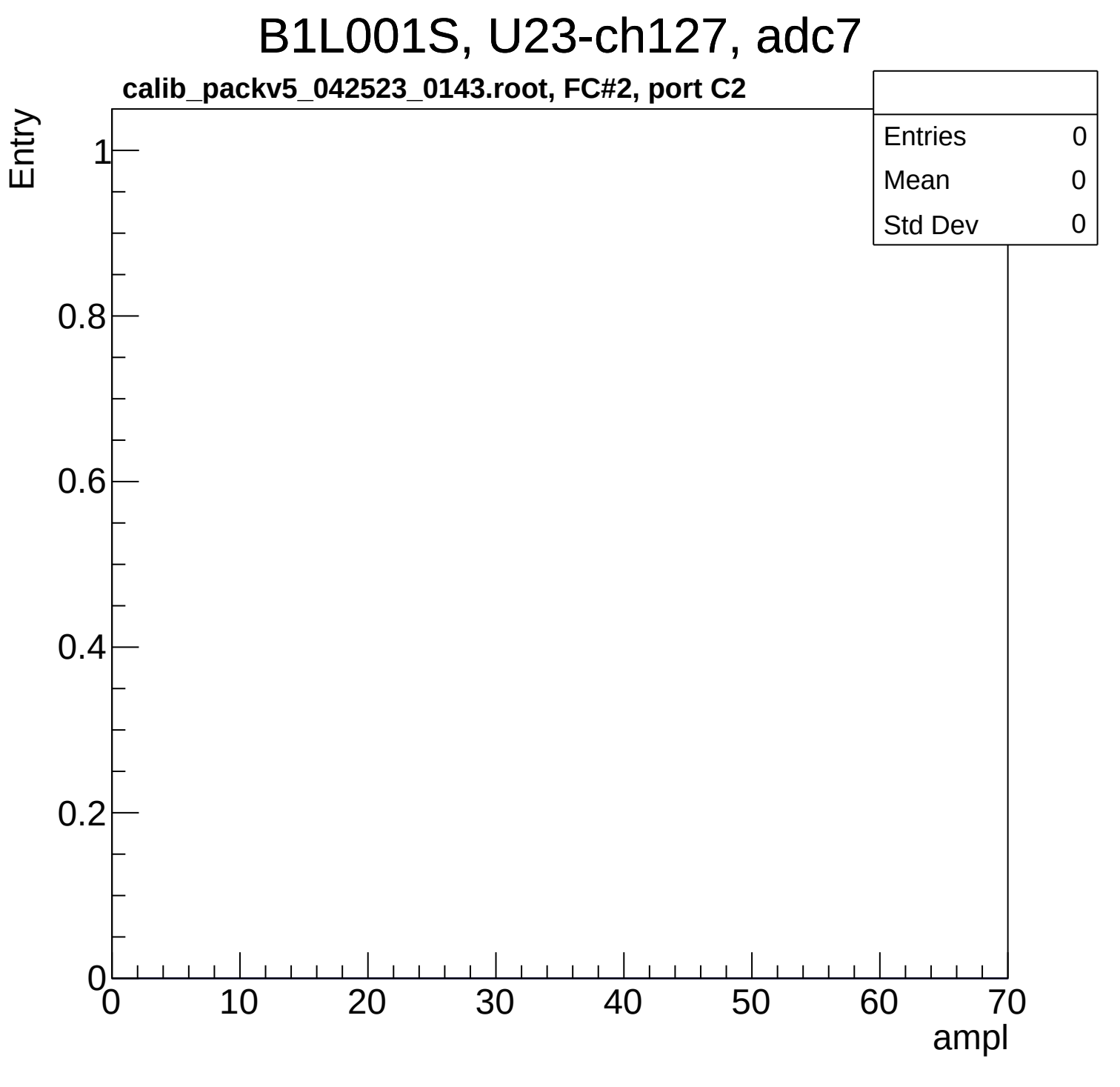

B1L001S, U23-ch127, adc2
calib_packv5_042523_0143.root, FC#2, port C2
Entries 103
Mean 44.28
Std Dev 3.251
Gaus mean : 45.0168
Gaus Width: 3.0190
Entry
ampl
0
2
4
6
8
10
12
14
0
10
20
30
40
50
60
70

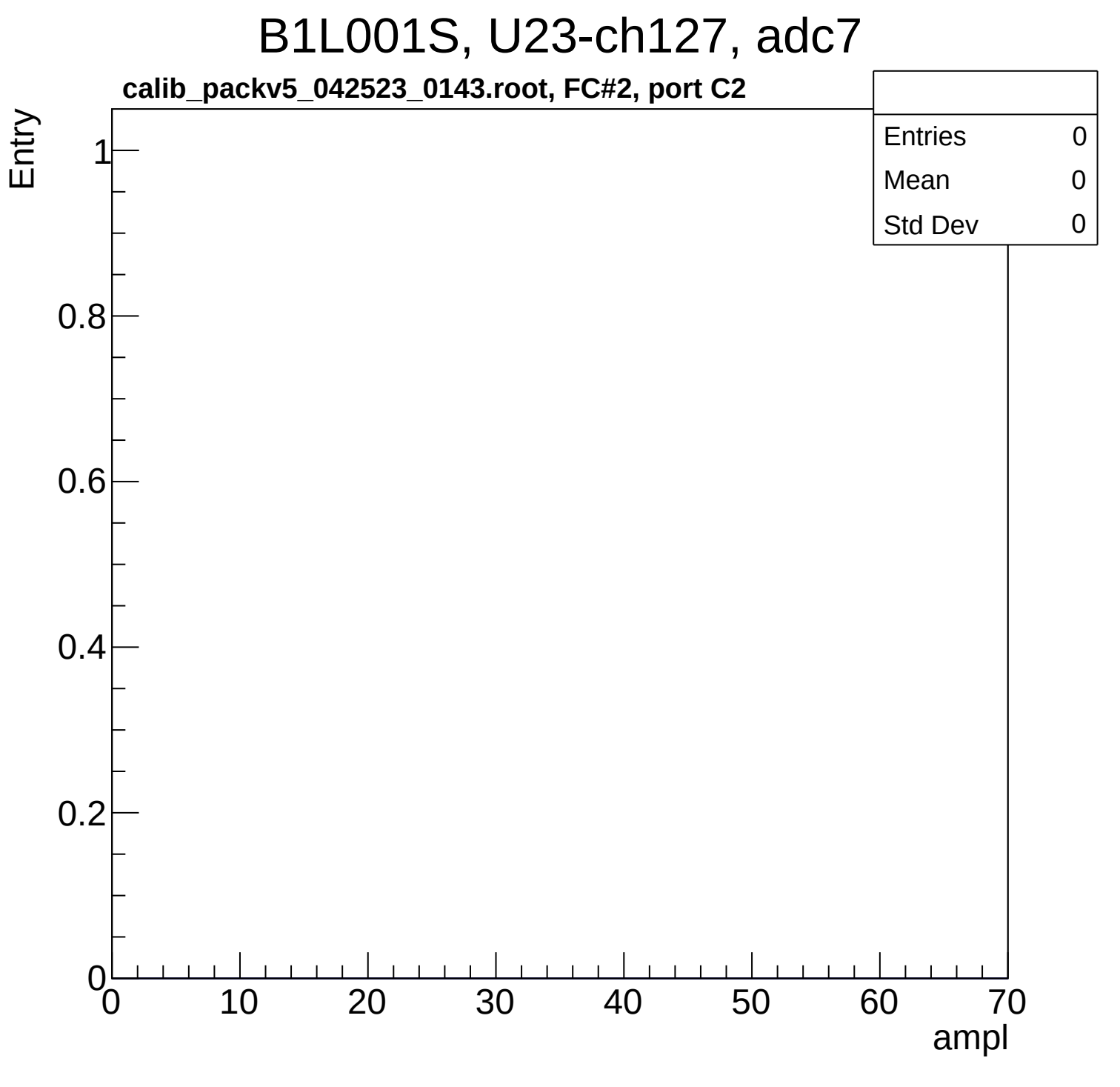

B1L001S, U23-ch127, adc3
calib_packv5_042523_0143.root, FC#2, port C2
Entries 151
Mean 50.49
Std Dev 3.487
Entry
0
2
4
6
8
10
12
14
16
18
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch127, adc4
calib_packv5_042523_0143.root, FC#2, port C2
Entries 107
Mean 57.21
Std Dev 3.01
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch127, adc5
calib_packv5_042523_0143.root, FC#2, port C2
Entries 74
Mean 58.89
Std Dev 10.04
Entry
0
2
4
6
8
10
12
14
16
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch127, adc6
calib_packv5_042523_0143.root, FC#2, port C2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L001S, U23-ch127, adc7
calib_packv5_042523_0143.root, FC#2, port C2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl