

B1L003S, U3-ch0

calib_packv5_042523_0143.root, FC#13, port D2

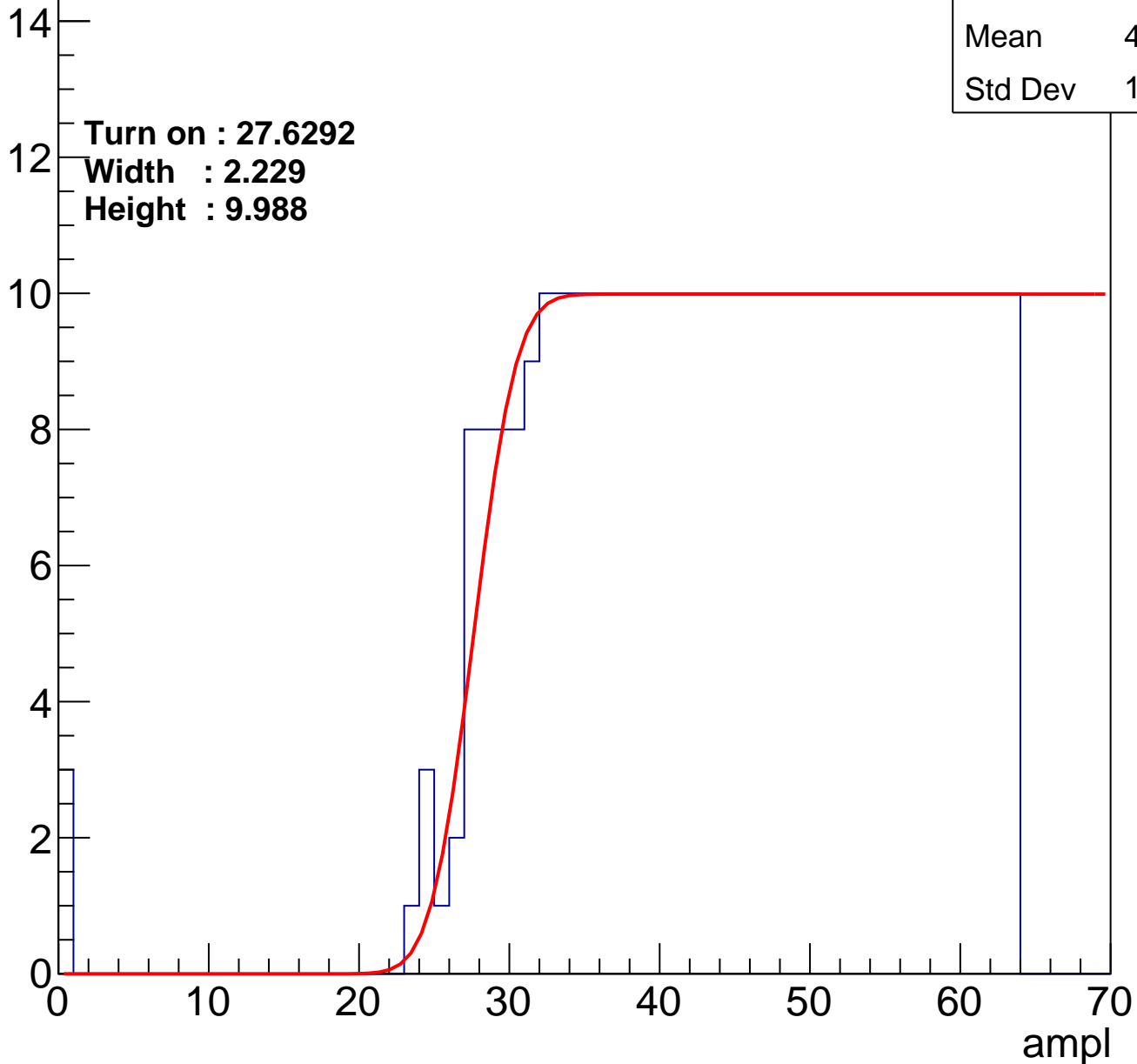
Entry

Entries	371
Mean	44.64
Std Dev	11.46

Turn on : 27.6292

Width : 2.229

Height : 9.988



B1L003S, U3-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.7
Std Dev	11.29

Turn on : 27.8835

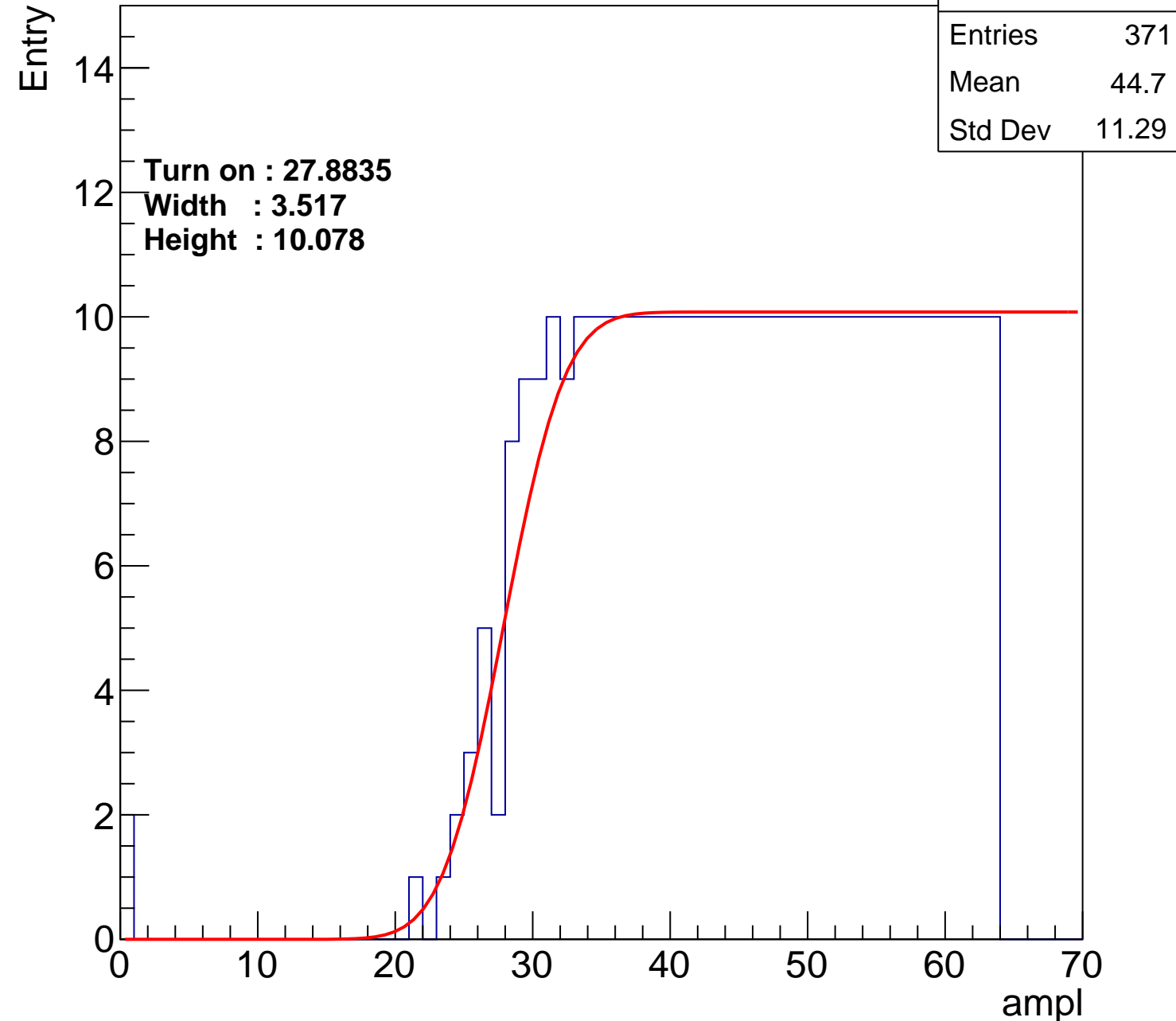
Width : 3.517

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.99
Std Dev	11.36

Turn on : 28.3924

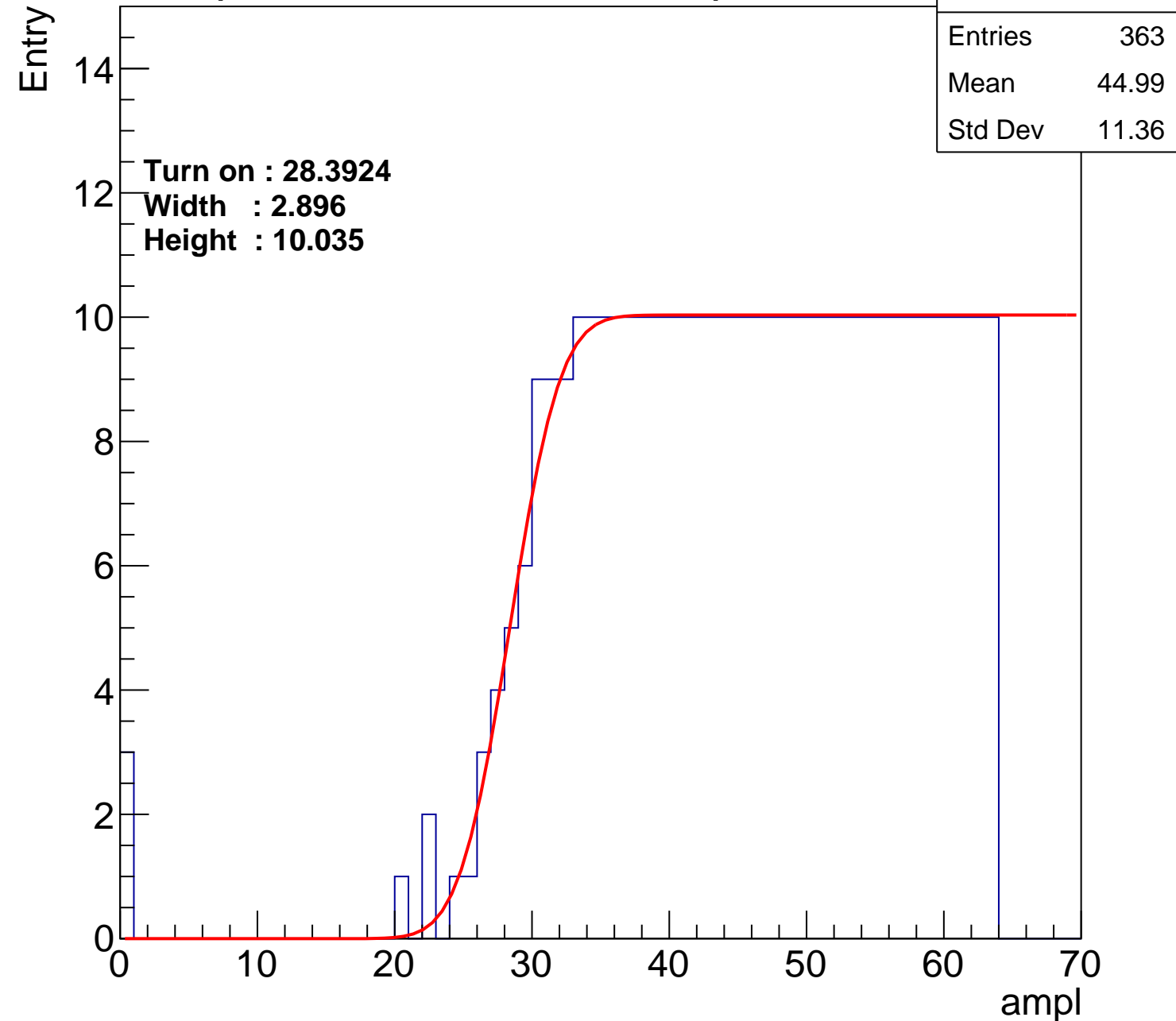
Width : 2.896

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch3

calib_packv5_042523_0143.root, FC#13, port D2

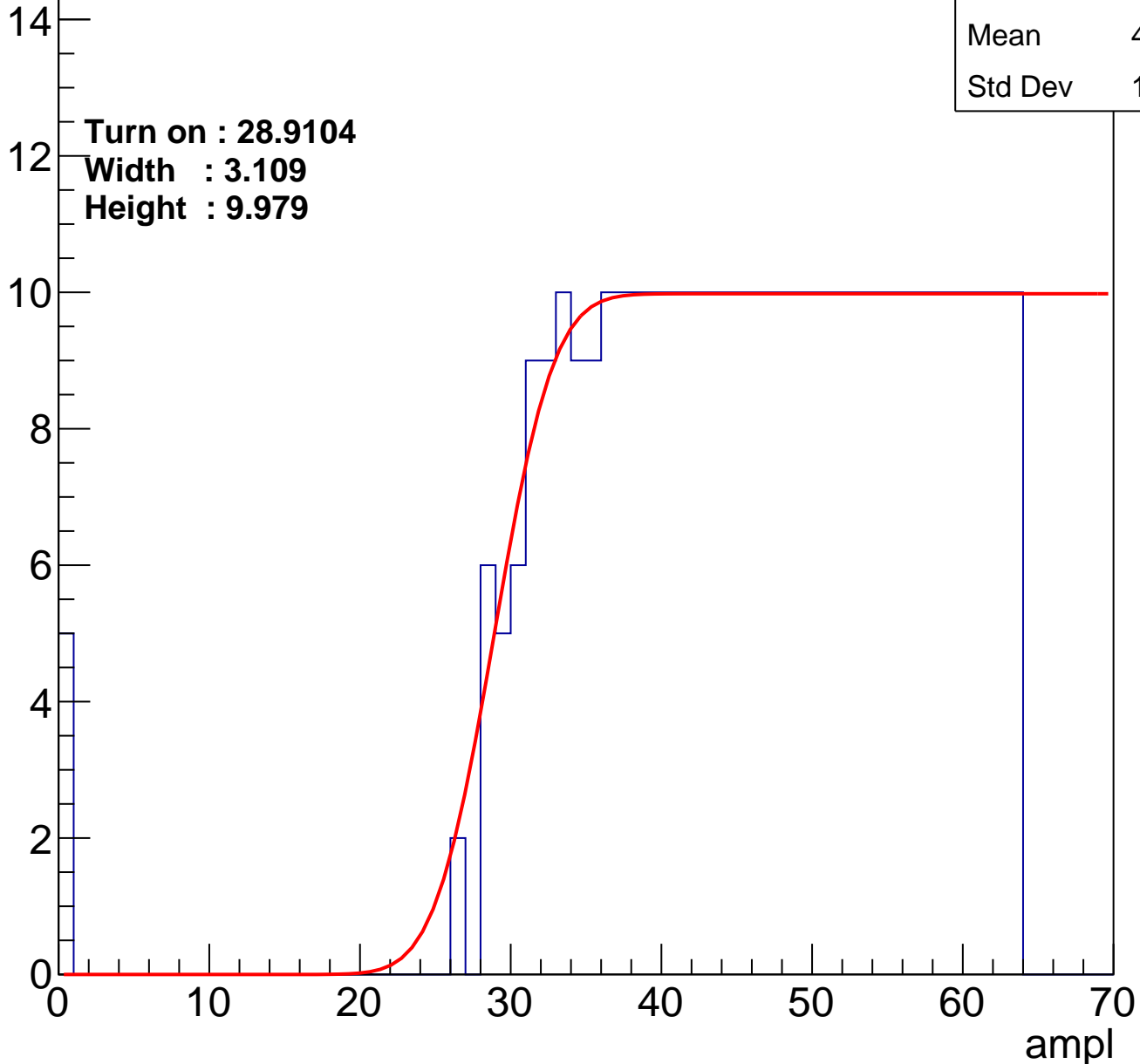
Entries	350
Mean	45.49
Std Dev	11.44

Turn on : 28.9104

Width : 3.109

Height : 9.979

Entry



B1L003S, U3-ch4

calib_packv5_042523_0143.root, FC#13, port D2

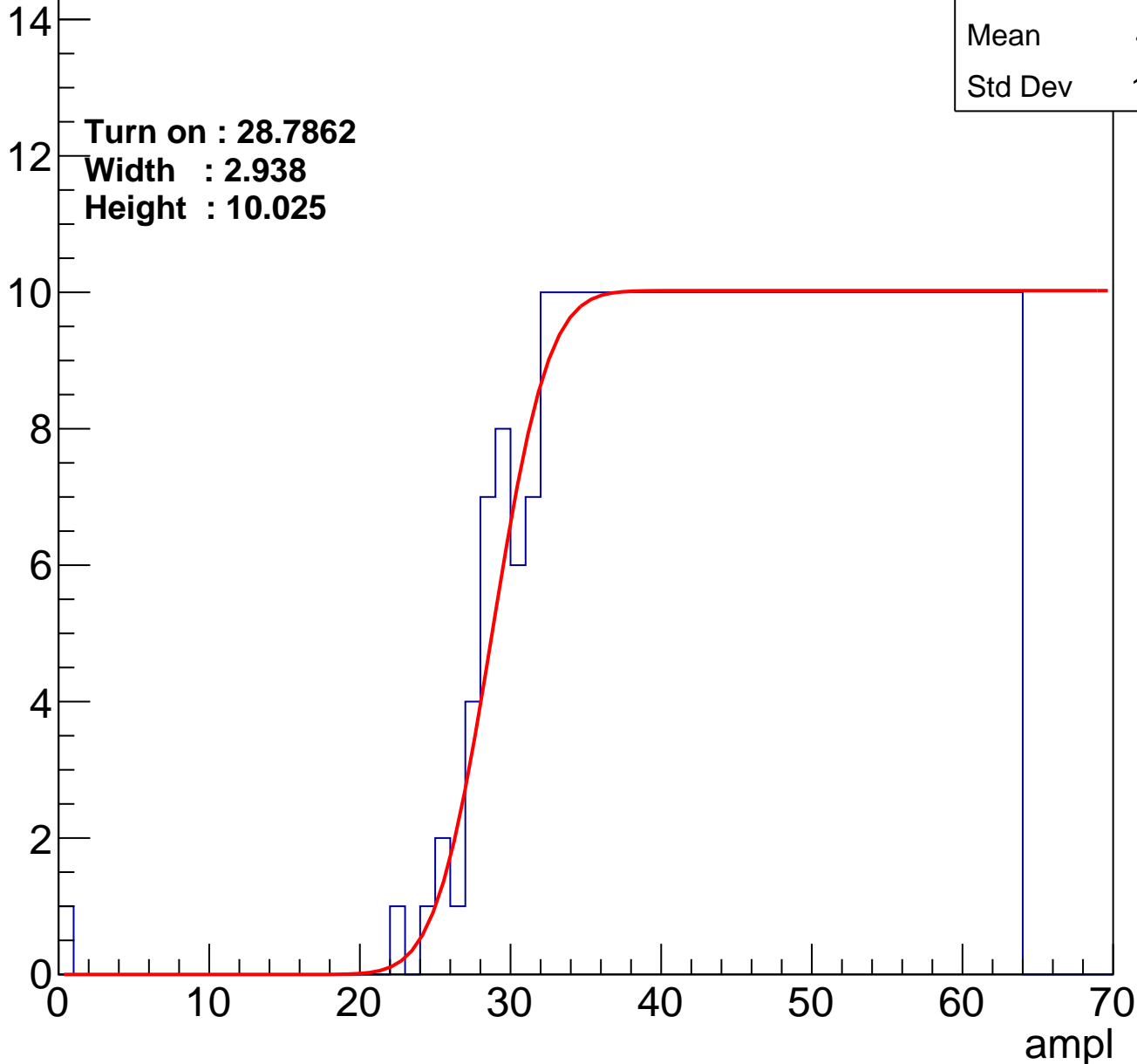
Entry

Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 28.7862

Width : 2.938

Height : 10.025



B1L003S, U3-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.96
Std Dev	11.35

Turn on : 28.2619

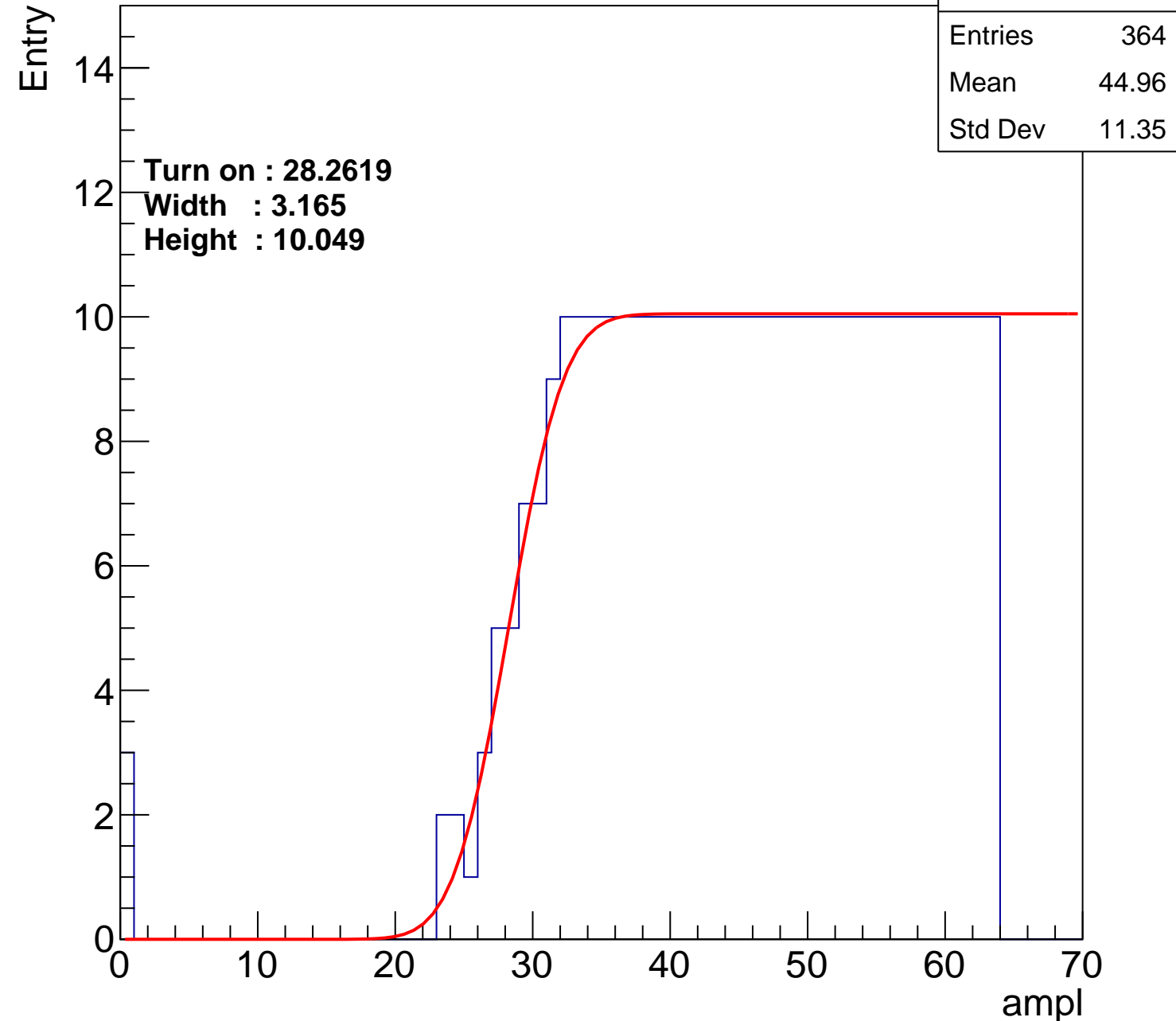
Width : 3.165

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.5
Std Dev	12.29

Turn on : 25.5356

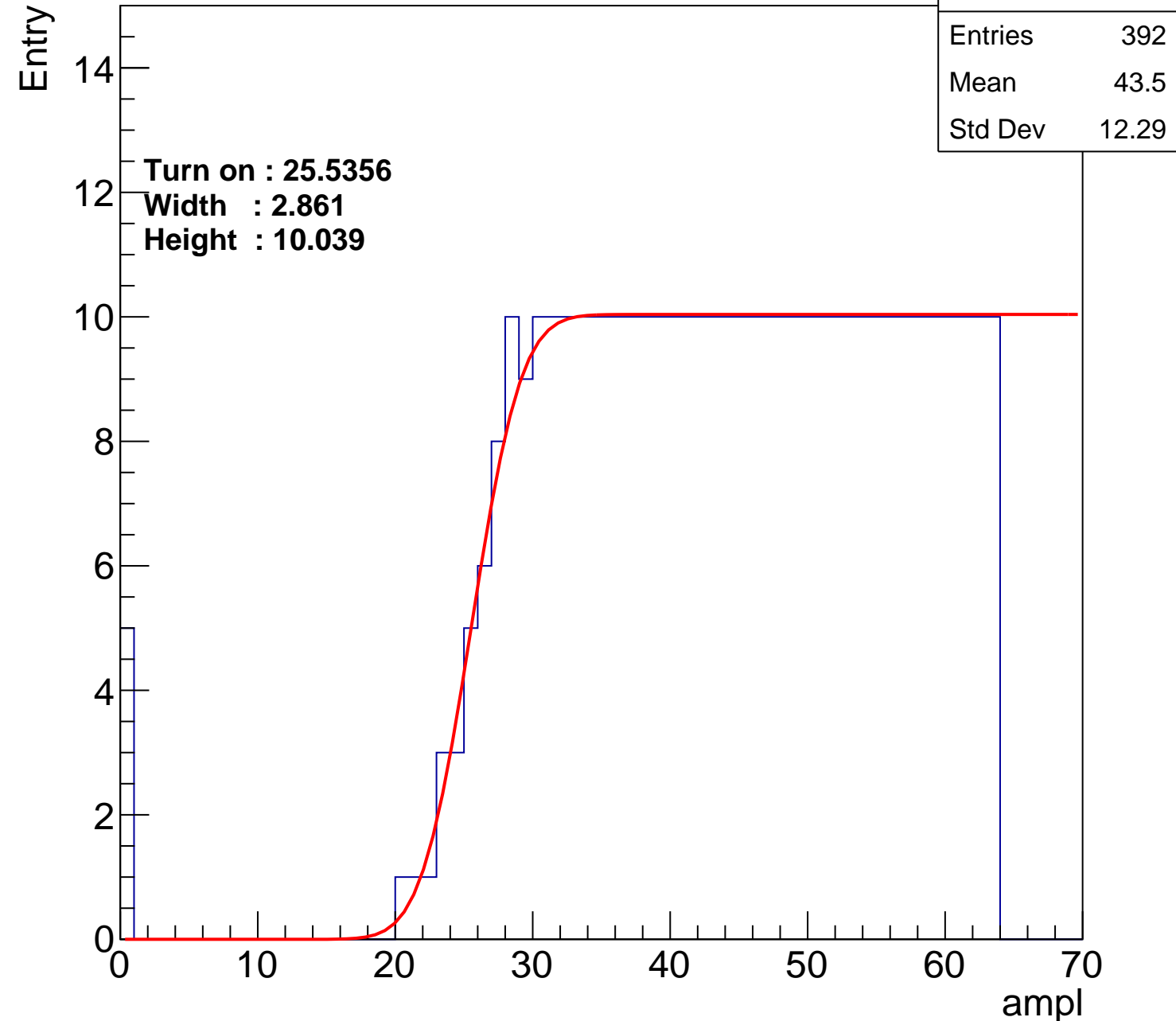
Width : 2.861

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	43.86
Std Dev	11.98

Turn on : 25.8197

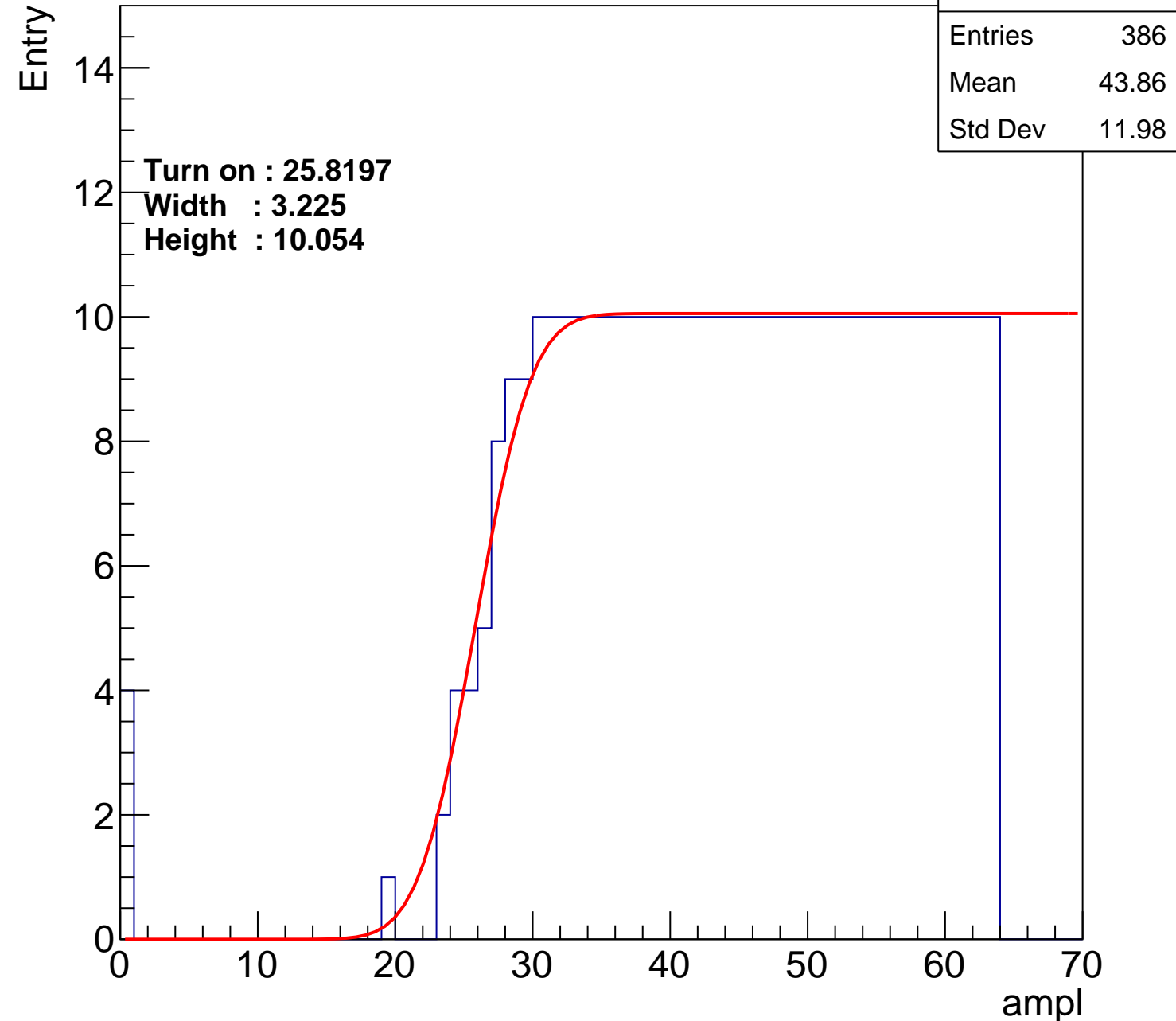
Width : 3.225

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch8

calib_packv5_042523_0143.root, FC#13, port D2

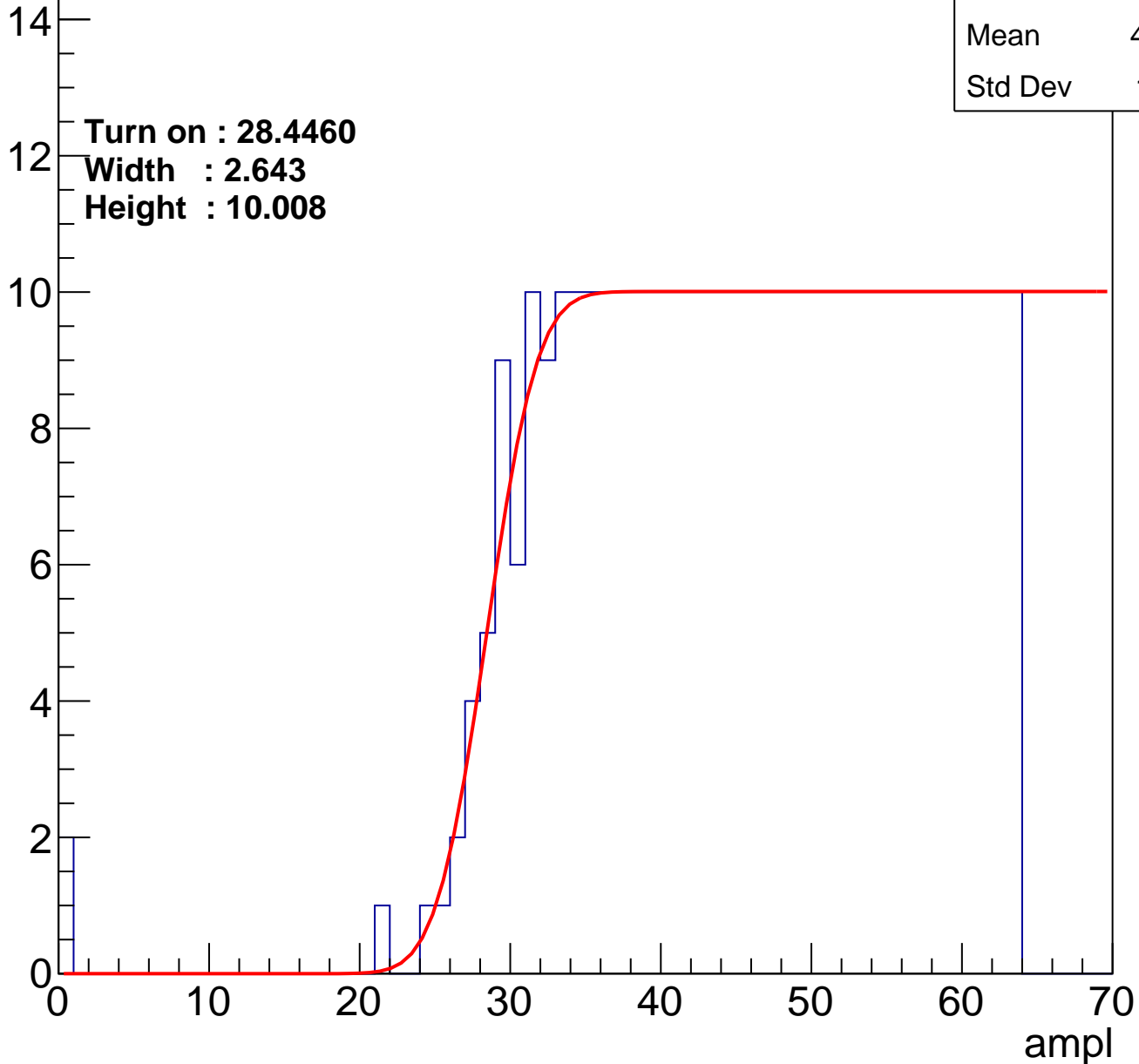
Entries	360
Mean	45.25
Std Dev	11.01

Turn on : 28.4460

Width : 2.643

Height : 10.008

Entry



B1L003S, U3-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.06
Std Dev	11.62

Turn on : 25.1724

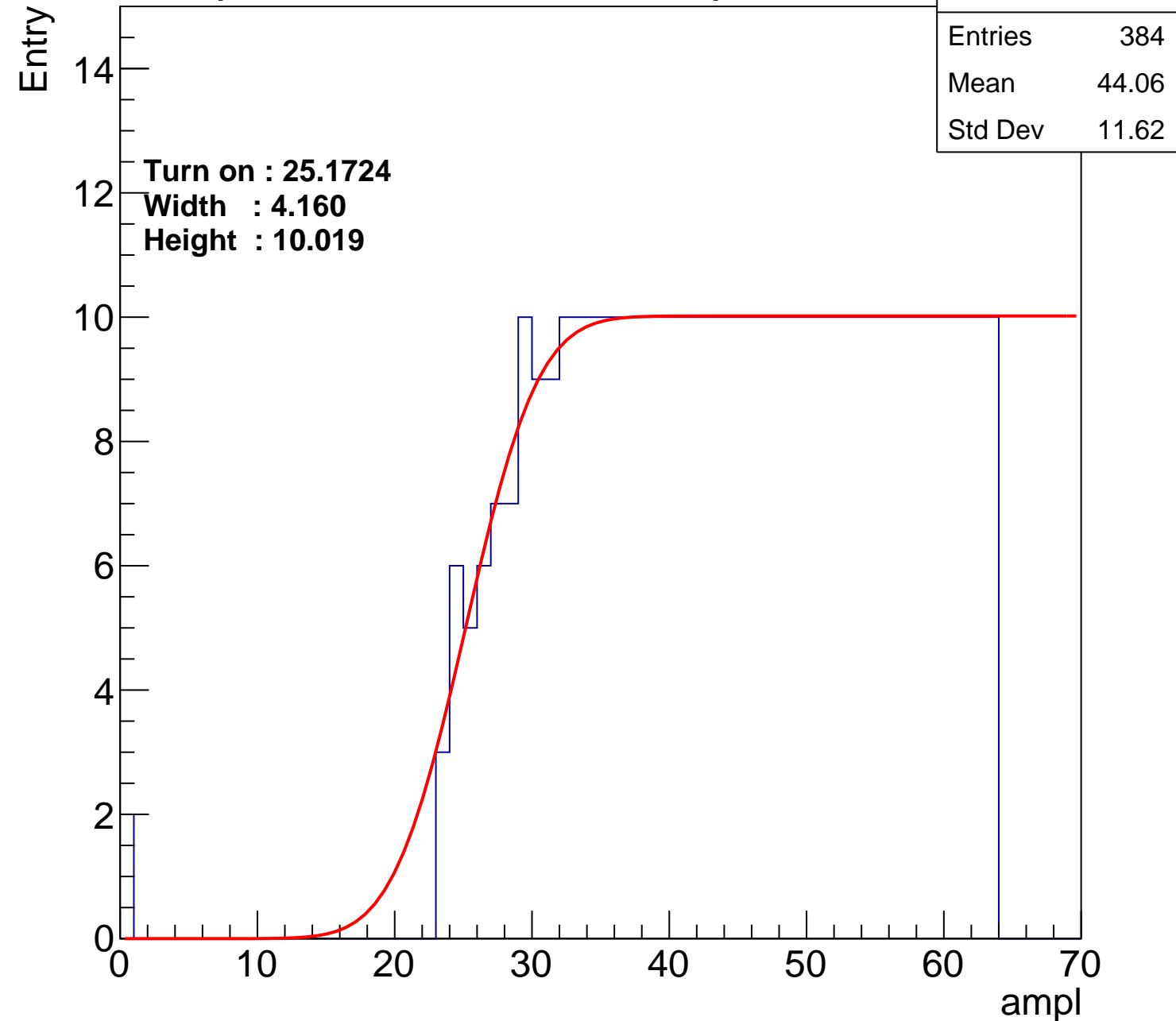
Width : 4.160

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.8
Std Dev	11.39

Turn on : 28.1069

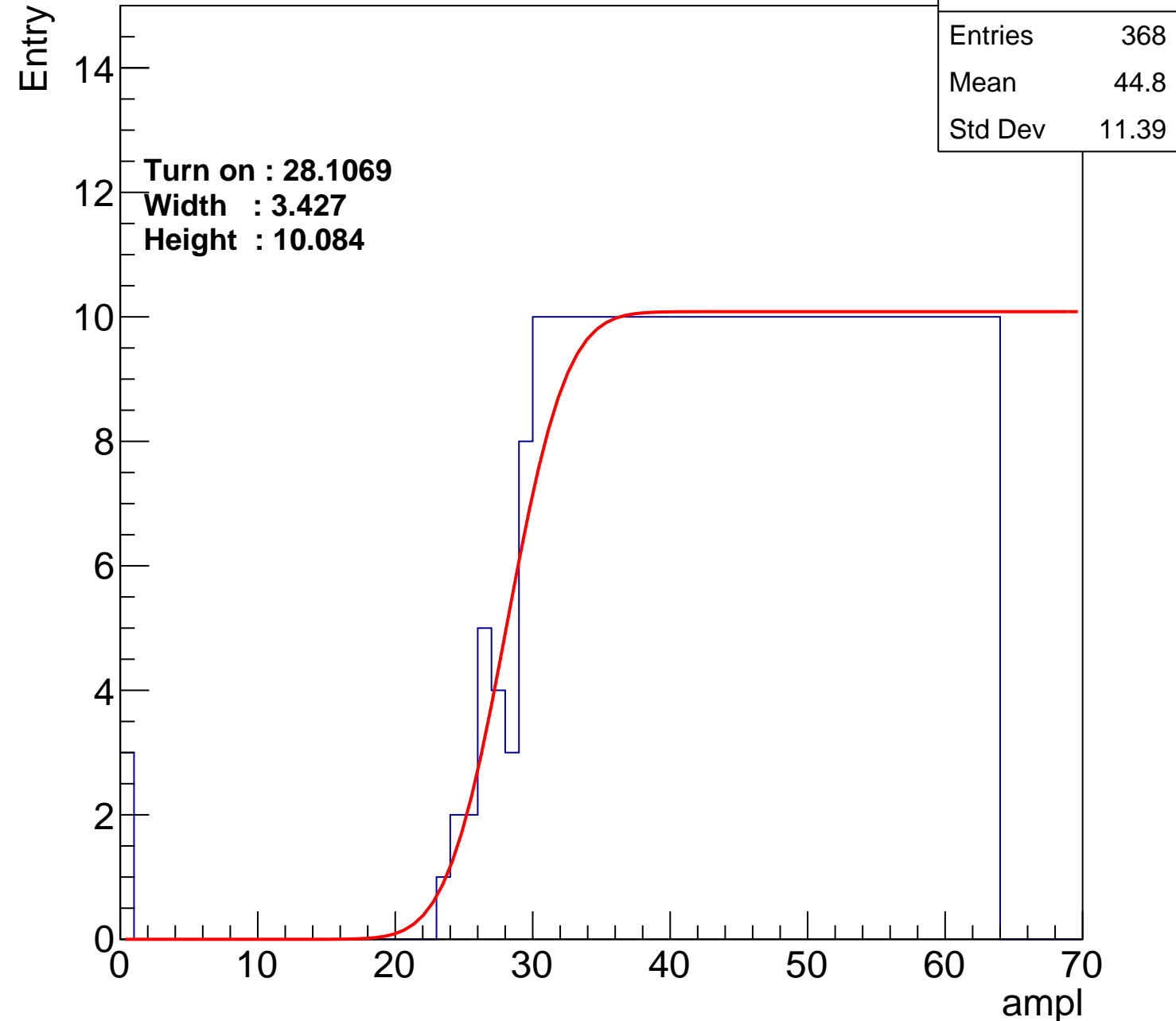
Width : 3.427

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch11

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.78
Std Dev	11.07

Turn on : 27.1315

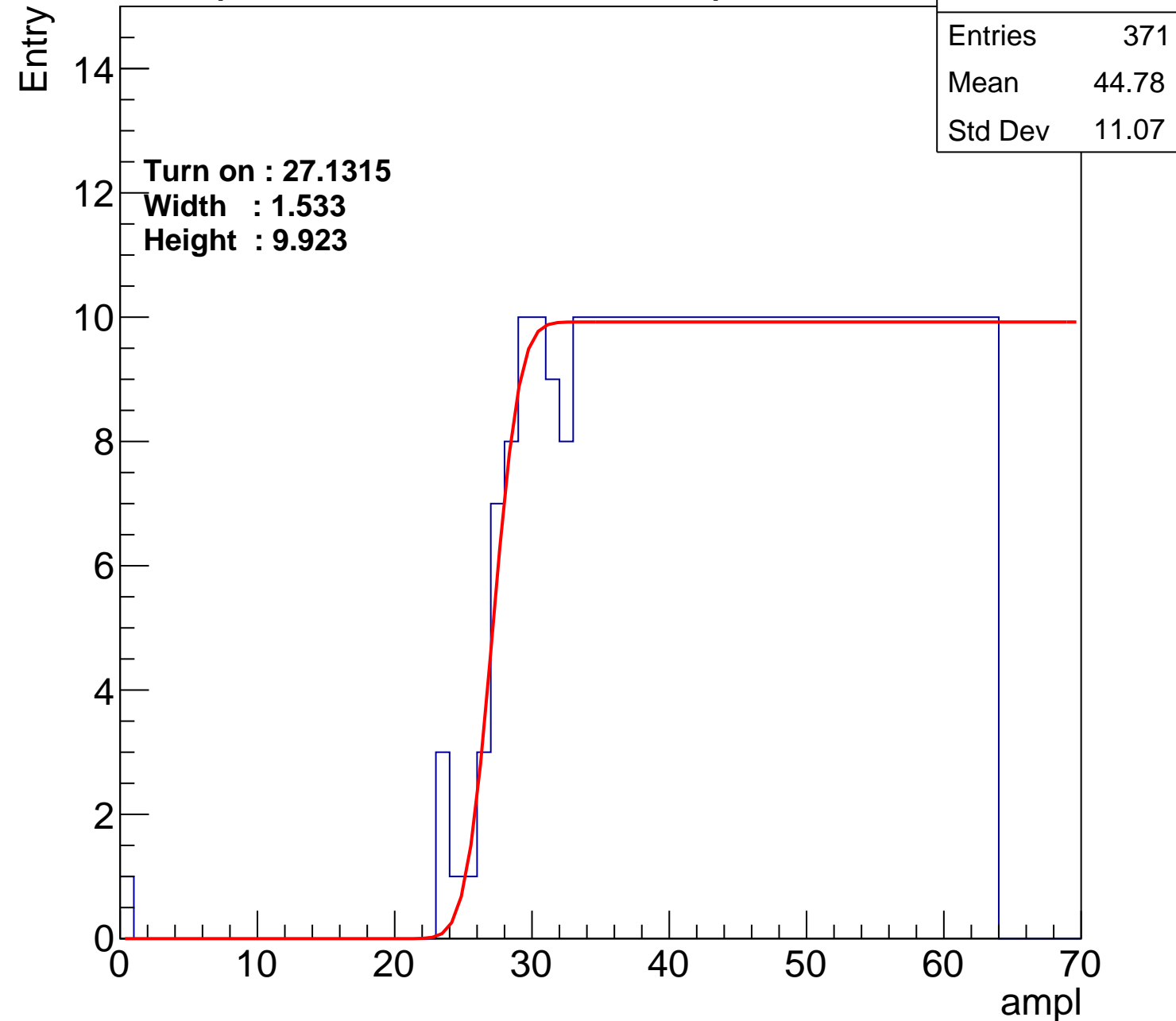
Width : 1.533

Height : 9.923

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch12

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.39
Std Dev	11.47

Turn on : 27.0657

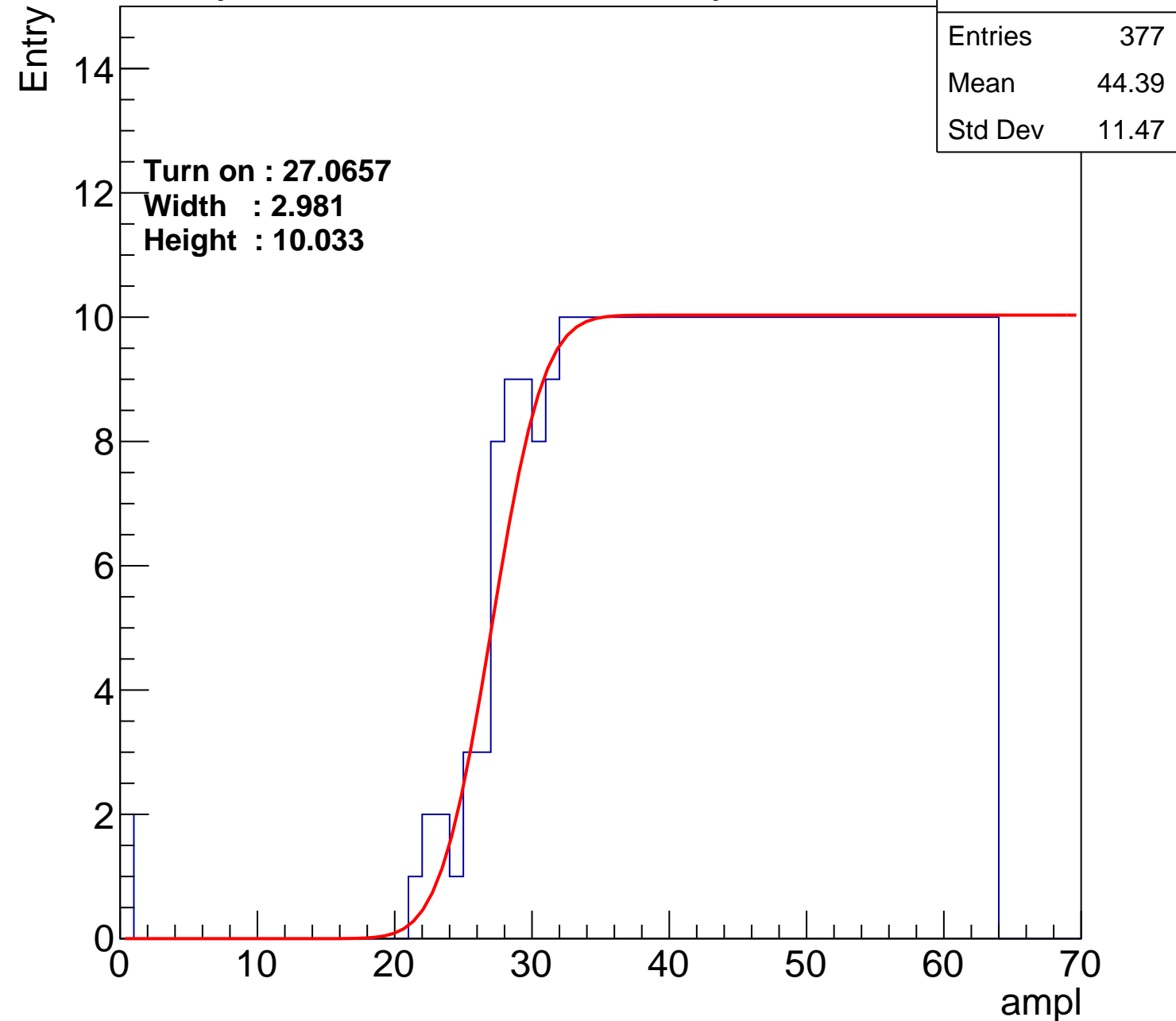
Width : 2.981

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch13

calib_packv5_042523_0143.root, FC#13, port D2

Entries	392
Mean	43.66
Std Dev	11.9

Turn on : 25.3136

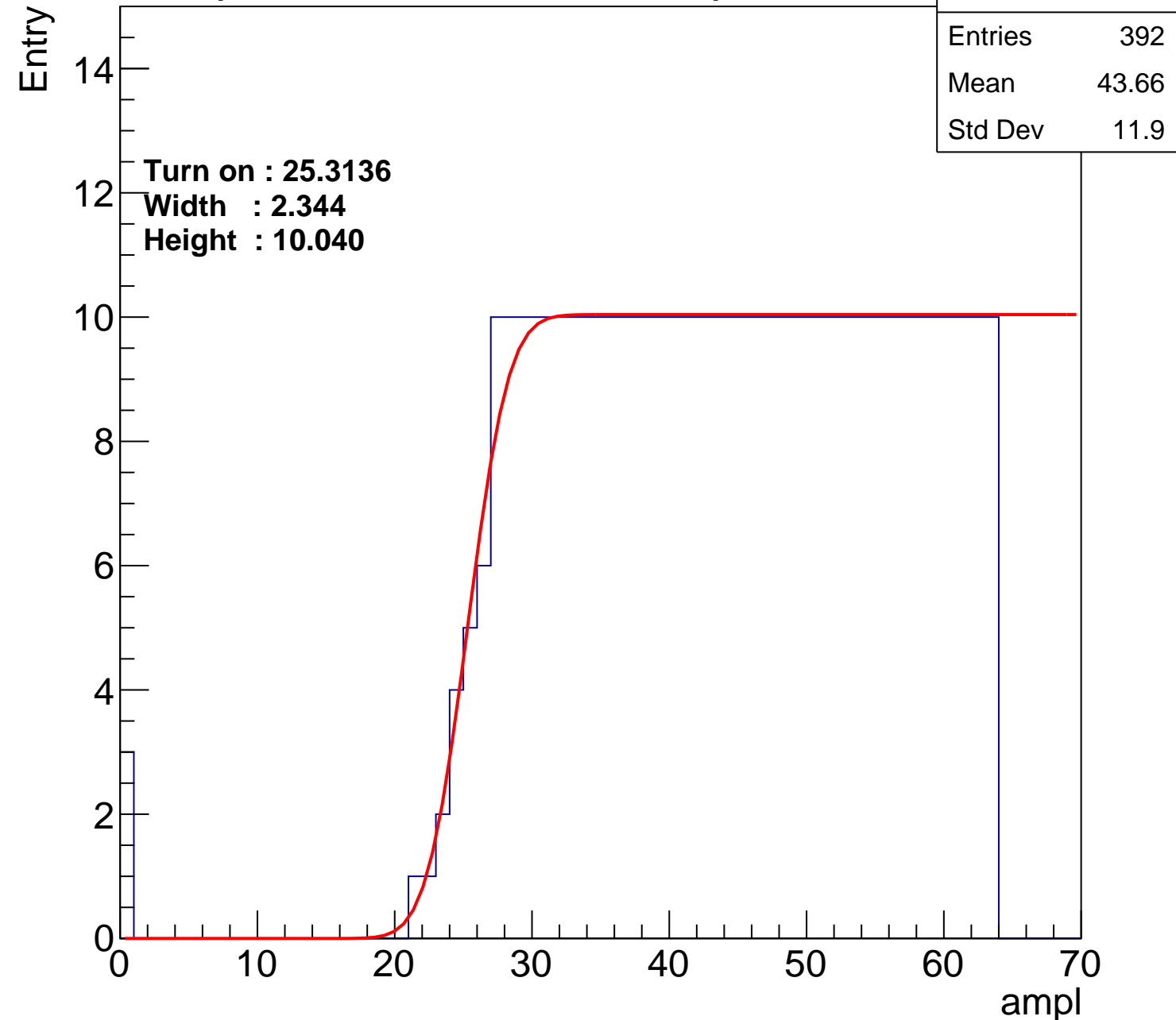
Width : 2.344

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.86
Std Dev	11.44

Turn on : 28.0046

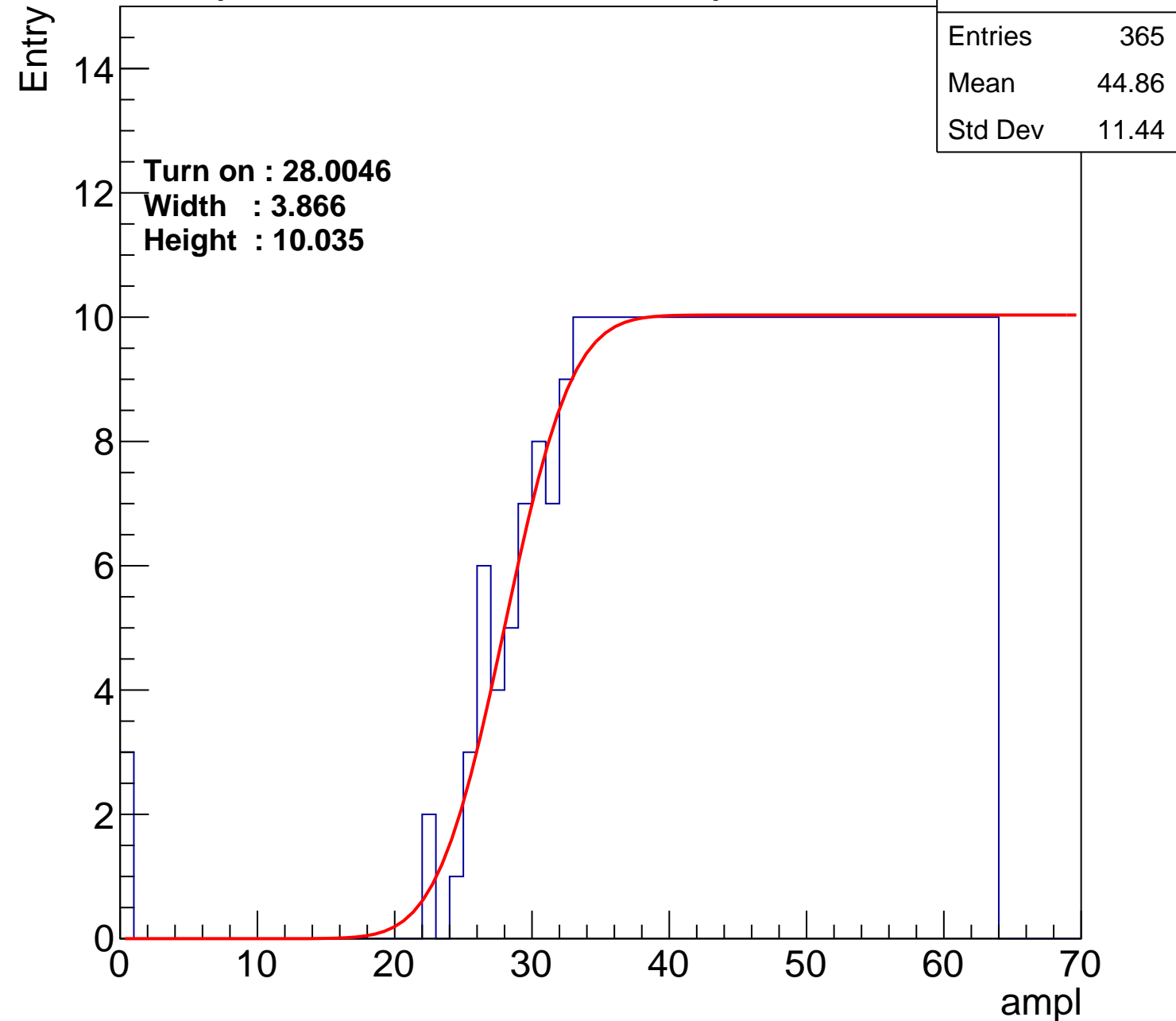
Width : 3.866

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch15

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.43
Std Dev	11.07

Turn on : 28.8686

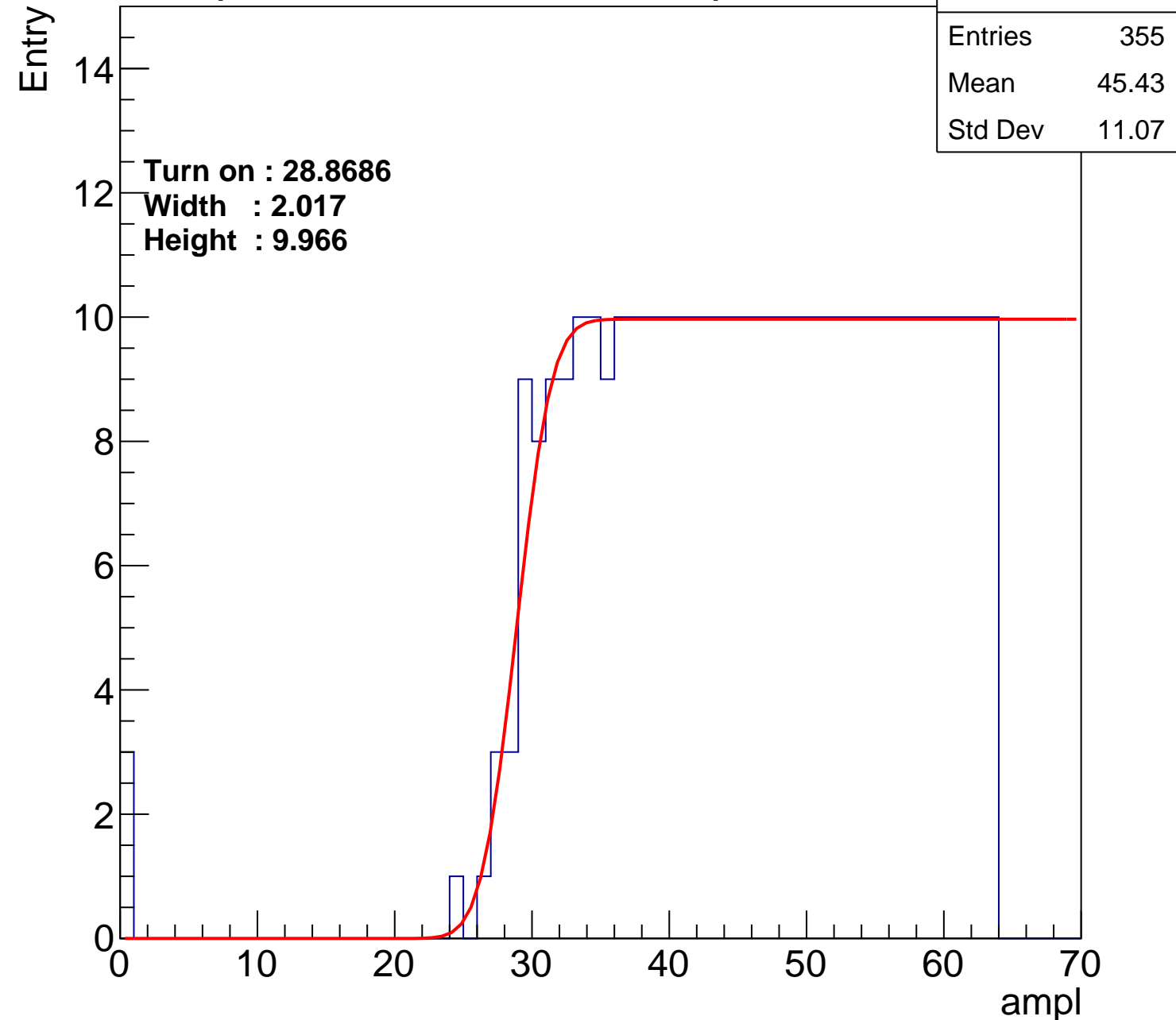
Width : 2.017

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.12
Std Dev	11.31

Turn on : 28.9432

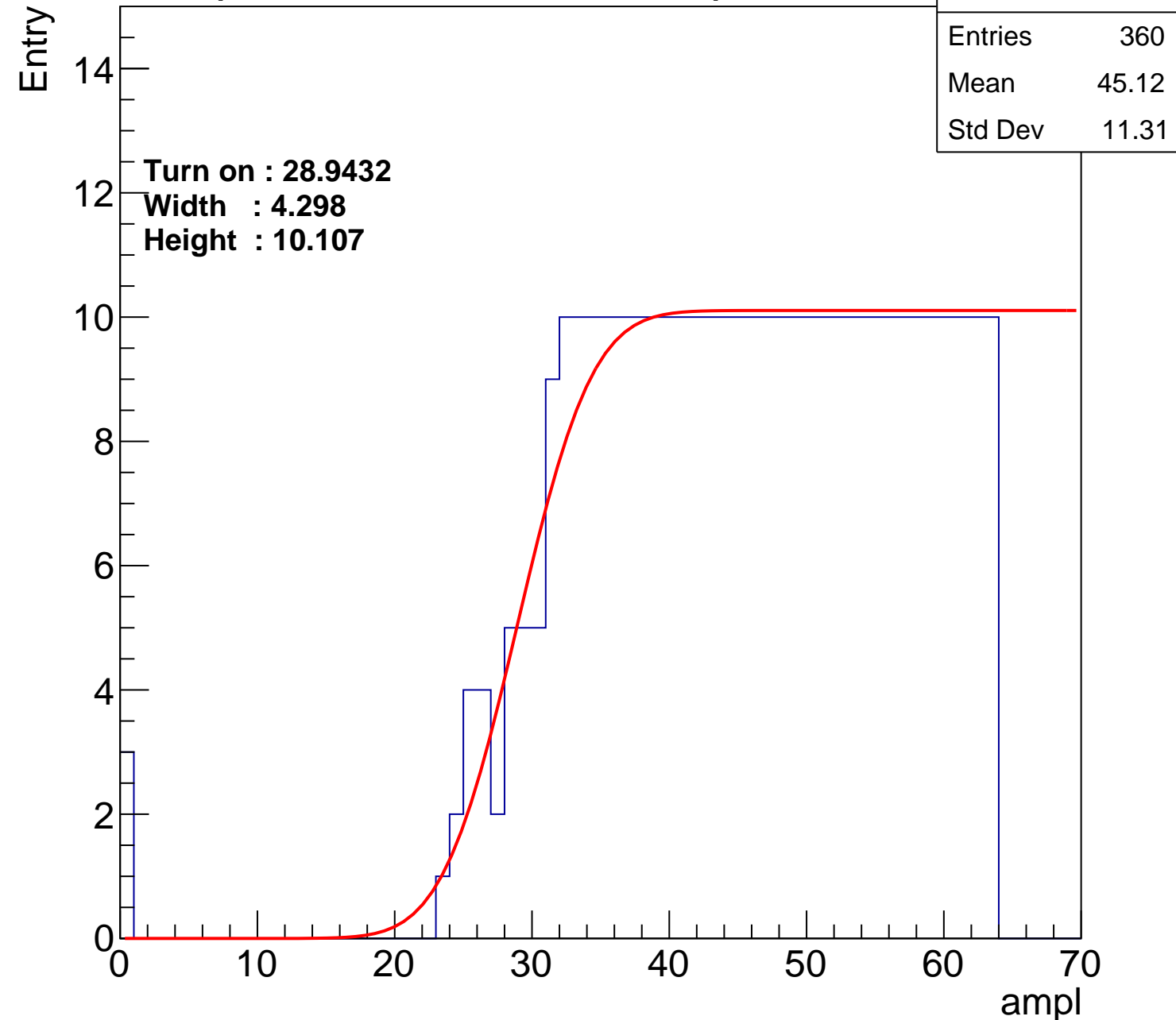
Width : 4.298

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch17

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.21
Std Dev	10.85

Turn on : 28.8887

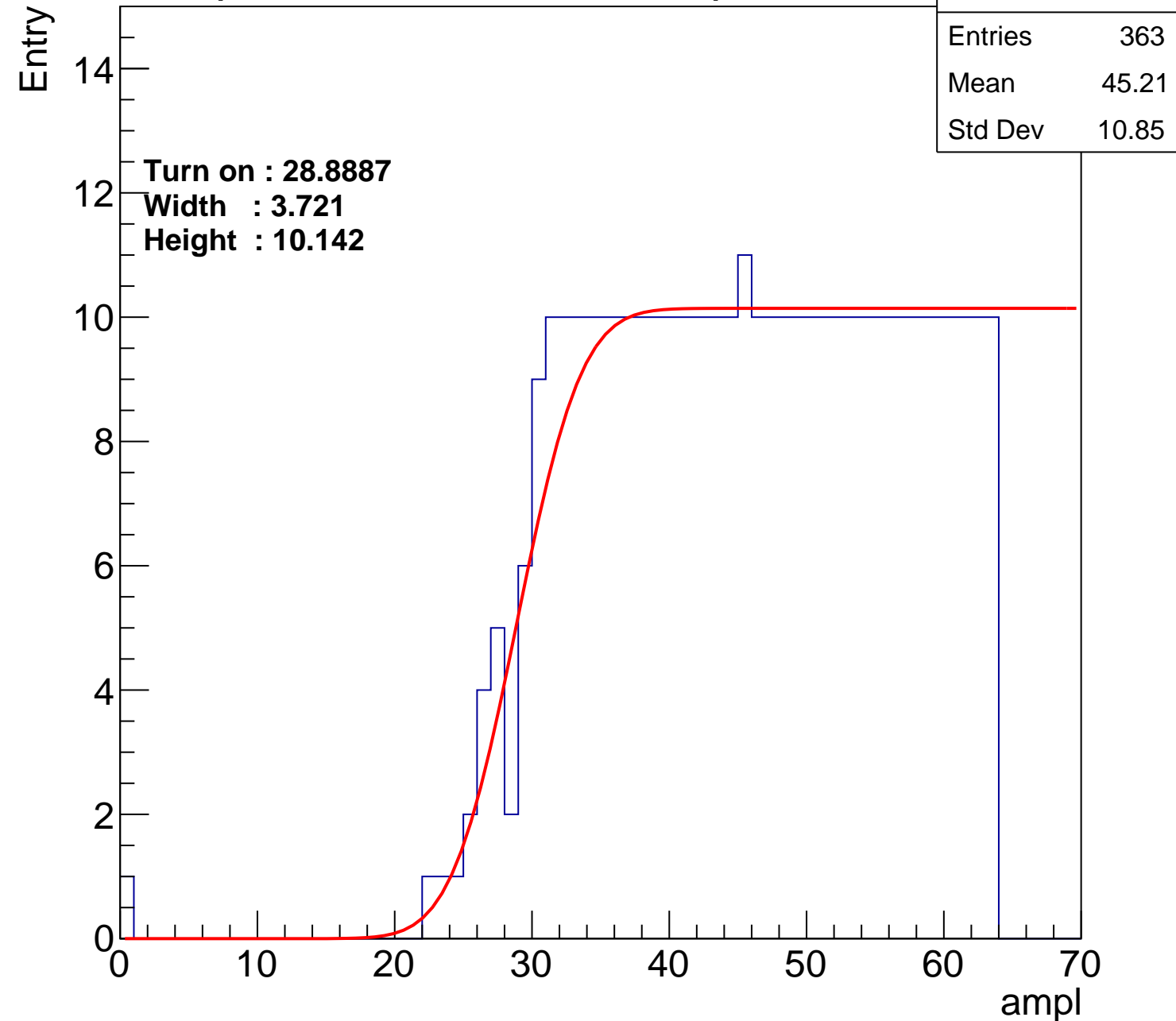
Width : 3.721

Height : 10.142

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch18

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.9
Std Dev	11.68

Turn on : 25.8002

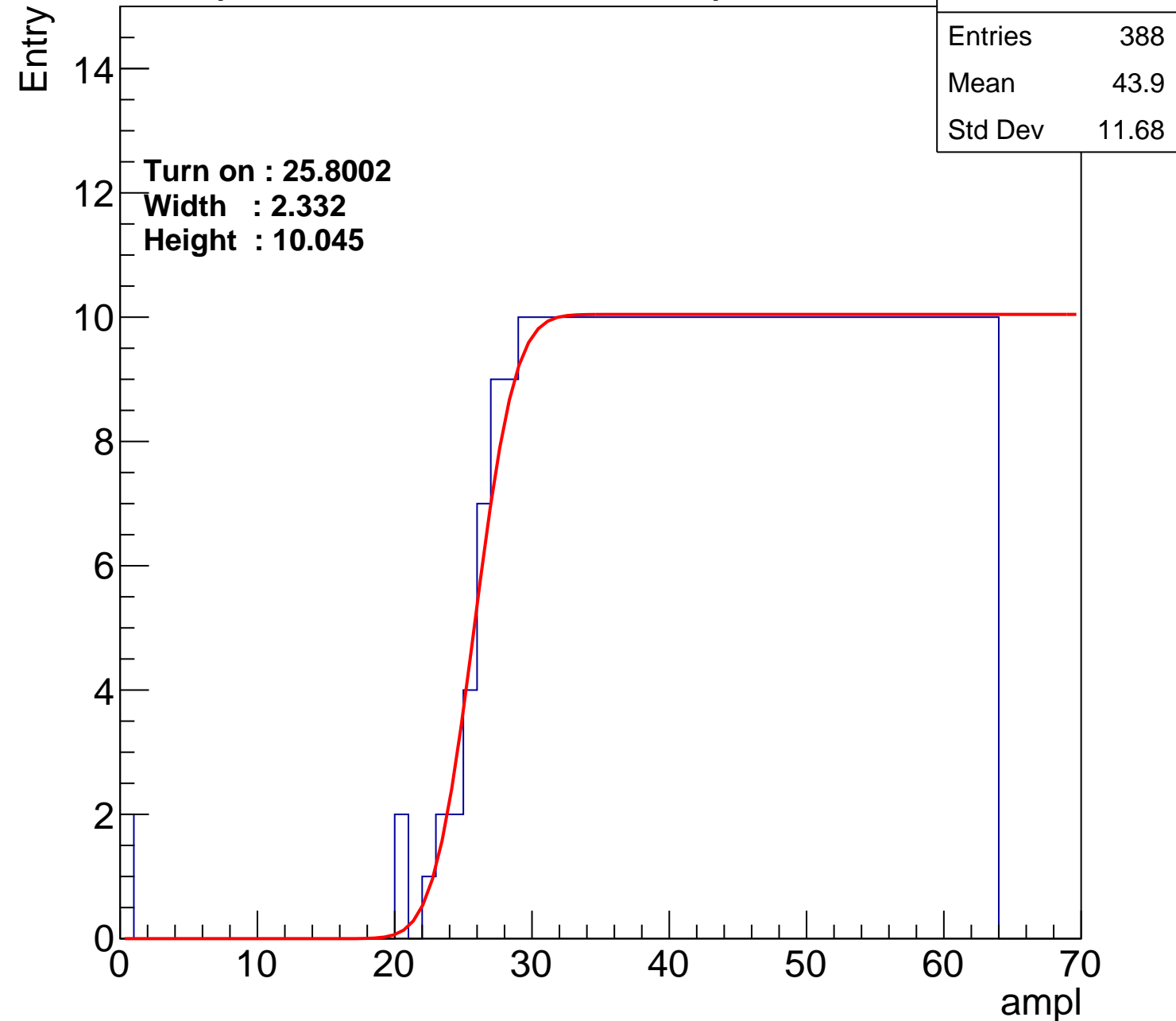
Width : 2.332

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.3579

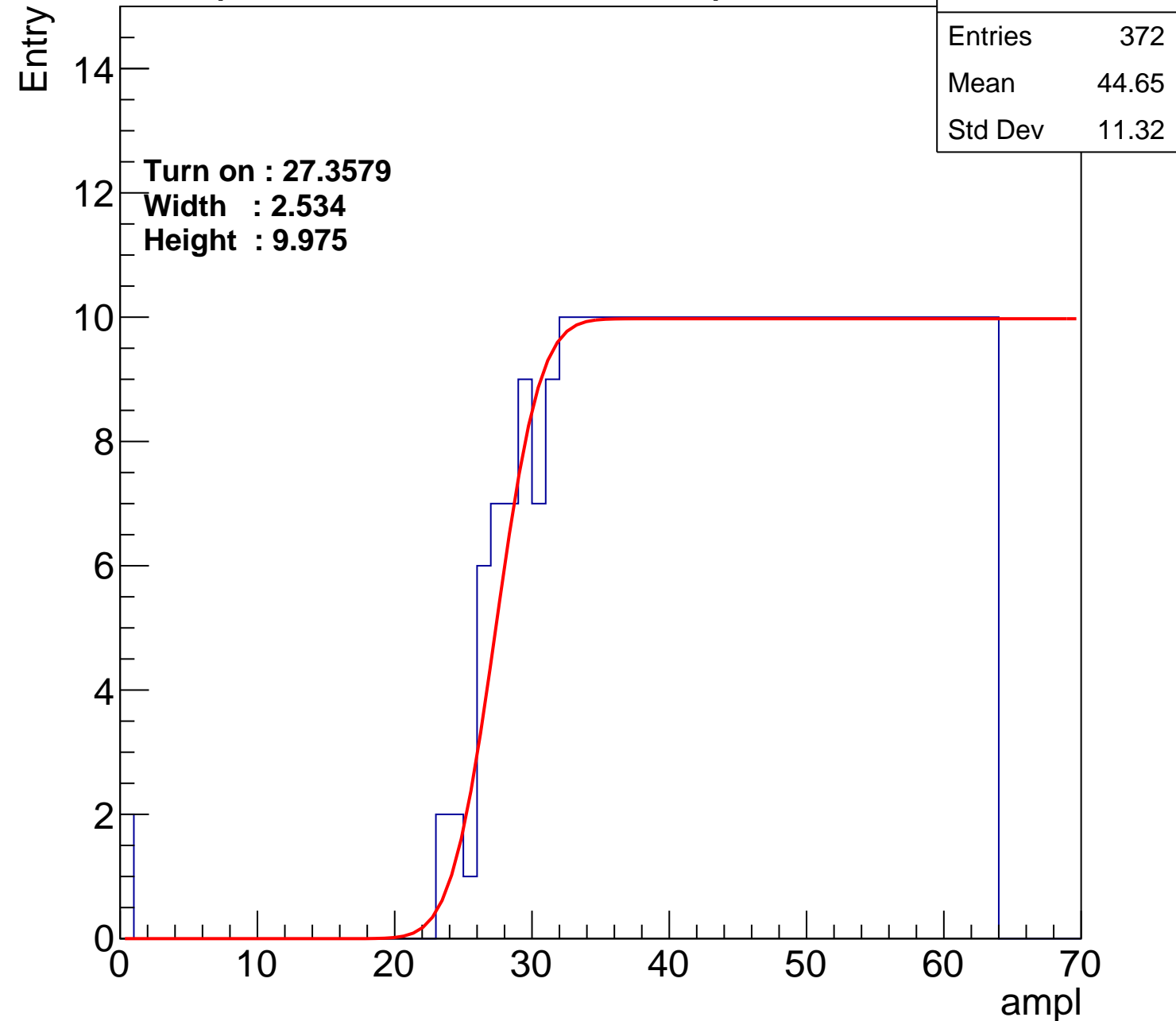
Width : 2.534

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.13
Std Dev	11.75

Turn on : 26.7229

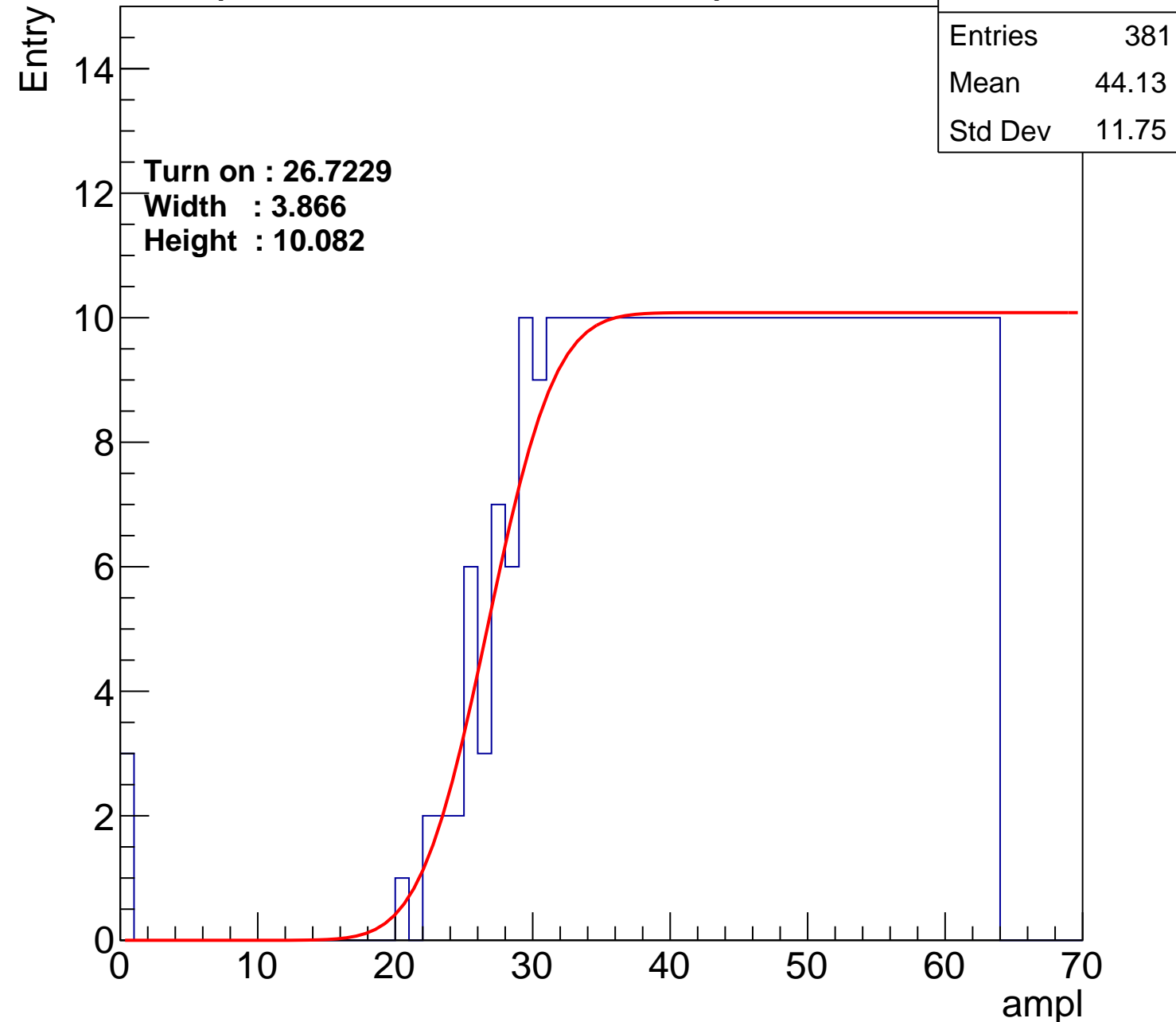
Width : 3.866

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.4176

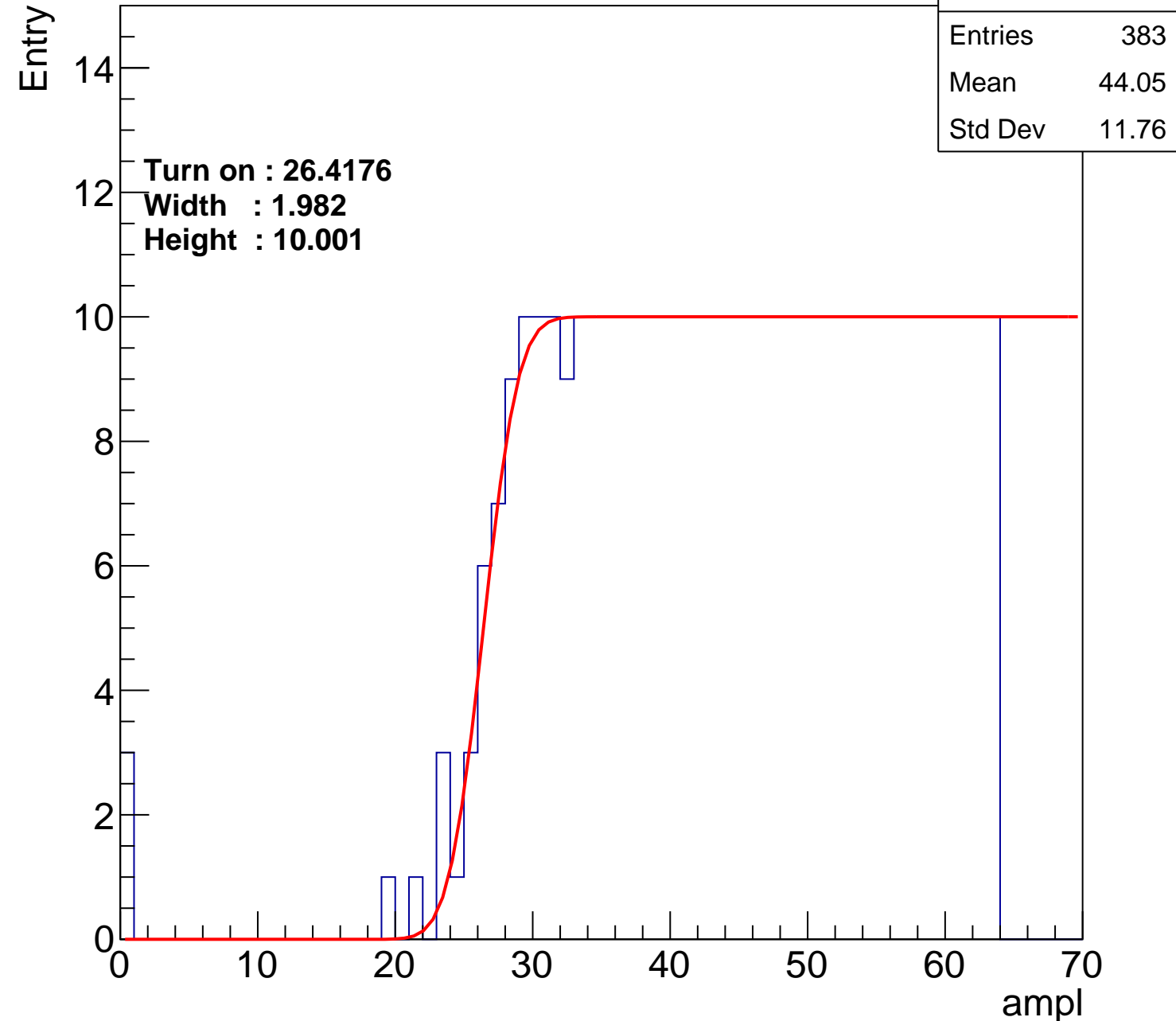
Width : 1.982

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch22

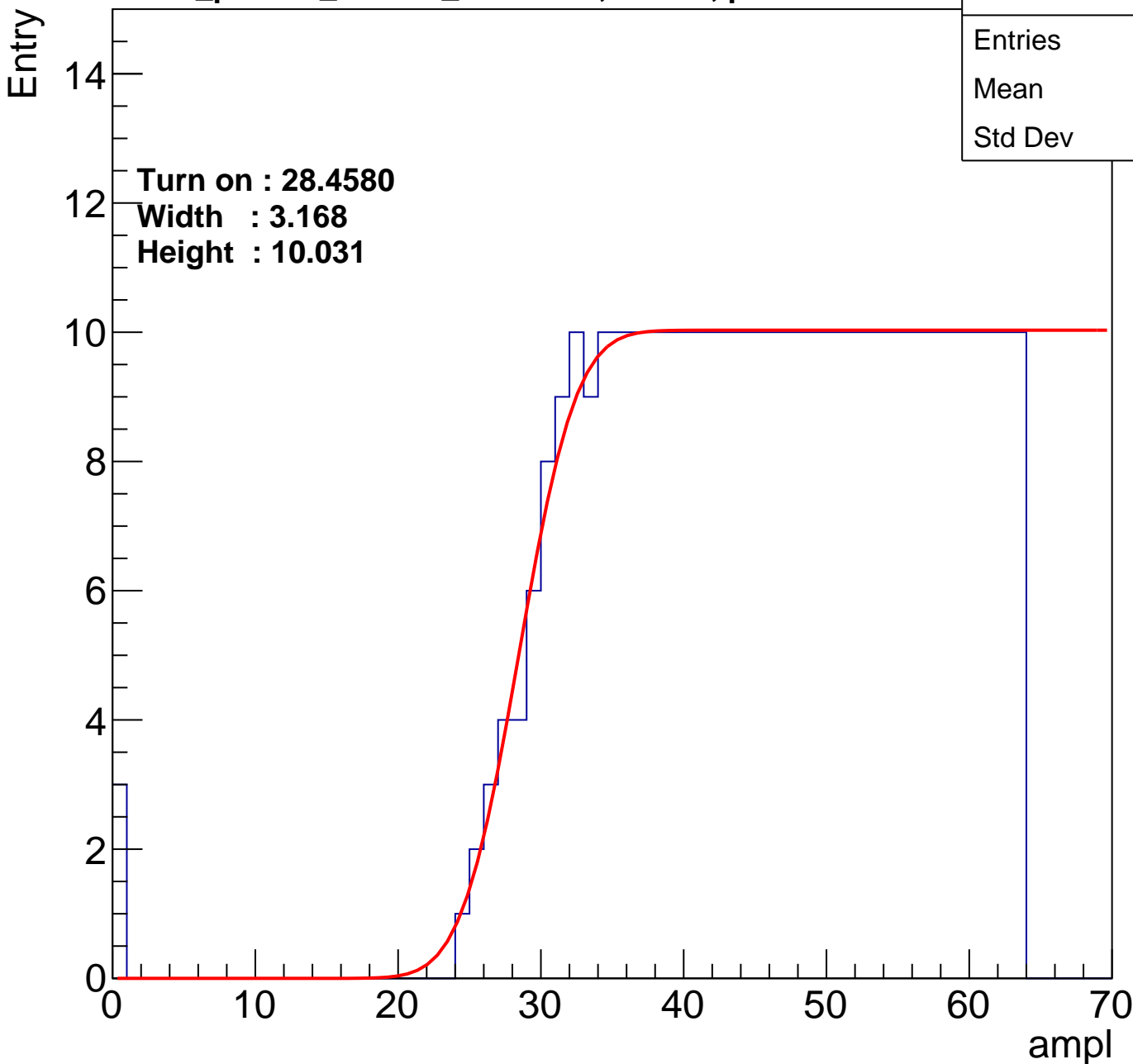
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 28.4580

Width : 3.168

Height : 10.031

Entries	359
Mean	45.21
Std Dev	11.2



B1L003S, U3-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	43.97
Std Dev	11.95

Turn on : 25.6909

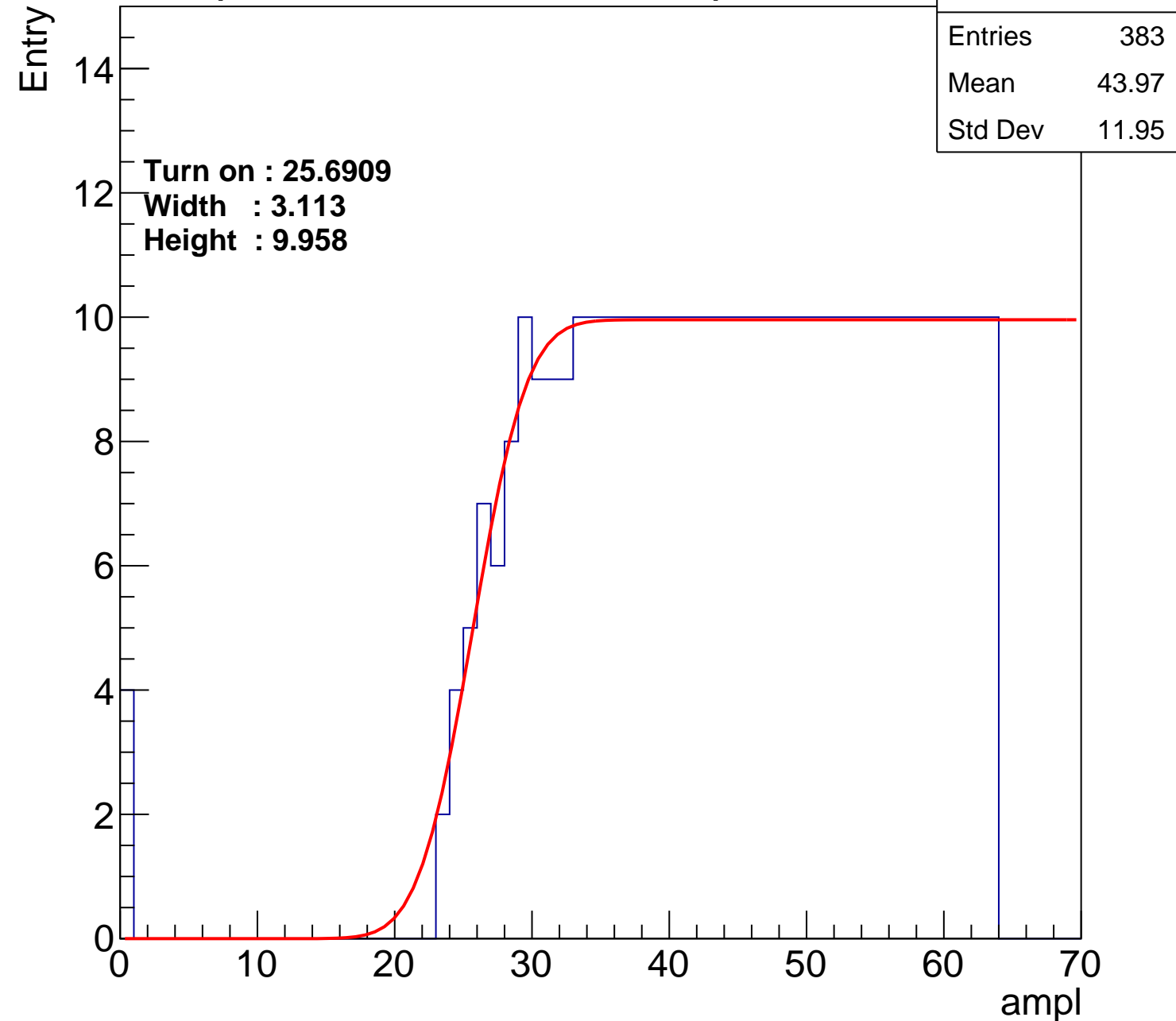
Width : 3.113

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.74
Std Dev	12.01

Turn on : 25.1034

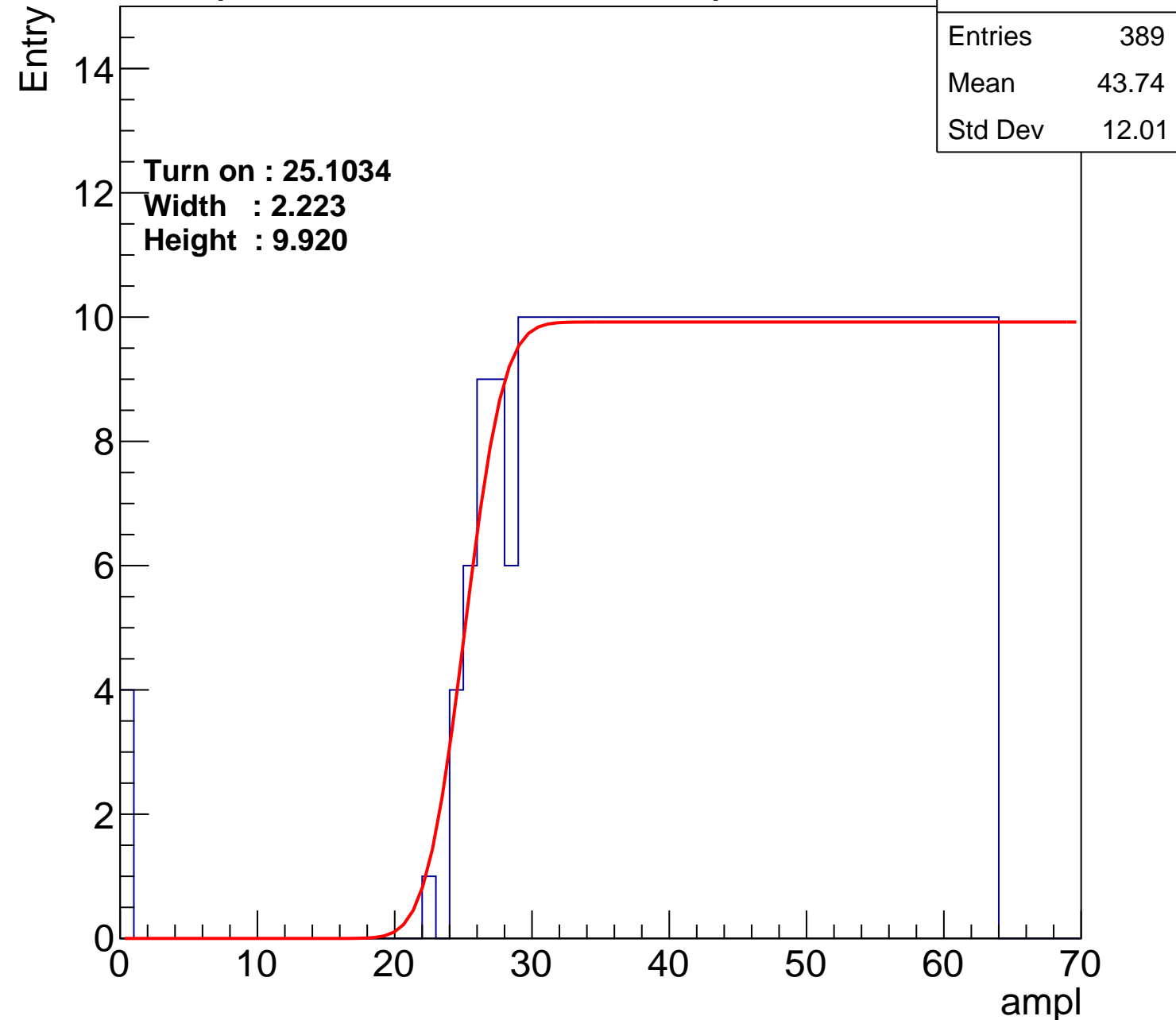
Width : 2.223

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch25

calib_packv5_042523_0143.root, FC#13, port D2

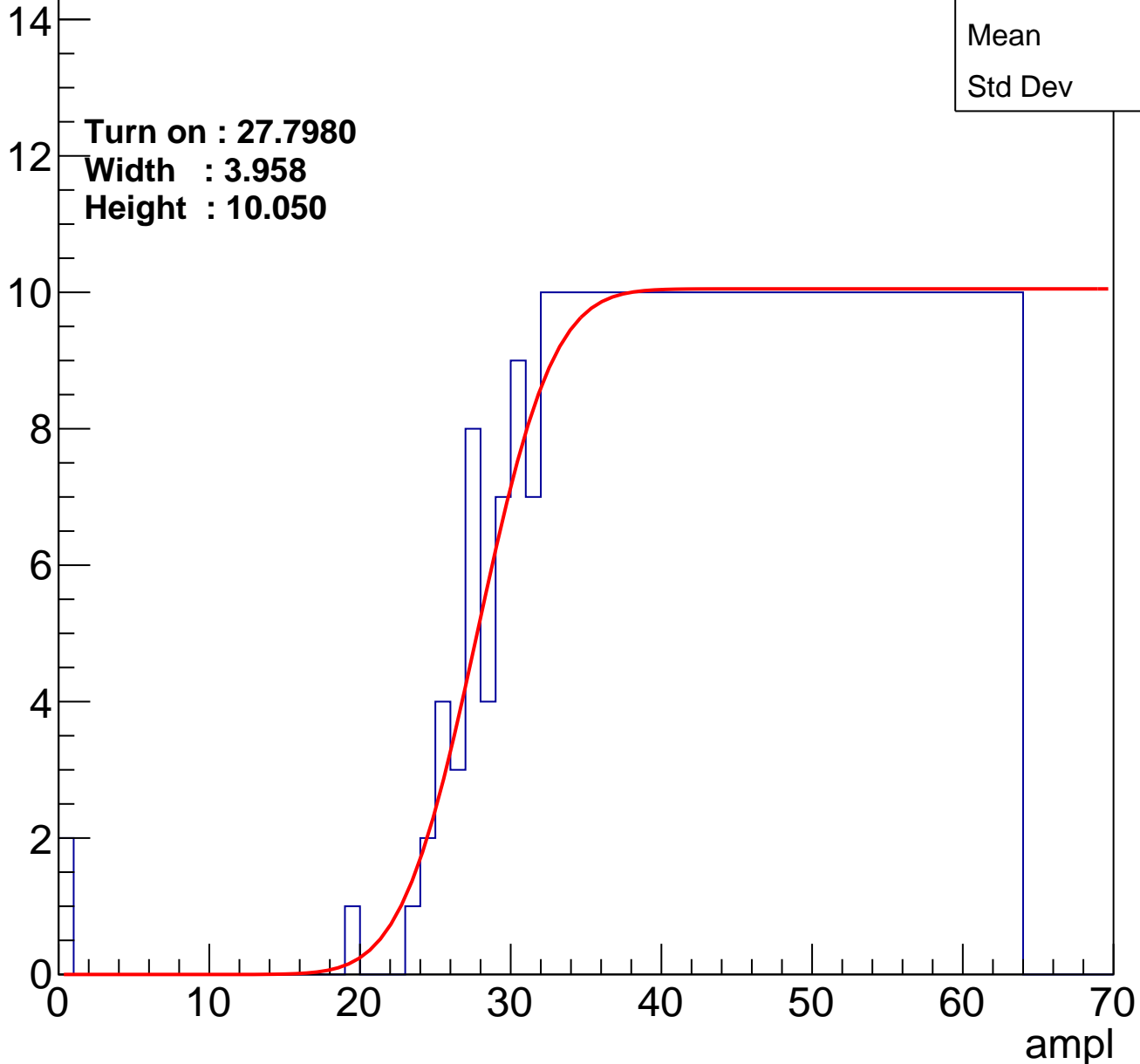
Entries	368
Mean	44.8
Std Dev	11.3

Turn on : 27.7980

Width : 3.958

Height : 10.050

Entry



B1L003S, U3-ch26

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	44.76
Std Dev	12.09

Turn on : 28.8271

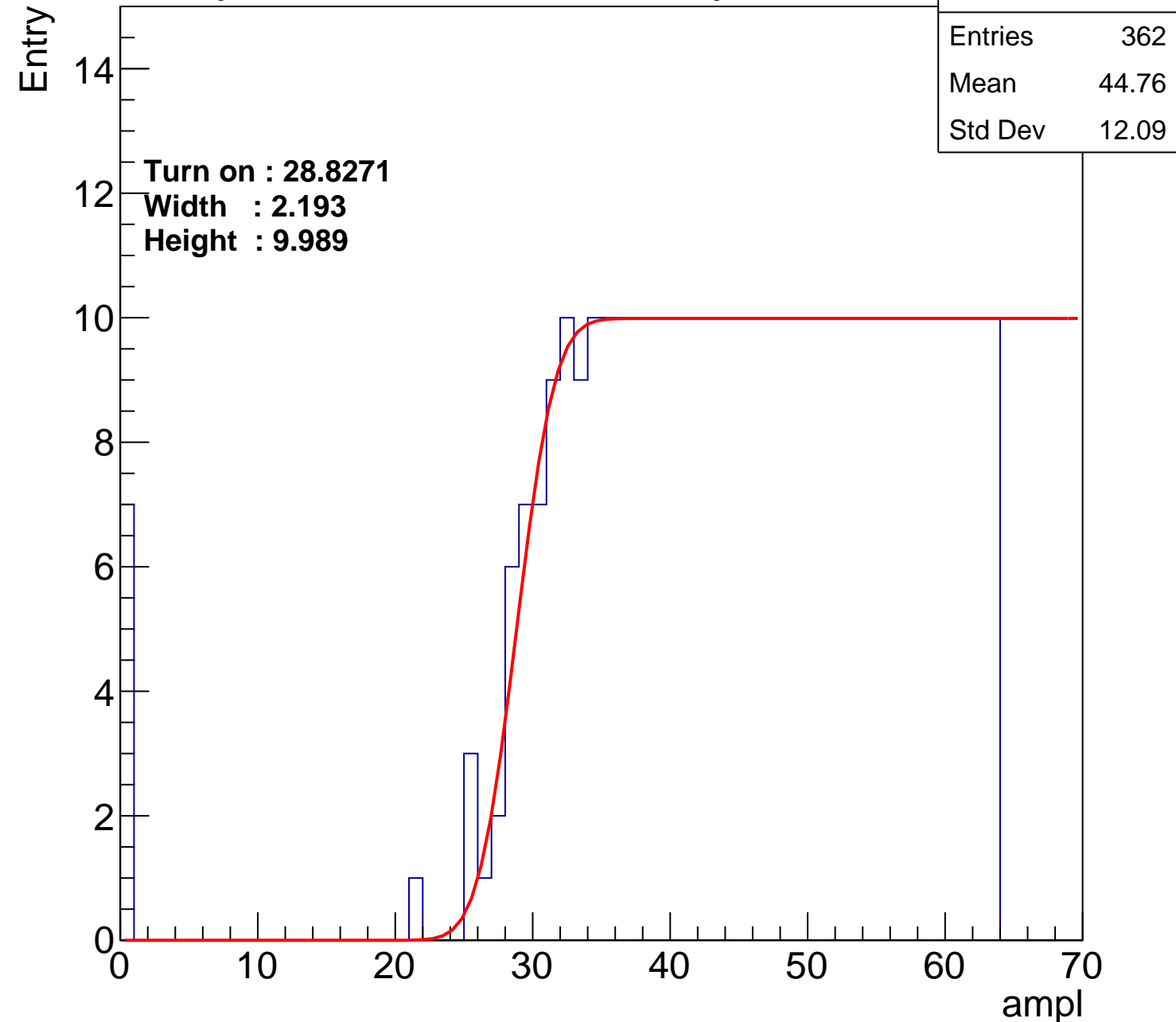
Width : 2.193

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.3
Std Dev	10.78

Turn on : 28.0186

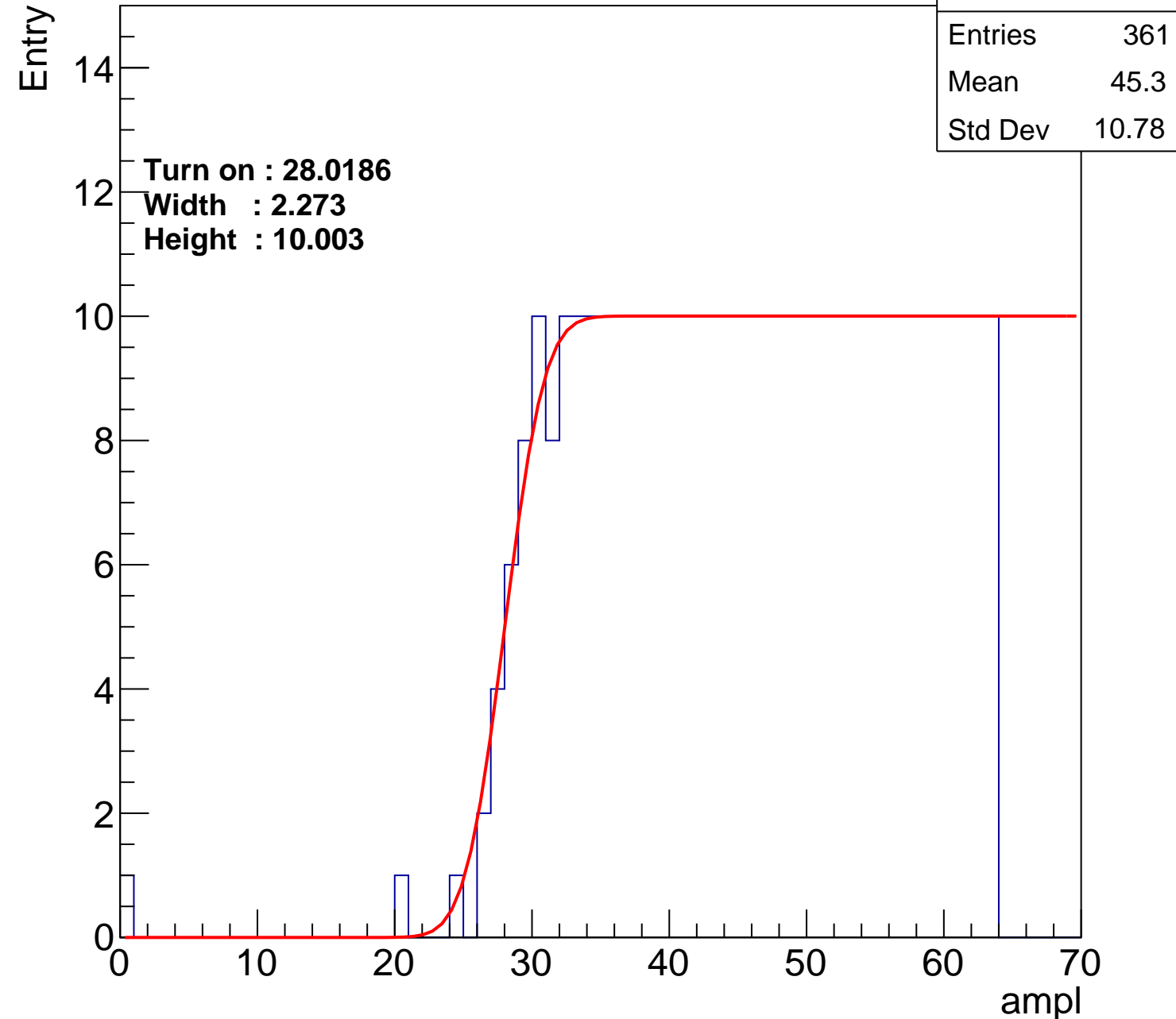
Width : 2.273

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.58
Std Dev	11.51

Turn on : 27.0671

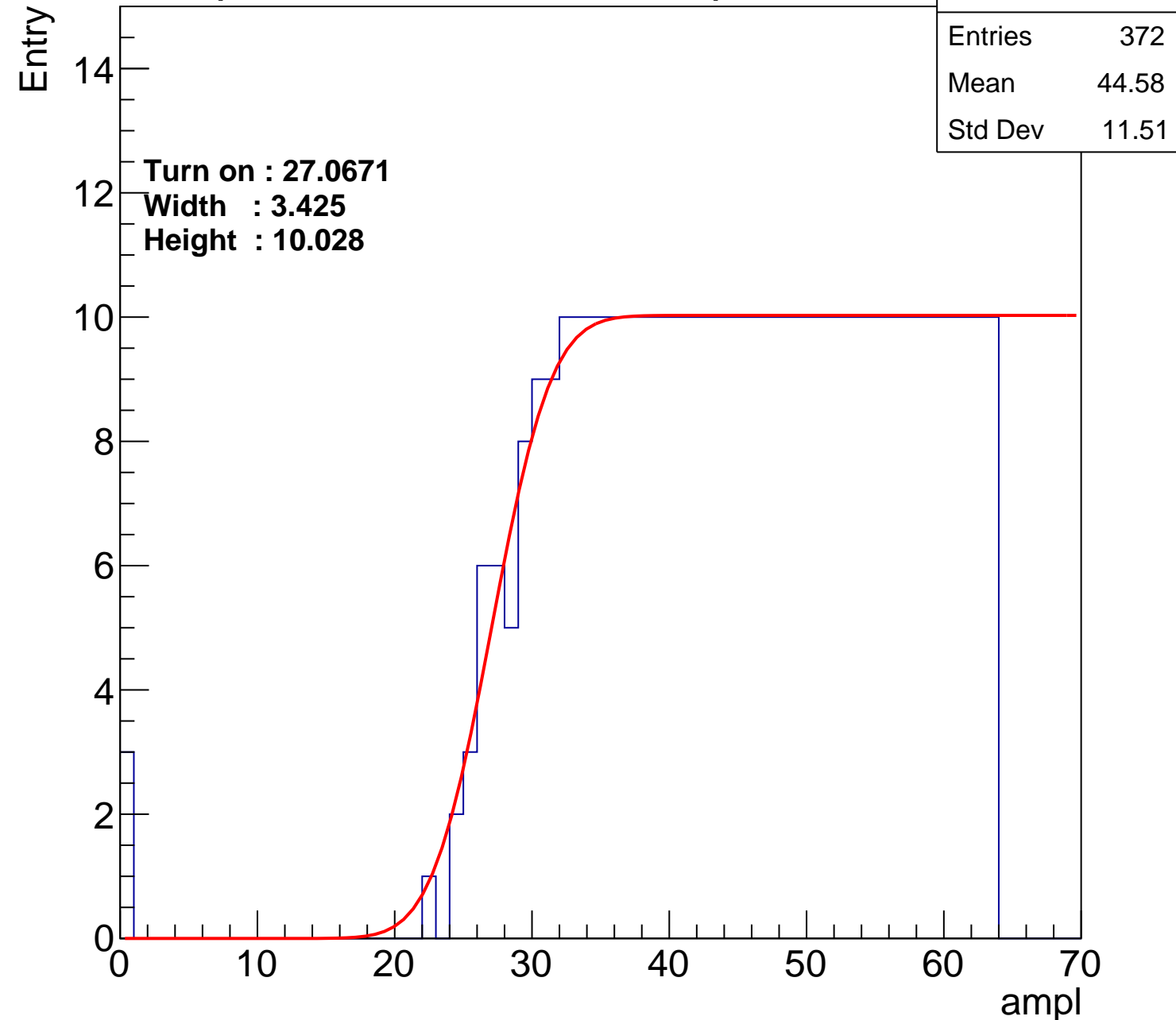
Width : 3.425

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.65
Std Dev	11.98

Turn on : 25.4340

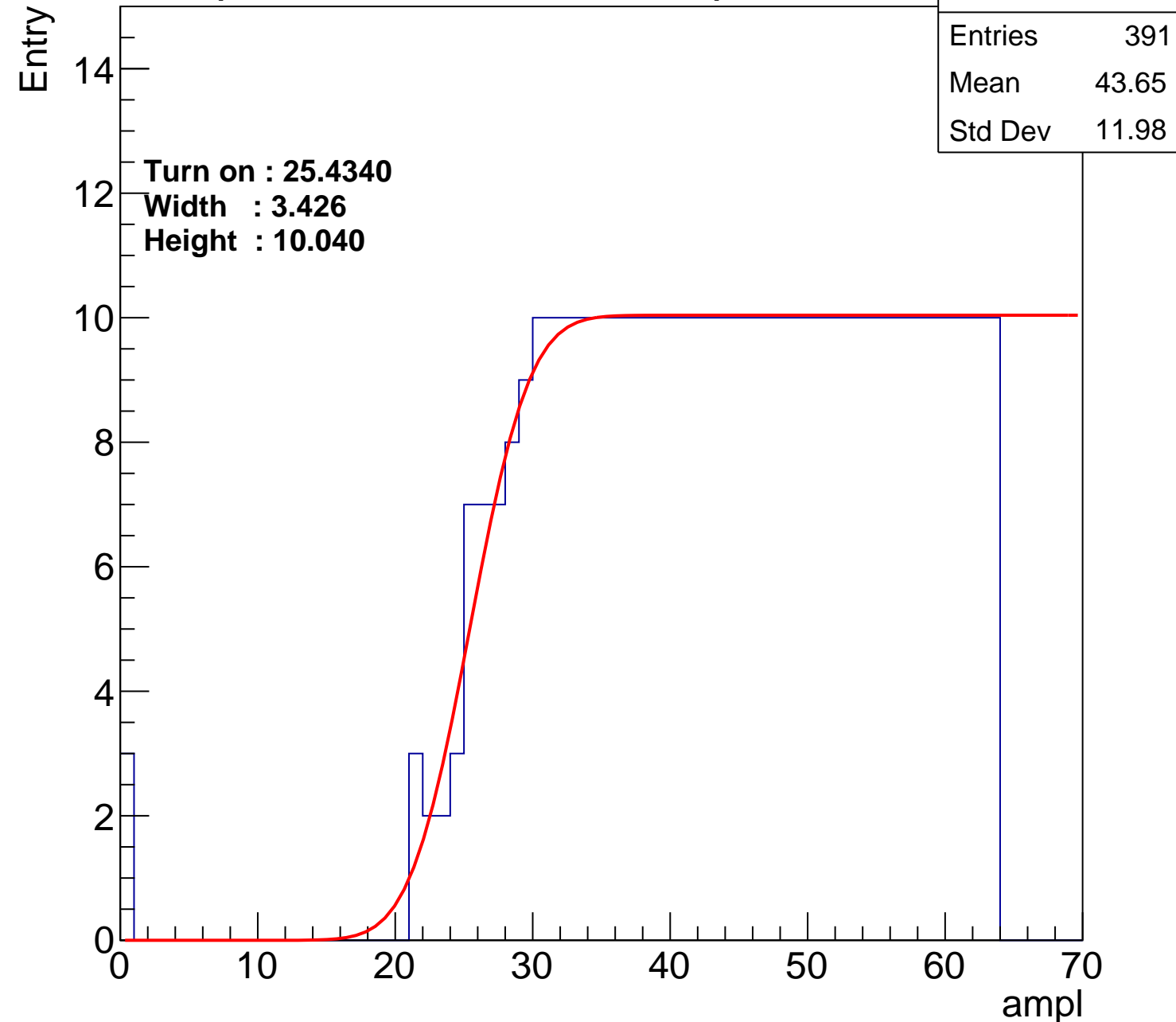
Width : 3.426

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch30

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.82
Std Dev	11

Turn on : 26.8961

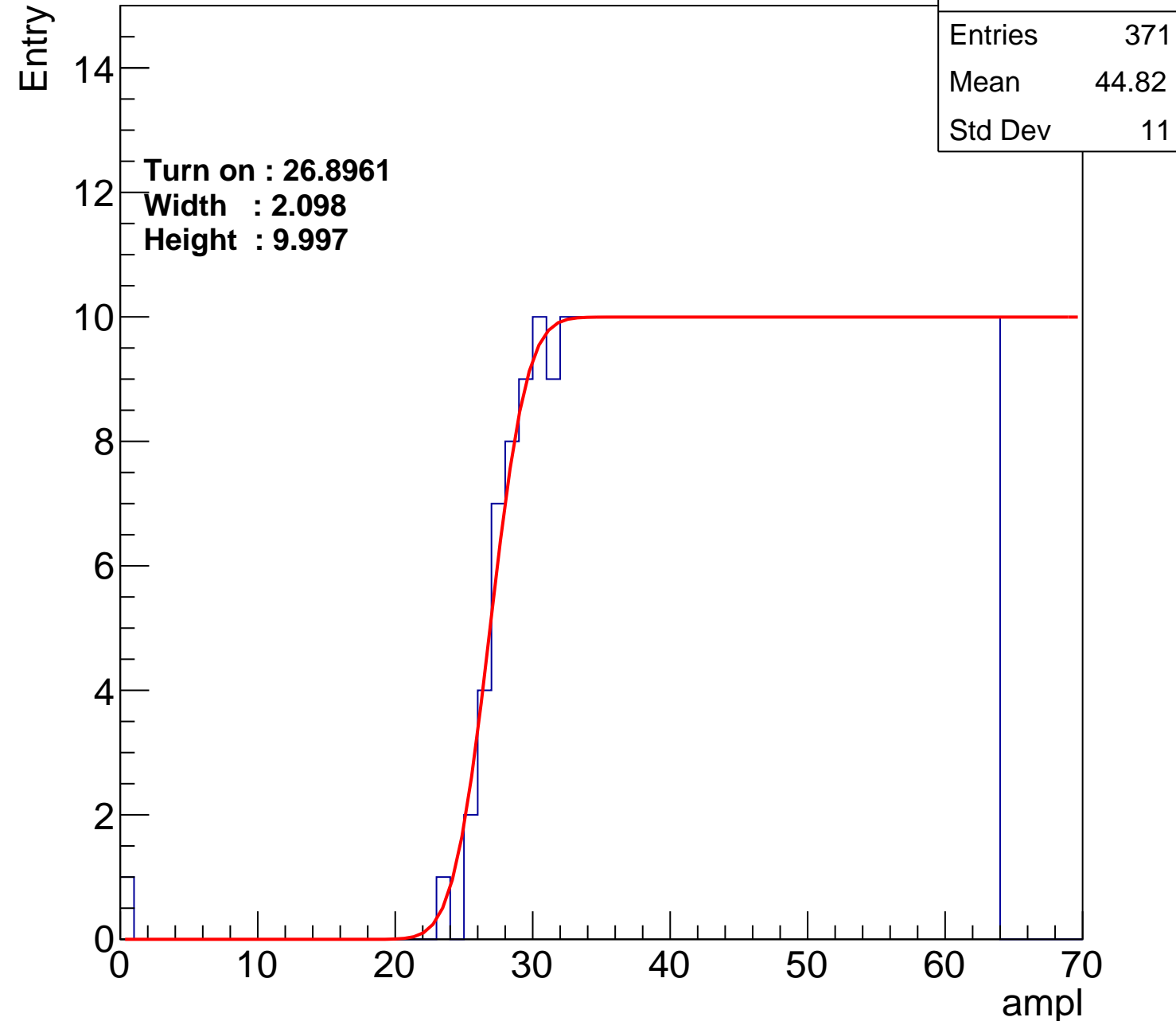
Width : 2.098

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.06
Std Dev	11.48

Turn on : 28.5815

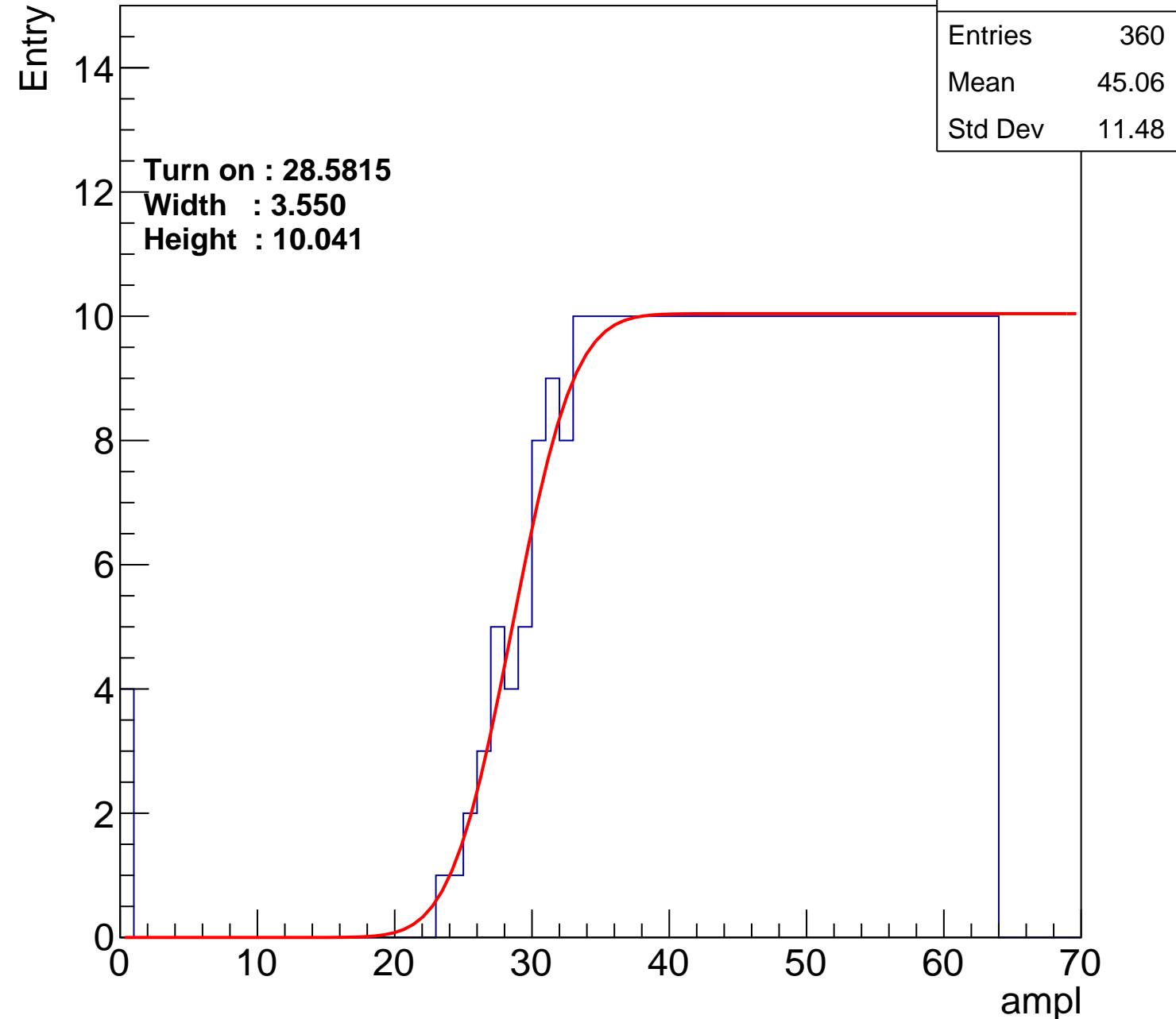
Width : 3.550

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.0253

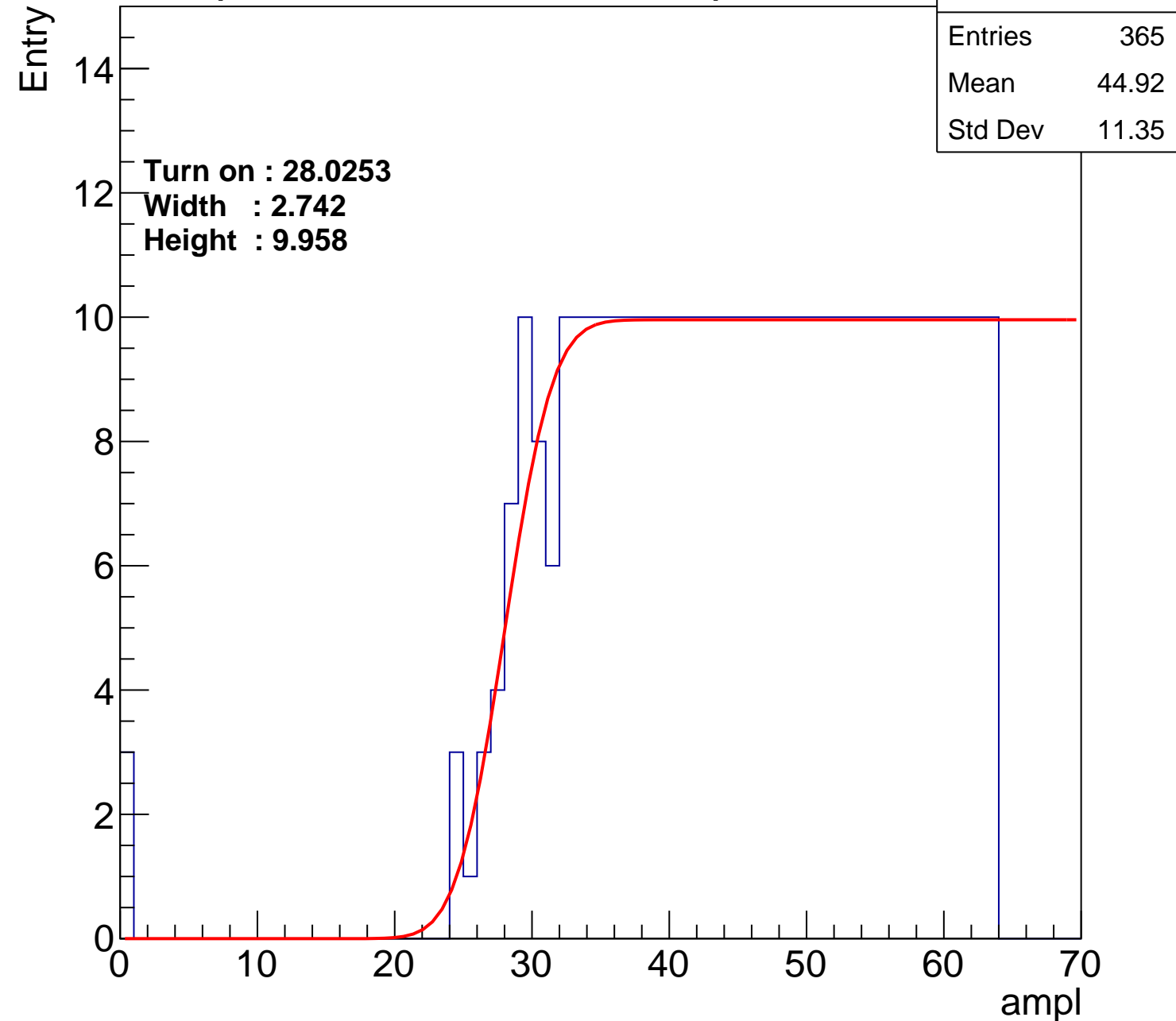
Width : 2.742

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch33

calib_packv5_042523_0143.root, FC#13, port D2

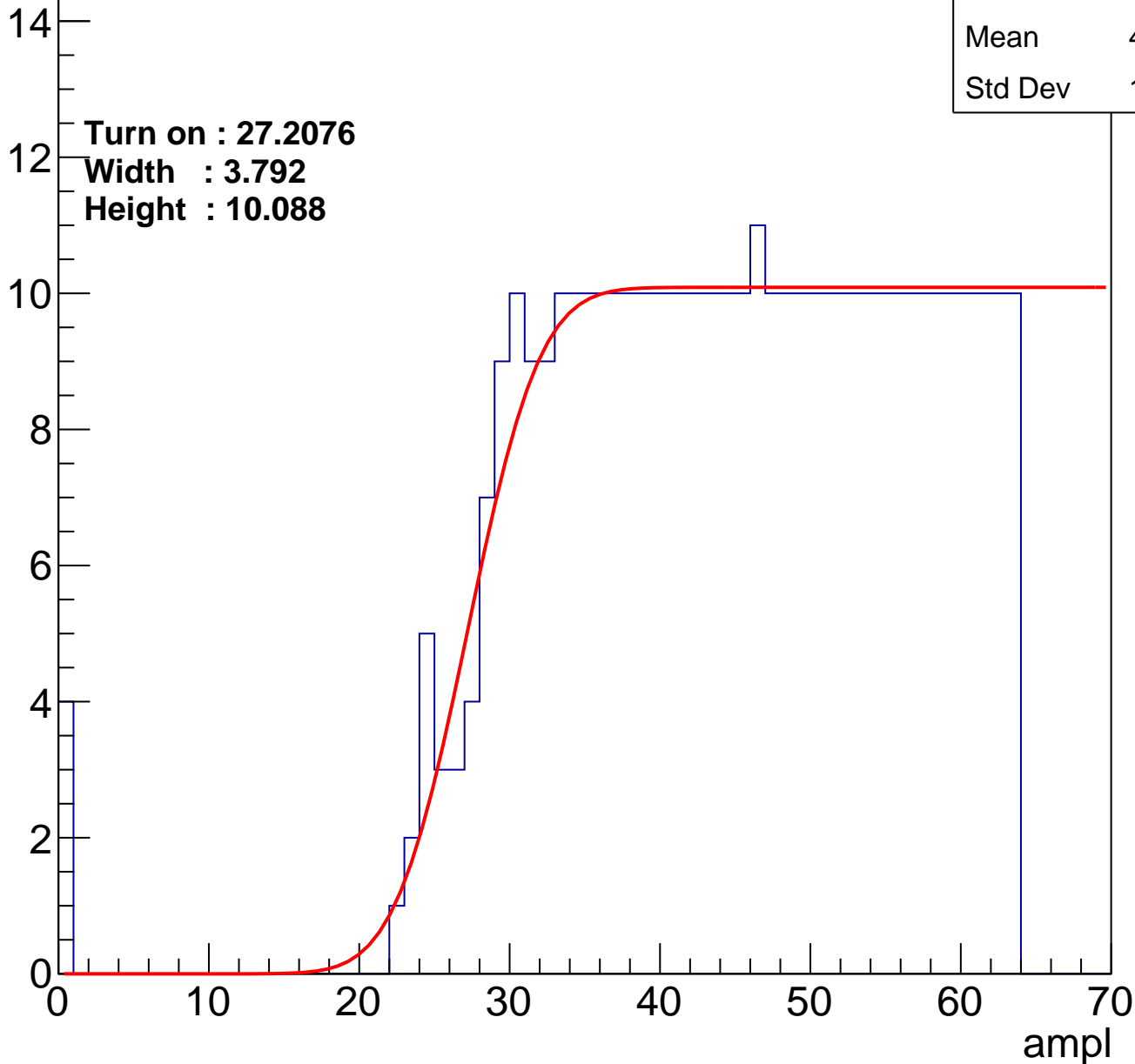
Entry

Entries	377
Mean	44.29
Std Dev	11.82

Turn on : 27.2076

Width : 3.792

Height : 10.088



B1L003S, U3-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	45.05
Std Dev	11.1

Turn on : 27.7584

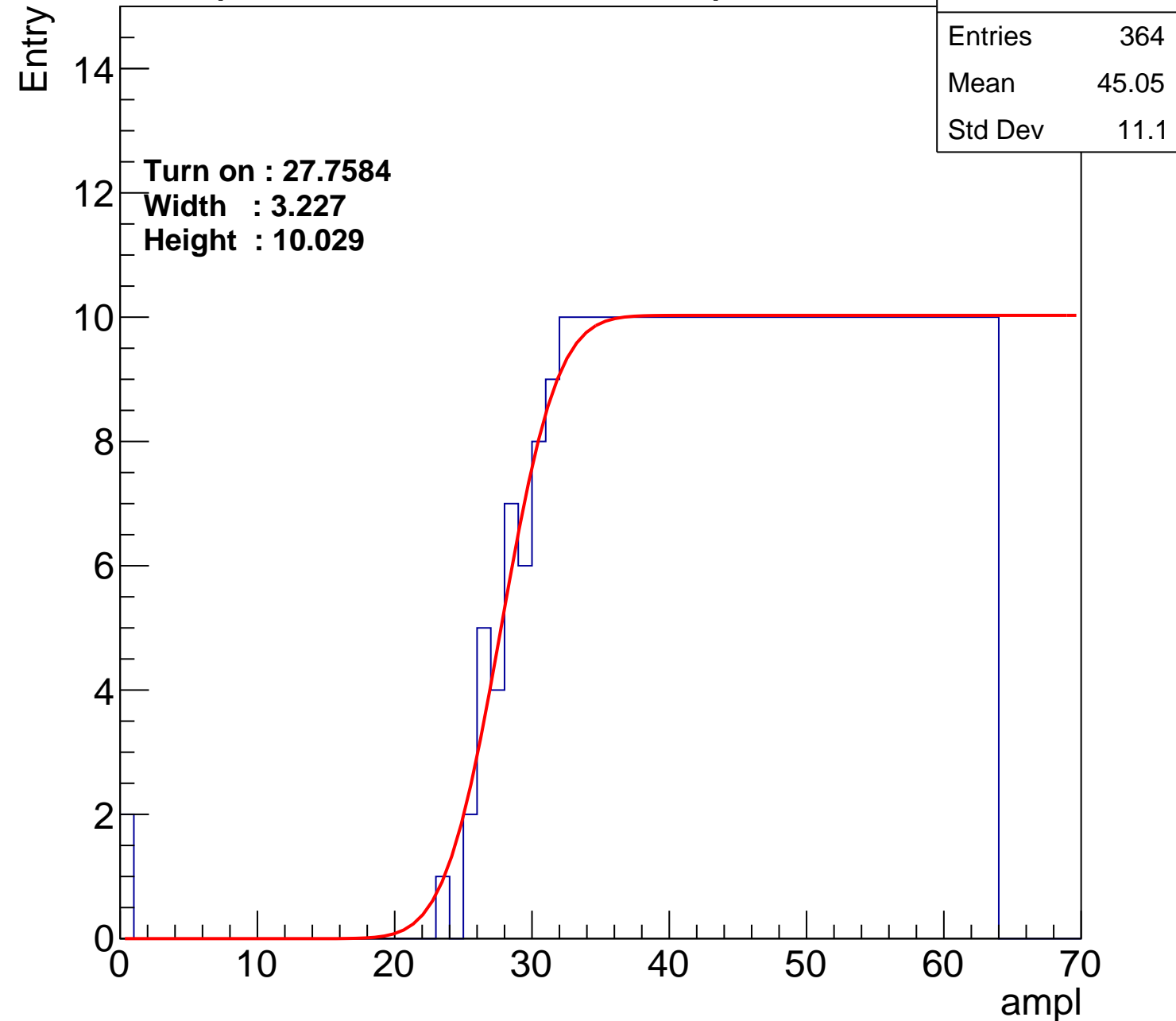
Width : 3.227

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch35

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.81
Std Dev	11.37

Turn on : 27.7630

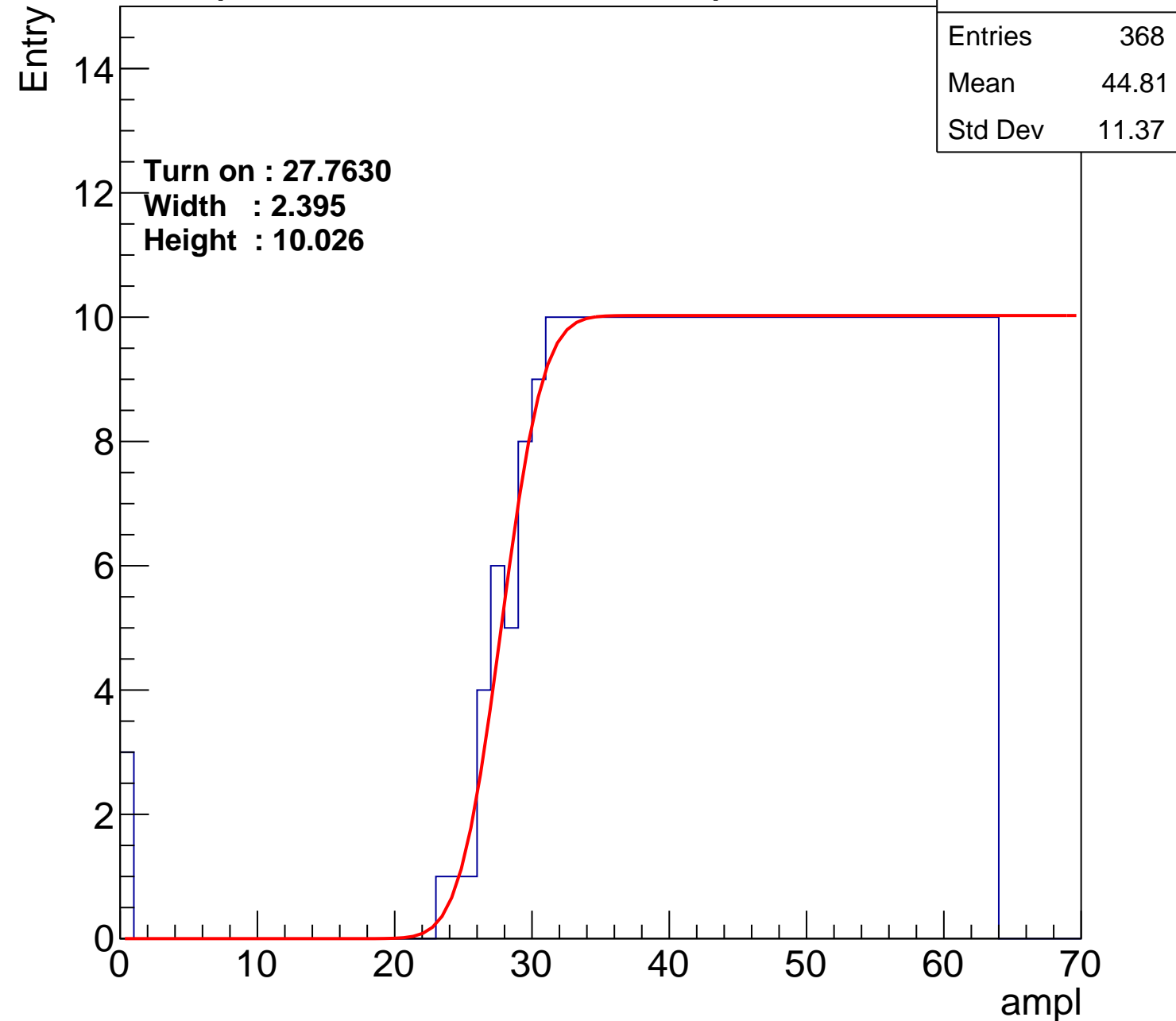
Width : 2.395

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch36

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.03
Std Dev	11.53

Turn on : 28.6087

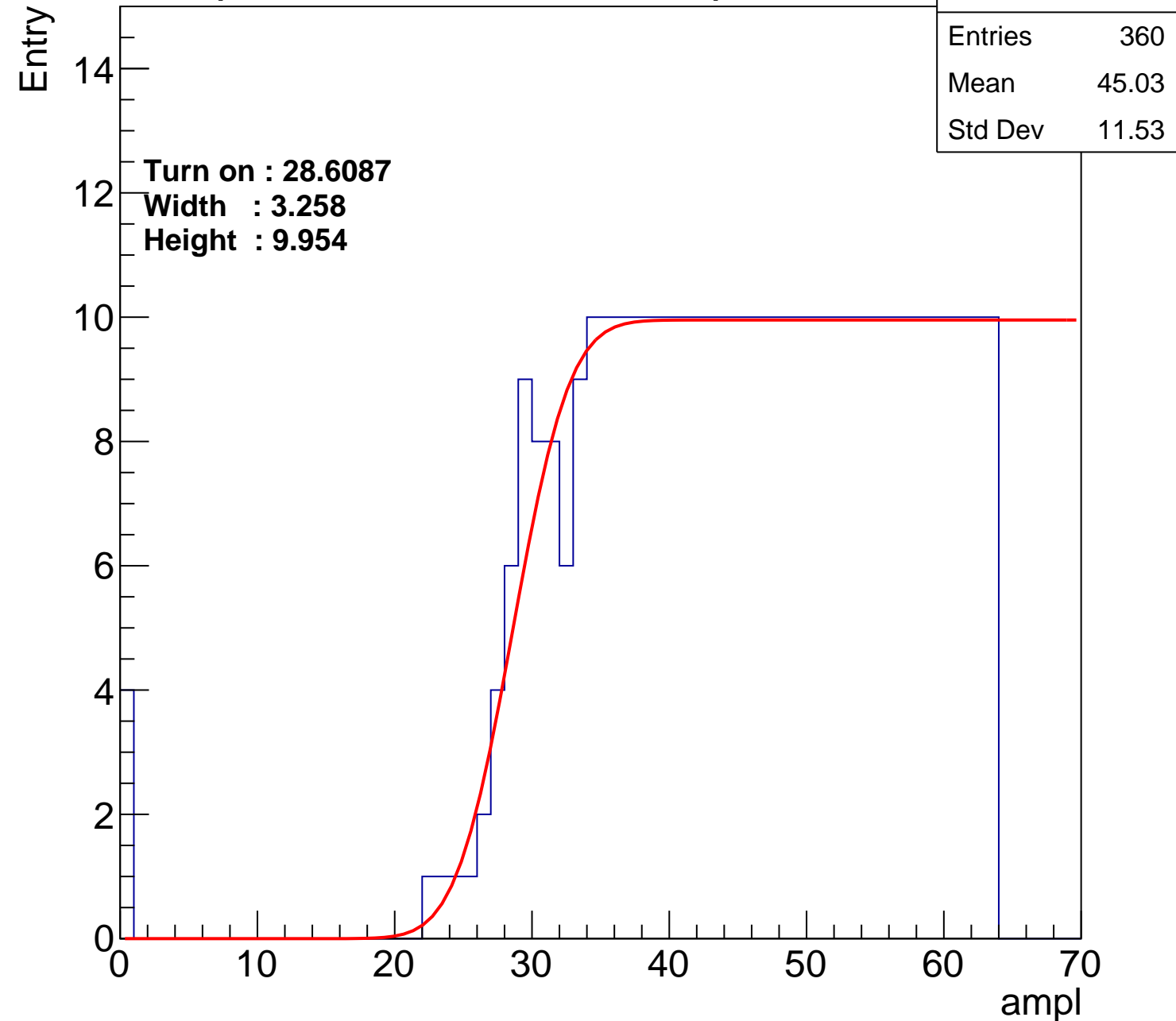
Width : 3.258

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.63
Std Dev	12.35

Turn on : 25.7769

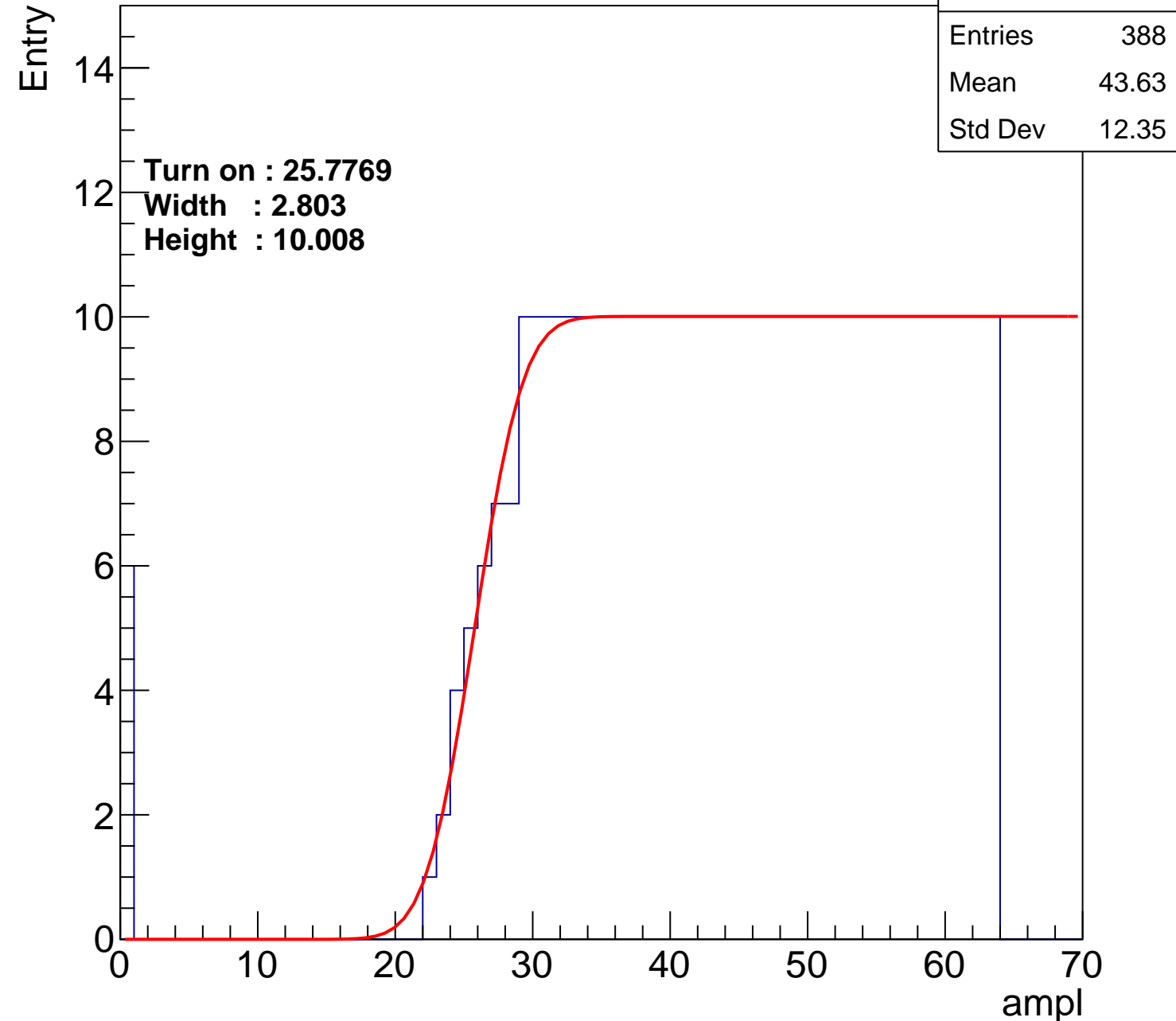
Width : 2.803

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch38

calib_packv5_042523_0143.root, FC#13, port D2

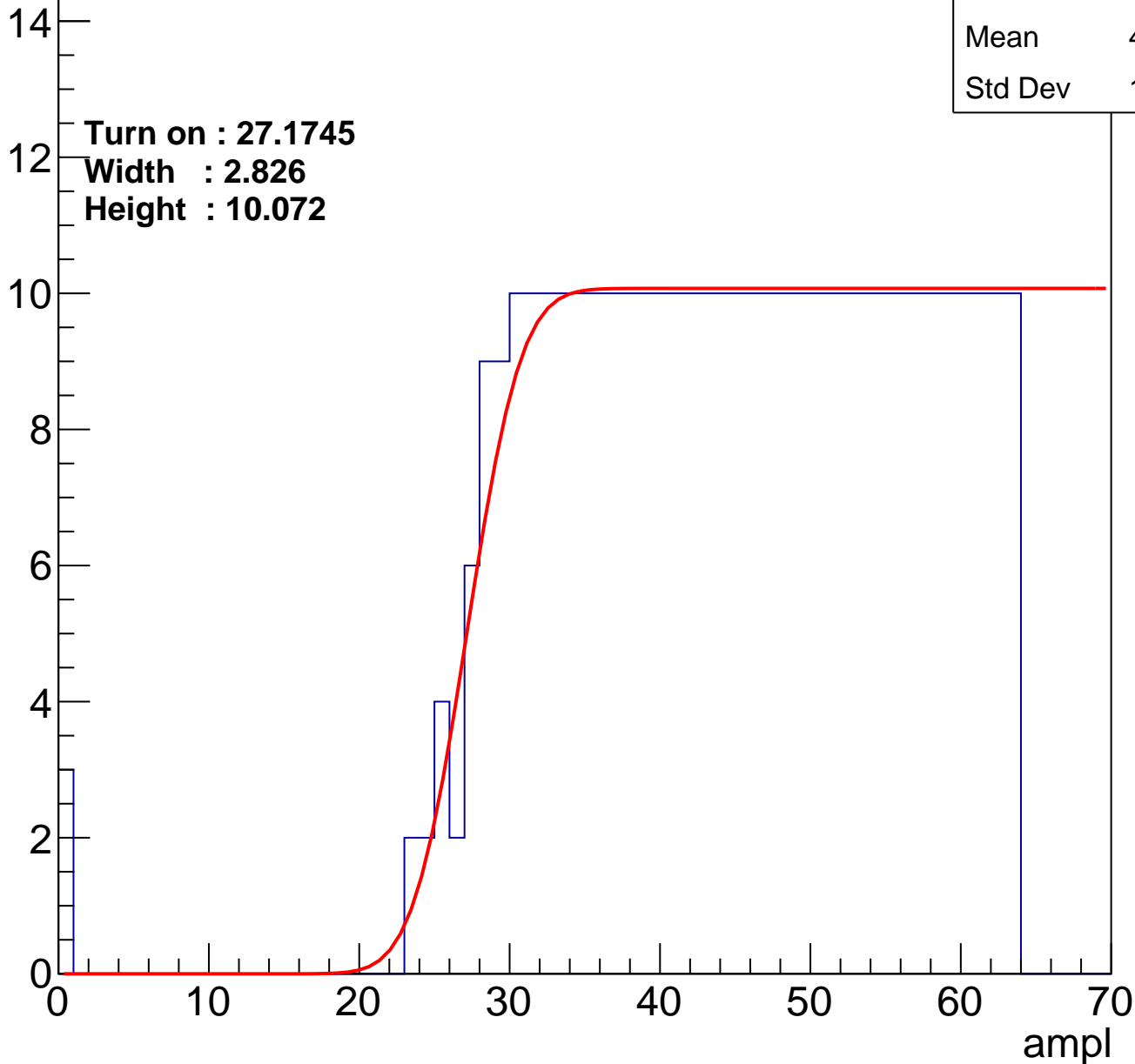
Entry

Entries	377
Mean	44.38
Std Dev	11.57

Turn on : 27.1745

Width : 2.826

Height : 10.072



B1L003S, U3-ch39

calib_packv5_042523_0143.root, FC#13, port D2

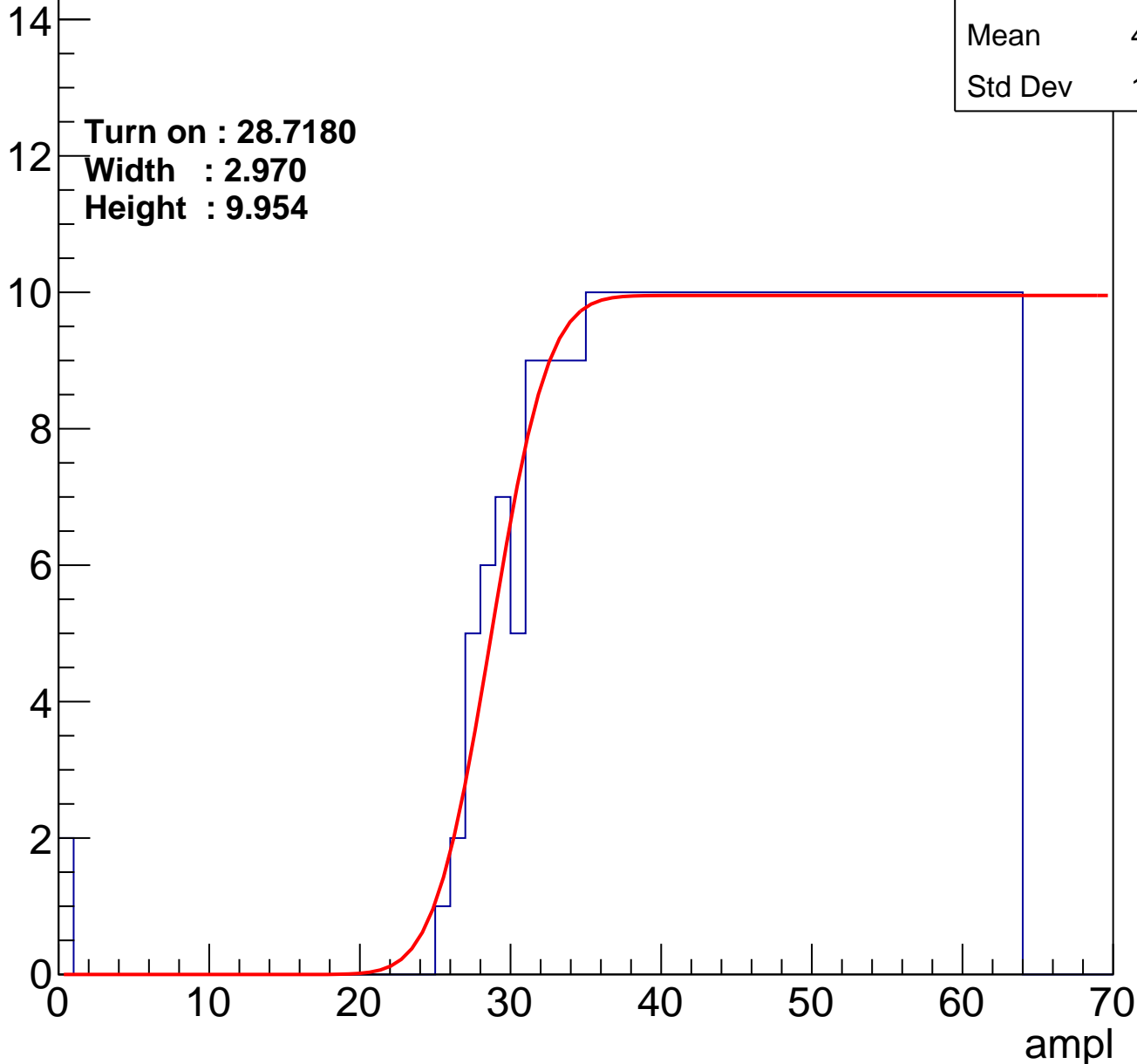
Entry

Entries	354
Mean	45.52
Std Dev	10.89

Turn on : 28.7180

Width : 2.970

Height : 9.954



B1L003S, U3-ch40

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 27.8367

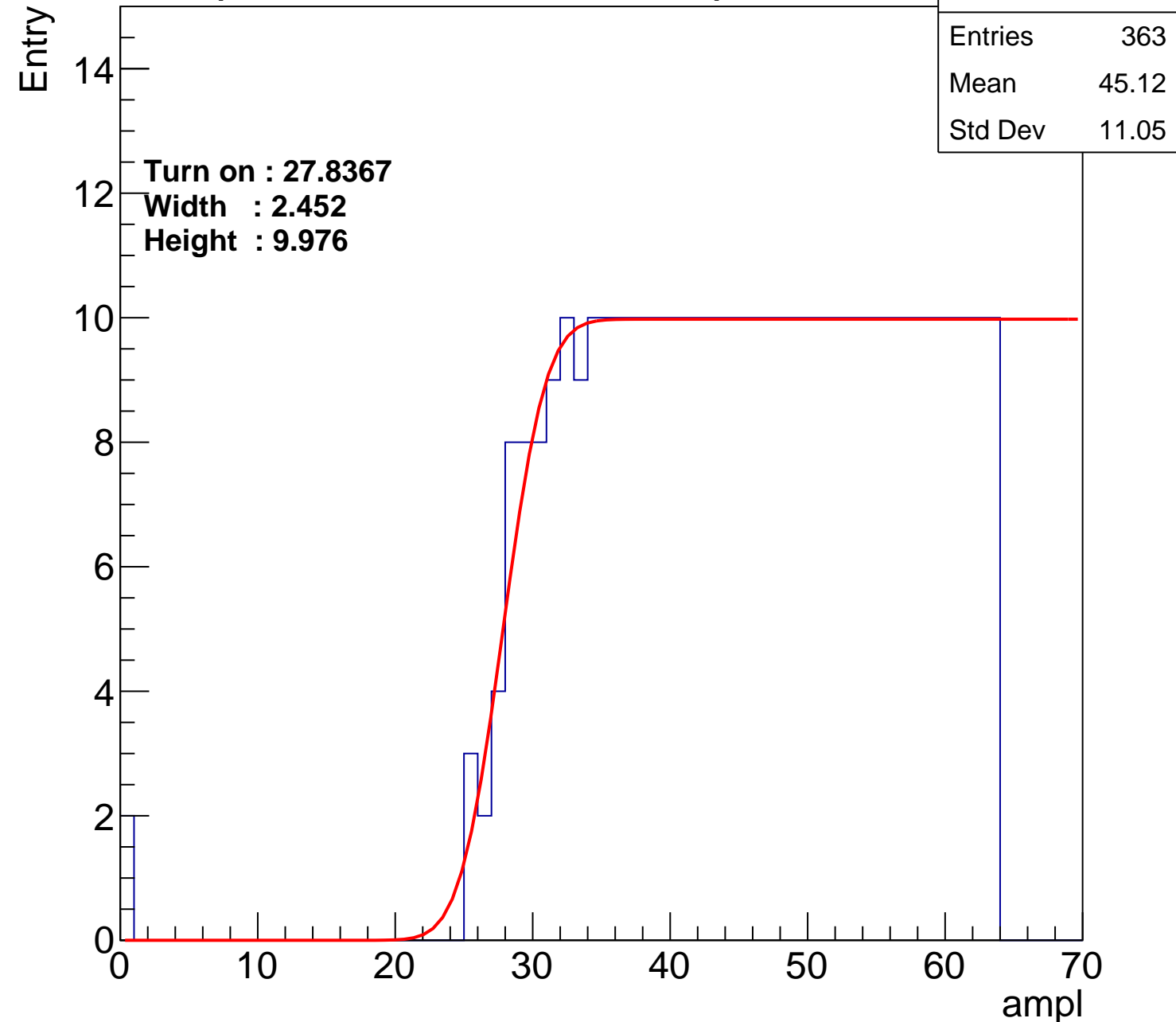
Width : 2.452

Height : 9.976

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch41

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.4386

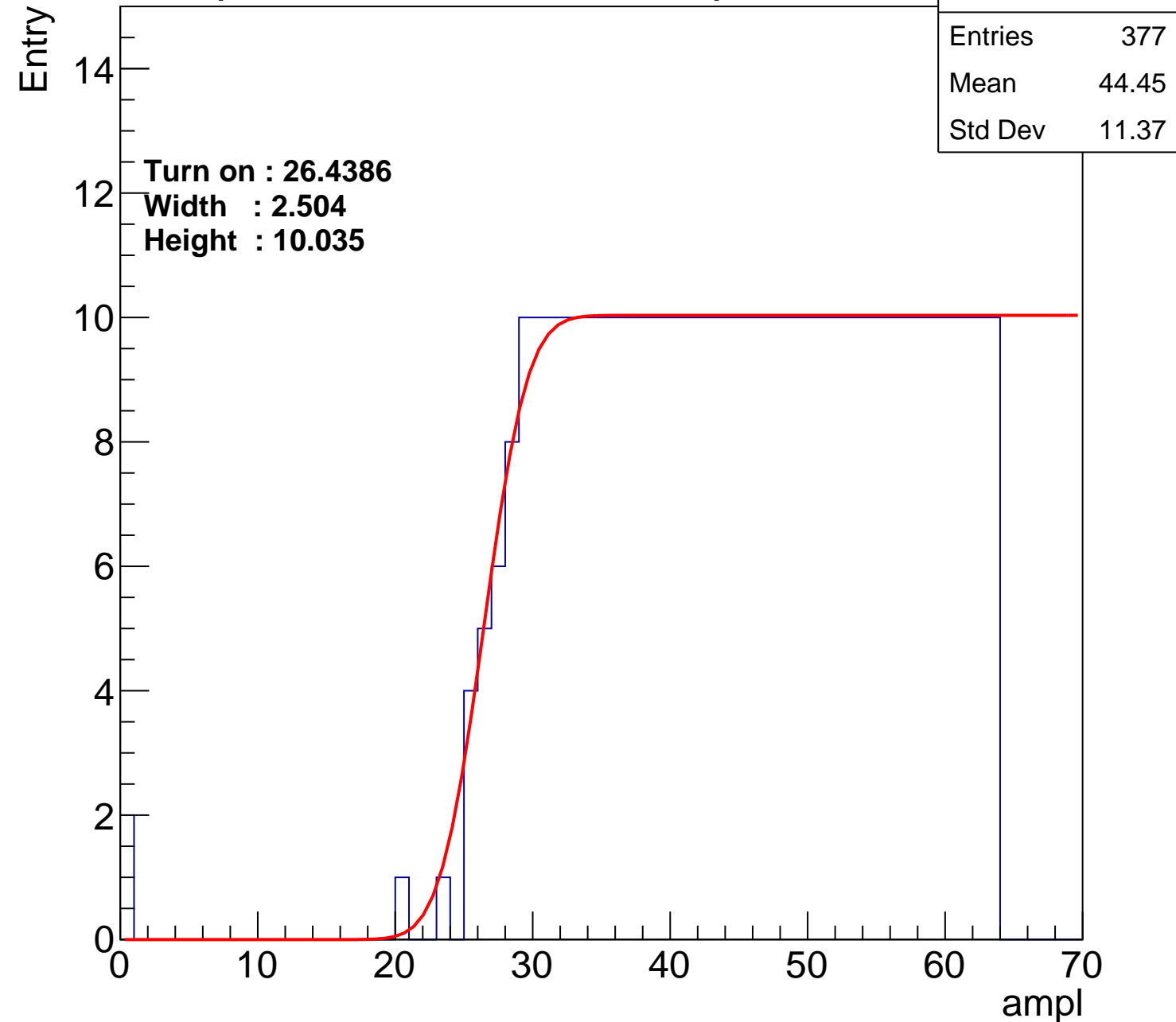
Width : 2.504

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.68
Std Dev	11.13

Turn on : 27.0449

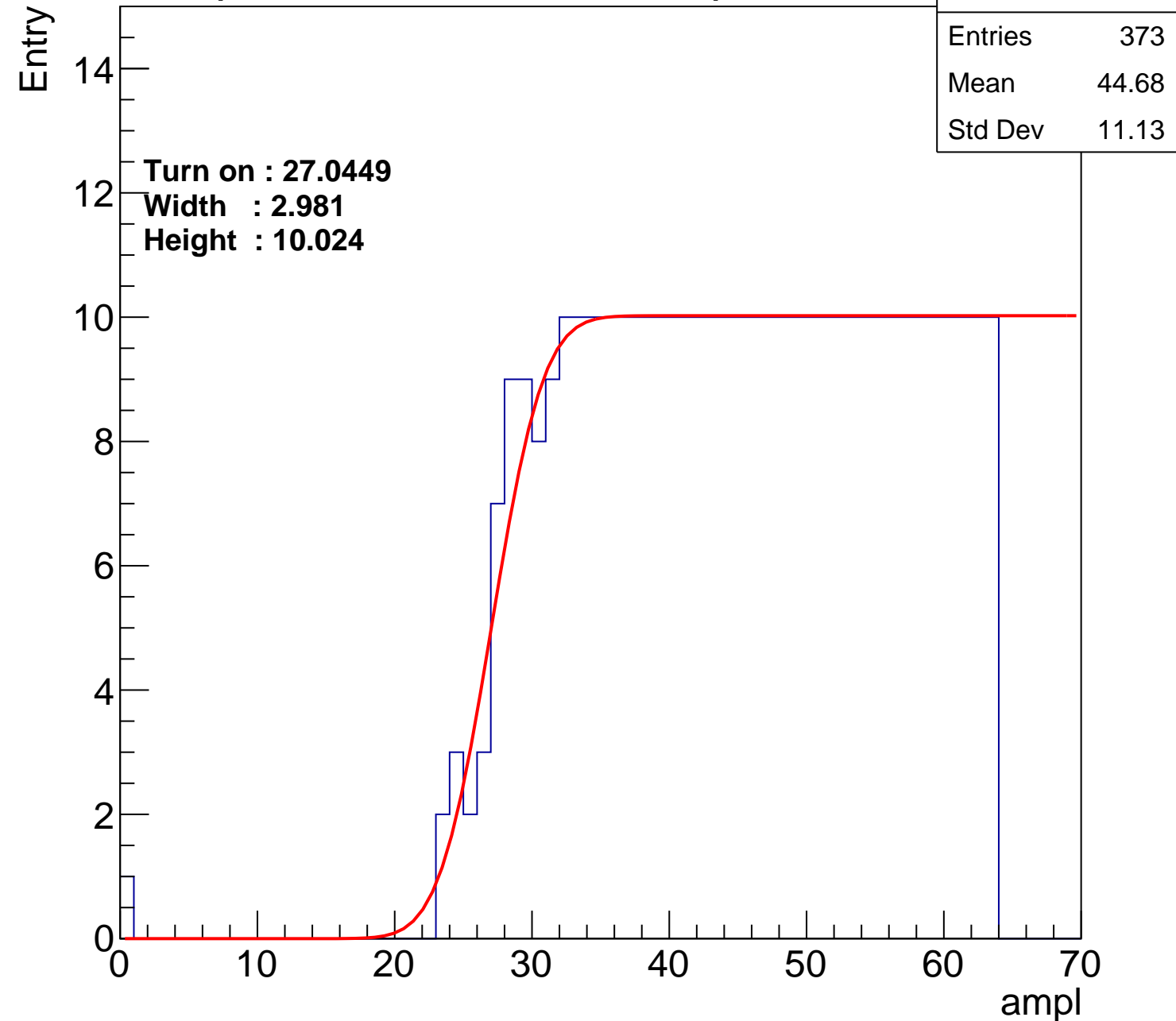
Width : 2.981

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	358
Mean	45.34
Std Dev	10.96

Turn on : 28.7563

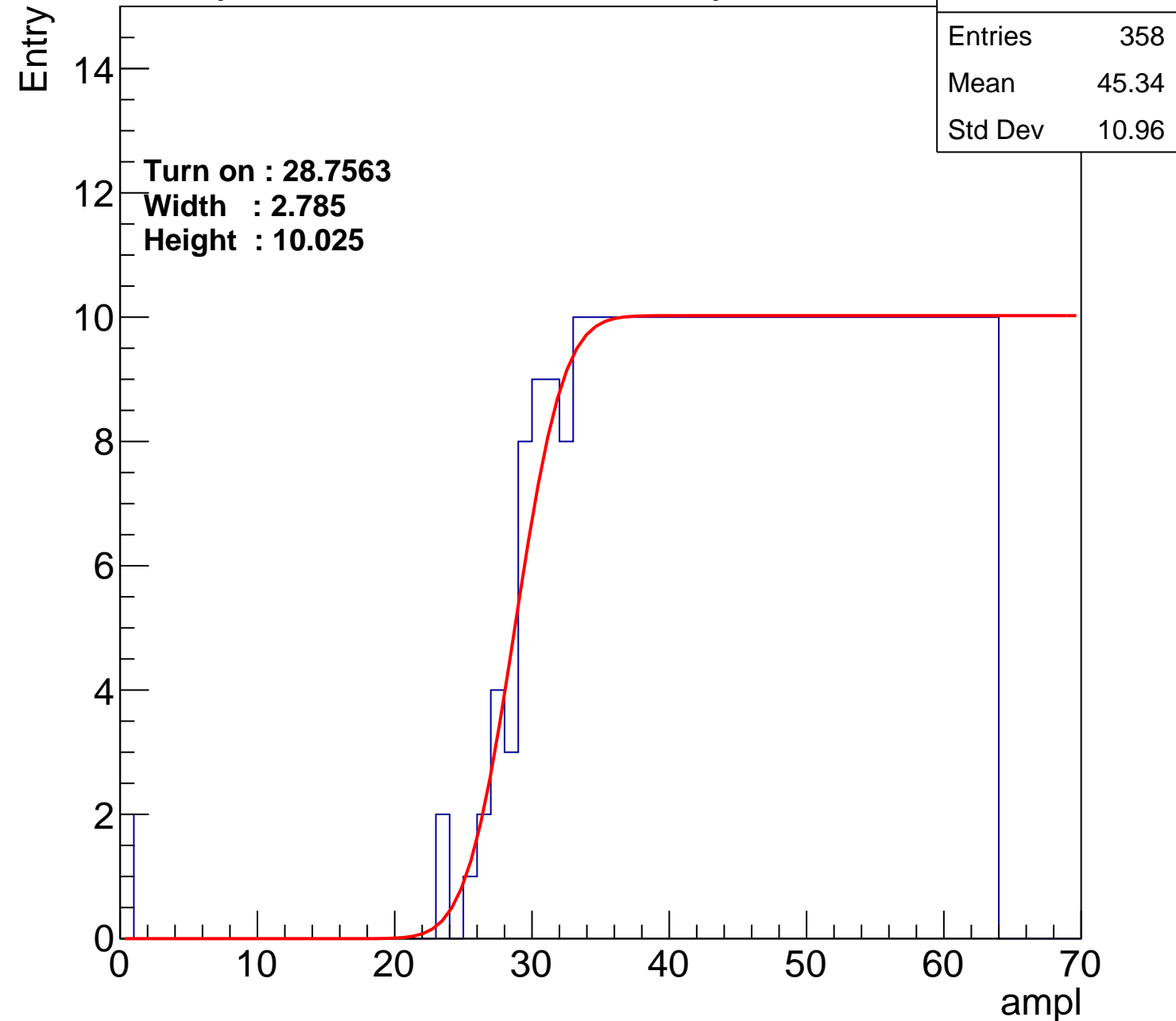
Width : 2.785

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.55
Std Dev	11.37

Turn on : 26.7140

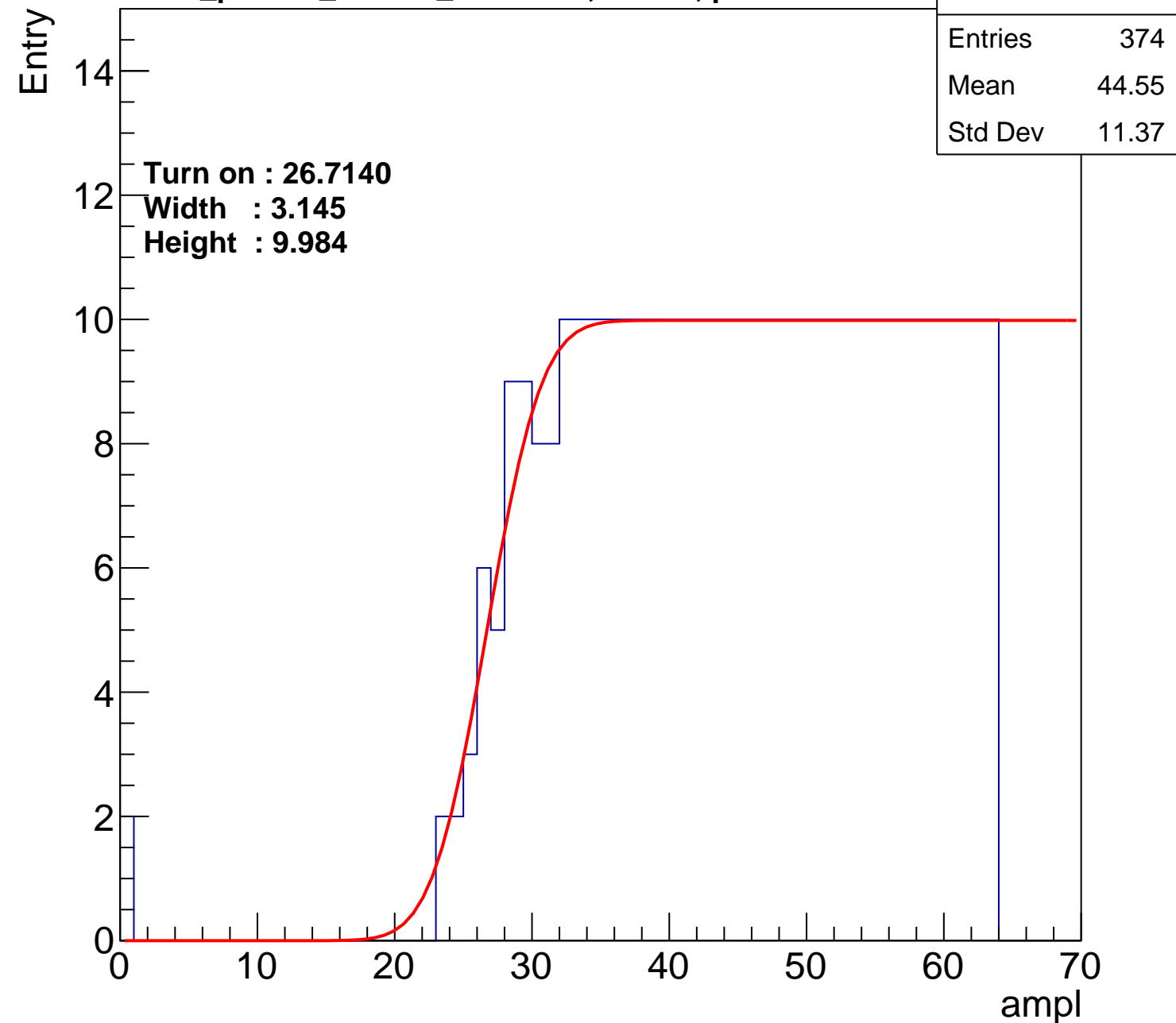
Width : 3.145

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch45

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.13
Std Dev	11.92

Turn on : 26.3409

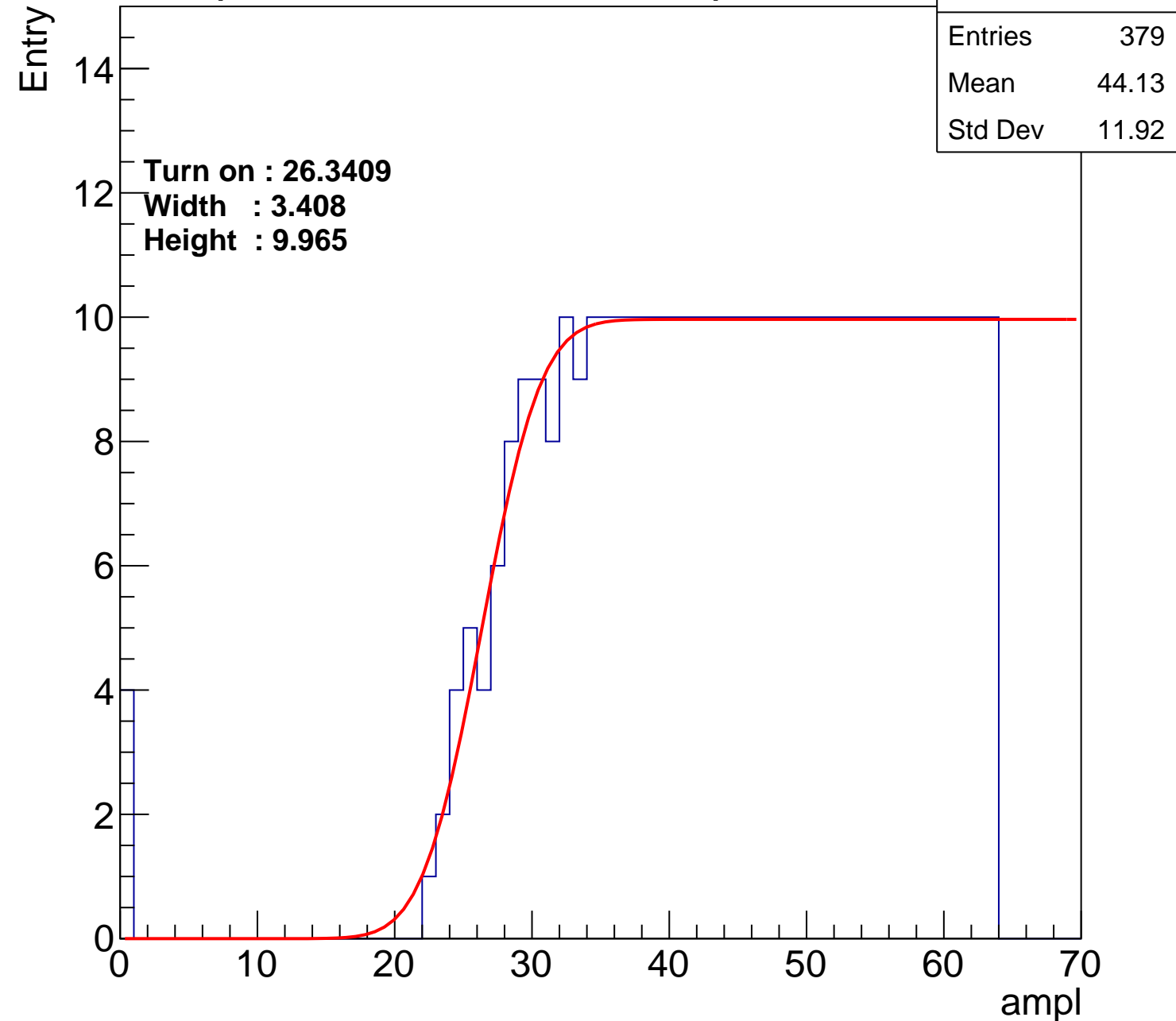
Width : 3.408

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch46

calib_packv5_042523_0143.root, FC#13, port D2

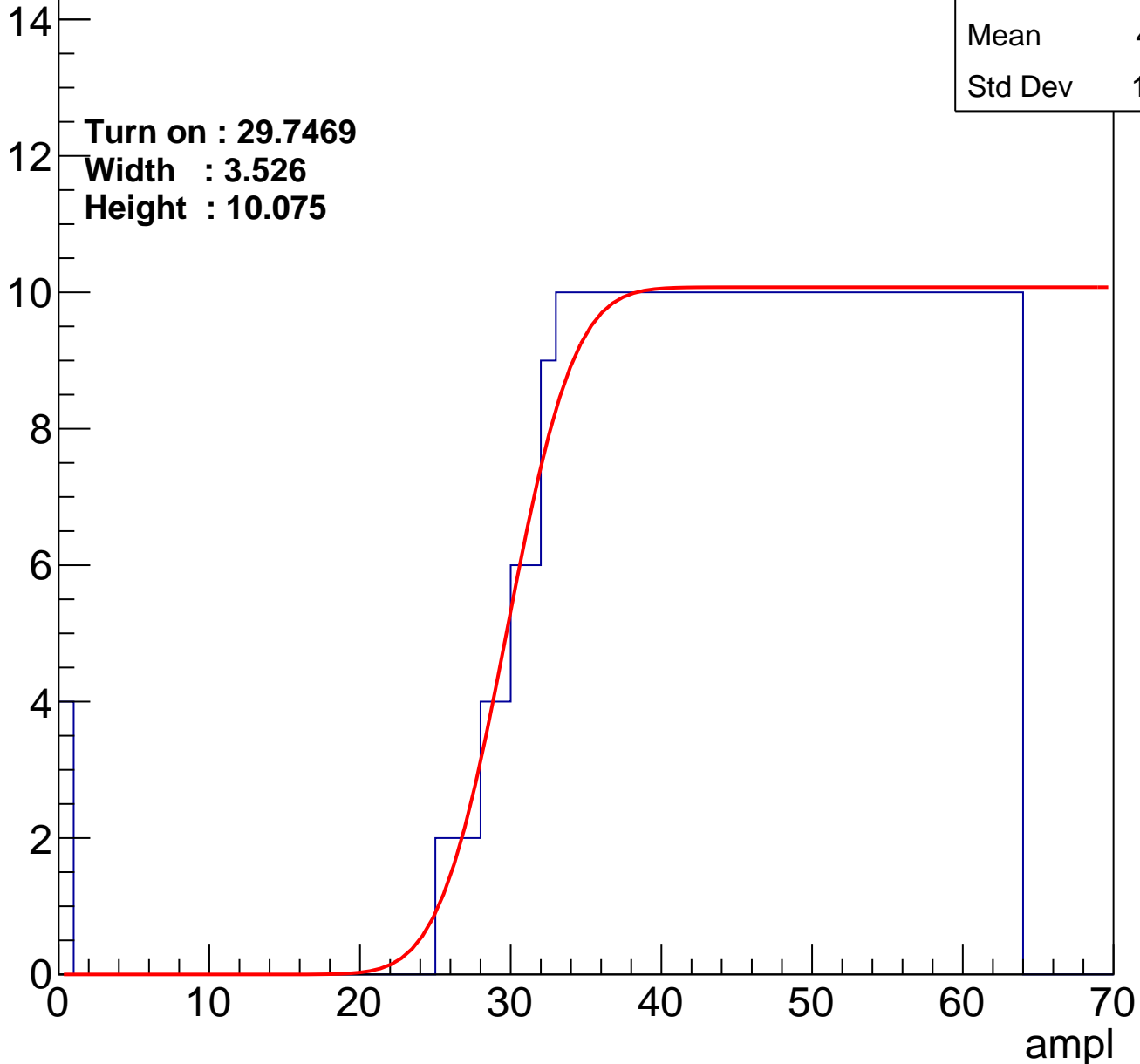
Entries	349
Mean	45.61
Std Dev	11.22

Turn on : 29.7469

Width : 3.526

Height : 10.075

Entry



B1L003S, U3-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	44.02
Std Dev	11.65

Turn on : 26.3923

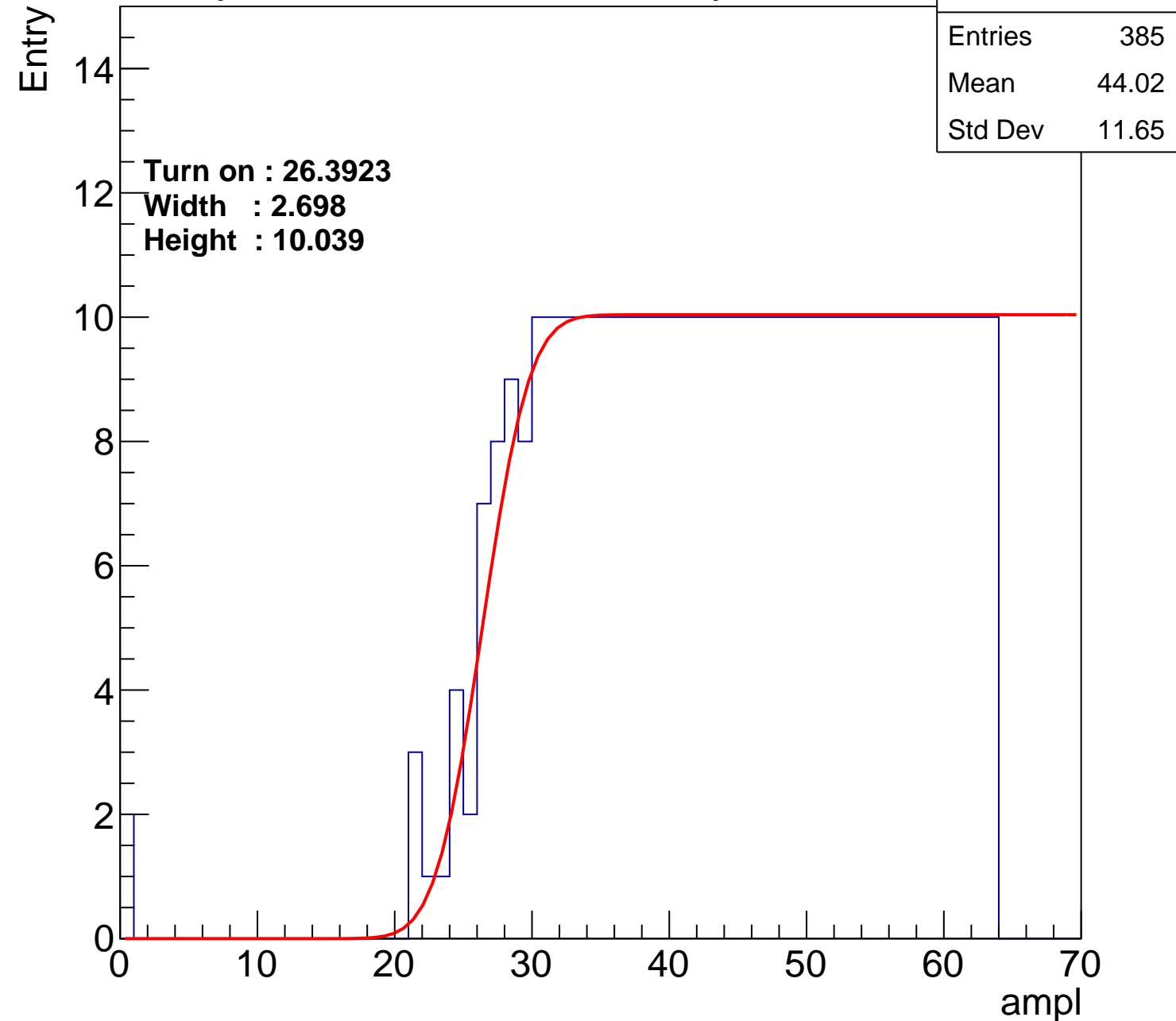
Width : 2.698

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.1
Std Dev	12.04

Turn on : 27.2674

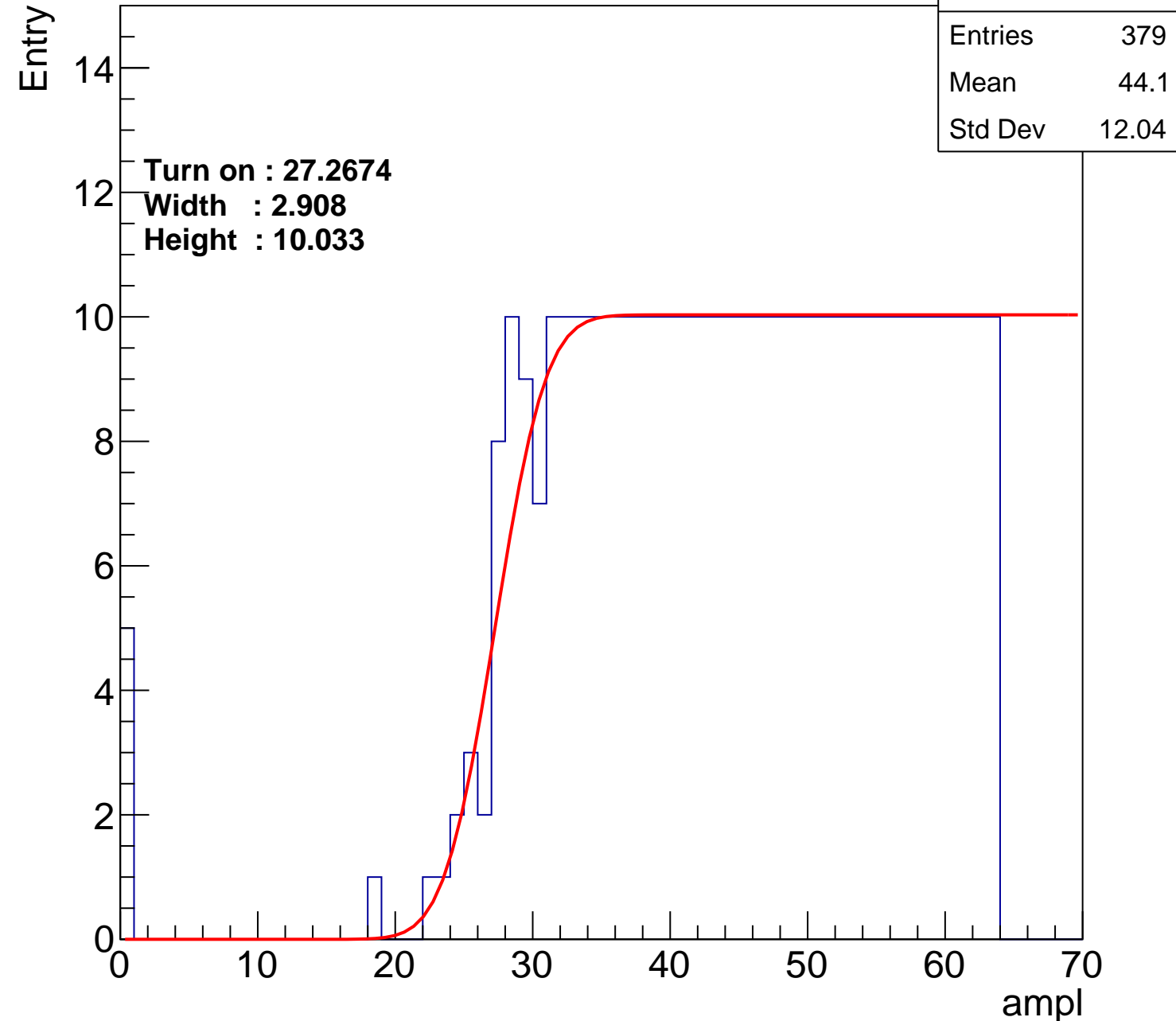
Width : 2.908

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.68
Std Dev	11.33

Turn on : 27.3974

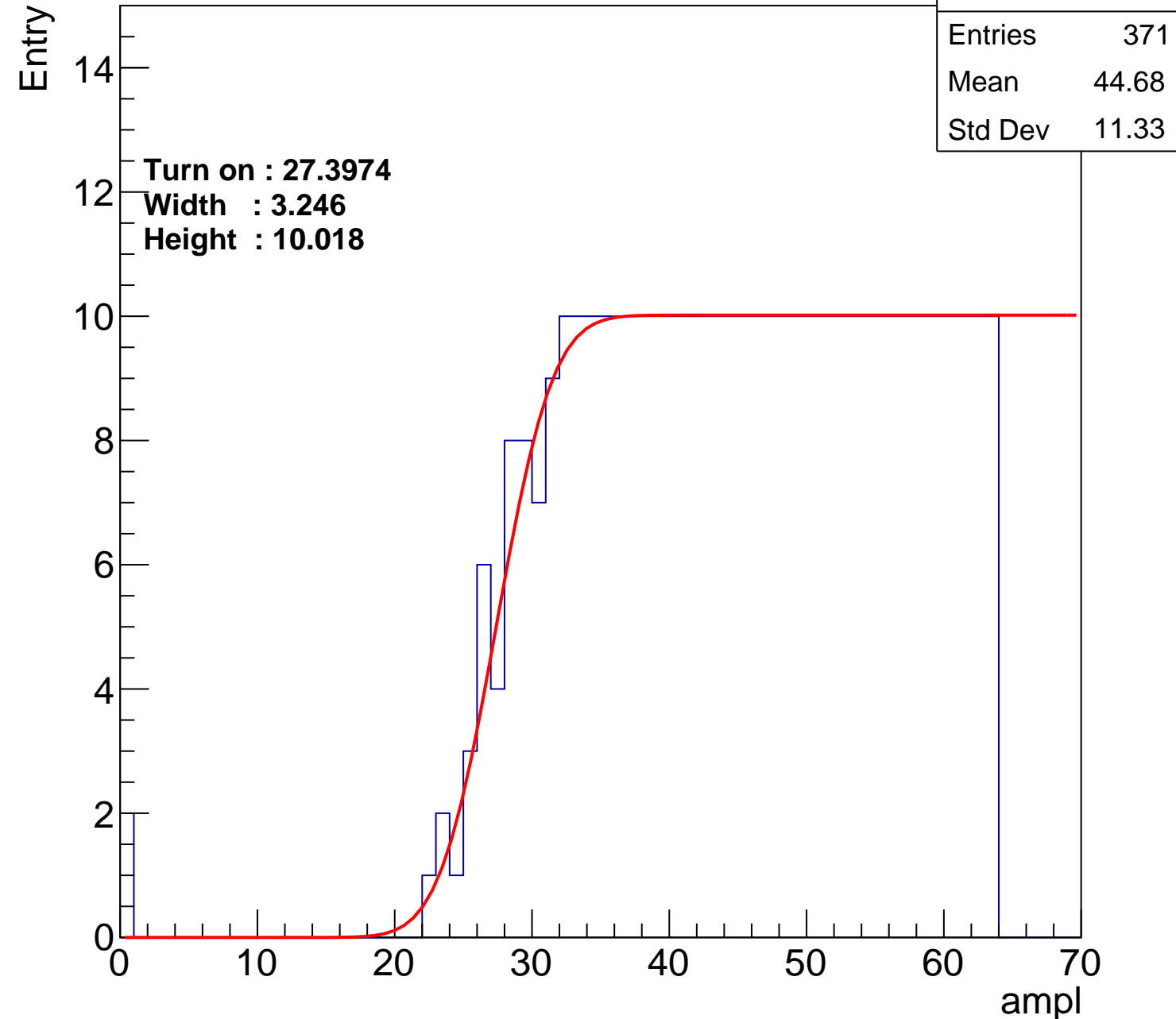
Width : 3.246

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.29
Std Dev	10.85

Turn on : 28.1555

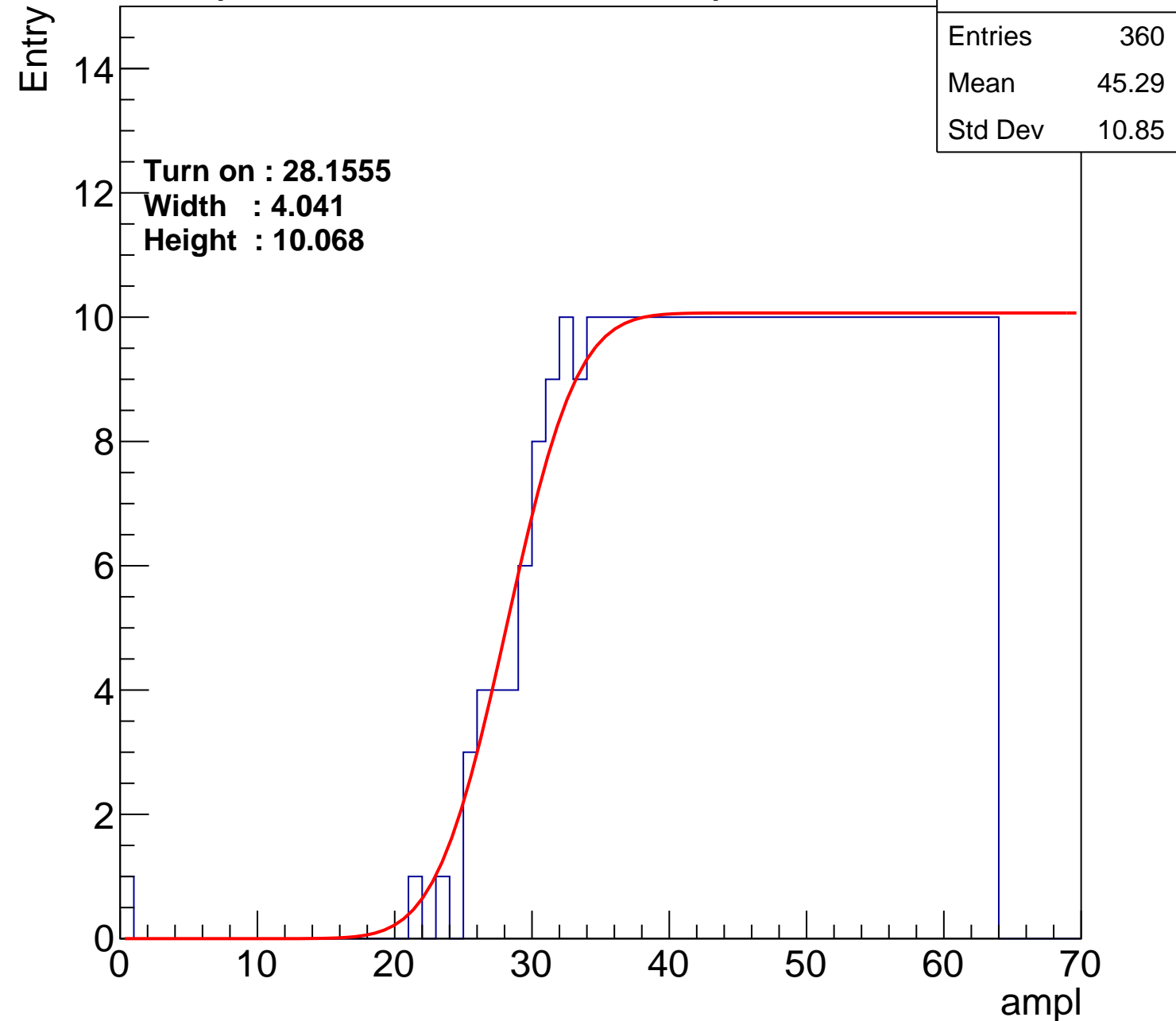
Width : 4.041

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.19
Std Dev	11.08

Turn on : 28.1510

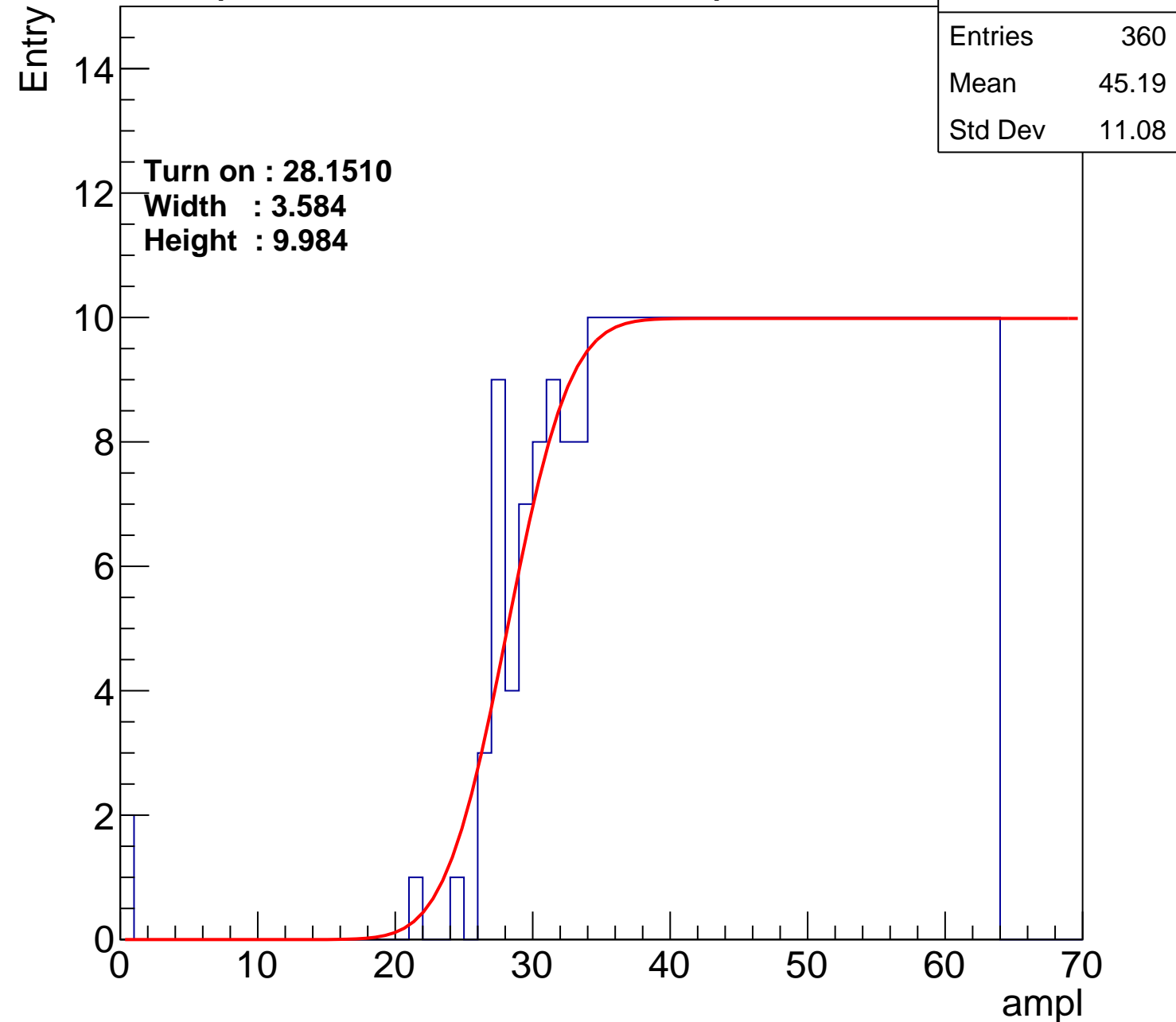
Width : 3.584

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch52

calib_packv5_042523_0143.root, FC#13, port D2

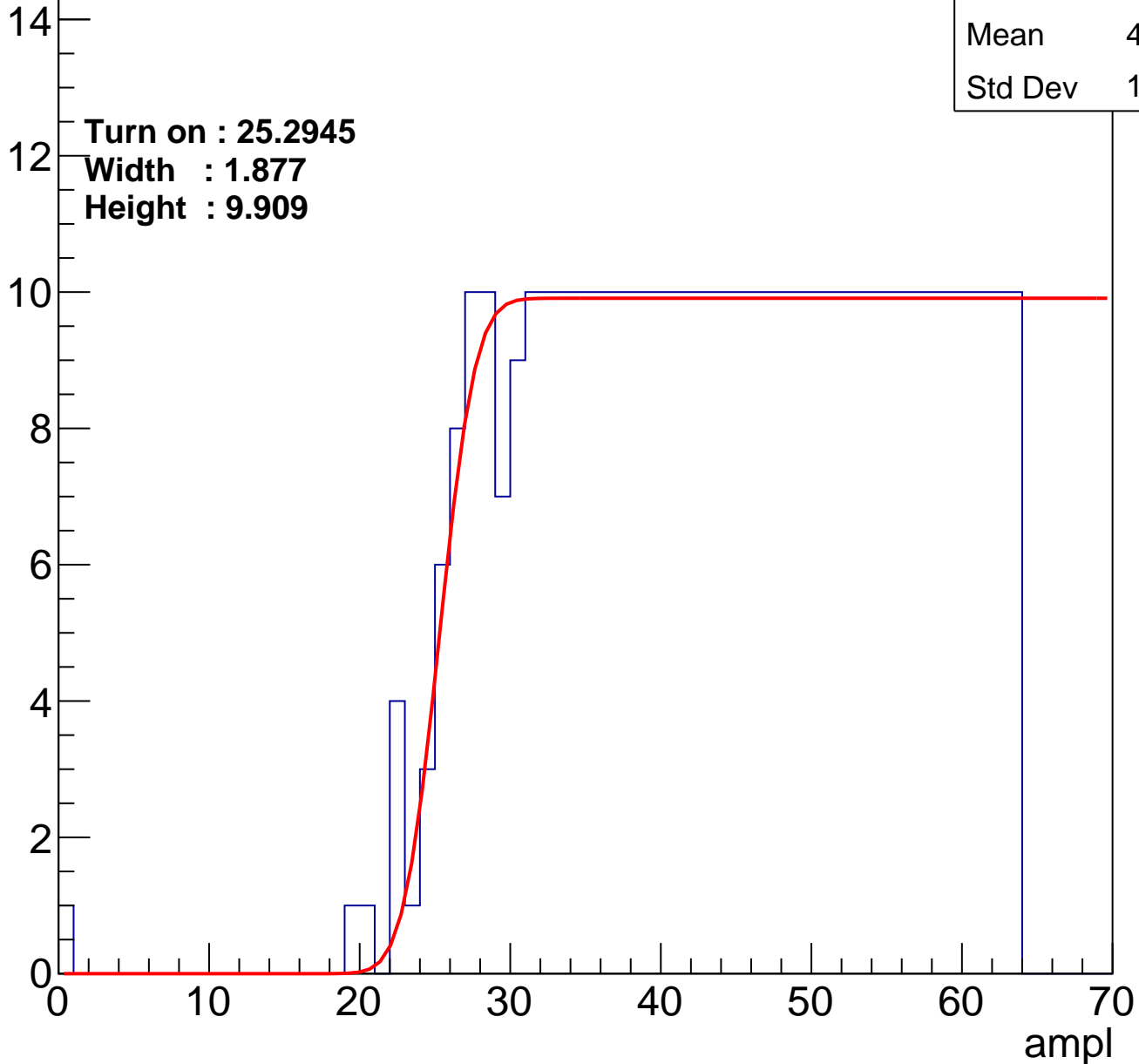
Entries	391
Mean	43.77
Std Dev	11.66

Turn on : 25.2945

Width : 1.877

Height : 9.909

Entry



B1L003S, U3-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.76
Std Dev	11.44

Turn on : 27.5702

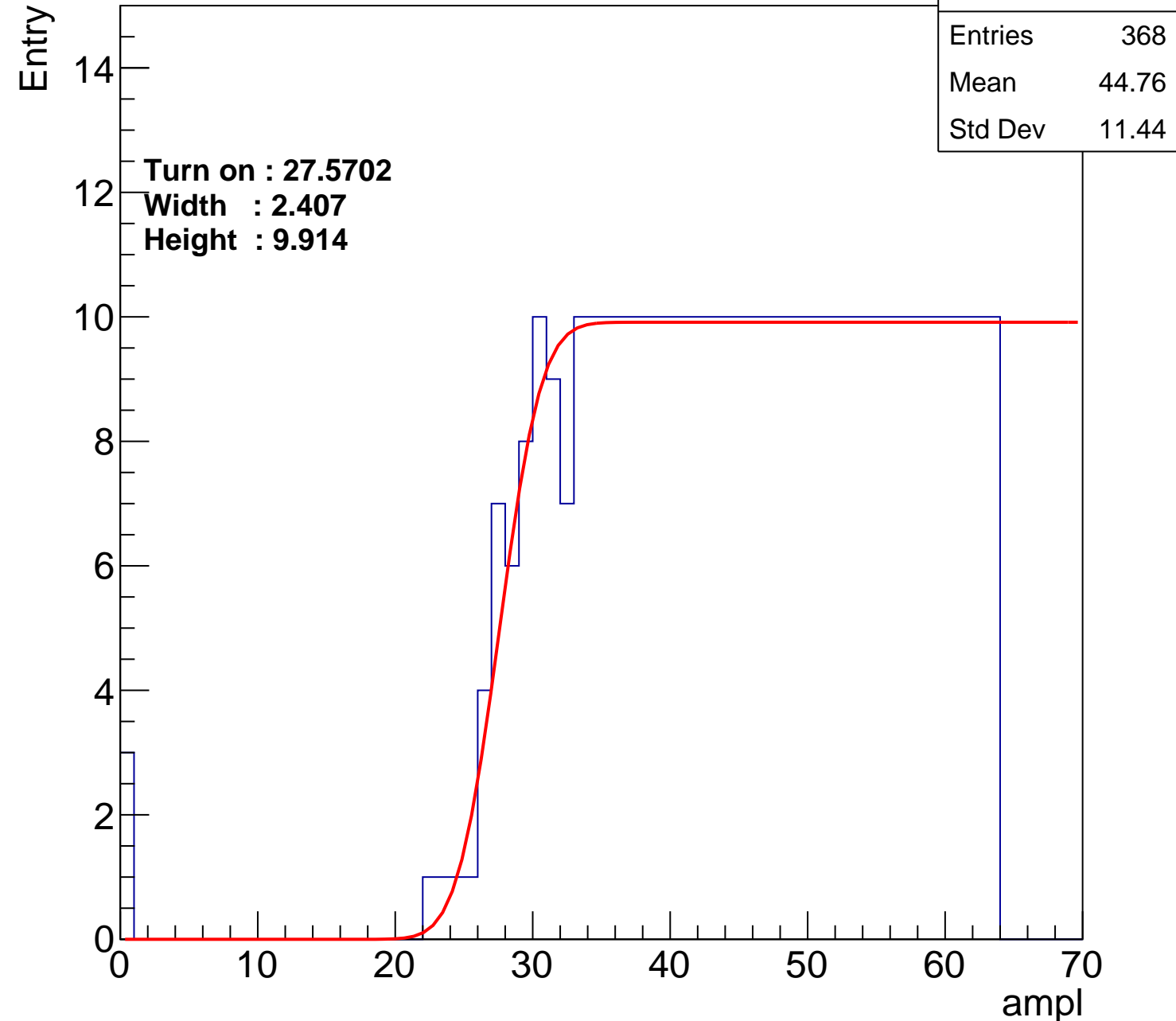
Width : 2.407

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.08
Std Dev	11.24

Turn on : 28.0167

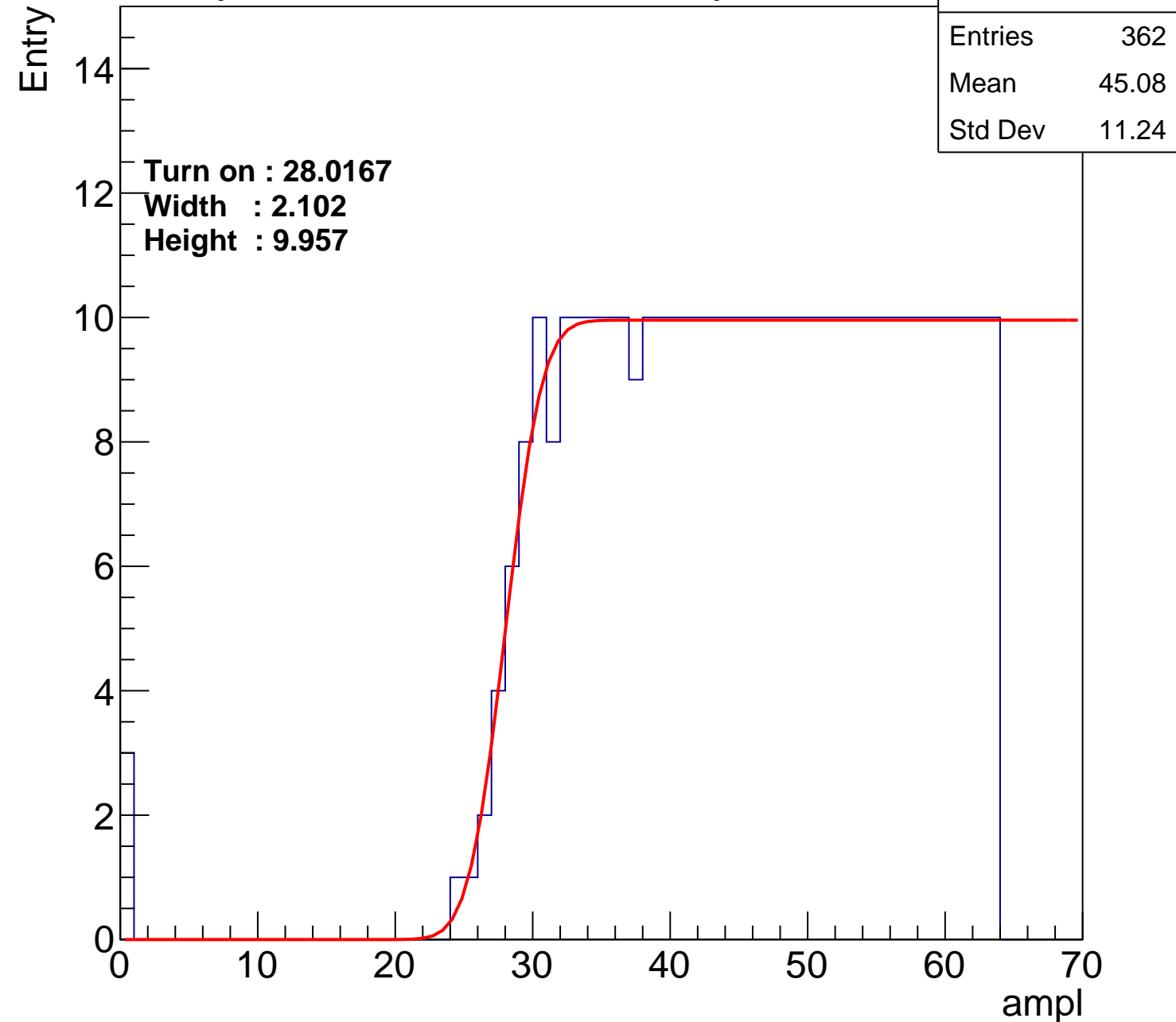
Width : 2.102

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 27.2232

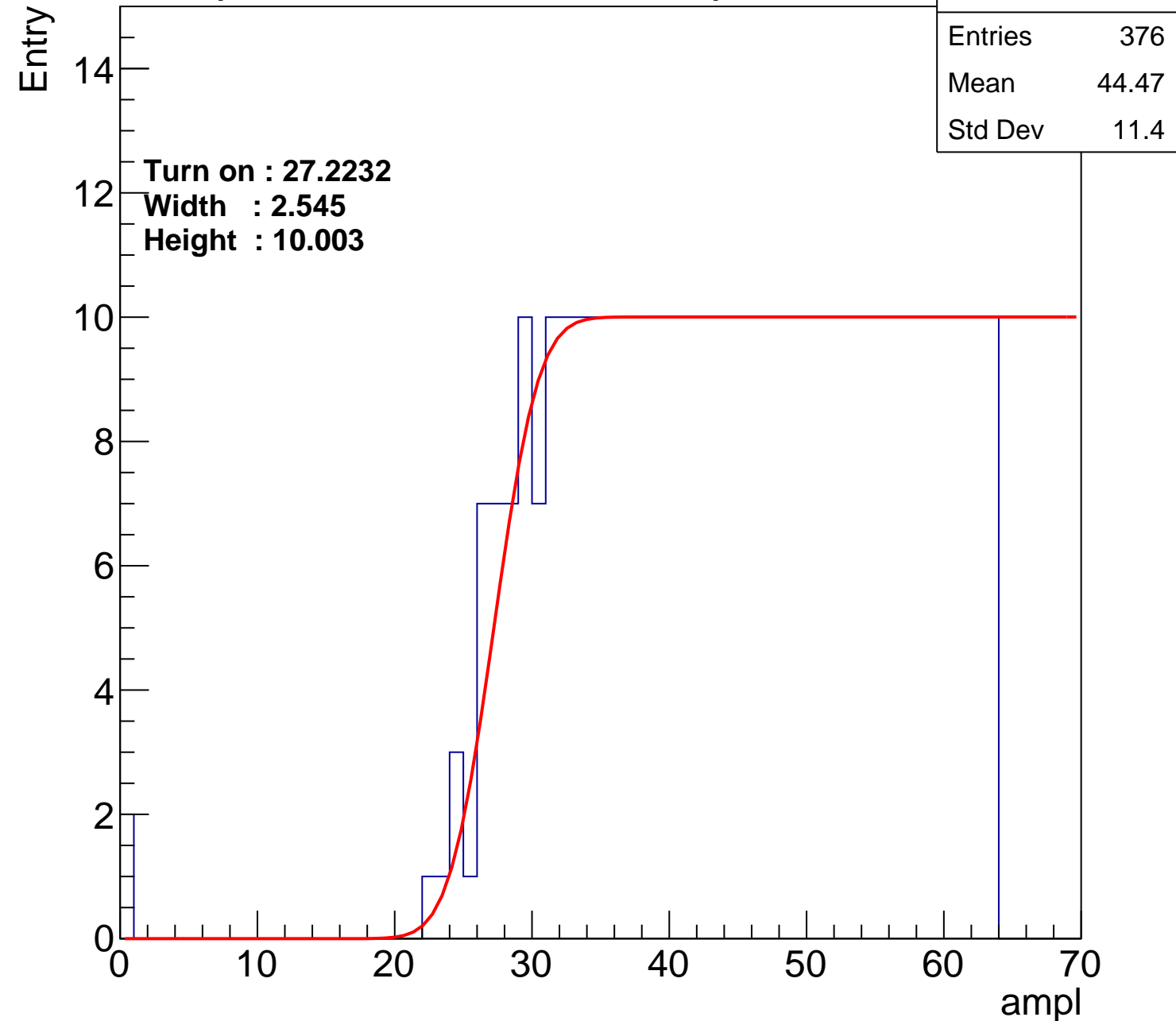
Width : 2.545

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch56

calib_packv5_042523_0143.root, FC#13, port D2

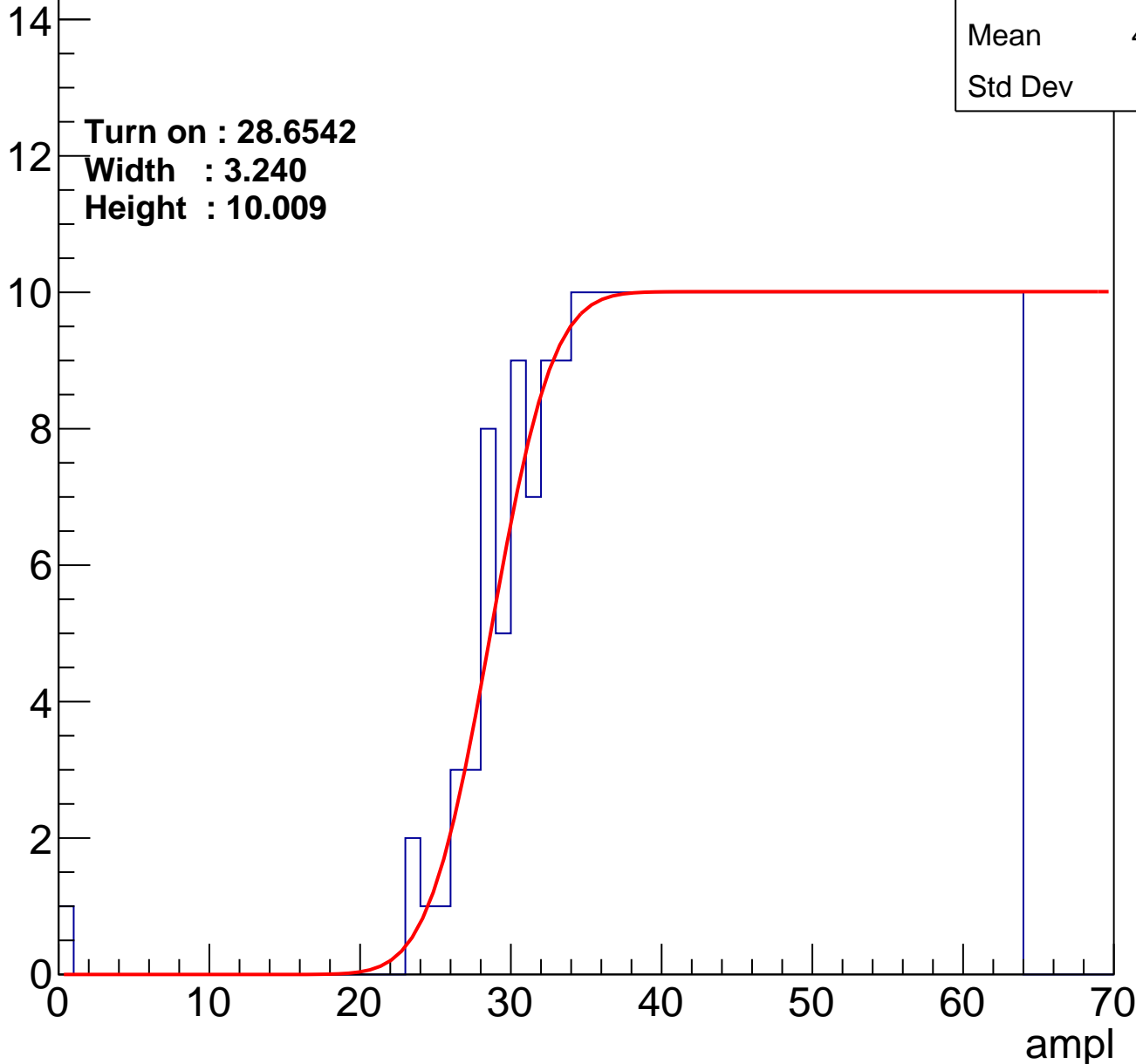
Entries	358
Mean	45.38
Std Dev	10.8

Turn on : 28.6542

Width : 3.240

Height : 10.009

Entry



B1L003S, U3-ch57

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.64
Std Dev	11.67

Turn on : 27.7343

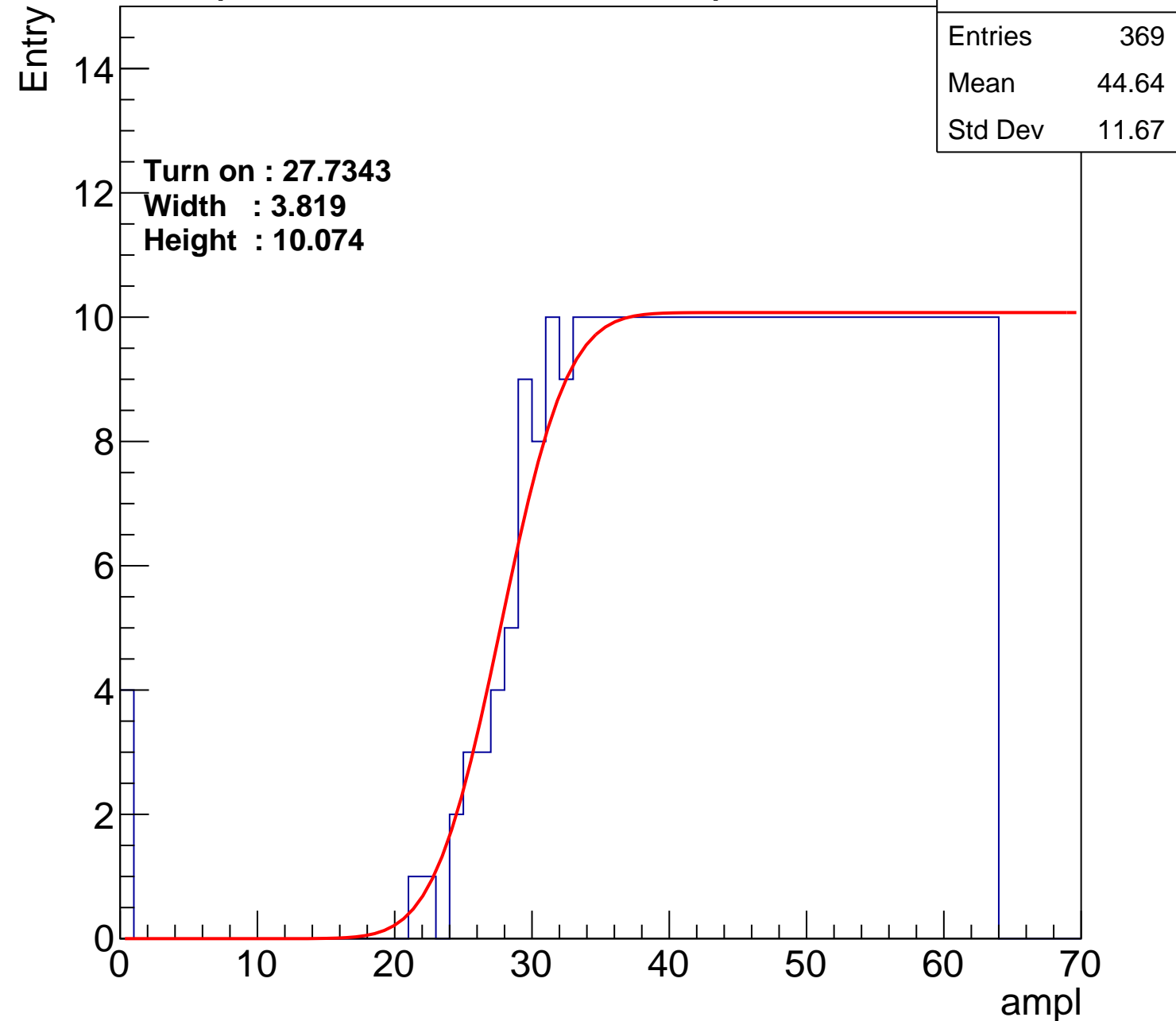
Width : 3.819

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.33
Std Dev	11.64

Turn on : 26.7074

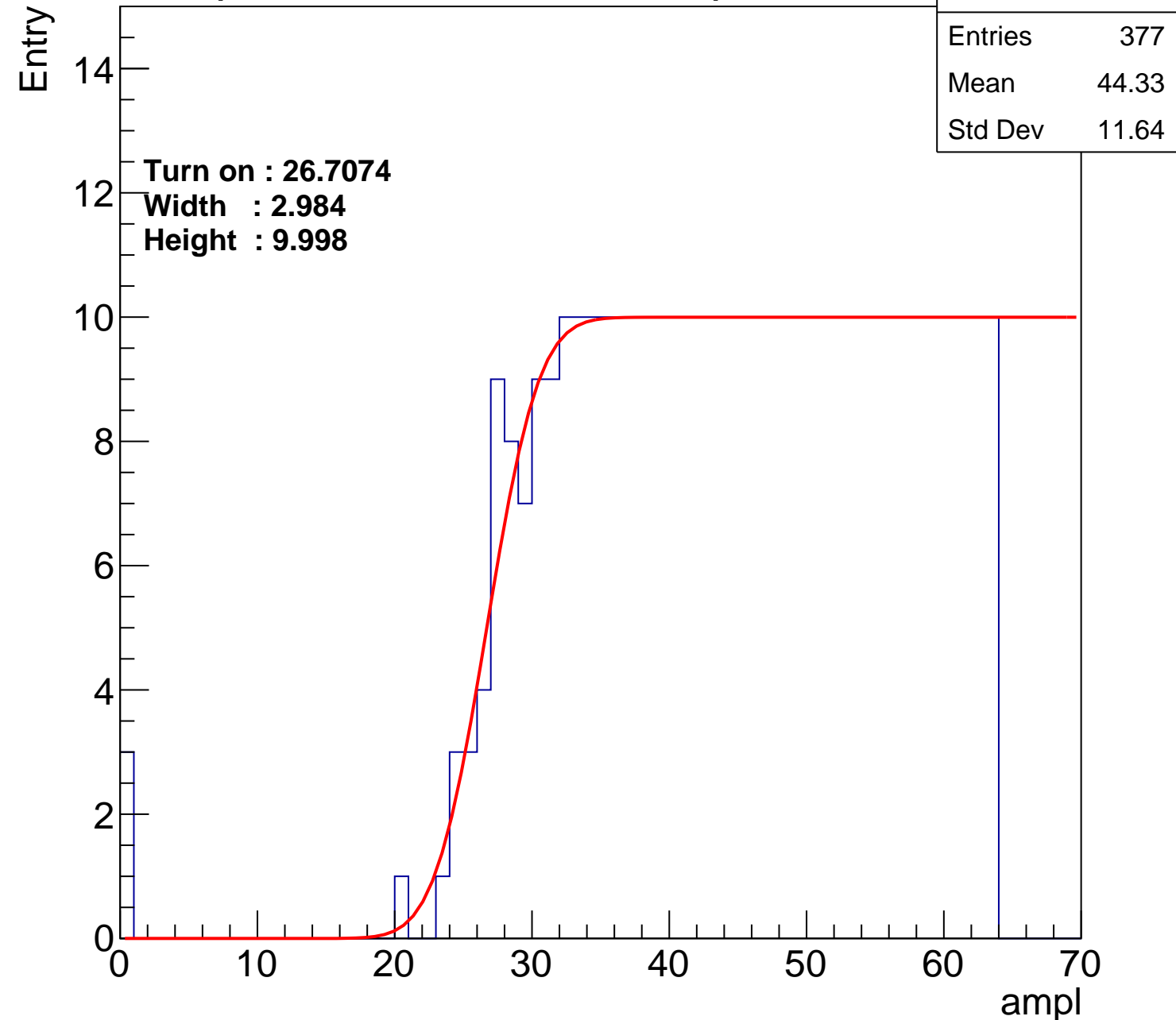
Width : 2.984

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.97
Std Dev	11.85

Turn on : 26.5916

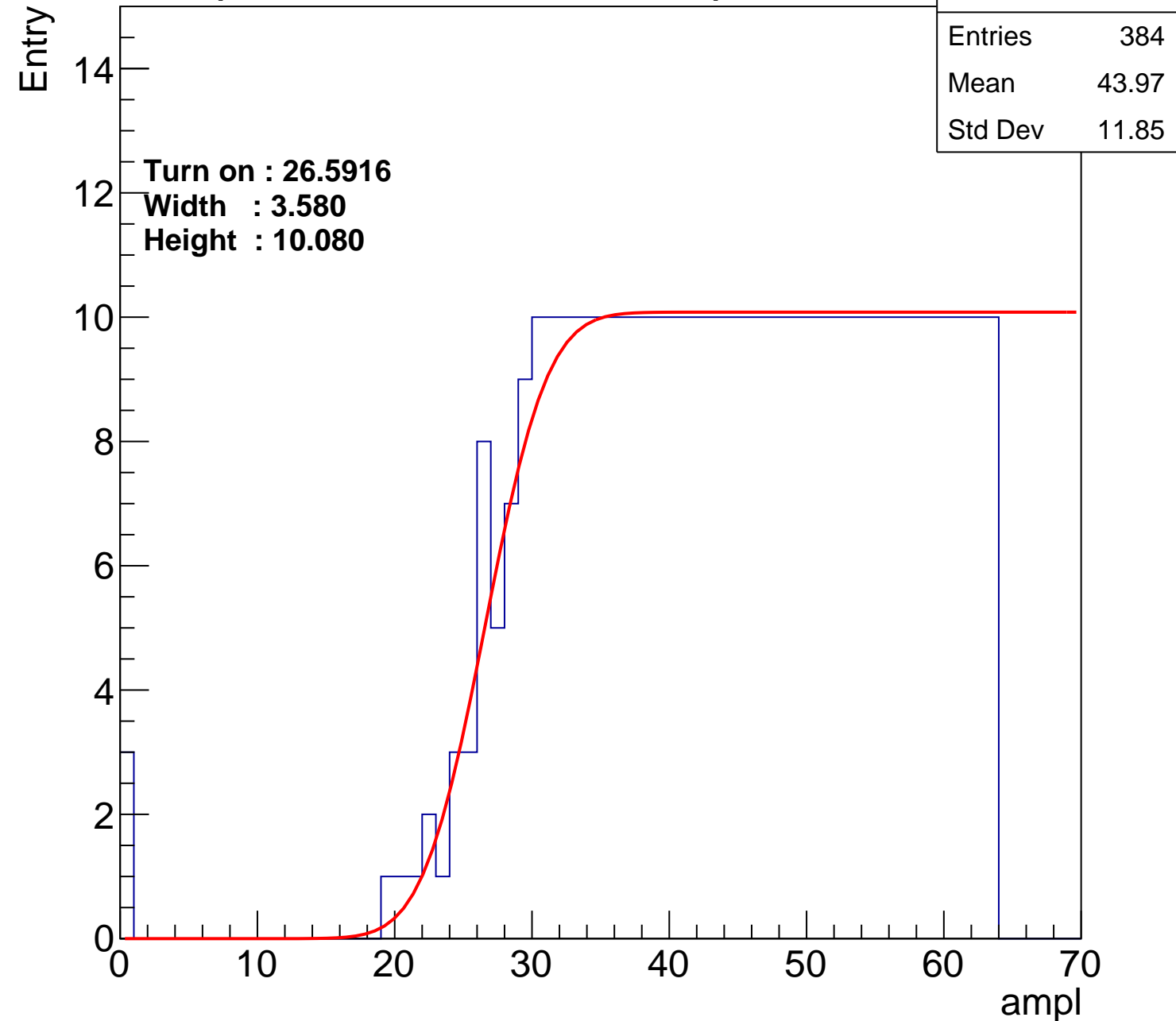
Width : 3.580

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.89
Std Dev	11.99

Turn on : 26.6526

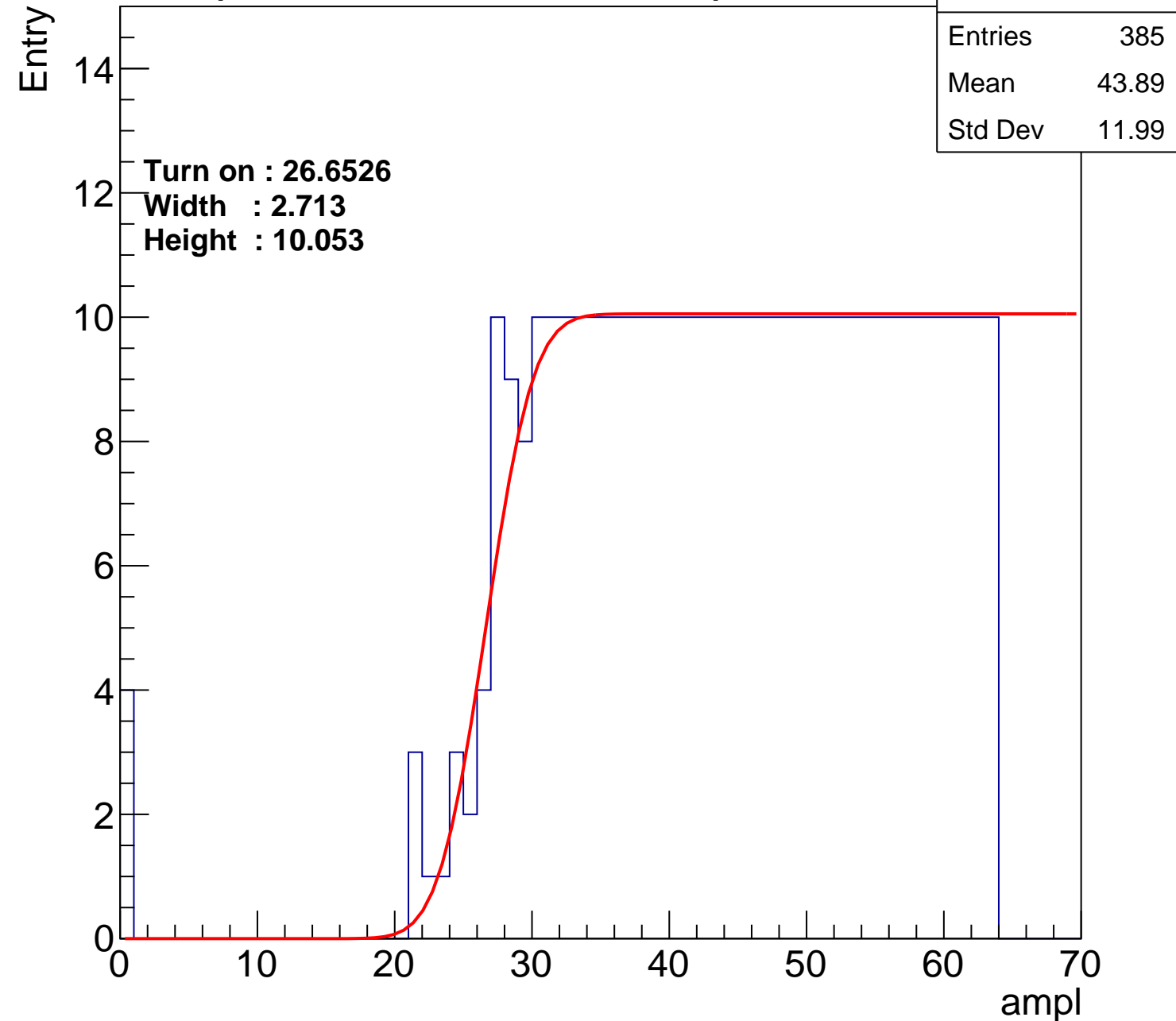
Width : 2.713

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.15
Std Dev	11.55

Turn on : 26.2677

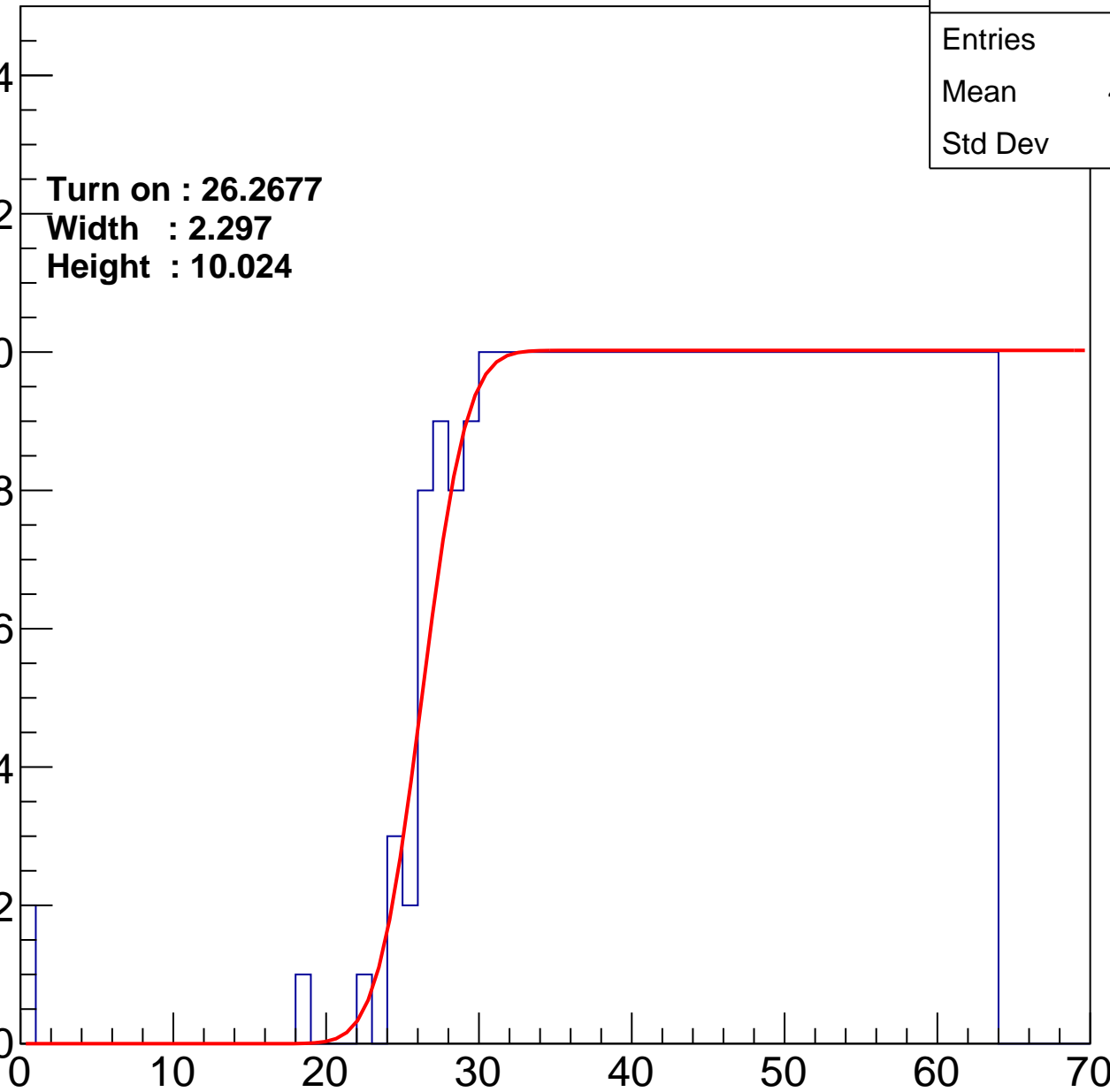
Width : 2.297

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.13
Std Dev	11.87

Turn on : 26.5727

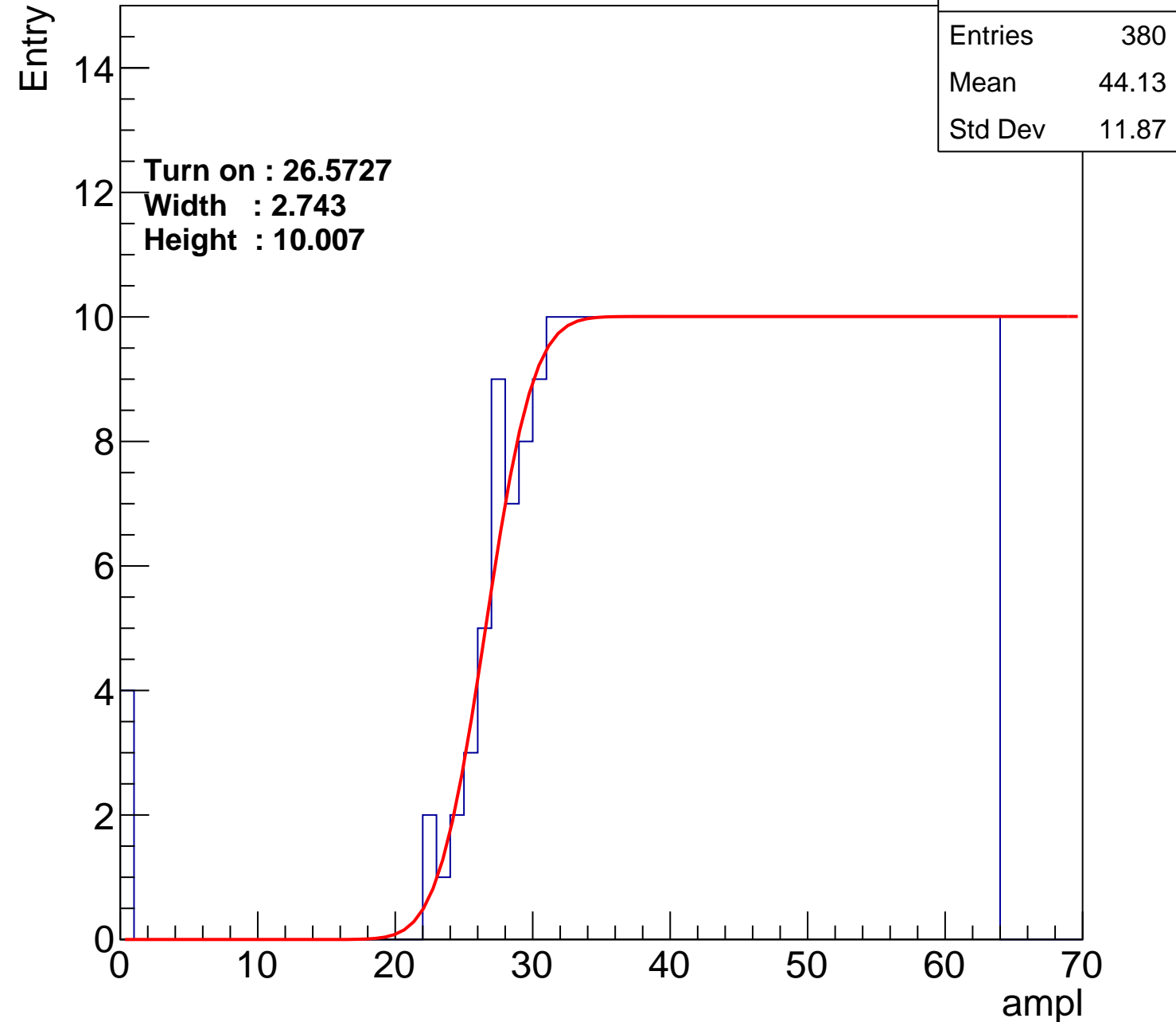
Width : 2.743

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch63

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.32
Std Dev	11.92

Turn on : 27.3446

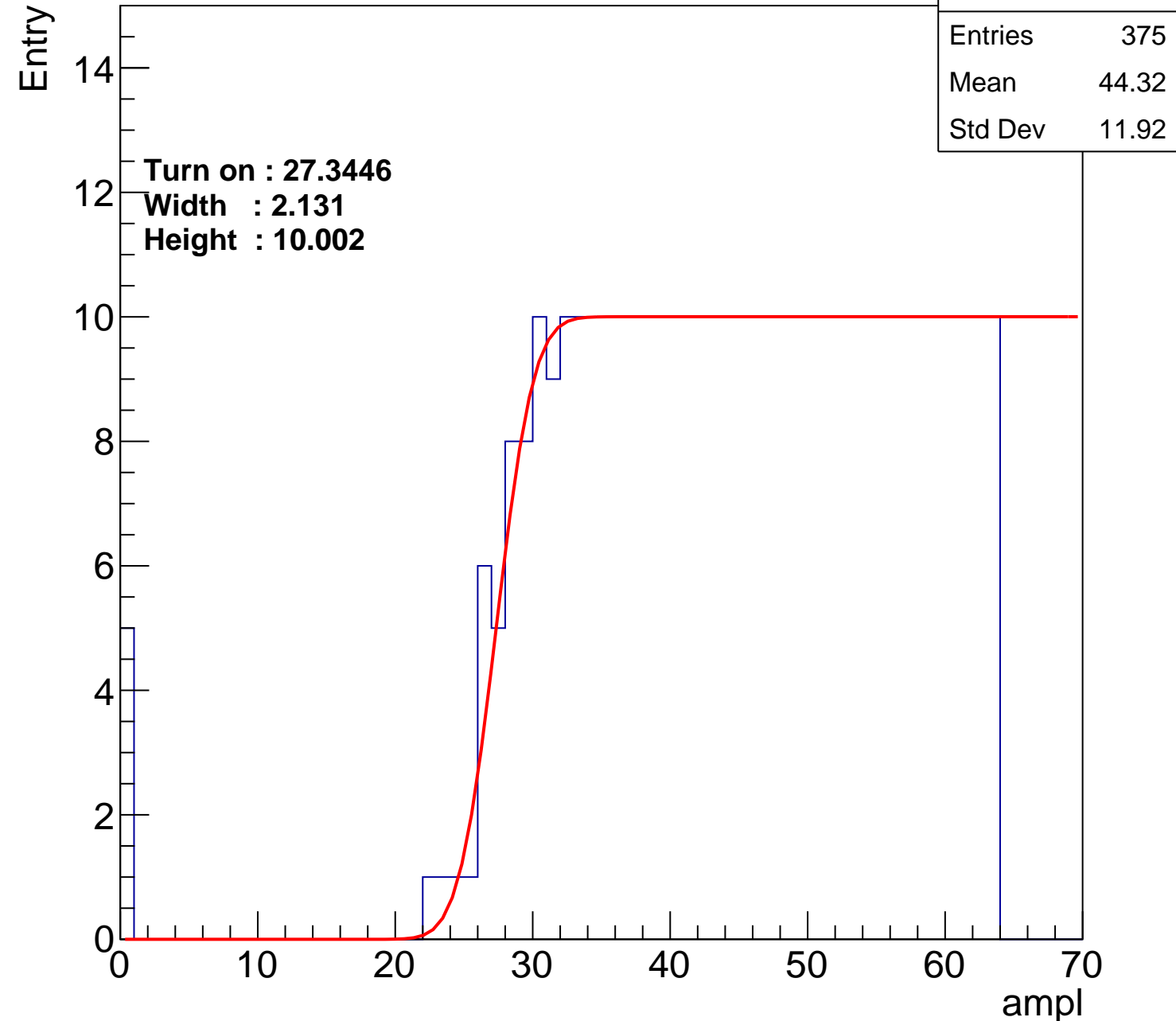
Width : 2.131

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch64

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.66
Std Dev	12.2

Turn on : 25.5779

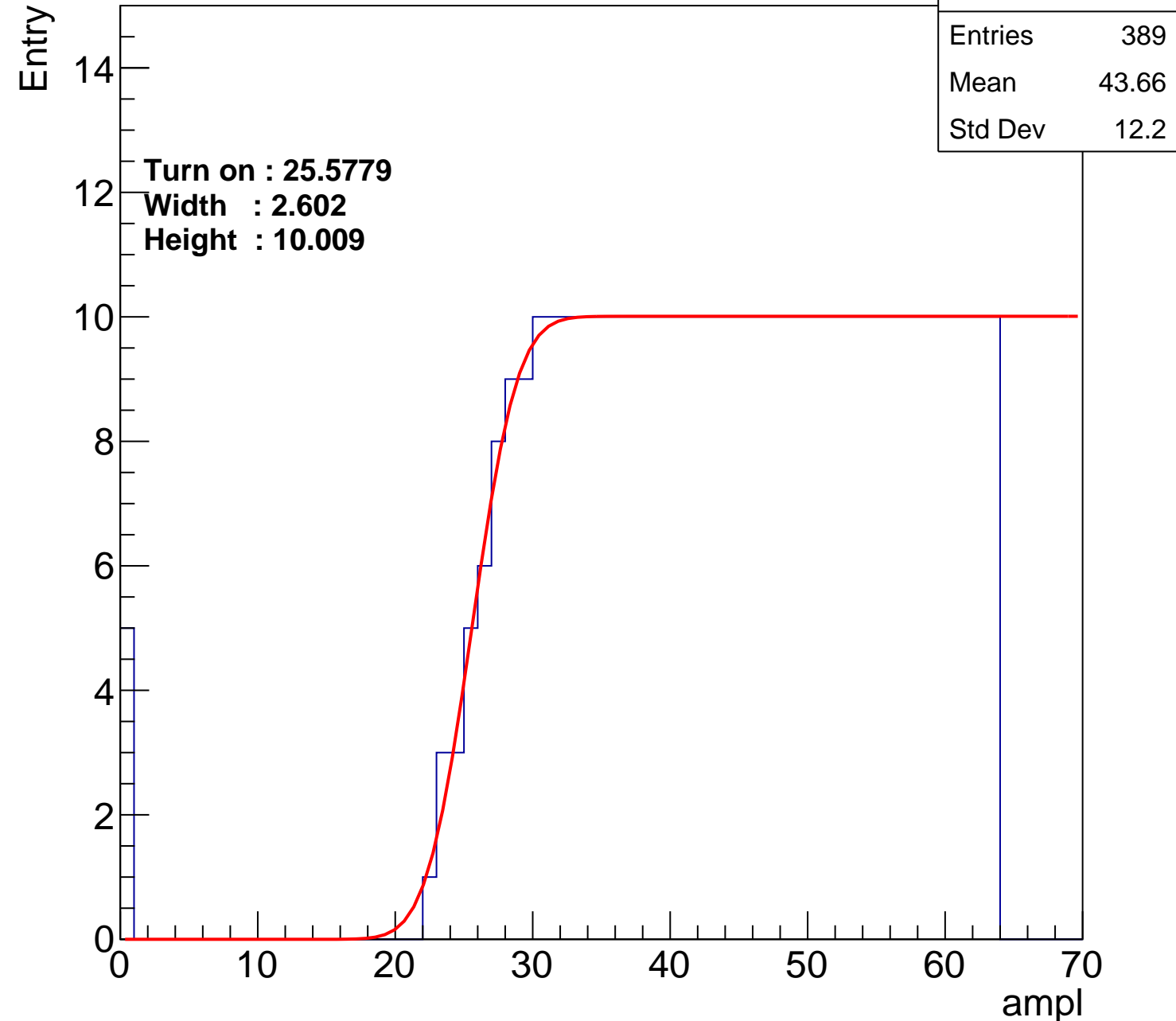
Width : 2.602

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch65

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.25
Std Dev	11.59

Turn on : 26.7221

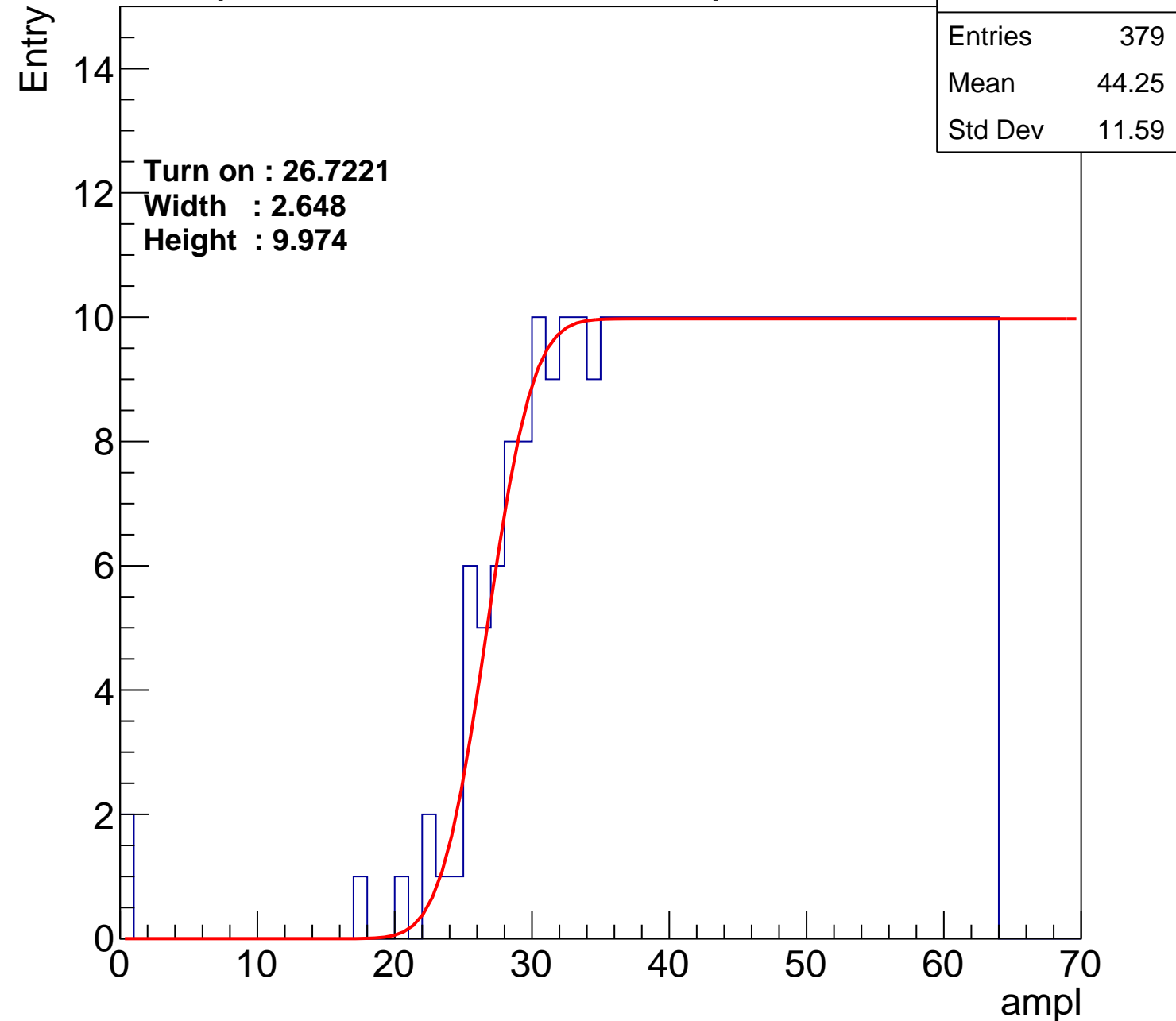
Width : 2.648

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 27.1936

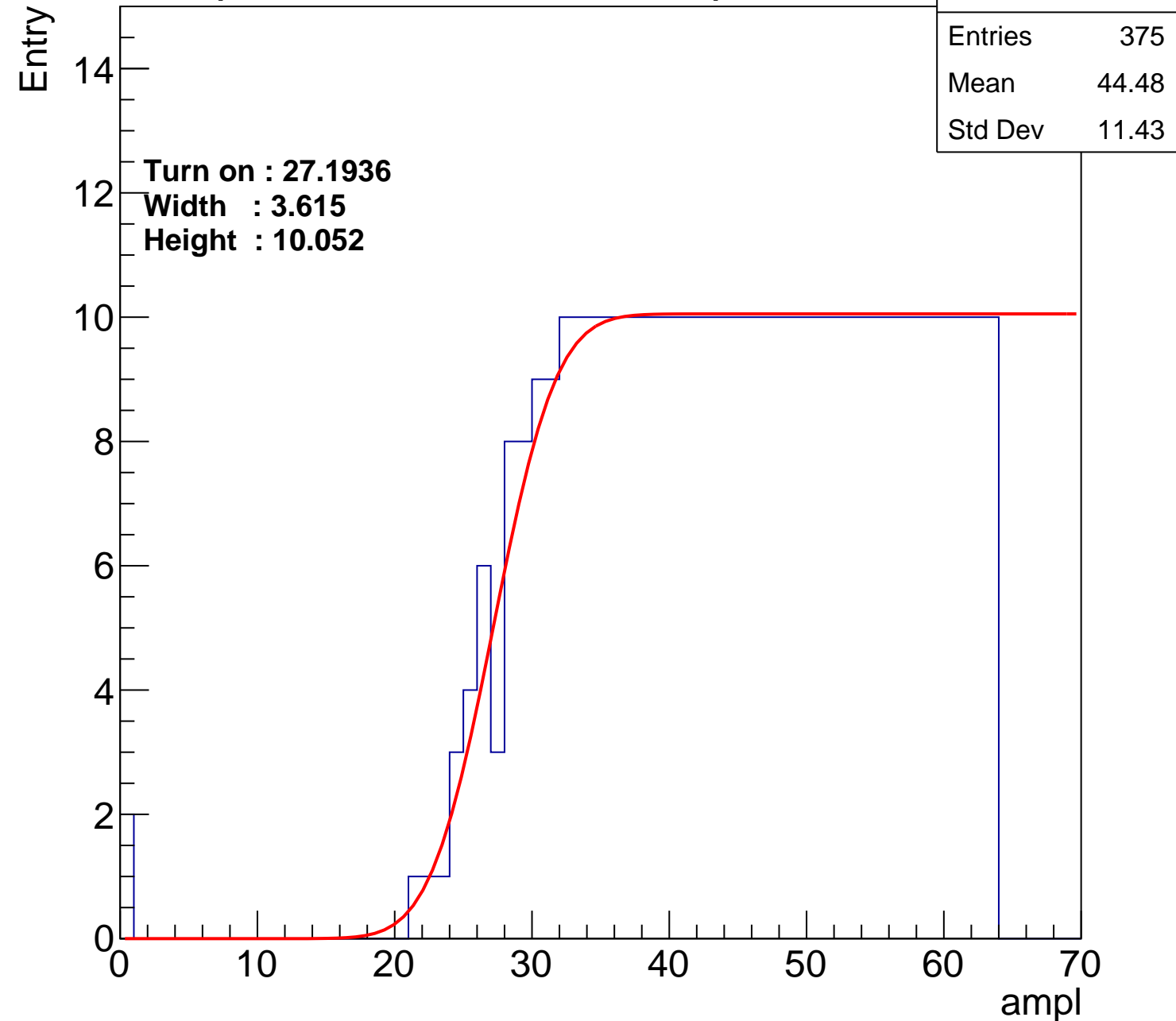
Width : 3.615

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.68
Std Dev	11.46

Turn on : 27.2290

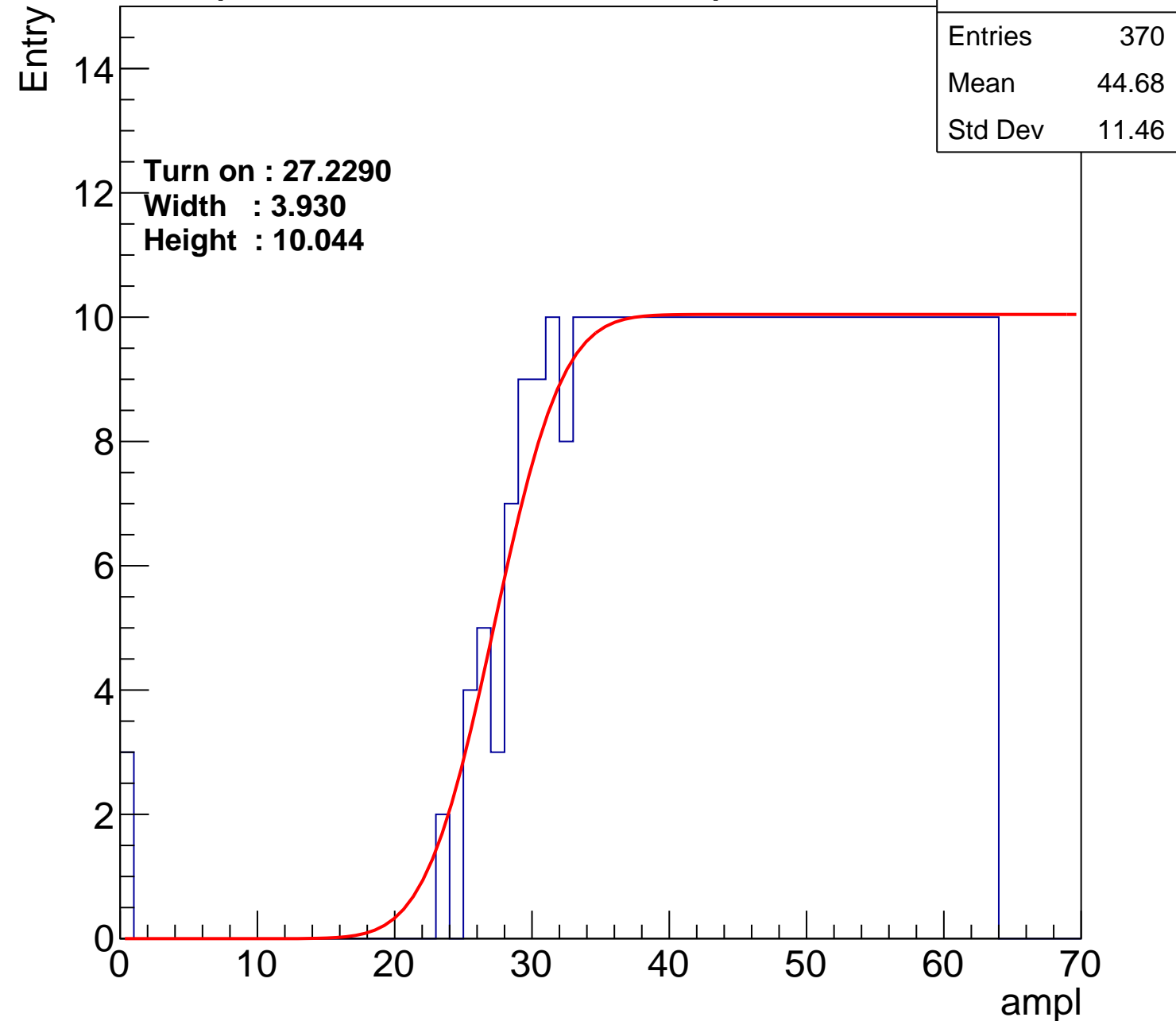
Width : 3.930

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch68

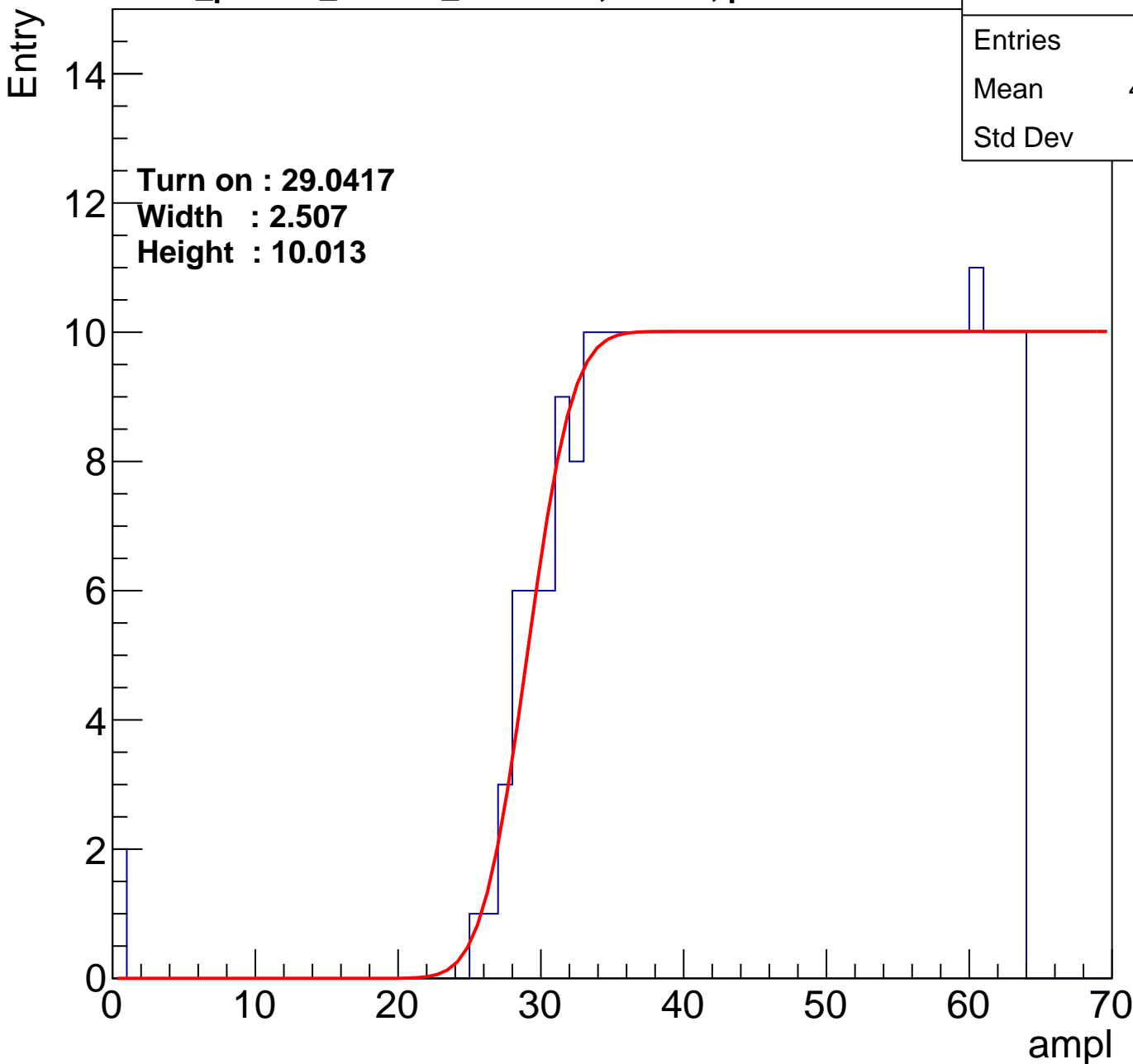
calib_packv5_042523_0143.root, FC#13, port D2

Entries	353
Mean	45.69
Std Dev	10.8

Turn on : 29.0417

Width : 2.507

Height : 10.013



B1L003S, U3-ch69

calib_packv5_042523_0143.root, FC#13, port D2

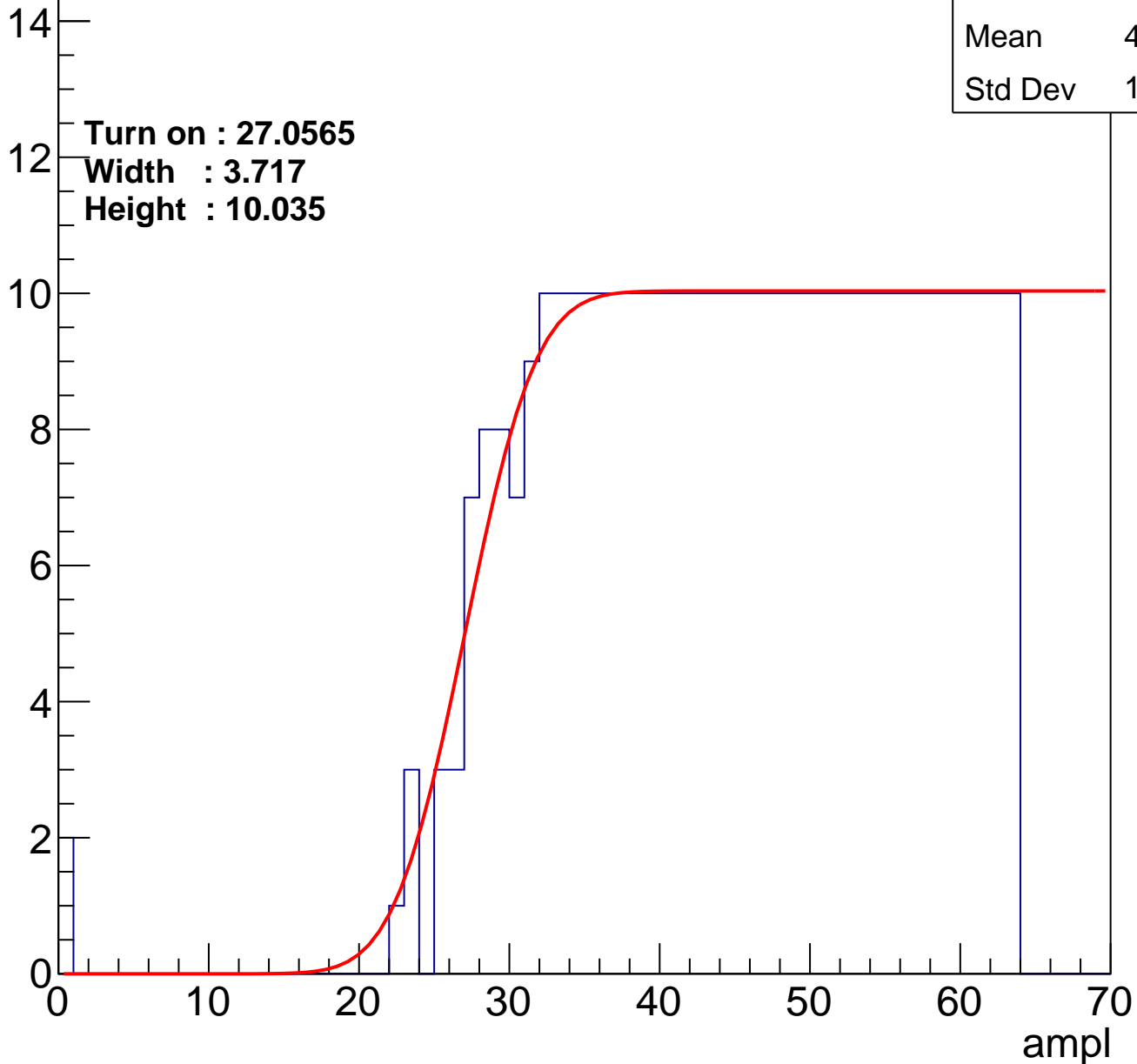
Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 27.0565

Width : 3.717

Height : 10.035

Entry



B1L003S, U3-ch70

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.52
Std Dev	11.38

Turn on : 27.1121

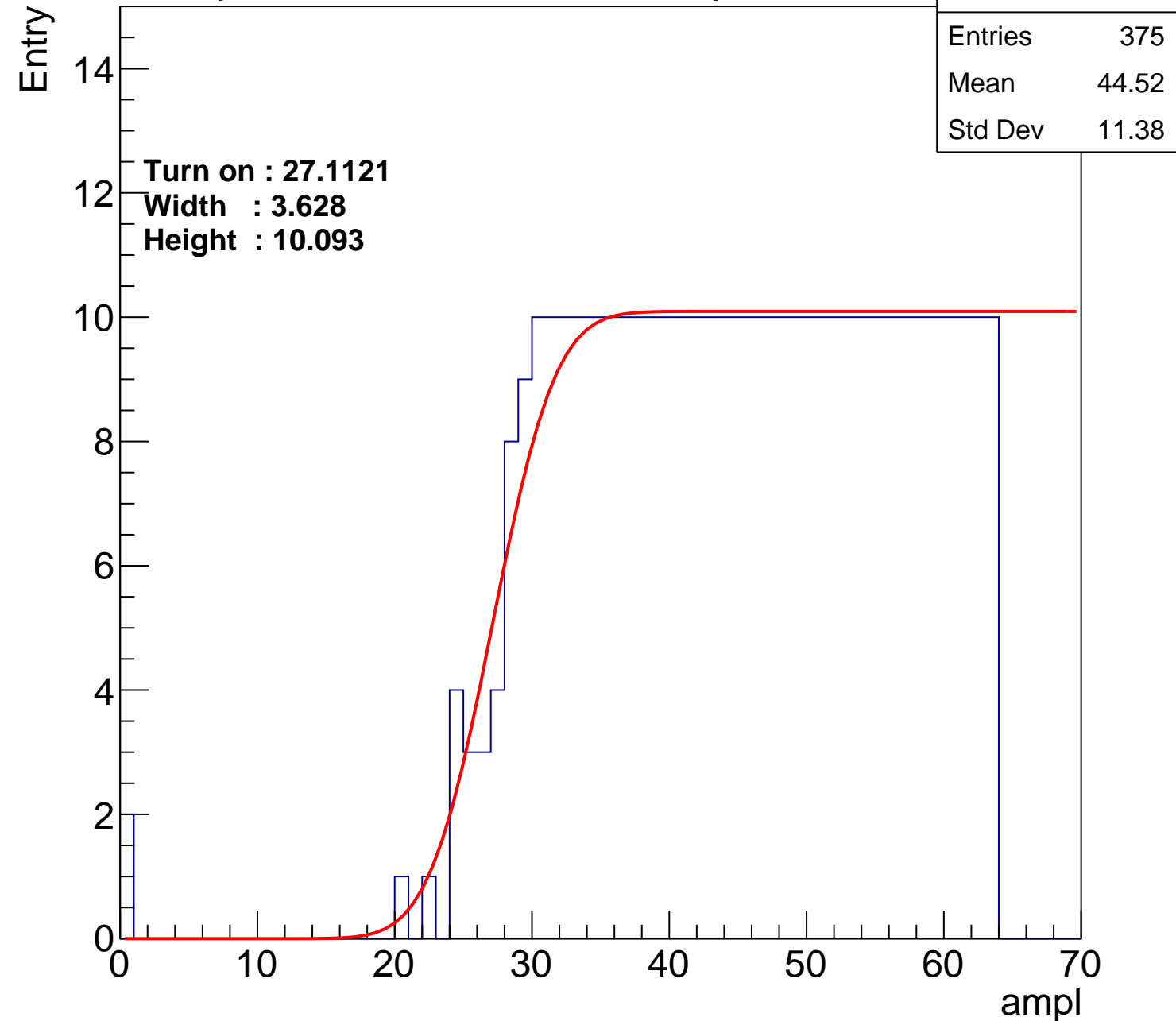
Width : 3.628

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.61
Std Dev	12.11

Turn on : 25.7130

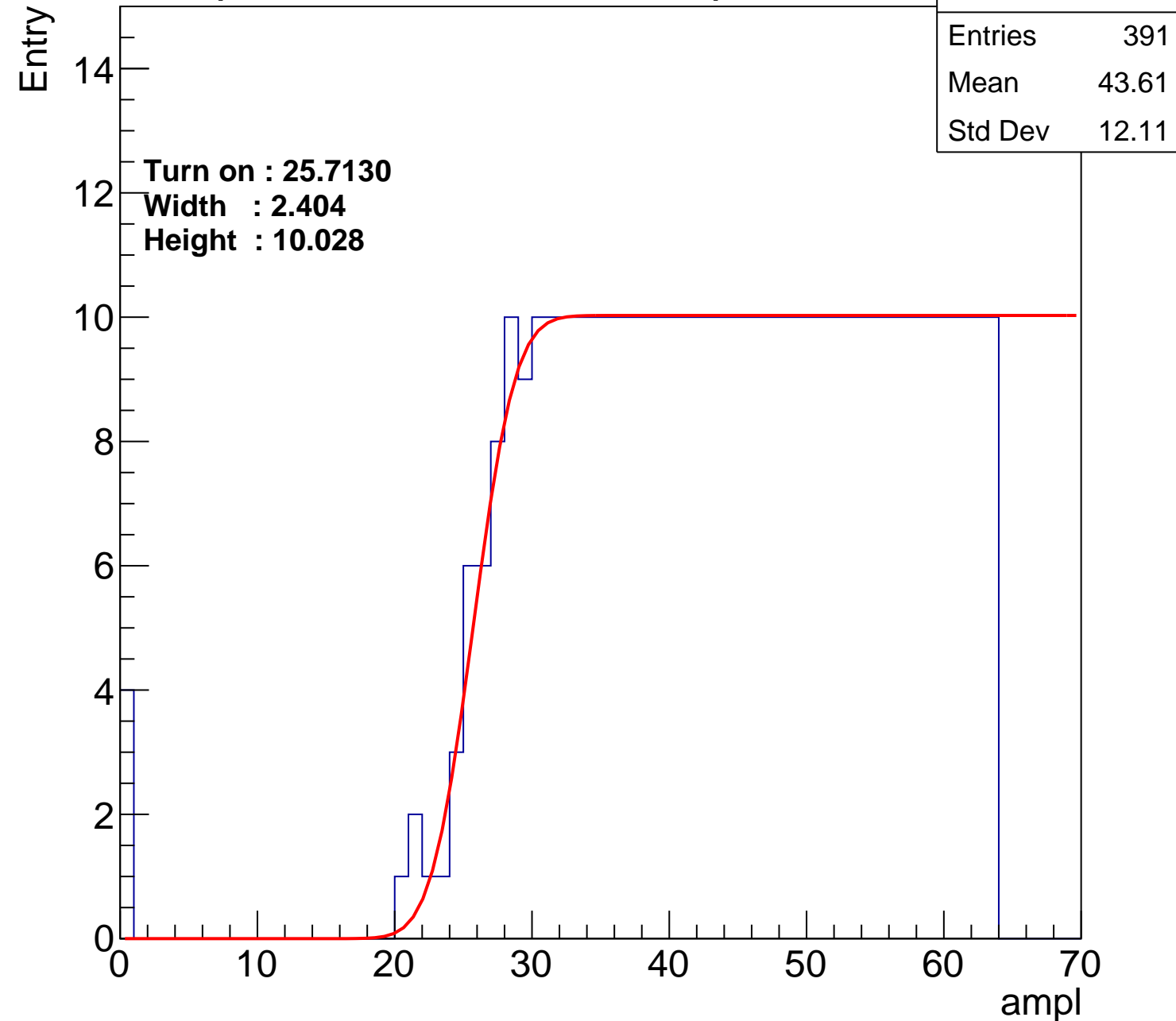
Width : 2.404

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch72

calib_packv5_042523_0143.root, FC#13, port D2

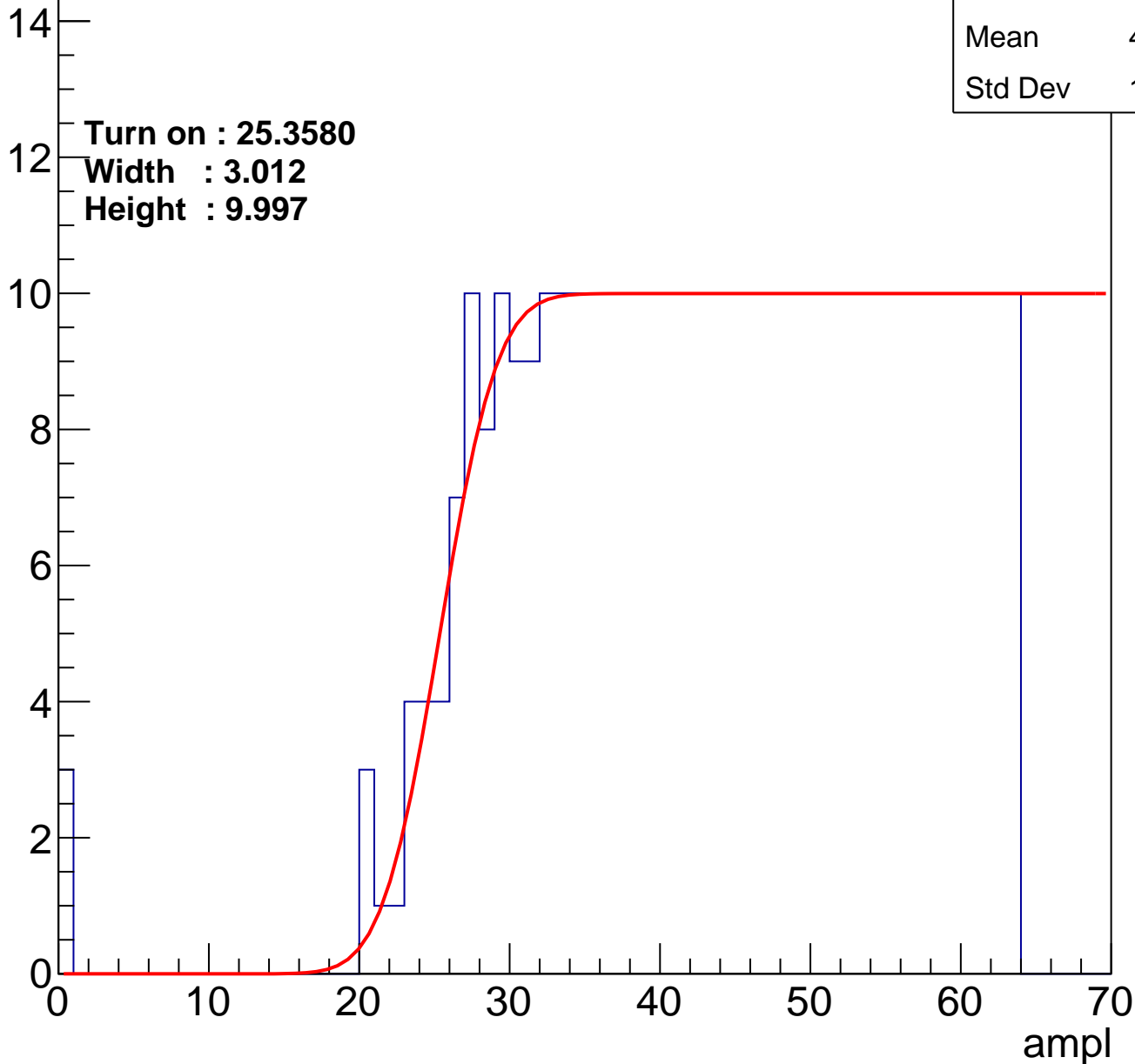
Entry

Entries	393
Mean	43.53
Std Dev	12.07

Turn on : 25.3580

Width : 3.012

Height : 9.997



B1L003S, U3-ch73

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.65
Std Dev	11.65

Turn on : 27.7450

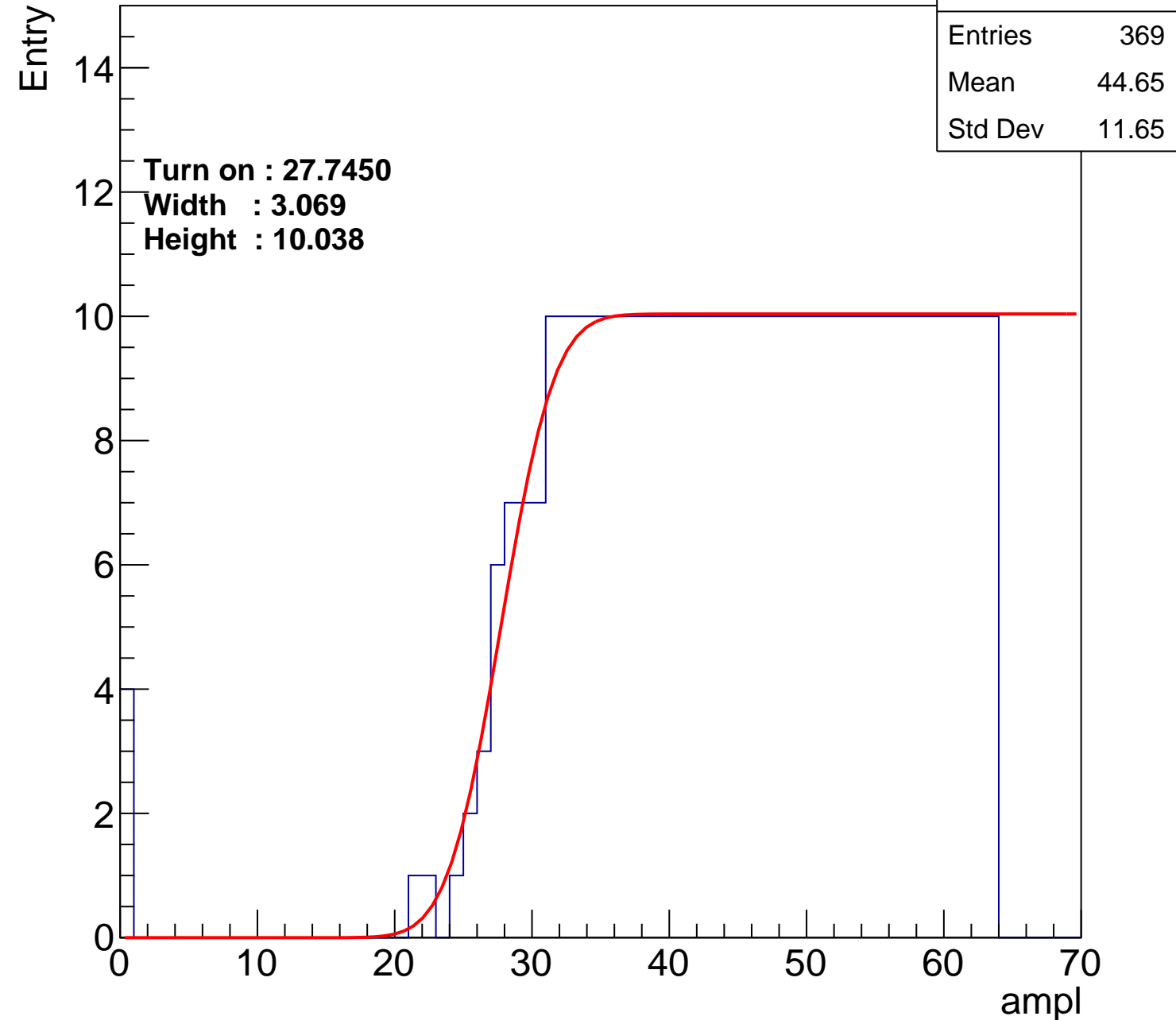
Width : 3.069

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.27
Std Dev	11.69

Turn on : 26.3506

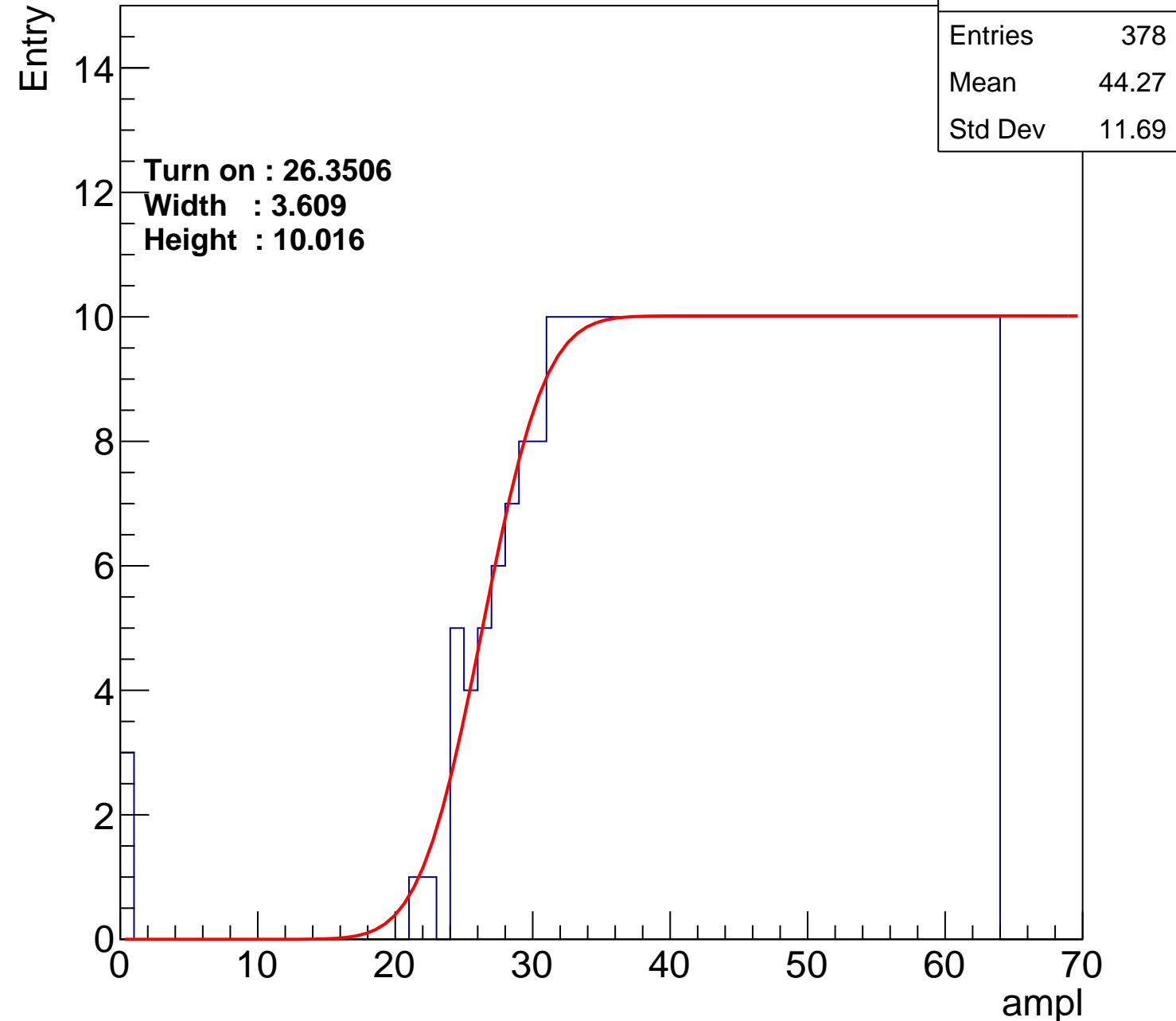
Width : 3.609

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch75

calib_packv5_042523_0143.root, FC#13, port D2

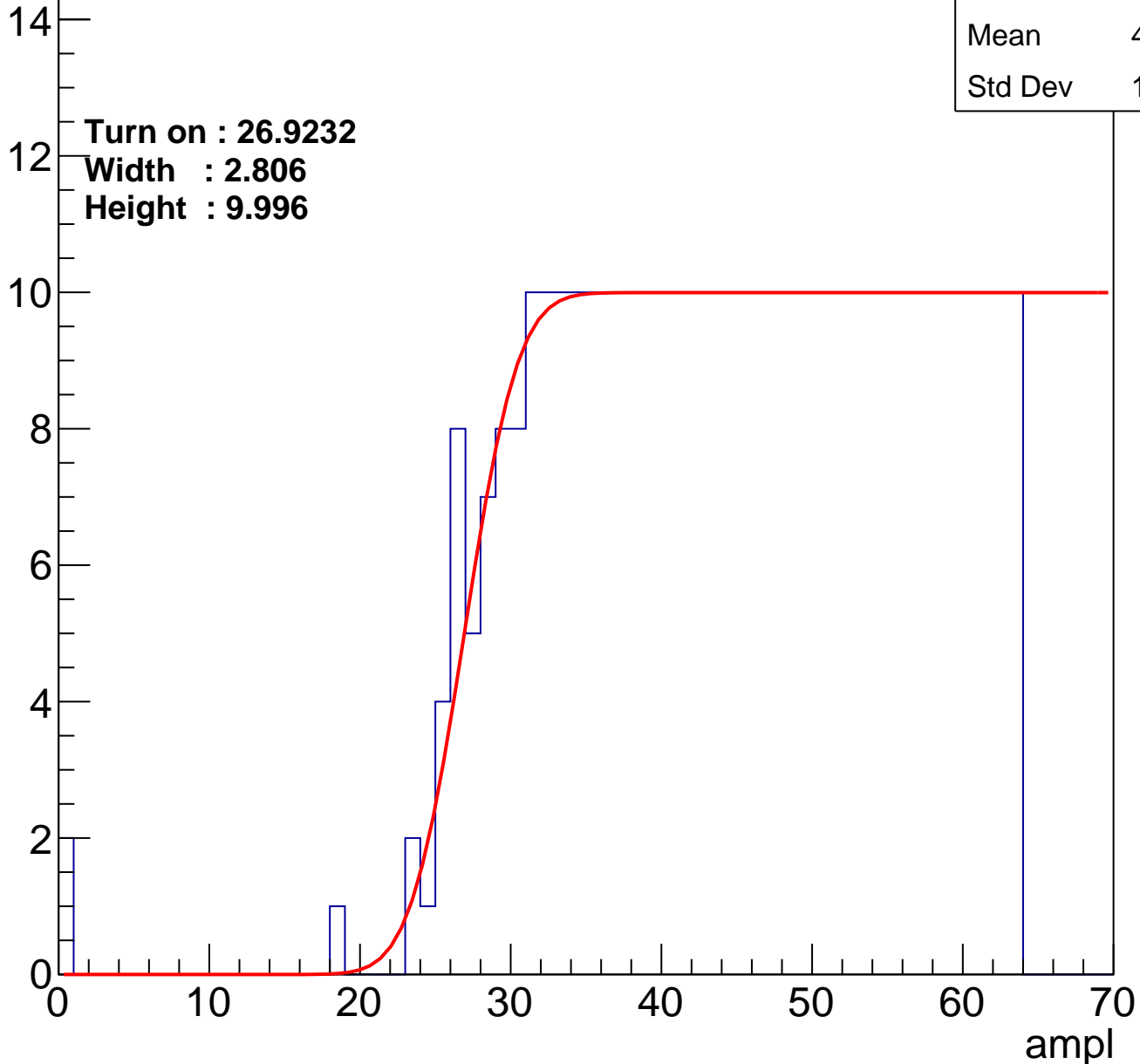
Entries	376
Mean	44.44
Std Dev	11.45

Turn on : 26.9232

Width : 2.806

Height : 9.996

Entry



B1L003S, U3-ch76

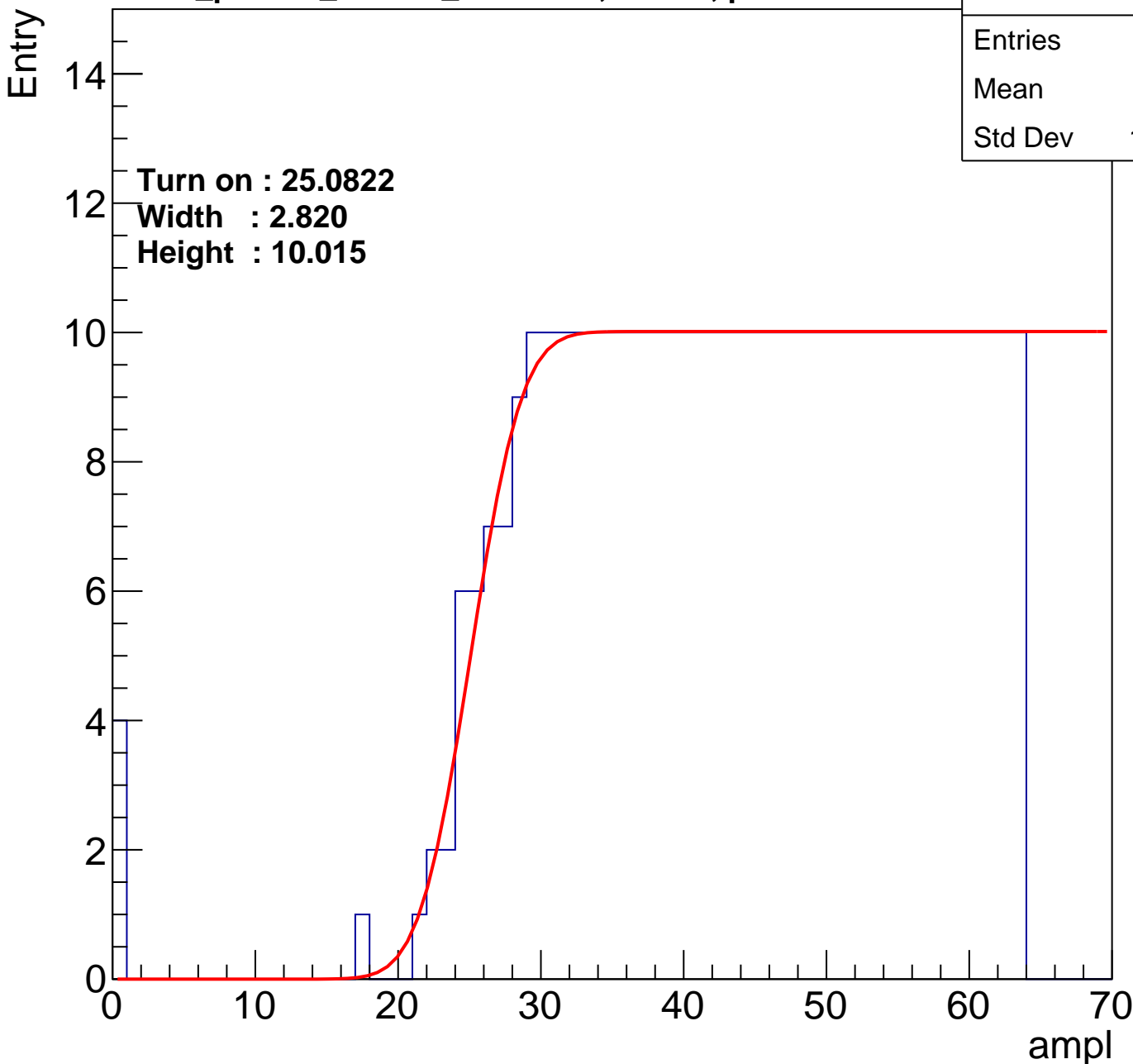
calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 25.0822

Width : 2.820

Height : 10.015

Entries	395
Mean	43.41
Std Dev	12.22



B1L003S, U3-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.67
Std Dev	11.99

Turn on : 25.6065

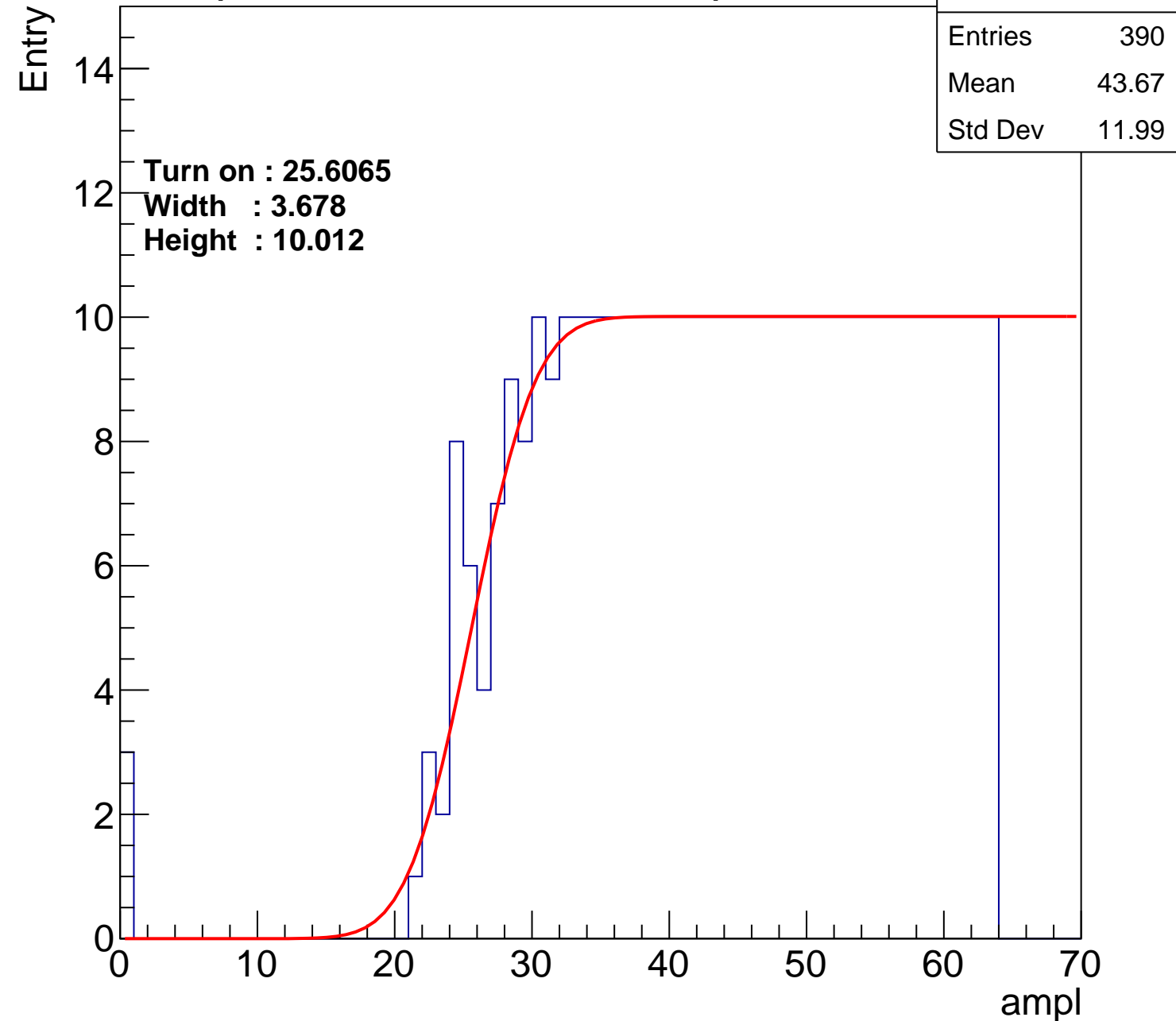
Width : 3.678

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch78

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.62
Std Dev	11.64

Turn on : 27.7860

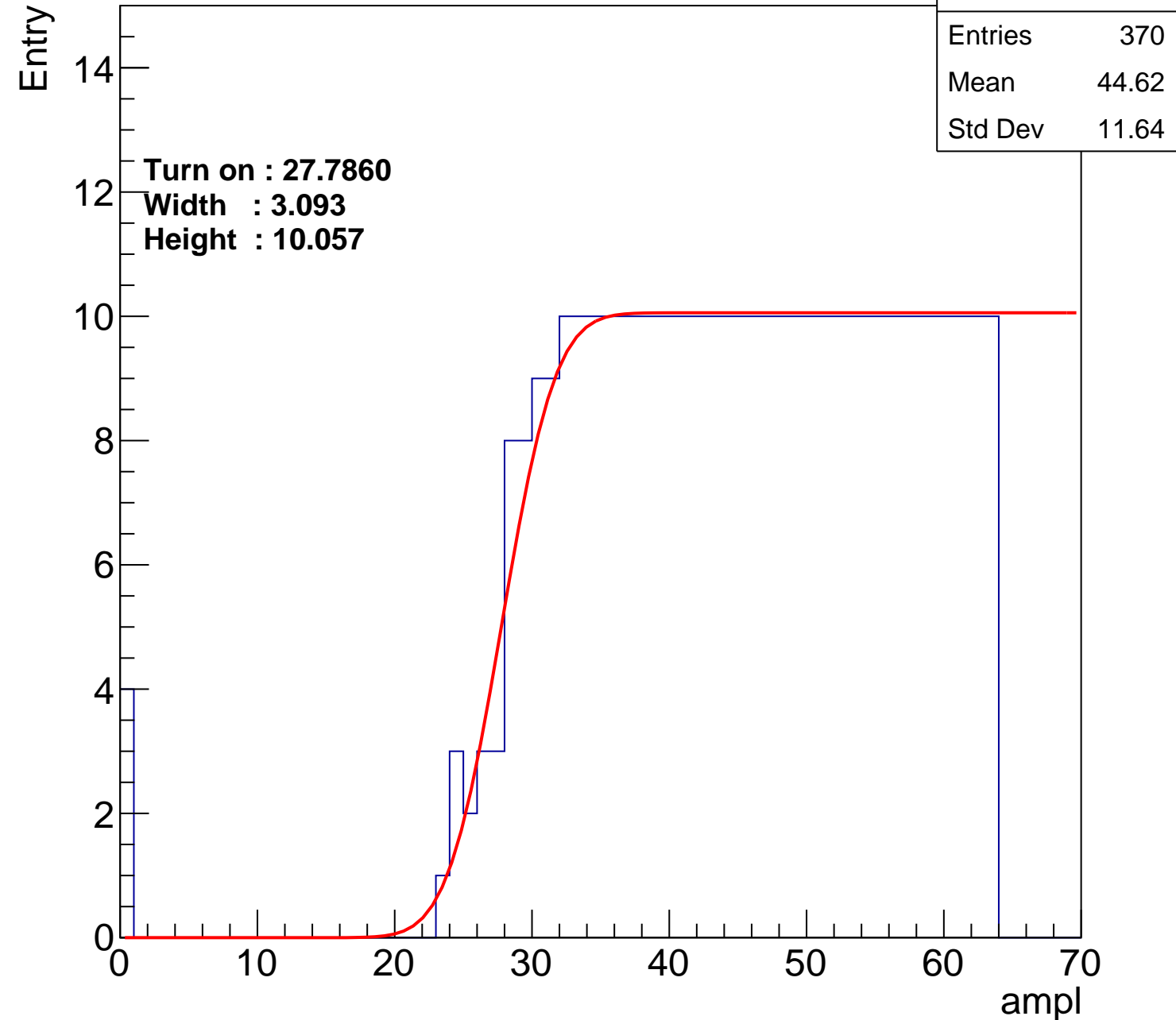
Width : 3.093

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch79

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.34
Std Dev	11.93

Turn on : 27.2353

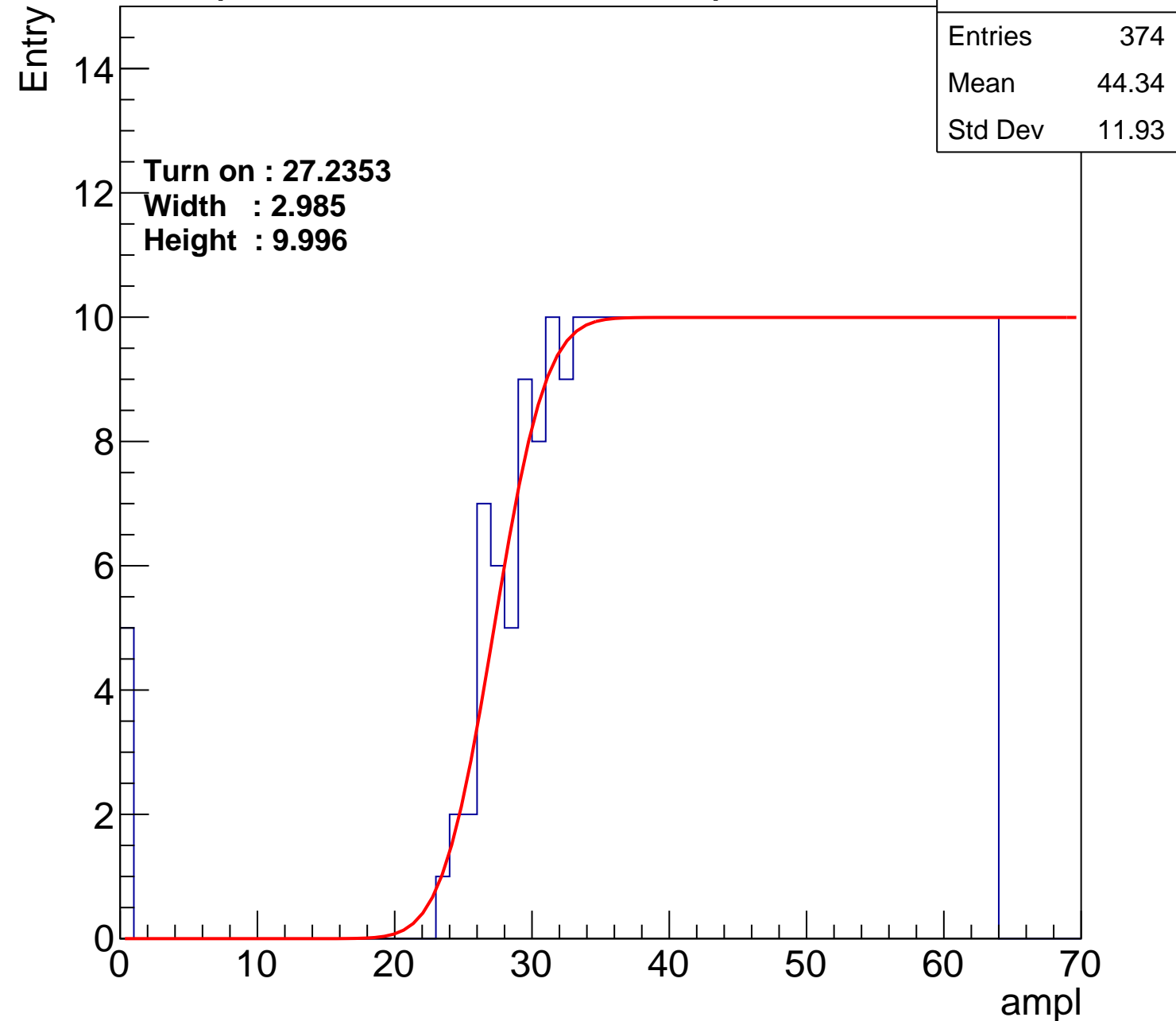
Width : 2.985

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.79
Std Dev	11.92

Turn on : 25.4054

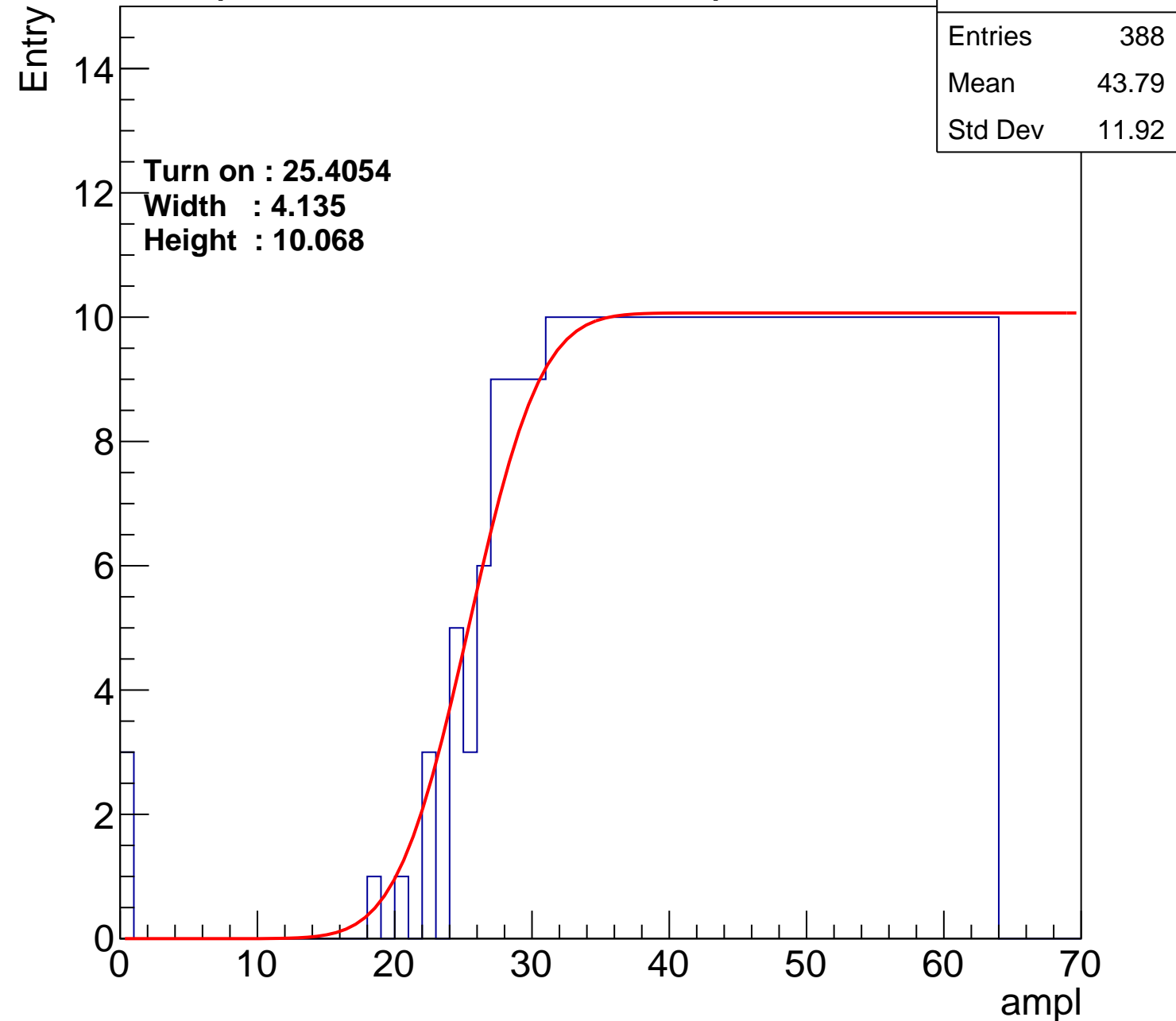
Width : 4.135

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch81

calib_packv5_042523_0143.root, FC#13, port D2

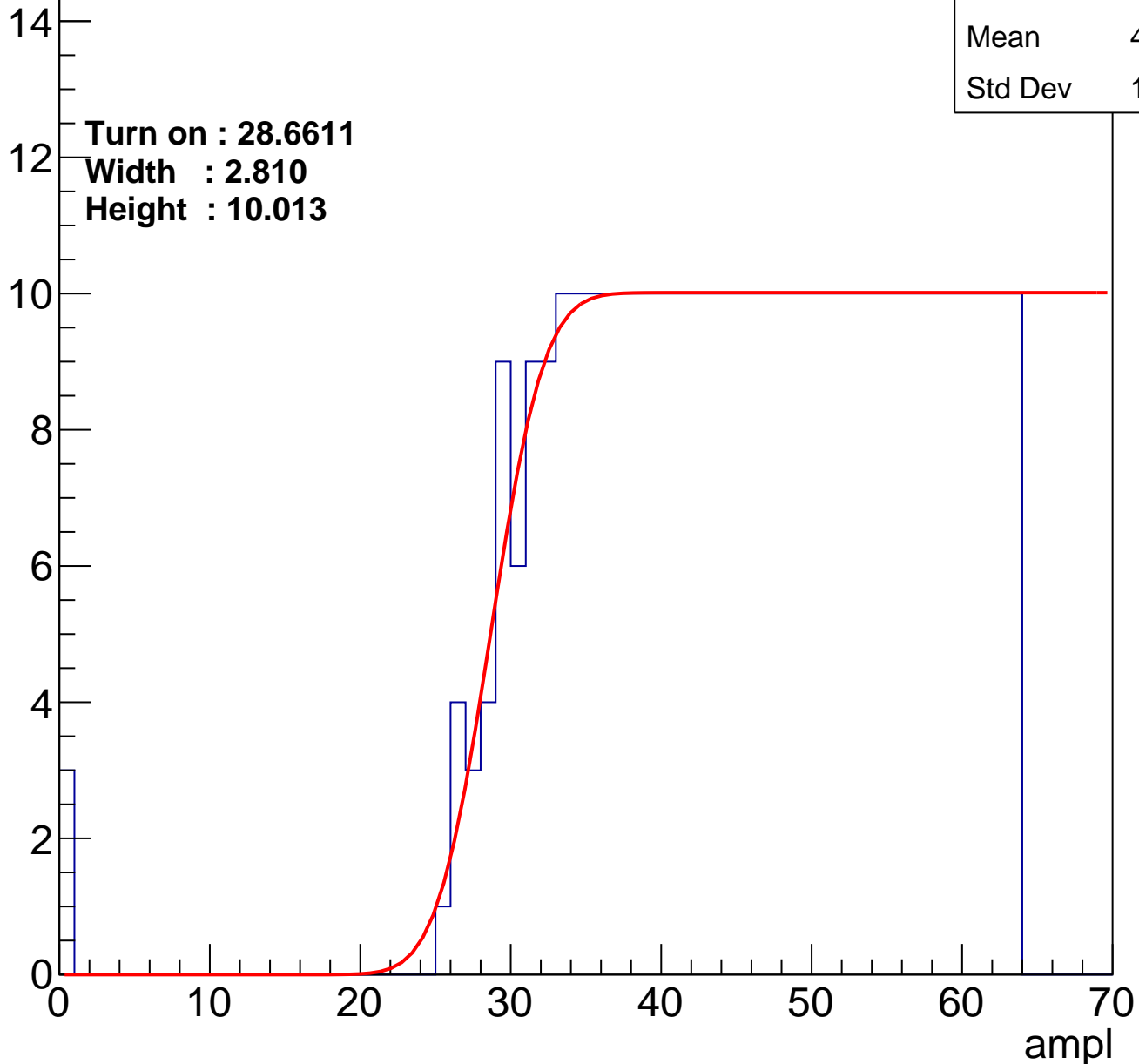
Entries	358
Mean	45.28
Std Dev	11.16

Turn on : 28.6611

Width : 2.810

Height : 10.013

Entry



B1L003S, U3-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	43.93
Std Dev	12.19

Turn on : 27.2285

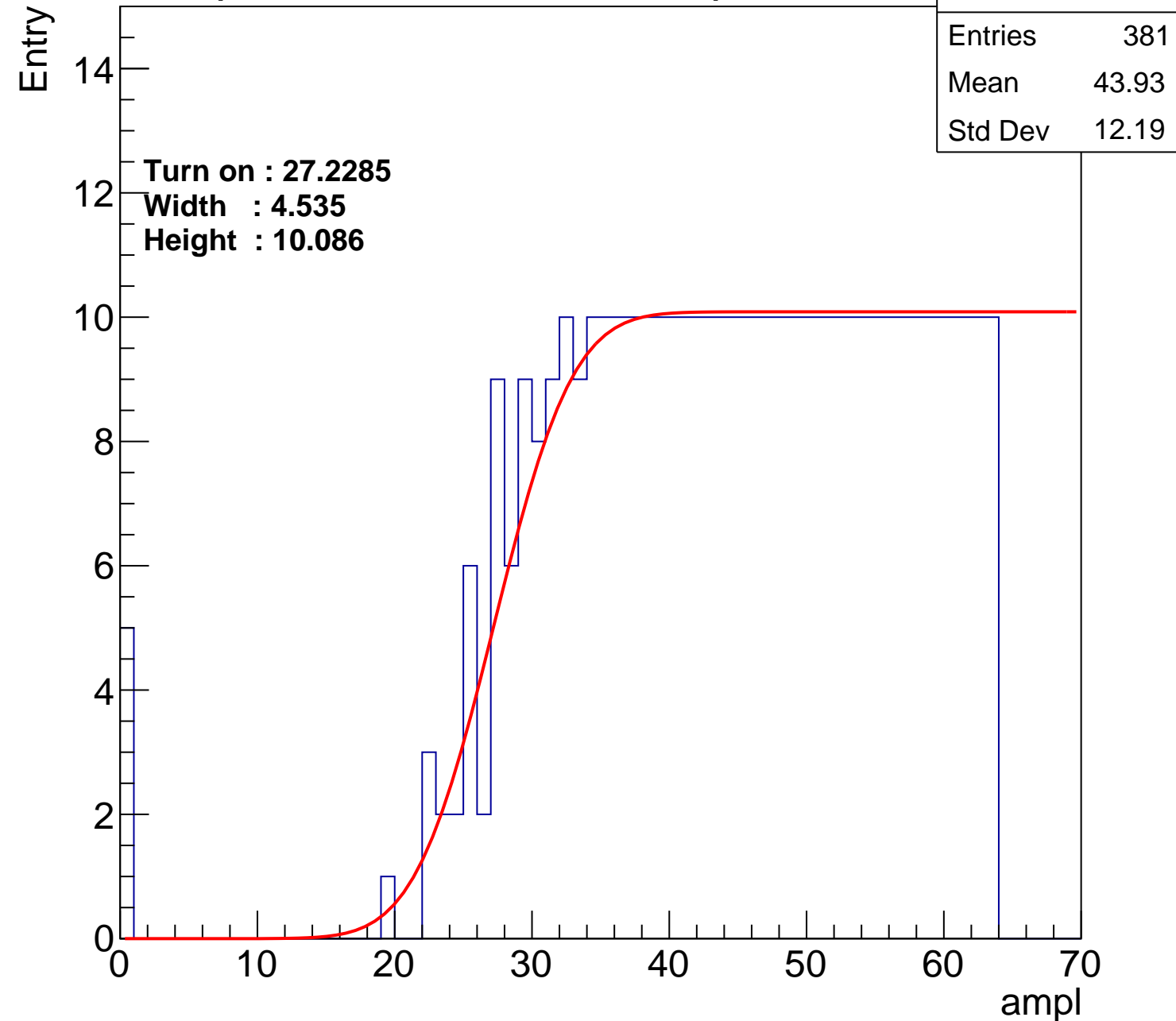
Width : 4.535

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.81
Std Dev	11.82

Turn on : 26.0186

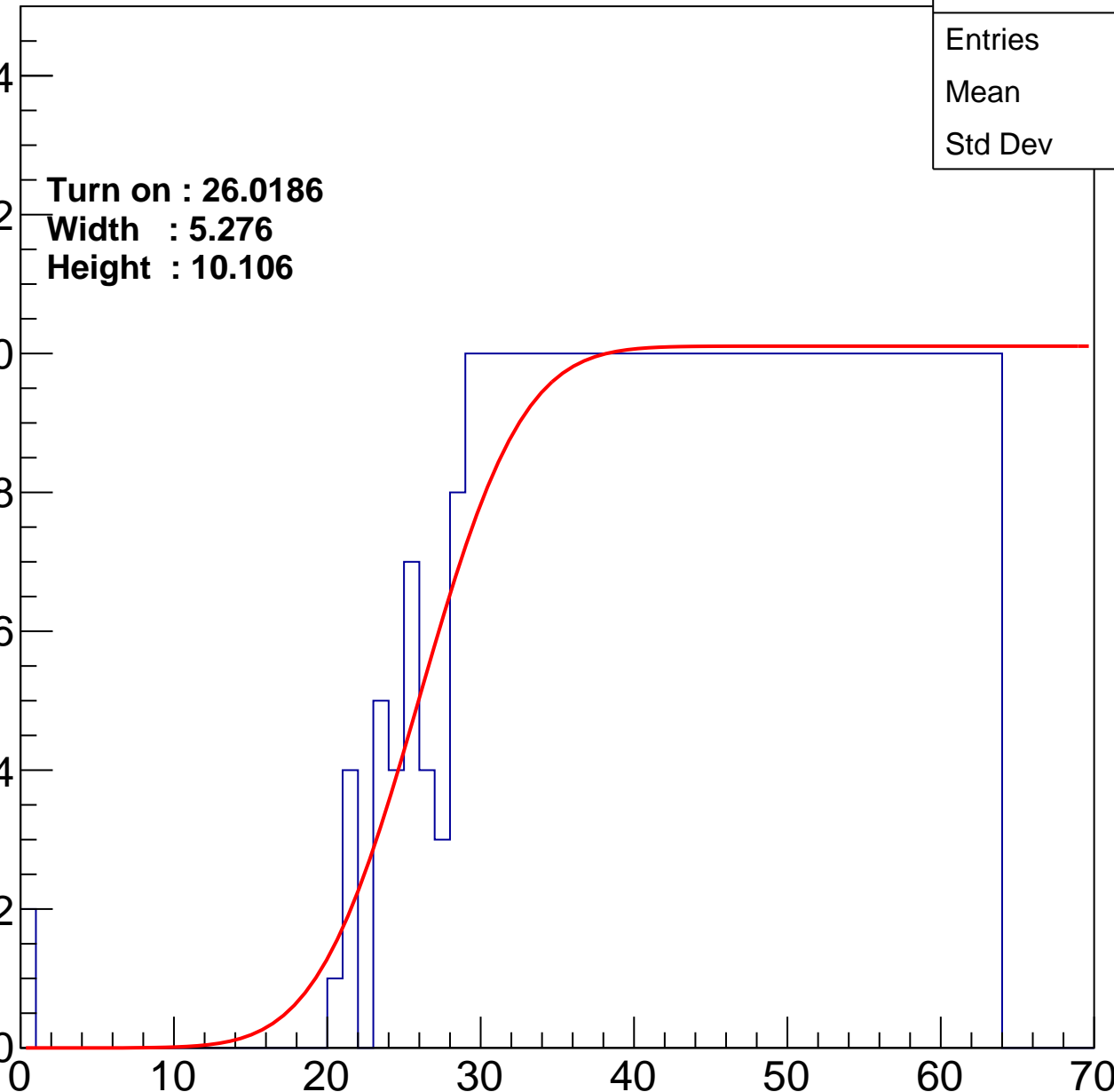
Width : 5.276

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	398
Mean	43.44
Std Dev	11.81

Turn on : 25.3936

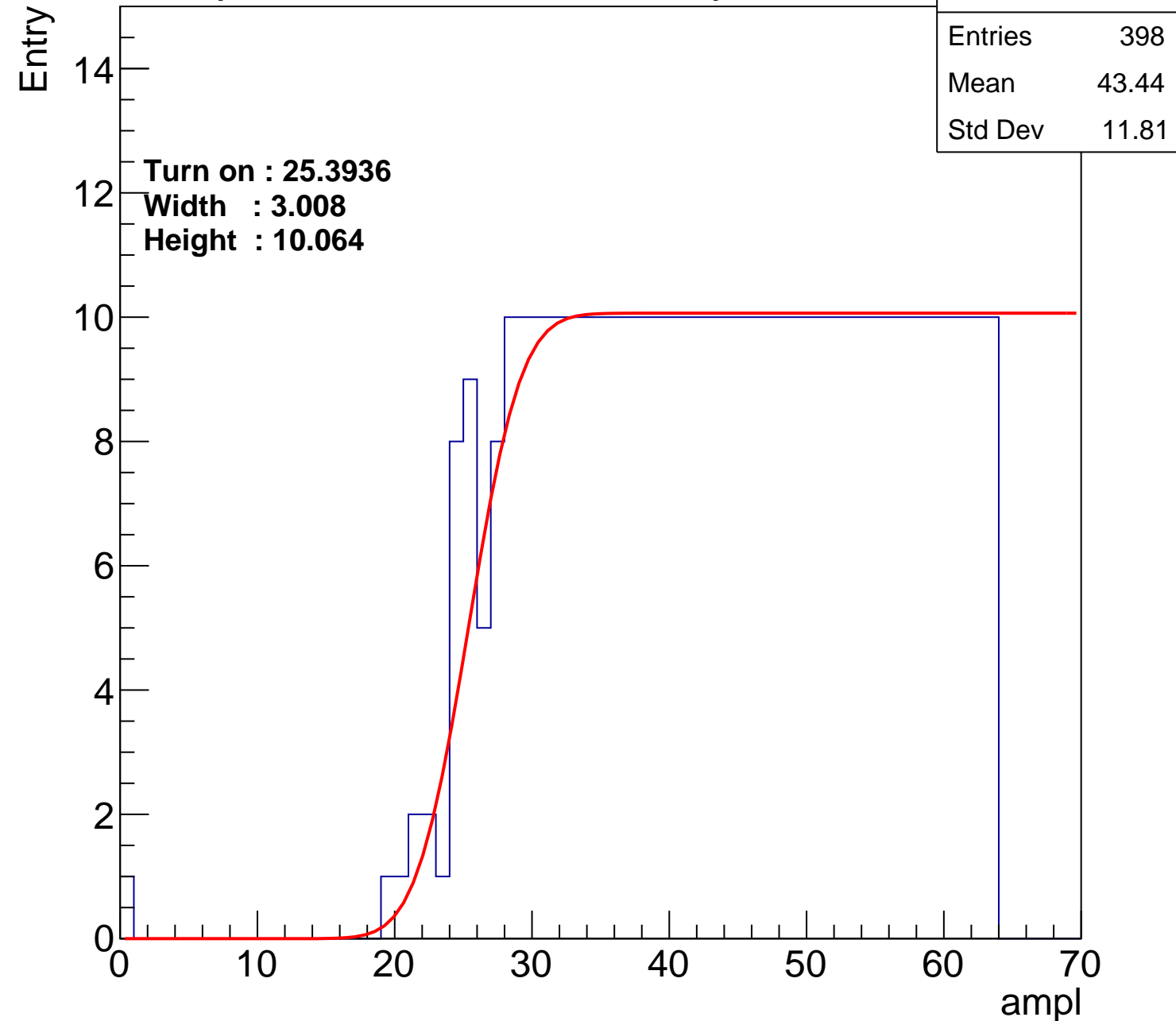
Width : 3.008

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch85

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.87
Std Dev	11.59

Turn on : 25.2752

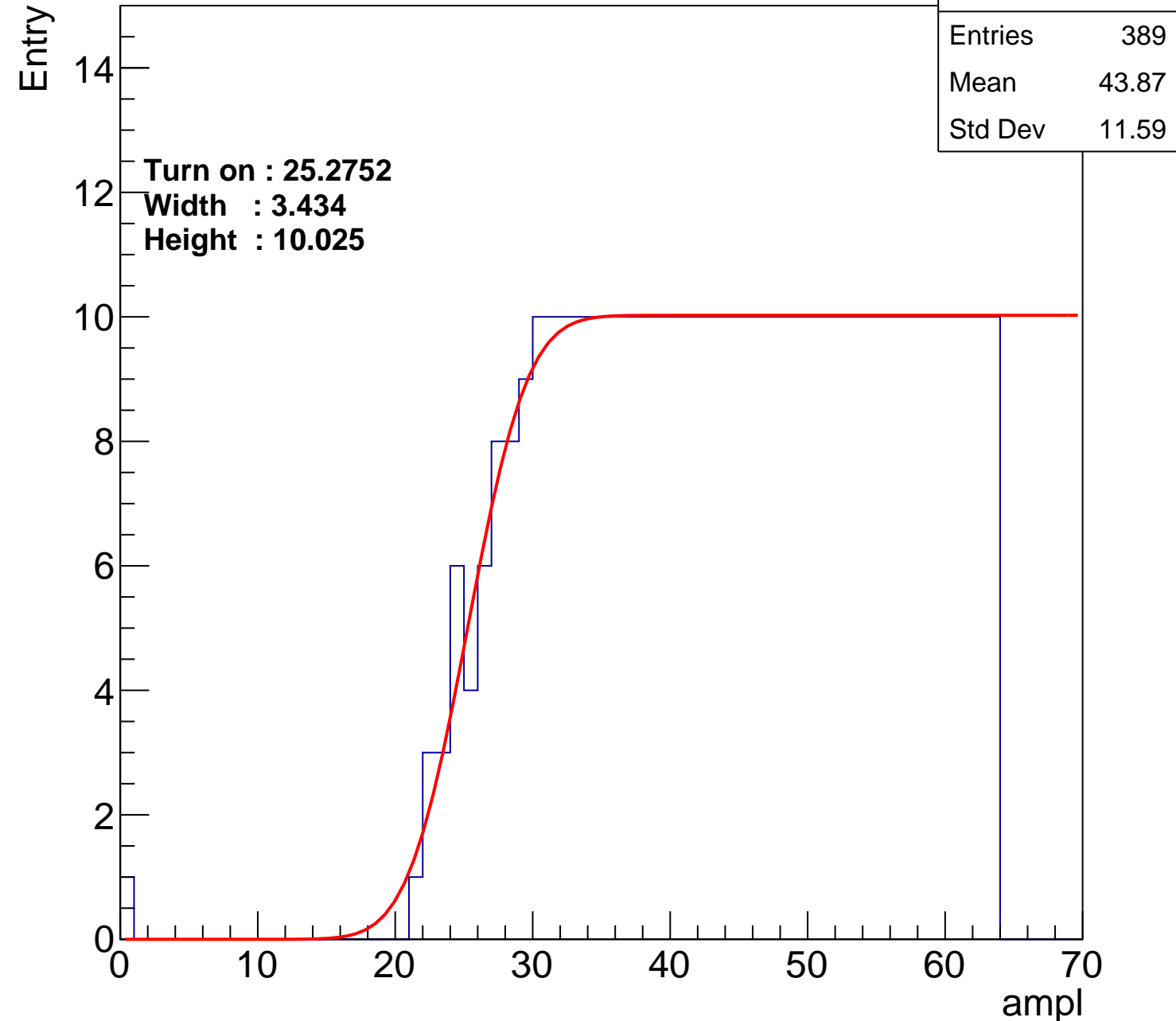
Width : 3.434

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.85
Std Dev	11.41

Turn on : 27.6176

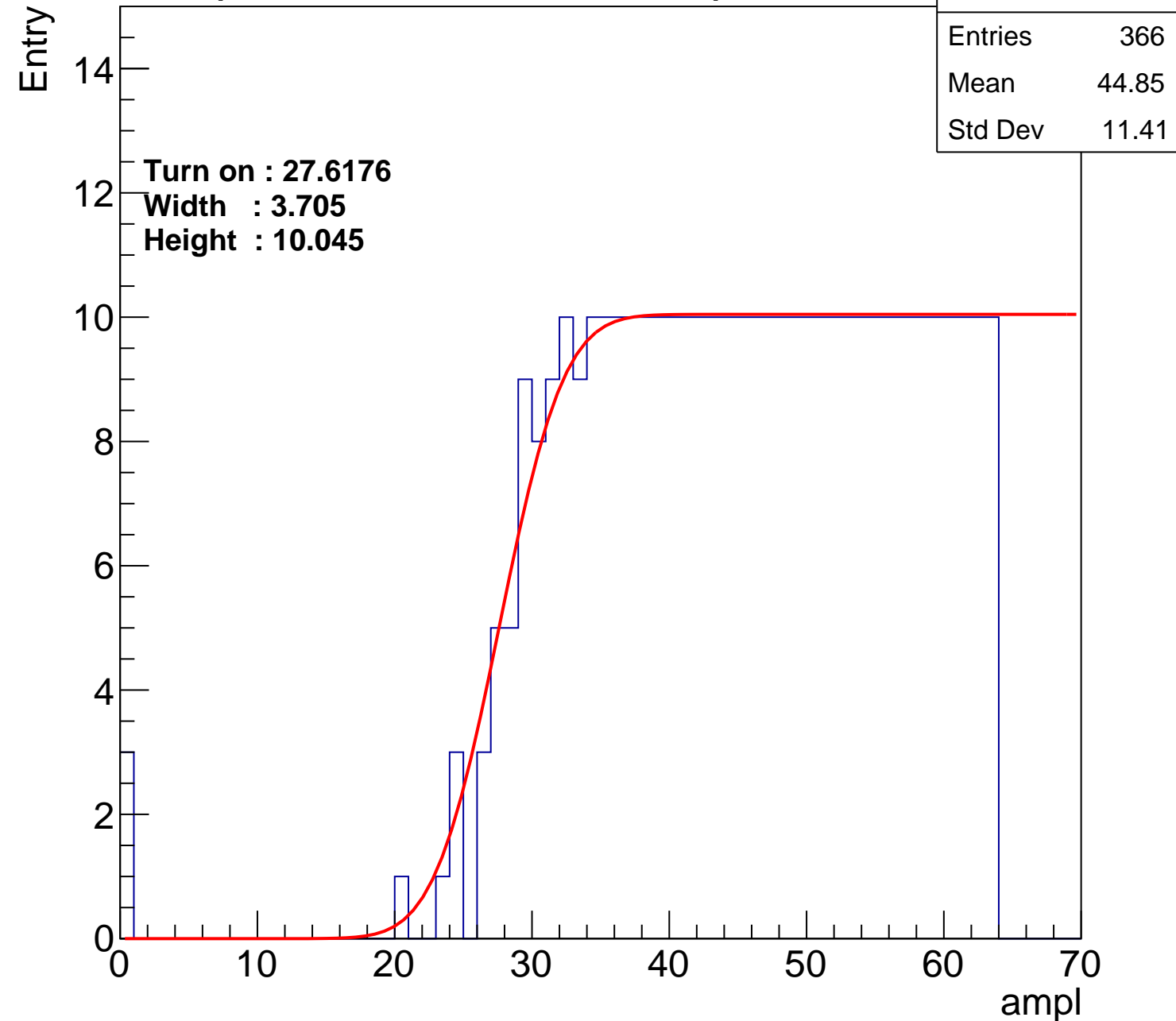
Width : 3.705

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.35
Std Dev	11.51

Turn on : 26.9724

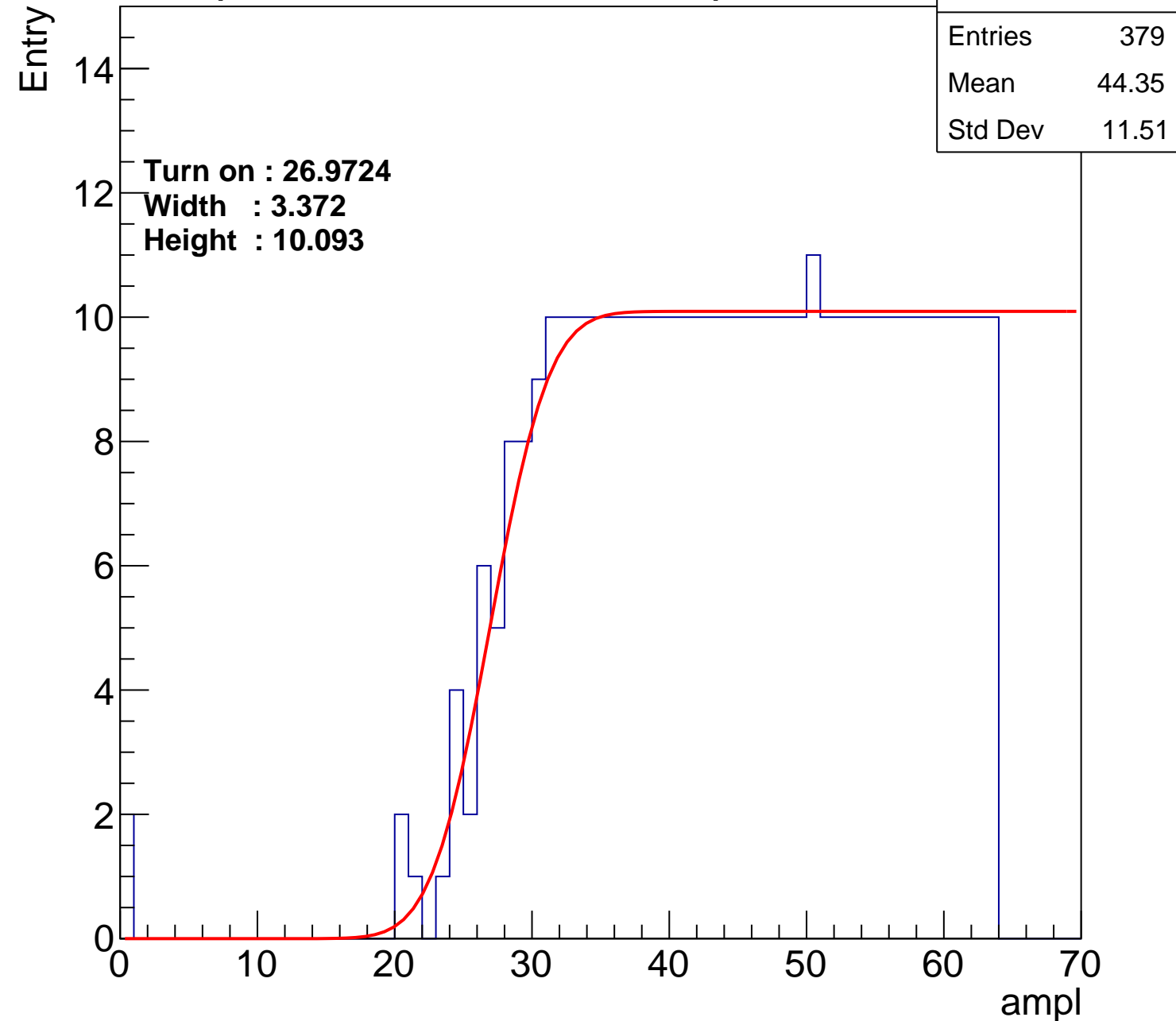
Width : 3.372

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.04
Std Dev	11.16

Turn on : 27.9263

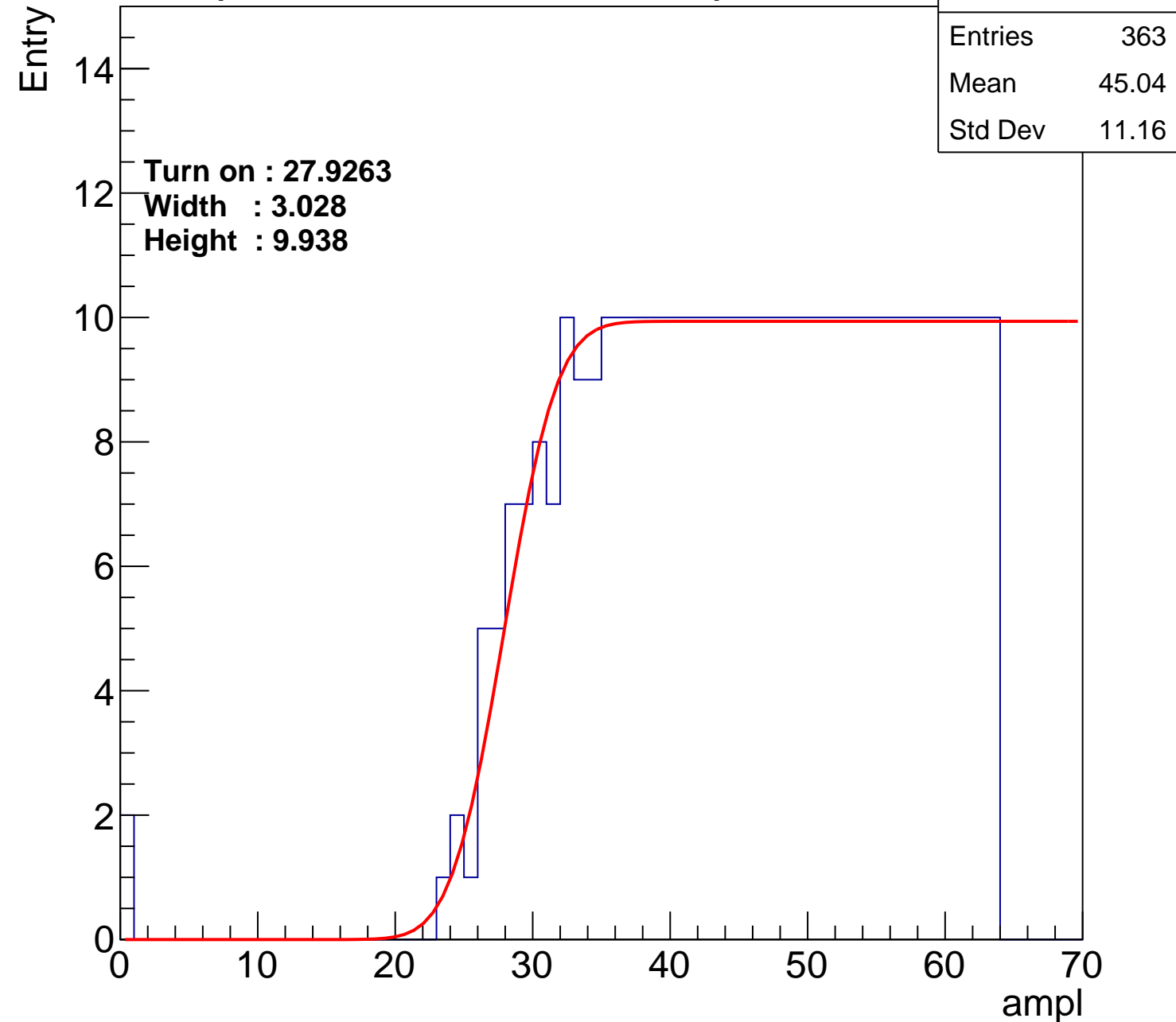
Width : 3.028

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch89

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.22
Std Dev	11.84

Turn on : 26.7675

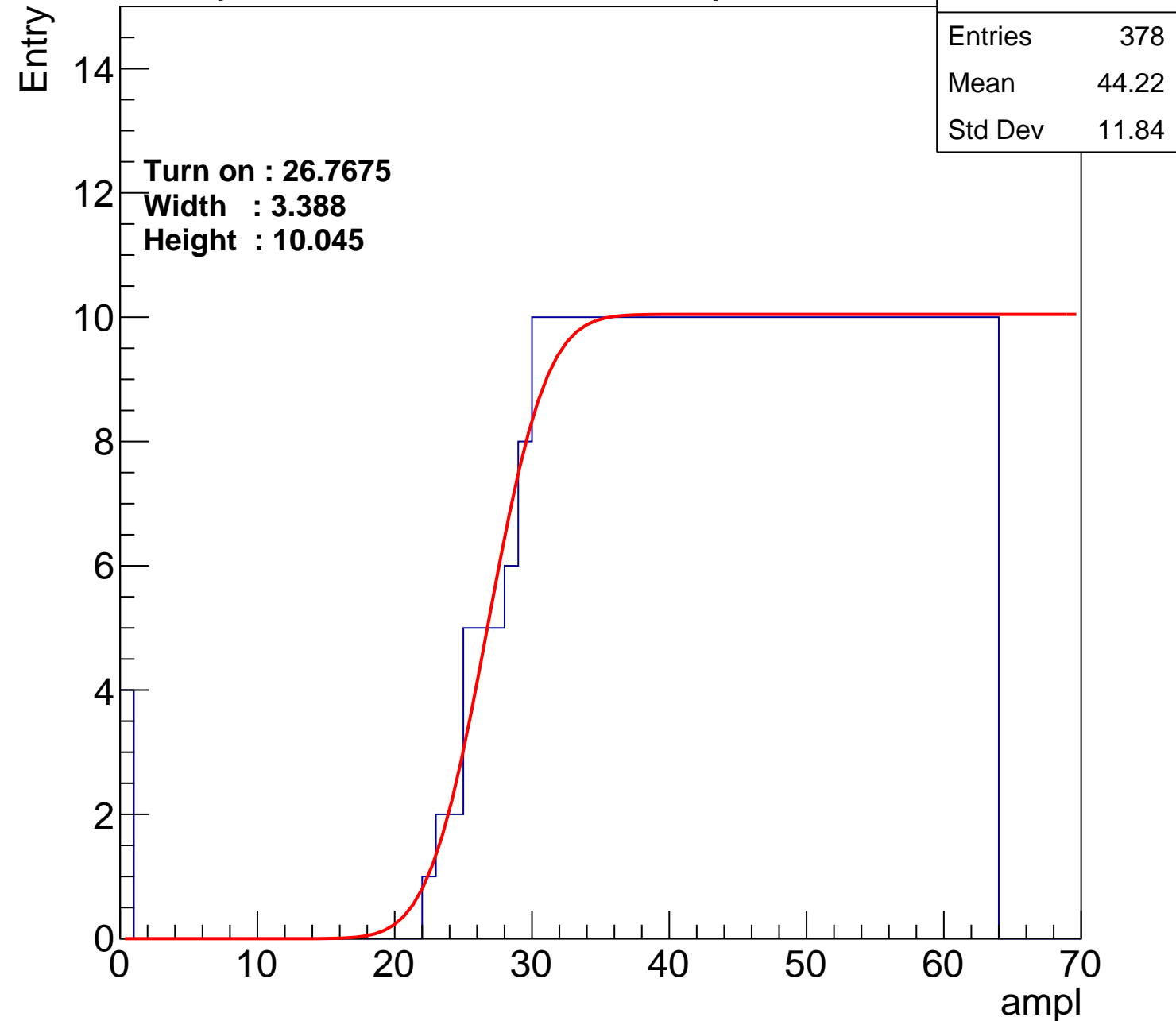
Width : 3.388

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.1776

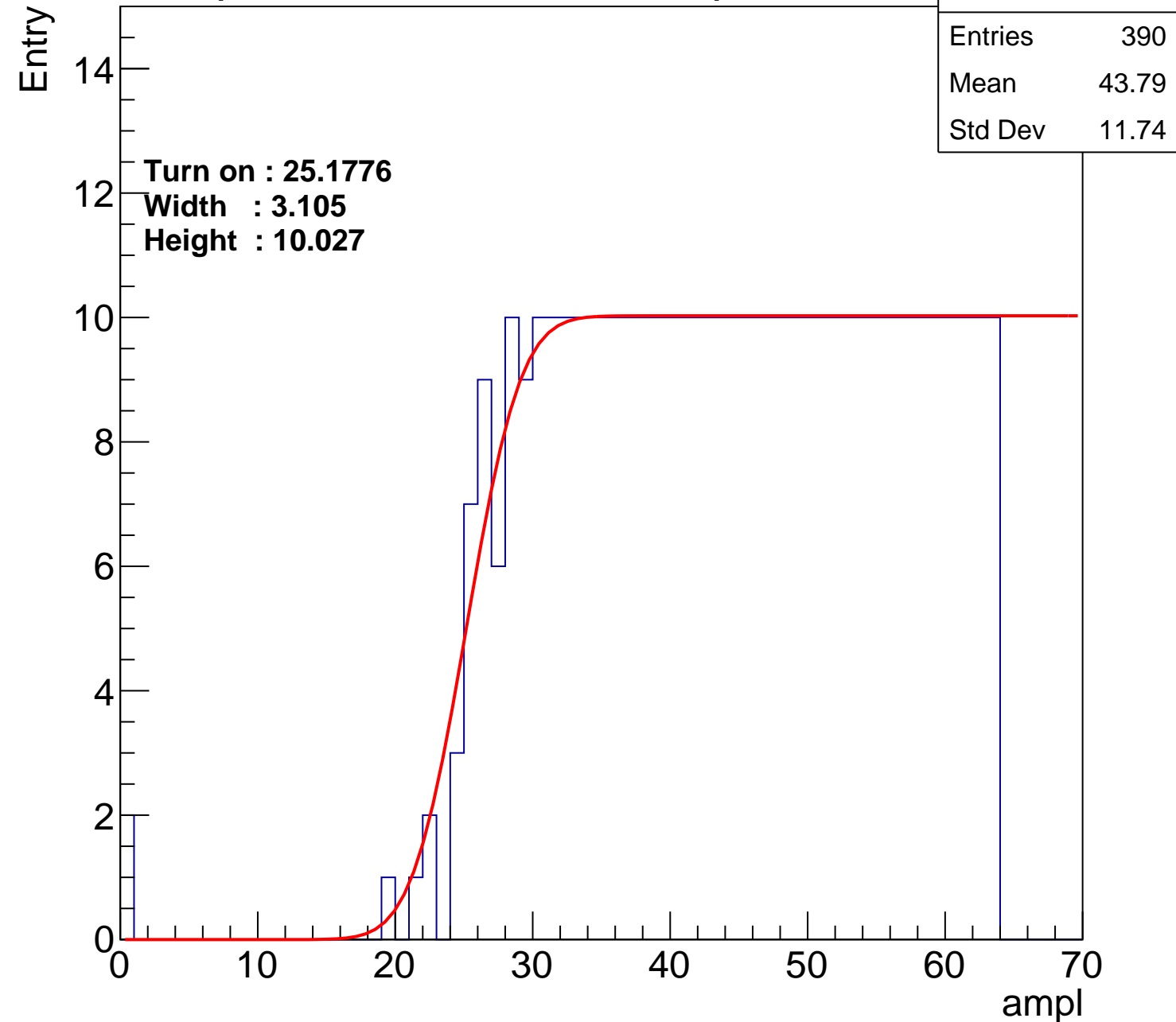
Width : 3.105

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch91

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.41
Std Dev	11.31

Turn on : 26.4319

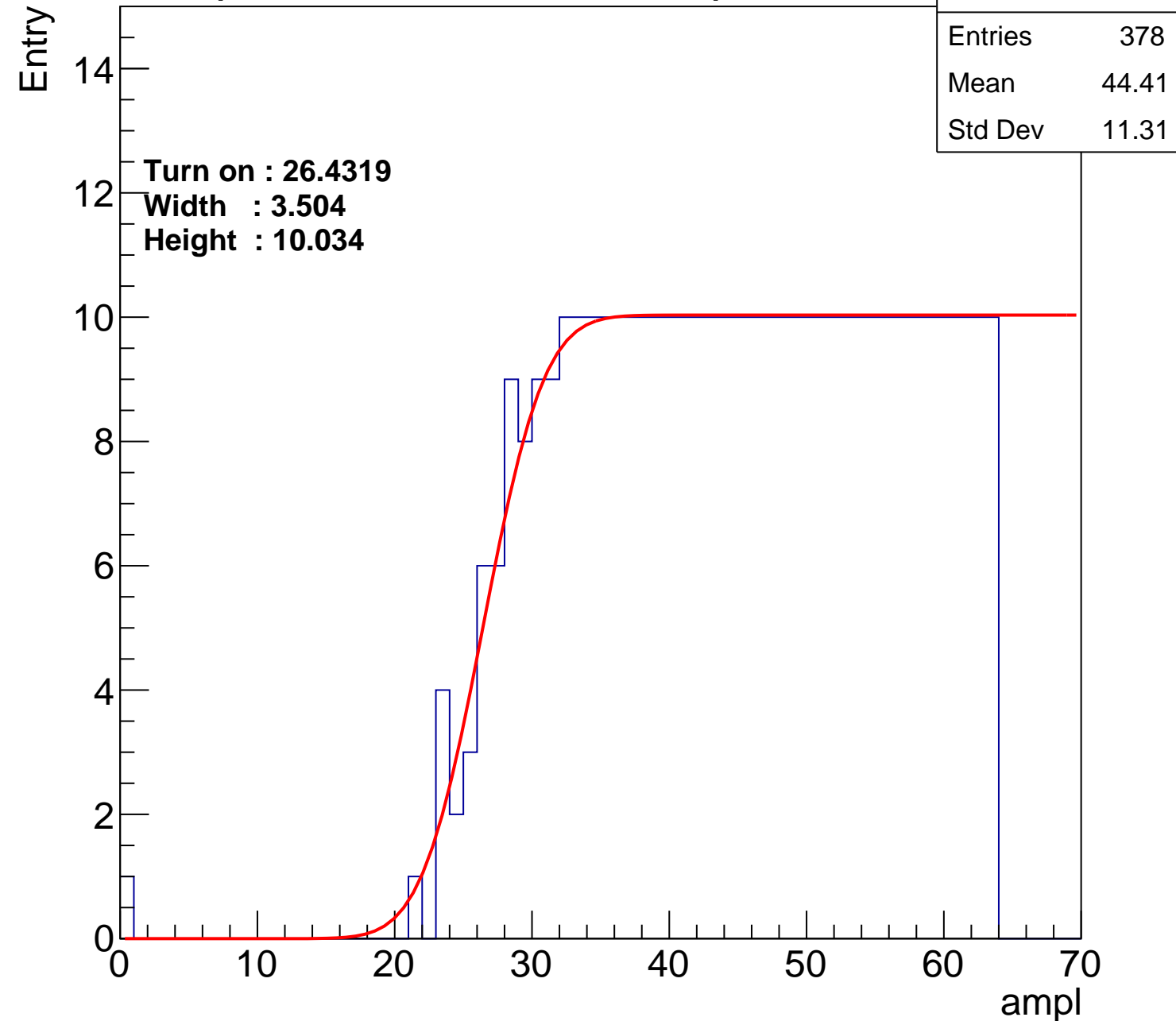
Width : 3.504

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.76
Std Dev	11.31

Turn on : 27.5407

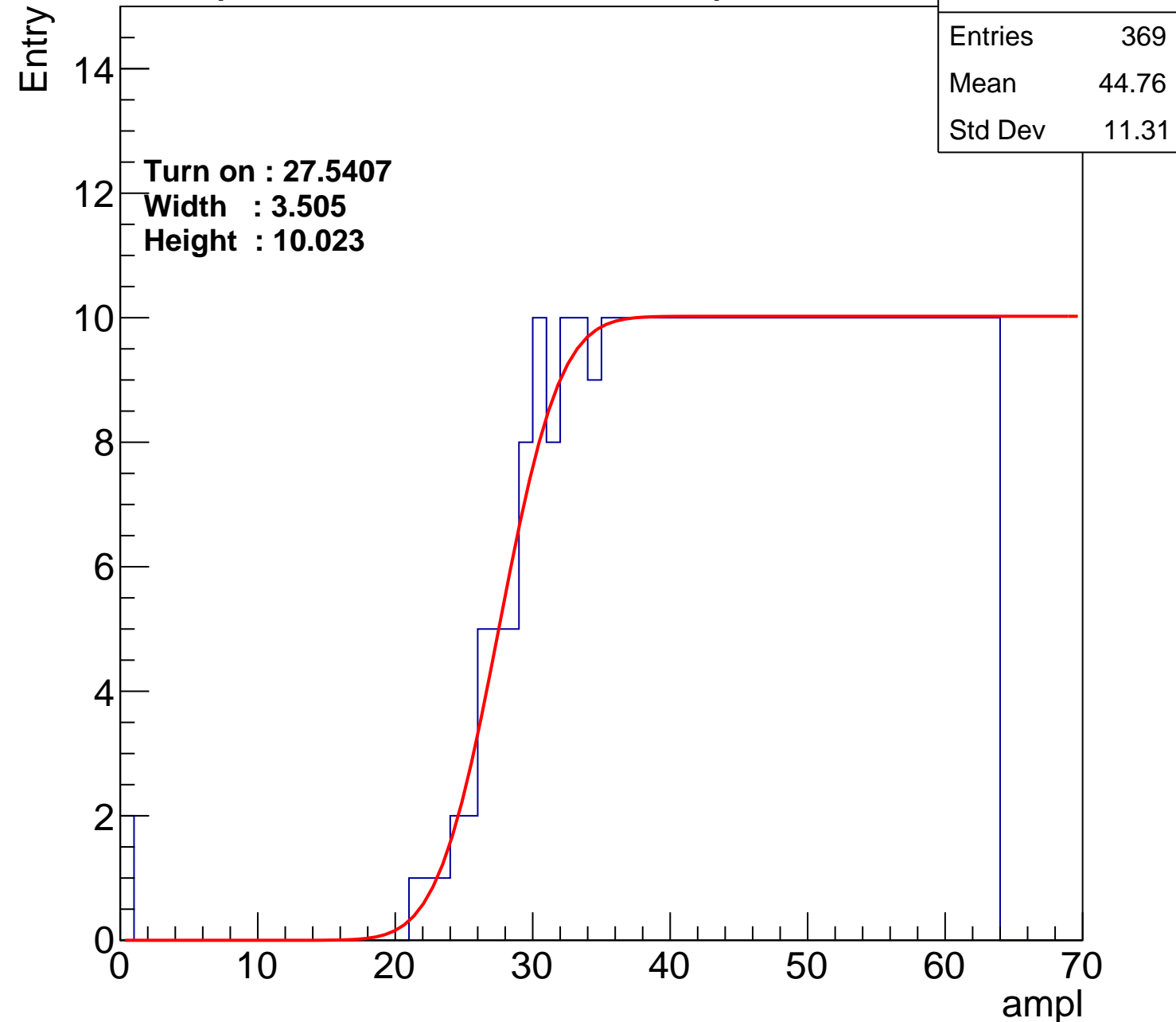
Width : 3.505

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.25
Std Dev	11.8

Turn on : 27.1841

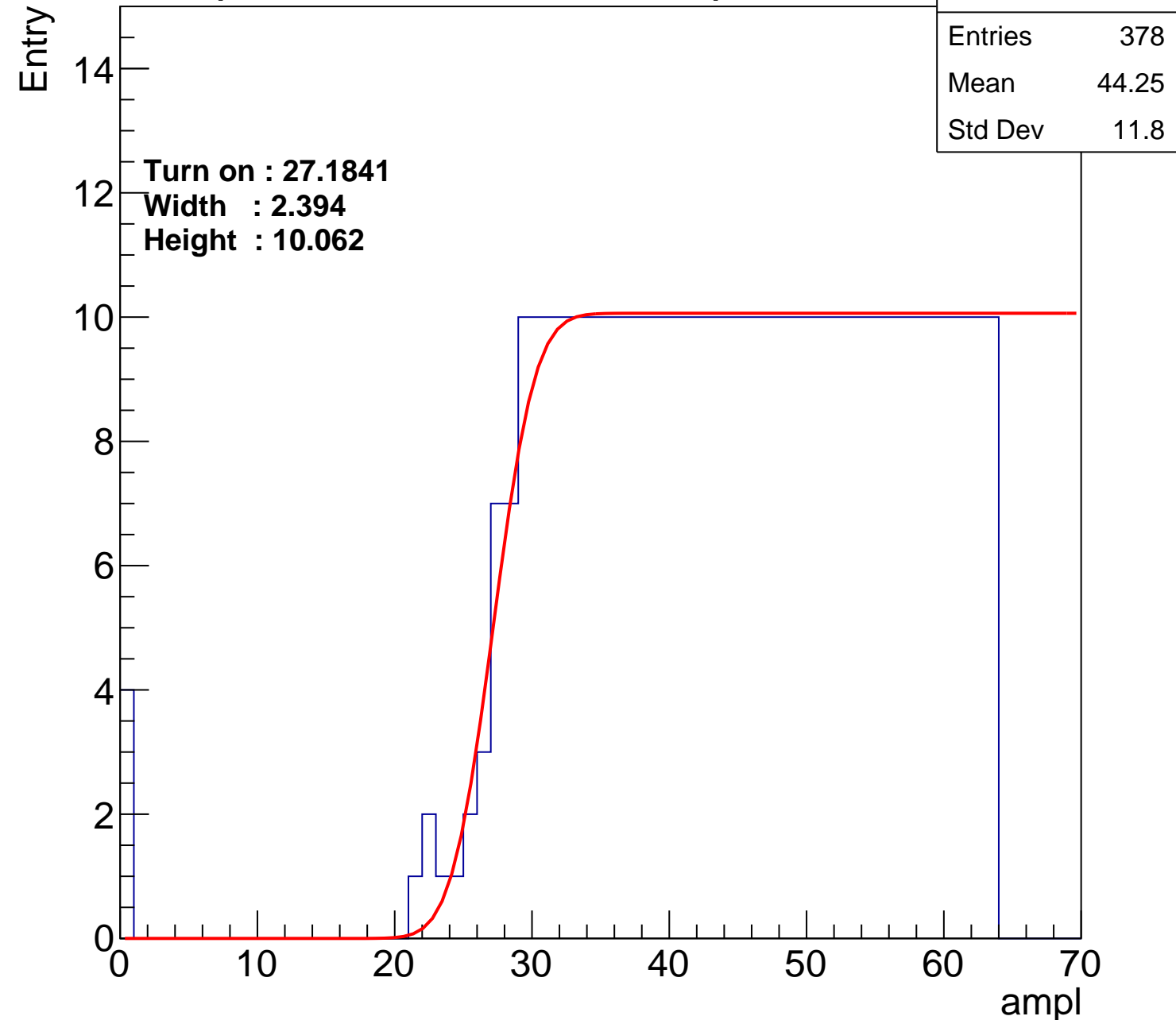
Width : 2.394

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch94

calib_packv5_042523_0143.root, FC#13, port D2

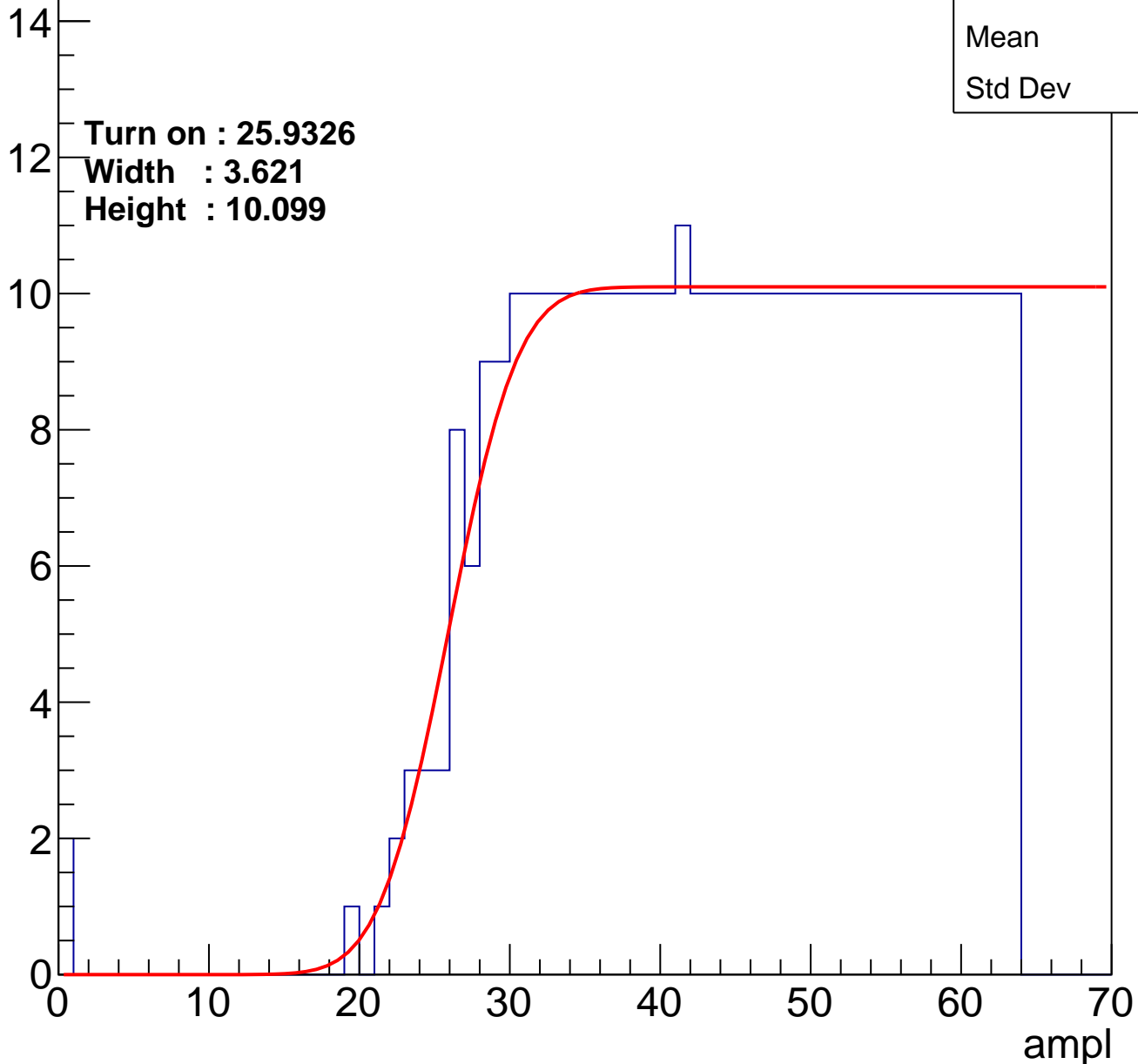
Entries	388
Mean	43.9
Std Dev	11.7

Turn on : 25.9326

Width : 3.621

Height : 10.099

Entry



B1L003S, U3-ch95

calib_packv5_042523_0143.root, FC#13, port D2

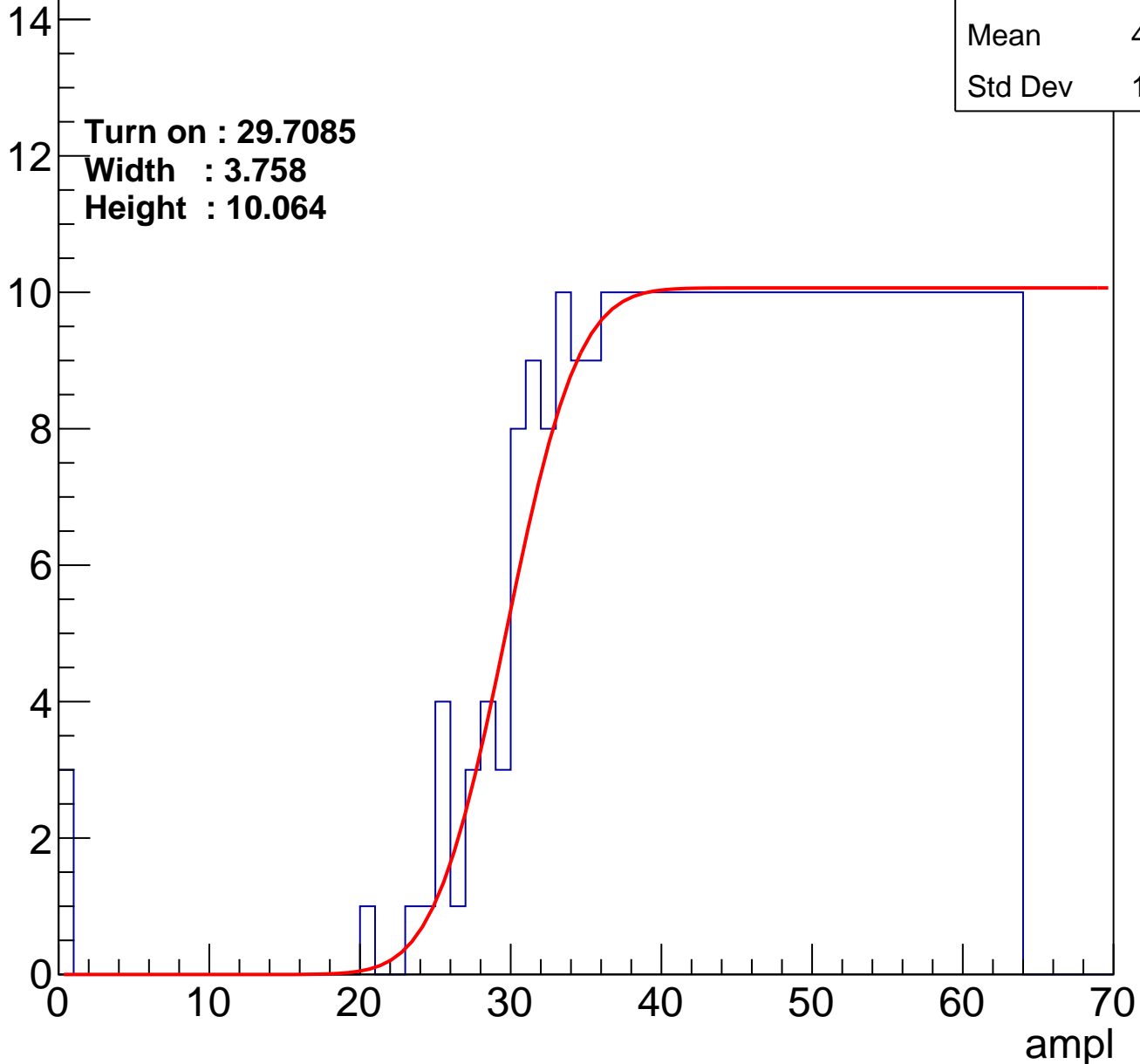
Entries	354
Mean	45.36
Std Dev	11.24

Turn on : 29.7085

Width : 3.758

Height : 10.064

Entry



B1L003S, U3-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.64
Std Dev	11.8

Turn on : 27.9696

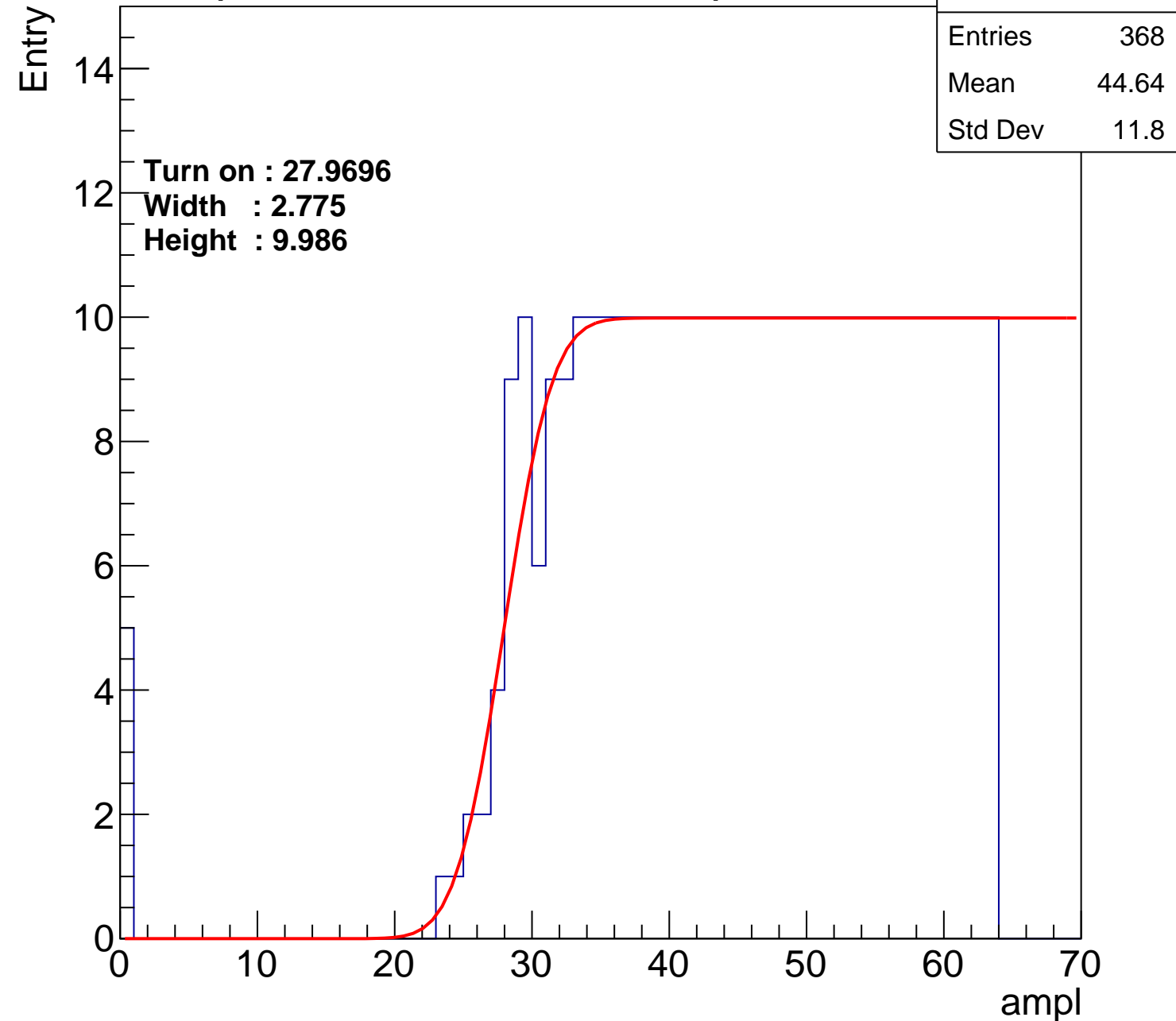
Width : 2.775

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch97

calib_packv5_042523_0143.root, FC#13, port D2

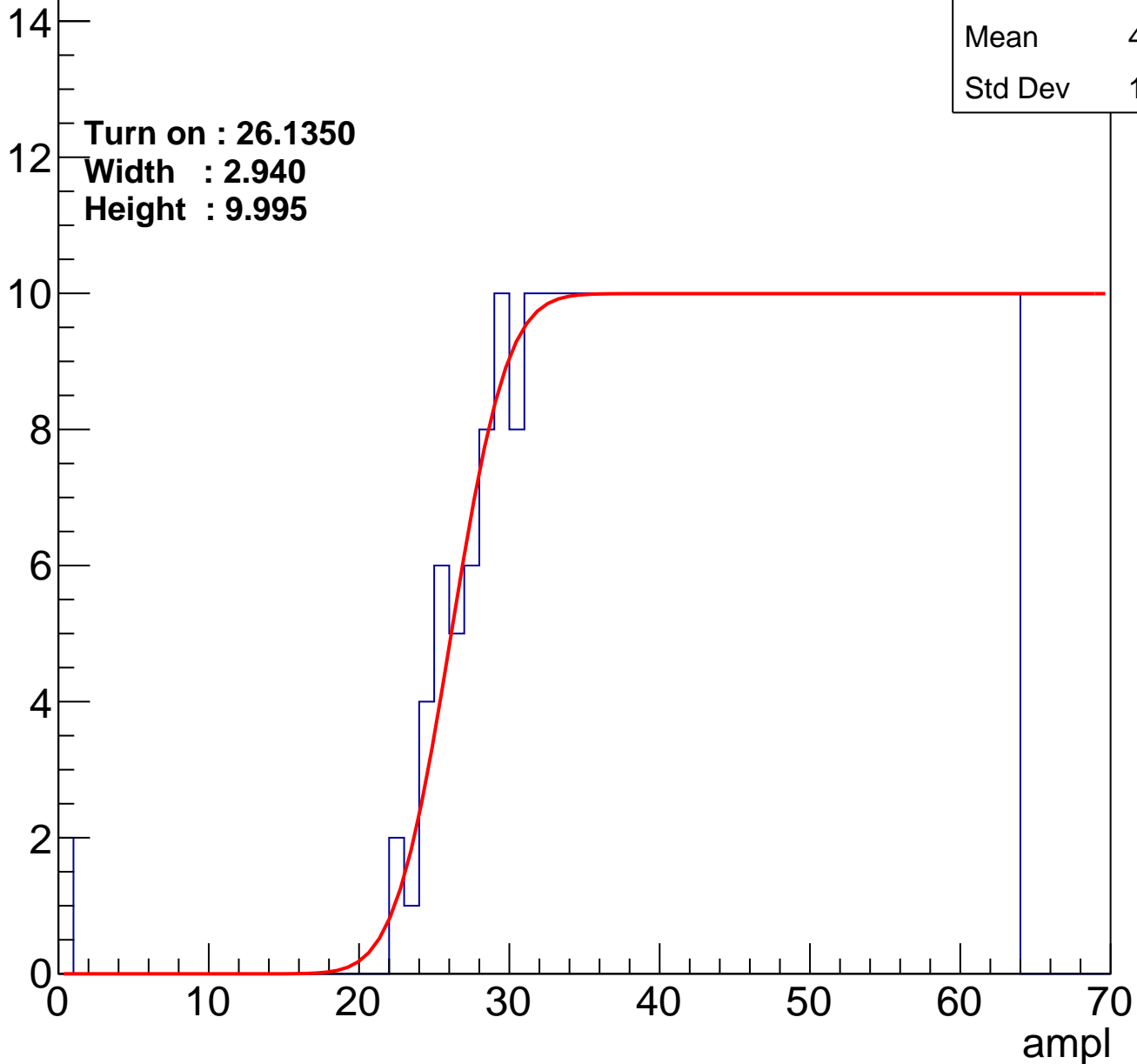
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1350

Width : 2.940

Height : 9.995

Entry



B1L003S, U3-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.9
Std Dev	11.44

Turn on : 28.5222

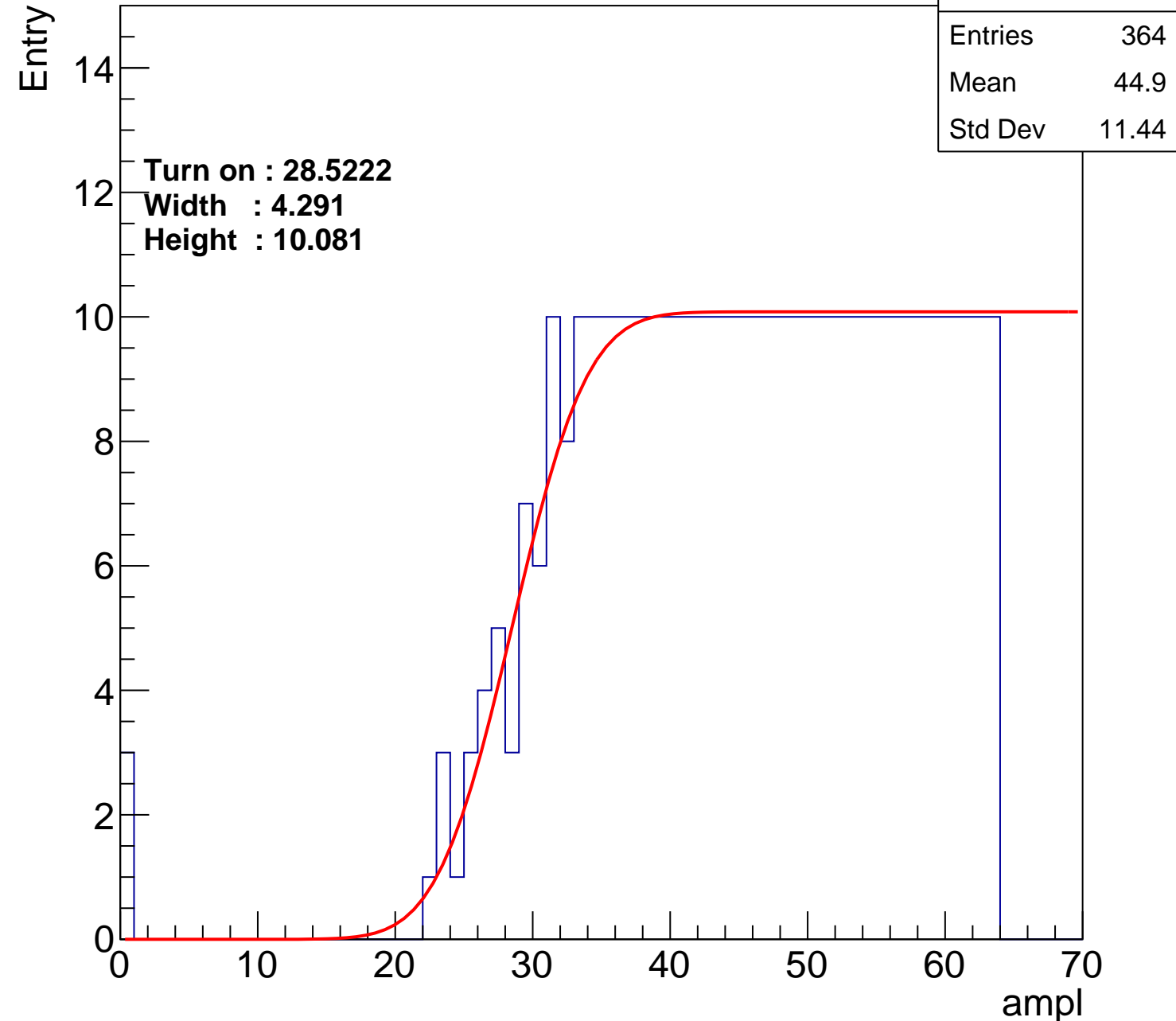
Width : 4.291

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch99

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.03
Std Dev	11.54

Turn on : 28.7791

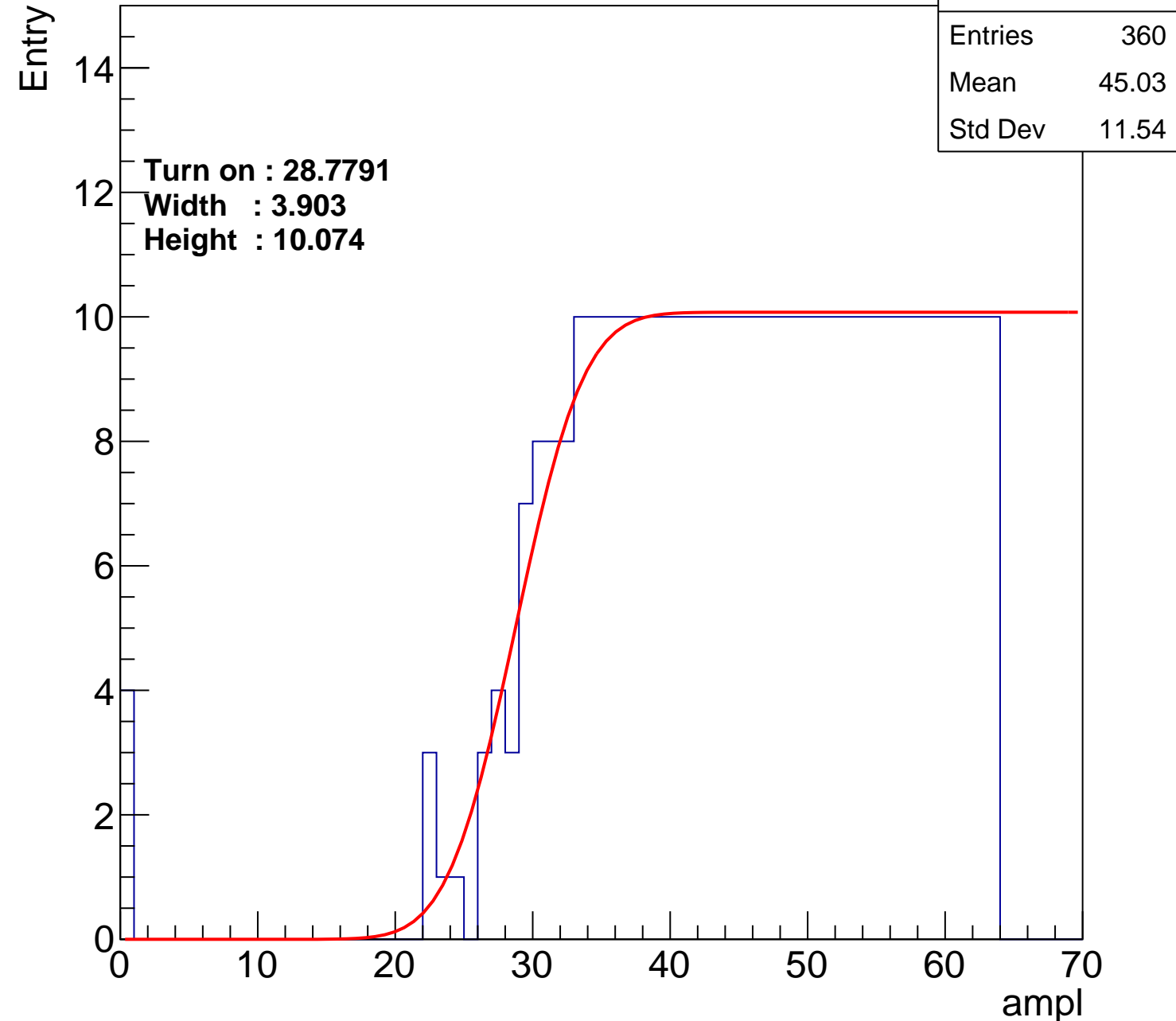
Width : 3.903

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl

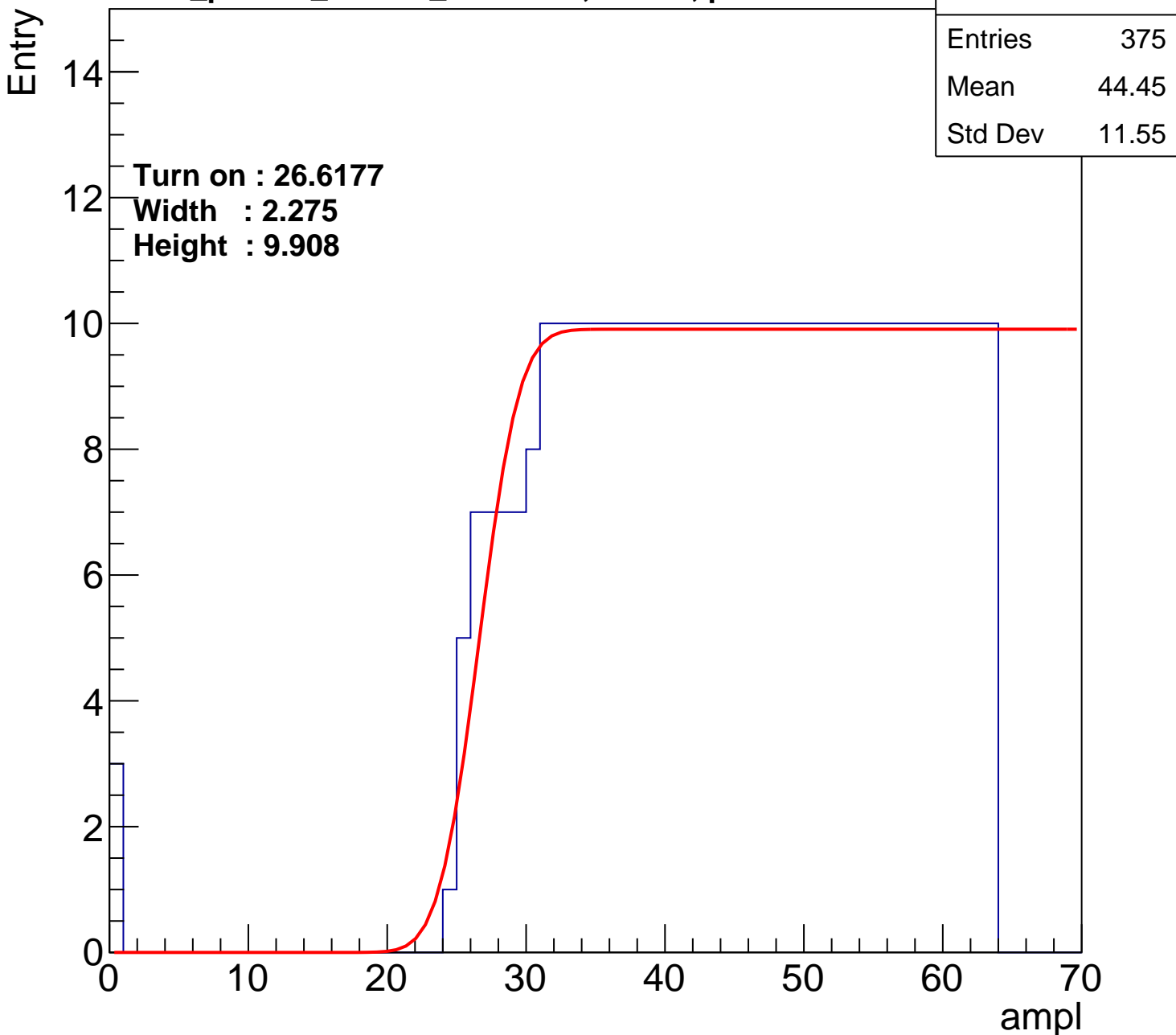


calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.45
Std Dev	11.55

Height : 9.908



B1L003S, U3-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	383
Mean	44.03
Std Dev	11.8

Turn on : 26.5139

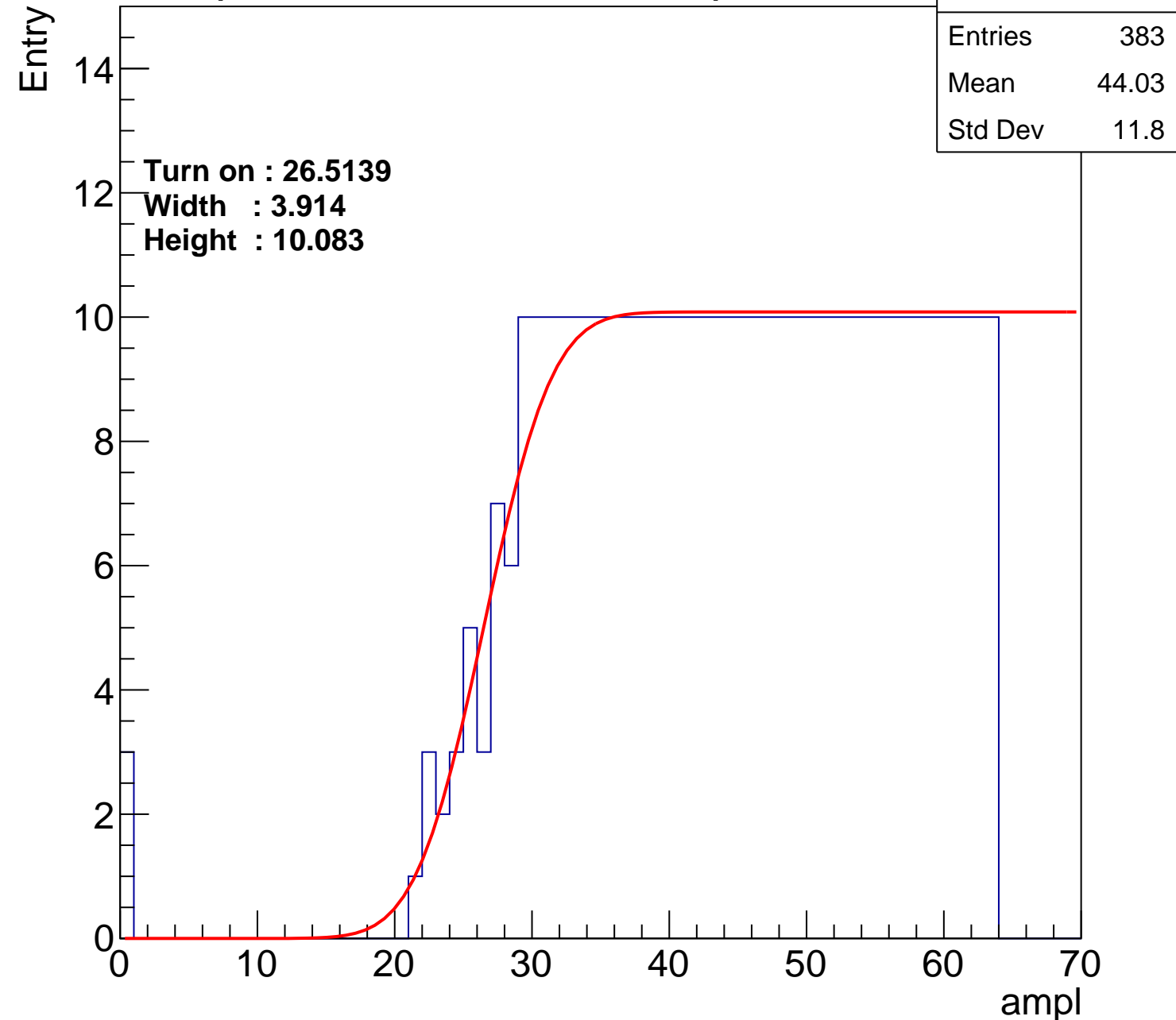
Width : 3.914

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch102

calib_packv5_042523_0143.root, FC#13, port D2

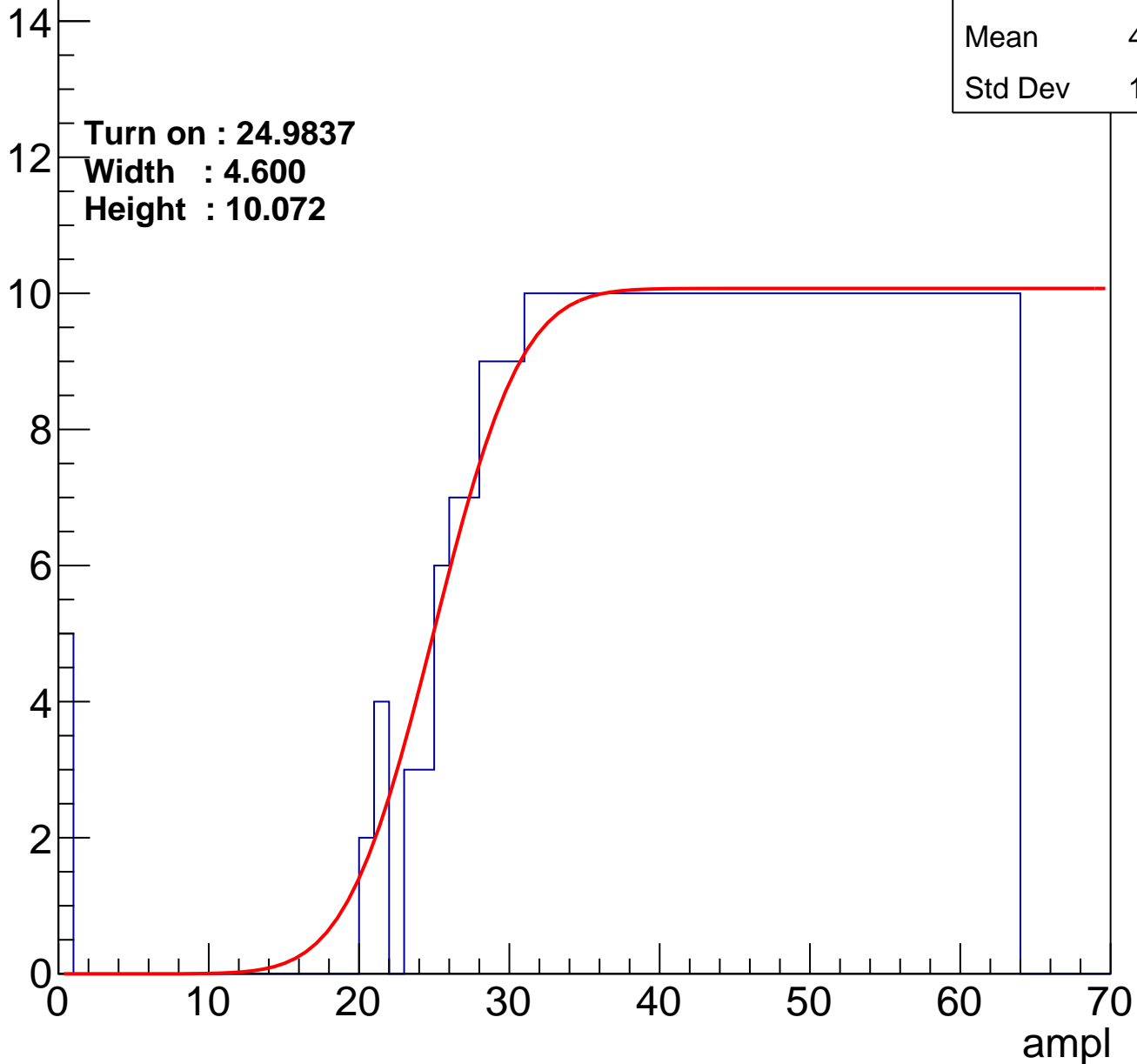
Entries	394
Mean	43.35
Std Dev	12.42

Turn on : 24.9837

Width : 4.600

Height : 10.072

Entry



B1L003S, U3-ch103

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.54
Std Dev	12.36

Turn on : 25.5233

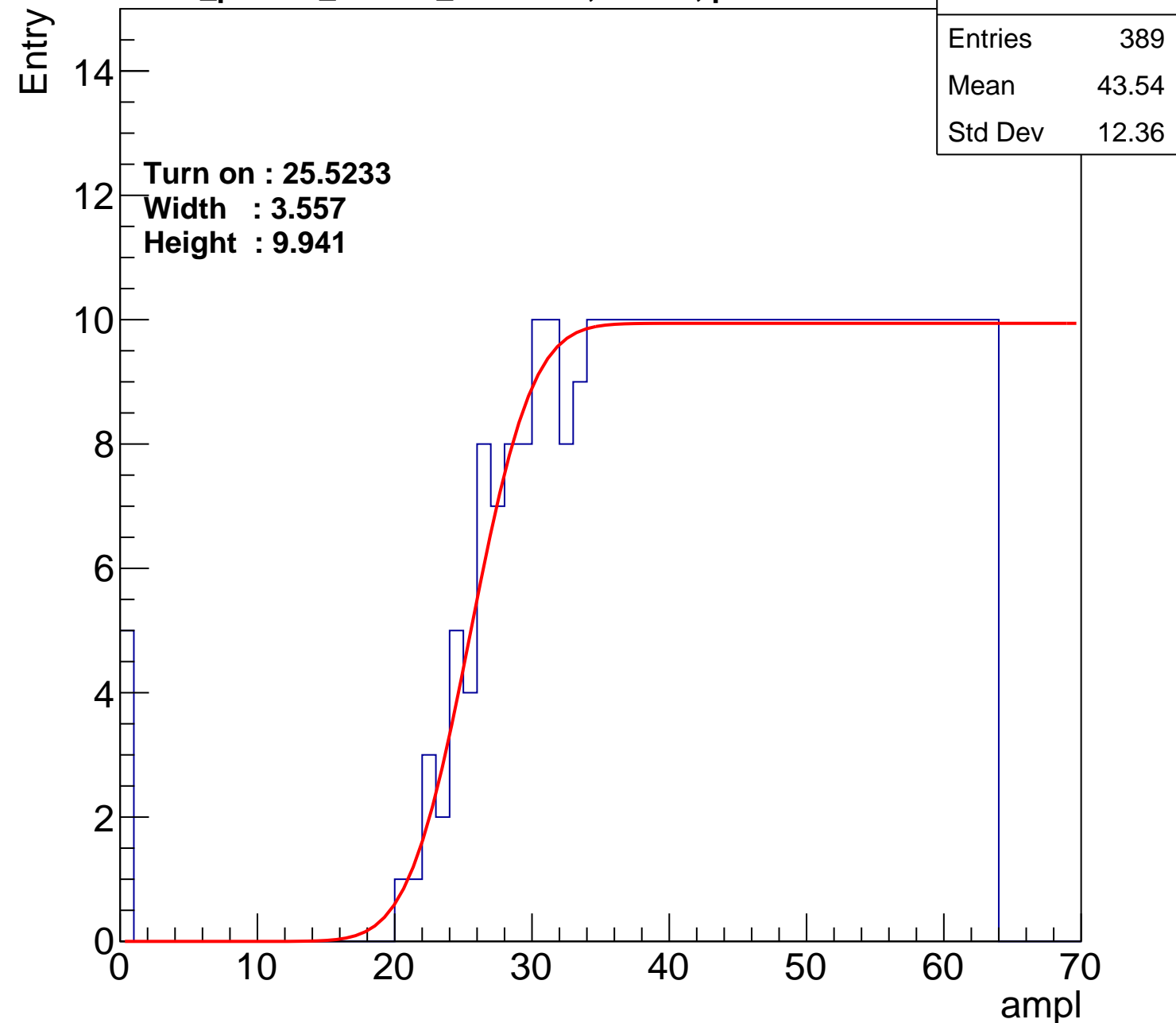
Width : 3.557

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch104

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.39
Std Dev	11.34

Turn on : 26.6288

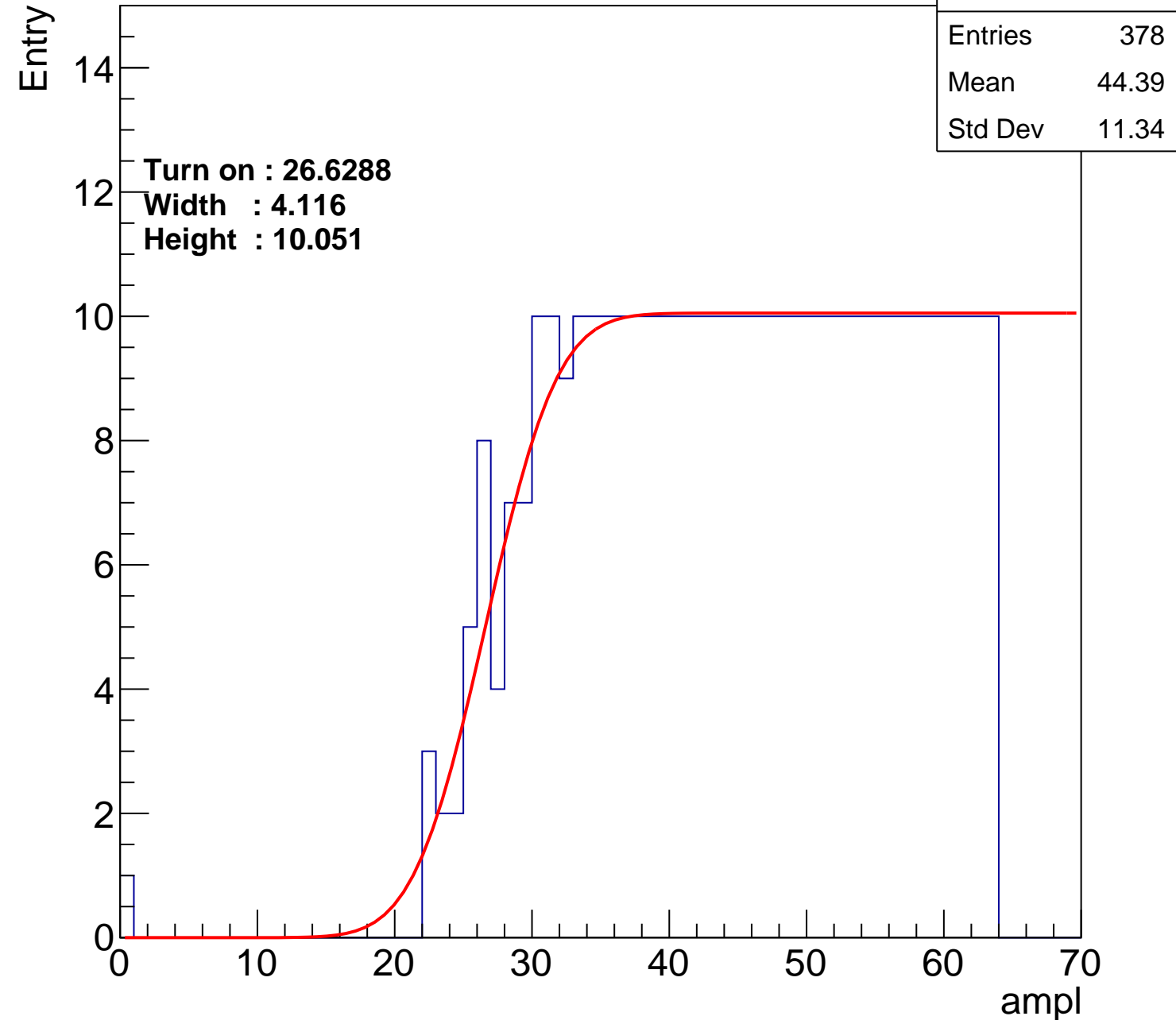
Width : 4.116

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.03
Std Dev	12.39

Turn on : 27.0663

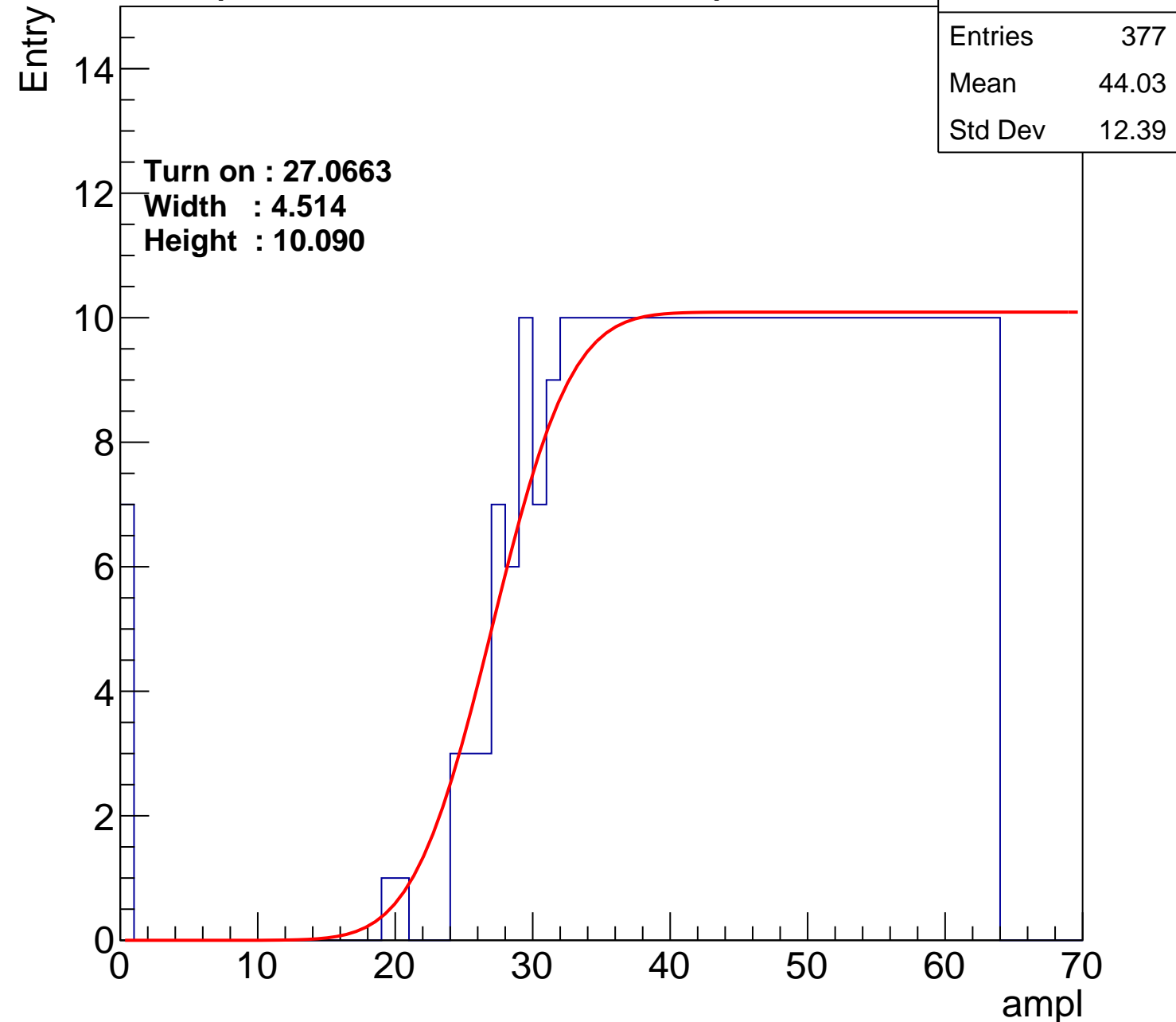
Width : 4.514

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch106

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.82
Std Dev	11.46

Turn on : 28.4442

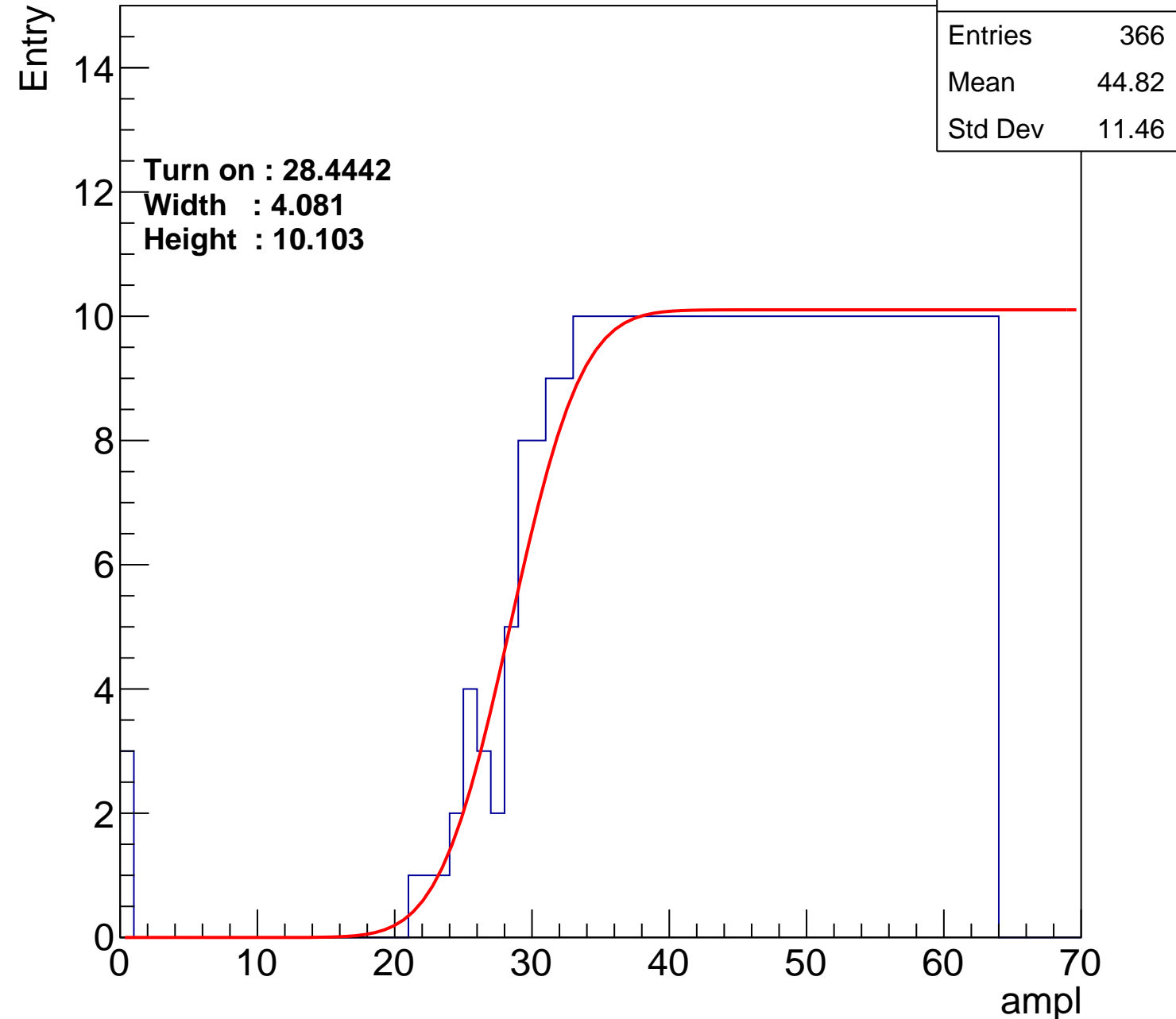
Width : 4.081

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.8
Std Dev	11.37

Turn on : 27.7056

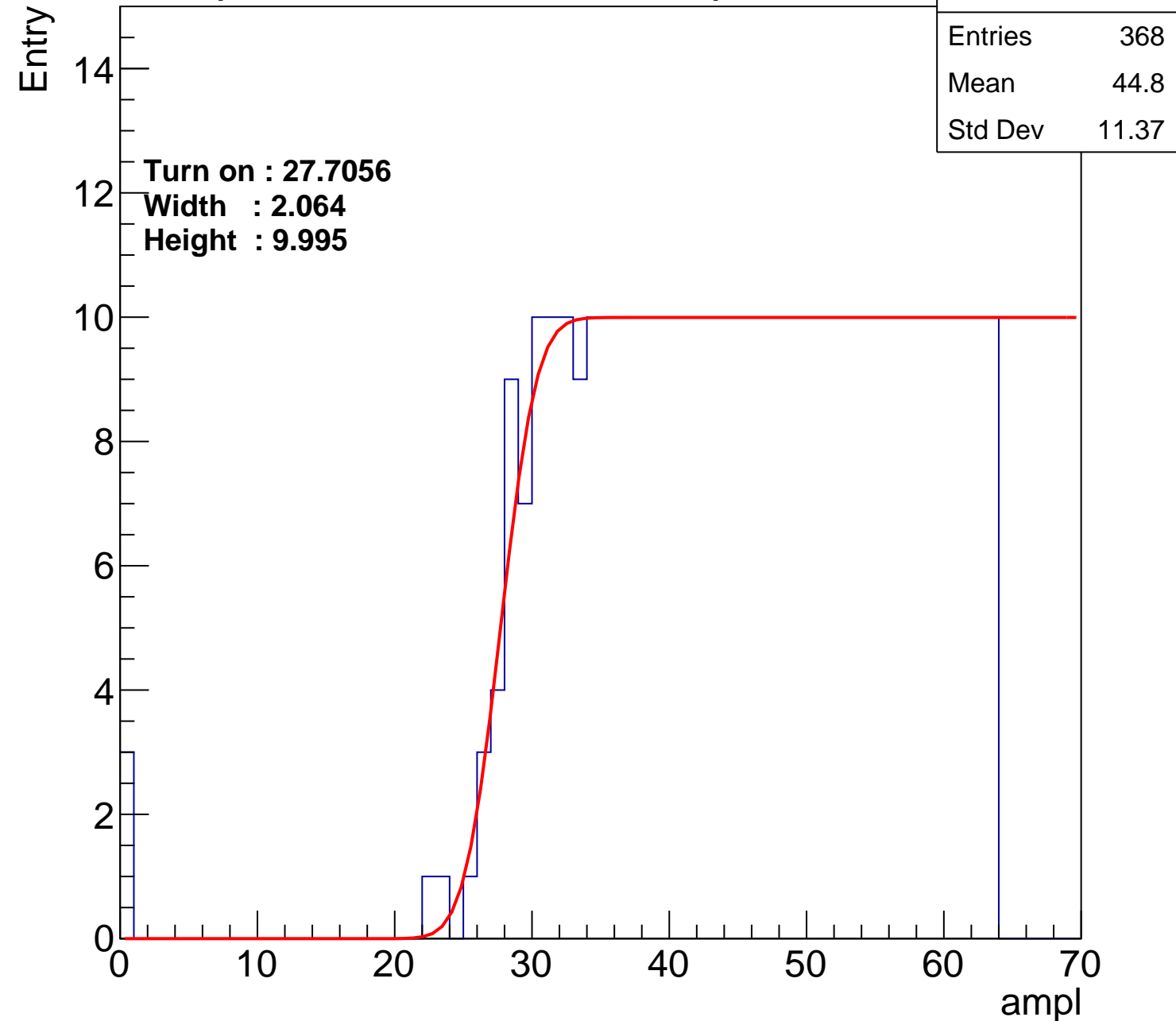
Width : 2.064

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 25.4422

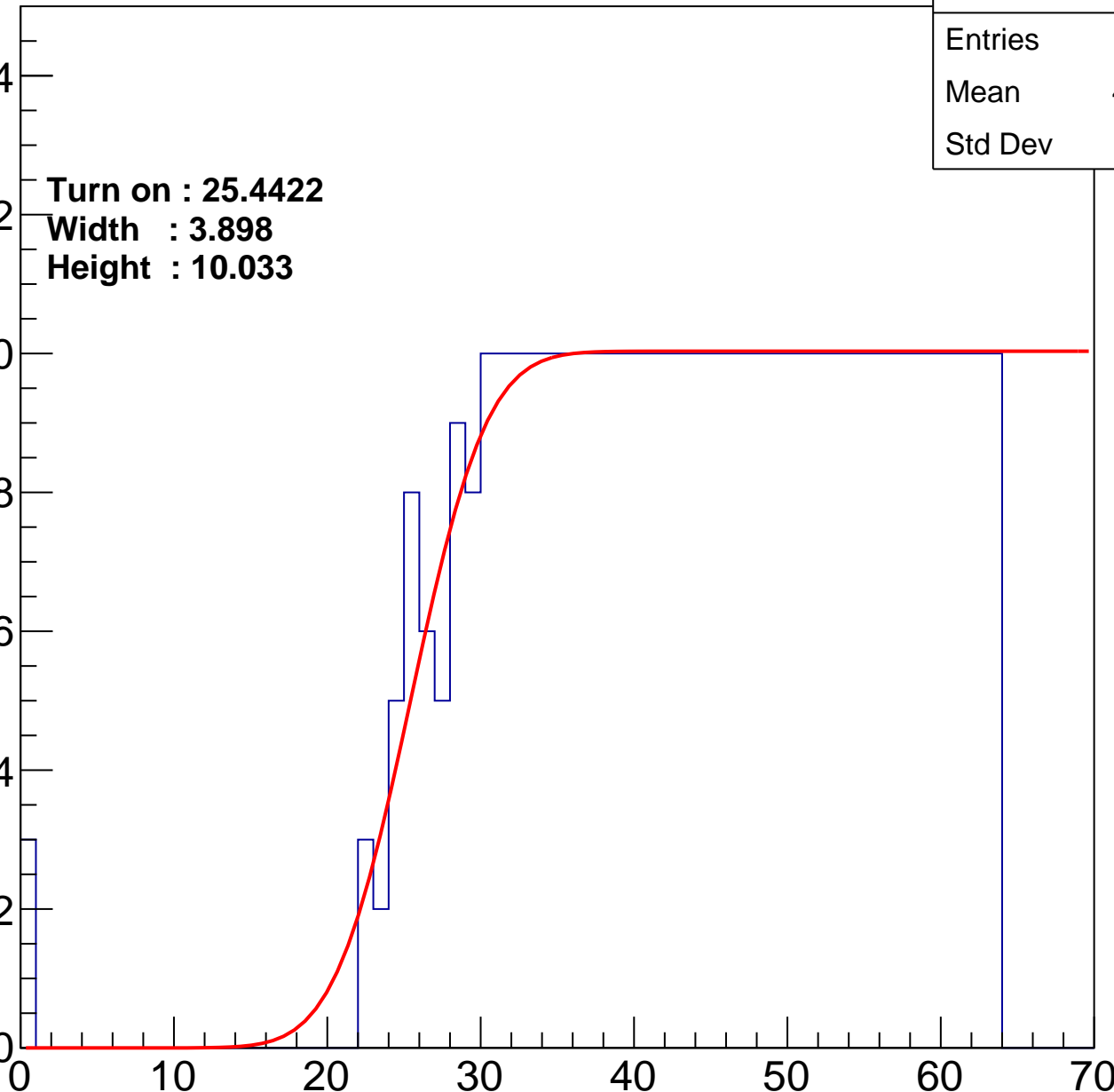
Width : 3.898

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.76
Std Dev	11.39

Turn on : 27.2140

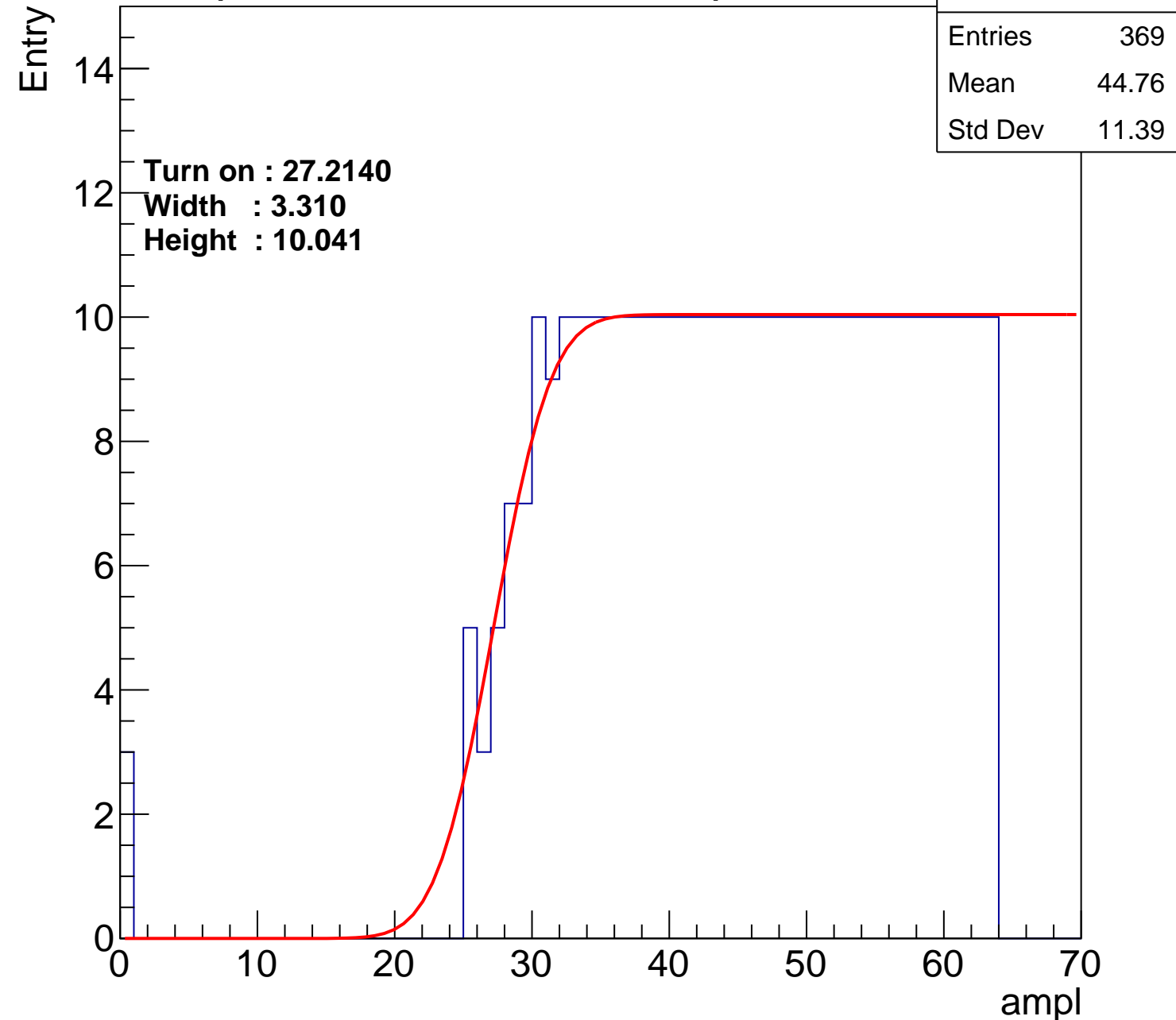
Width : 3.310

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch110

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.49
Std Dev	11.24

Turn on : 26.4744

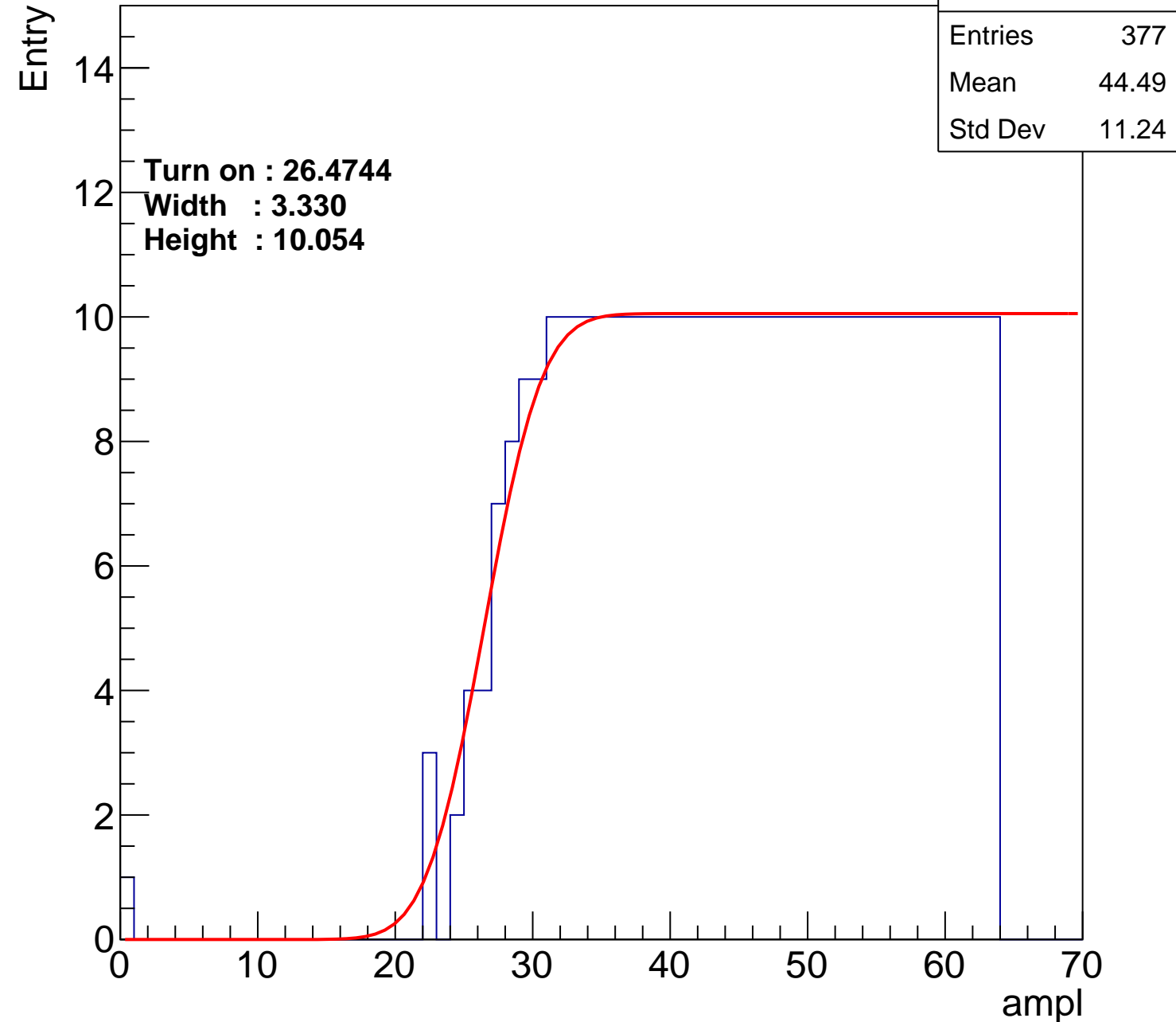
Width : 3.330

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch111

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	44.03
Std Dev	11.49

Turn on : 25.7170

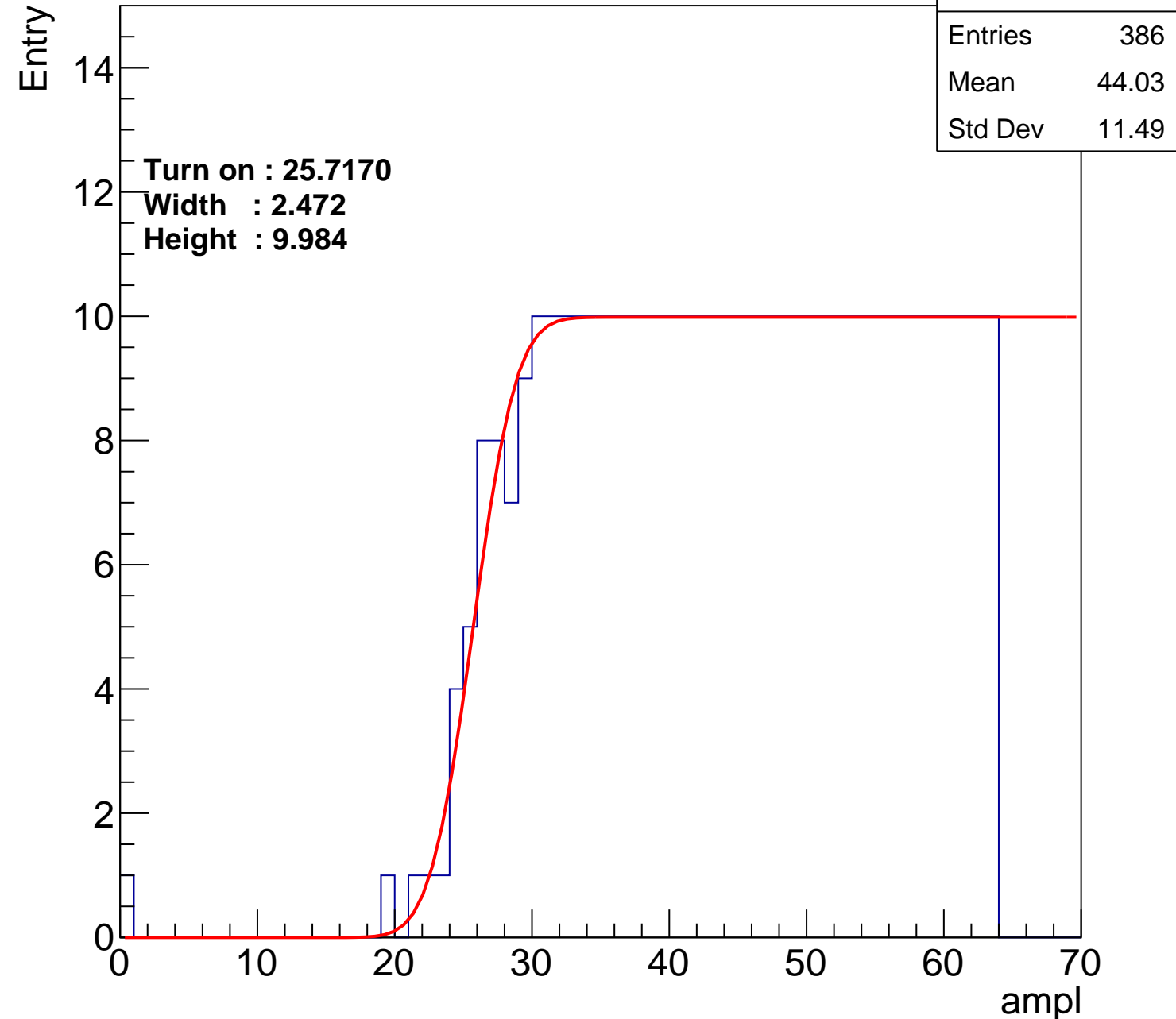
Width : 2.472

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.26
Std Dev	11.88

Turn on : 27.3346

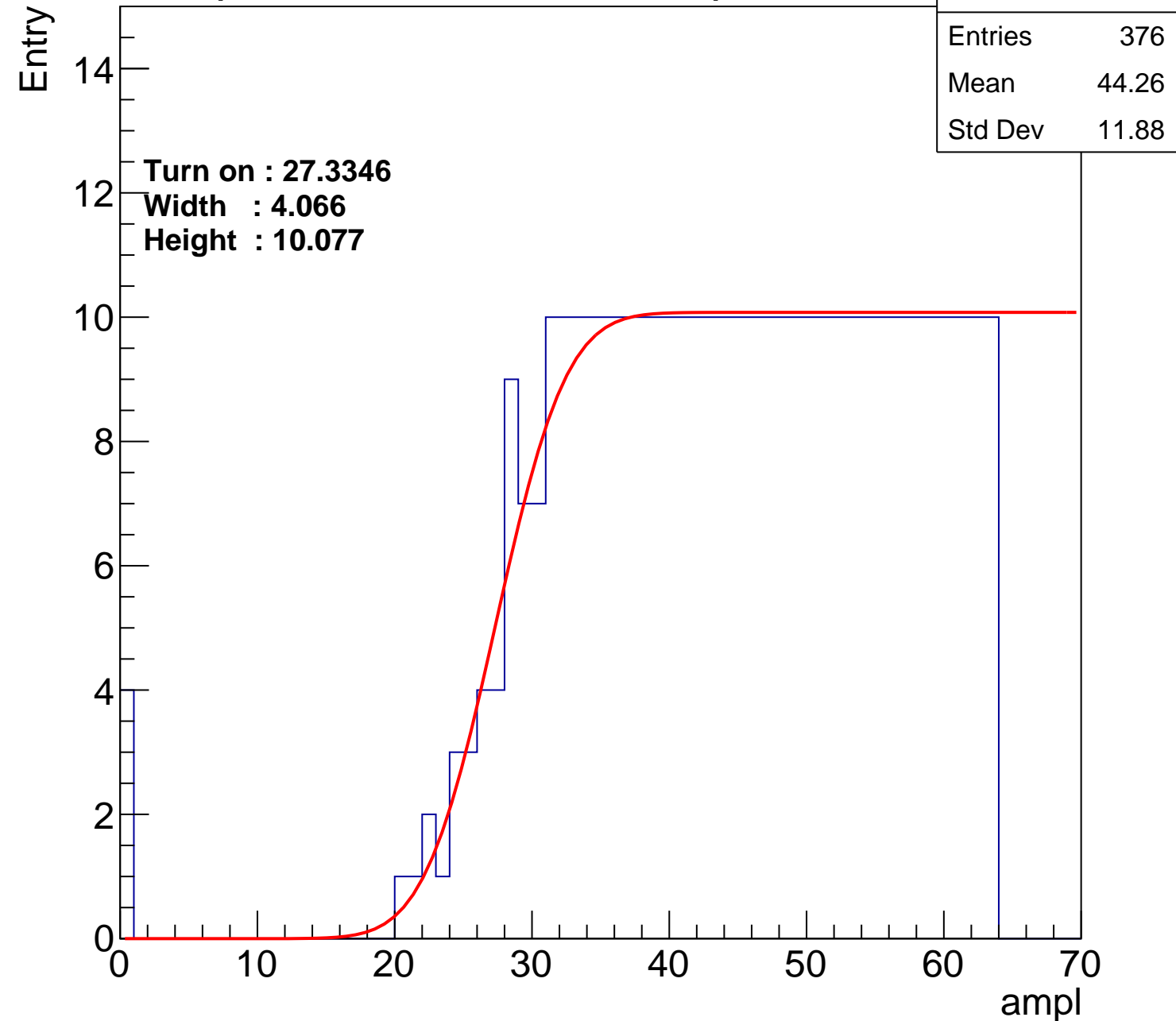
Width : 4.066

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch113

calib_packv5_042523_0143.root, FC#13, port D2

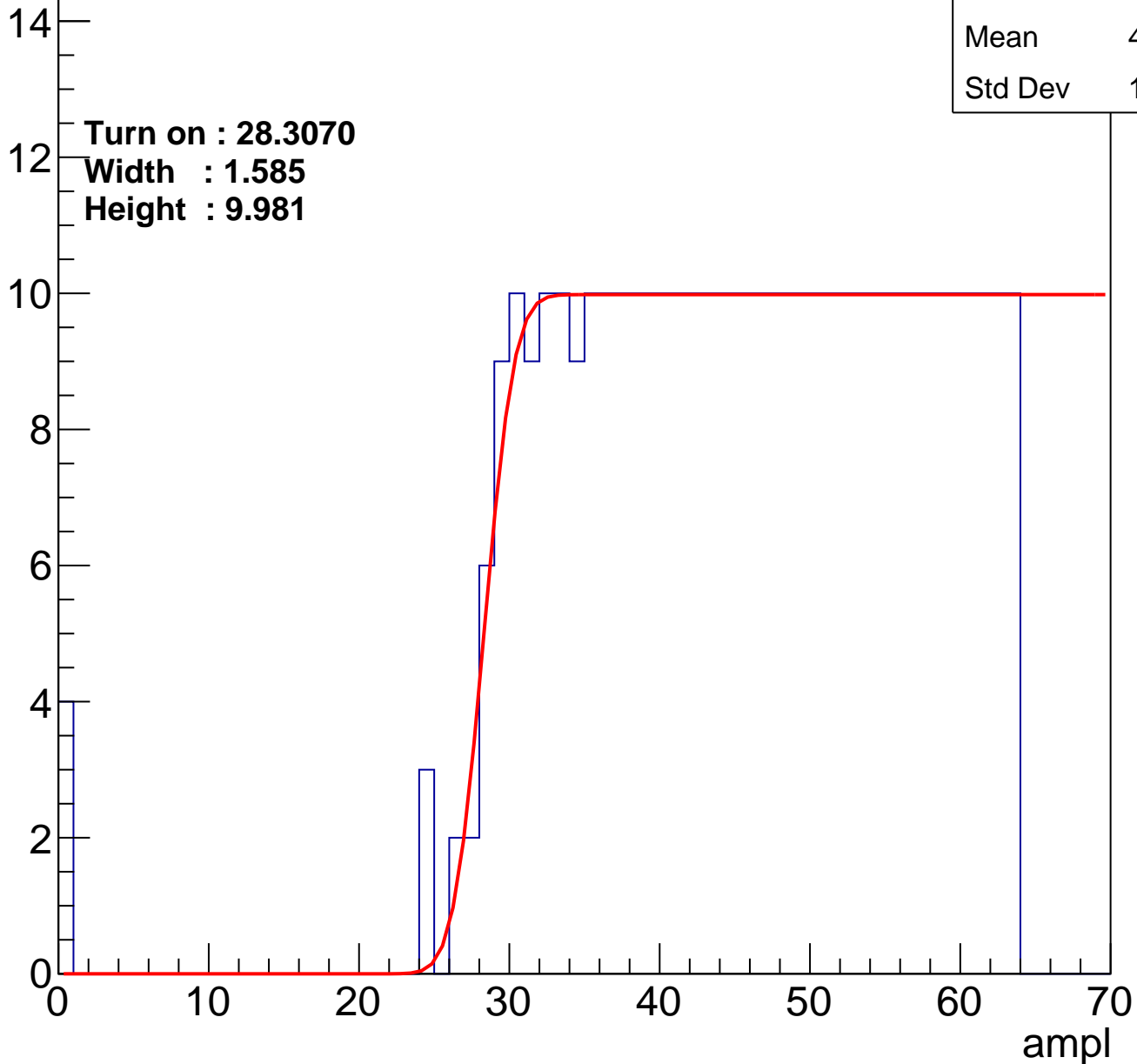
Entry

Entries	364
Mean	44.92
Std Dev	11.48

Turn on : 28.3070

Width : 1.585

Height : 9.981



B1L003S, U3-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.98
Std Dev	11.2

Turn on : 28.1137

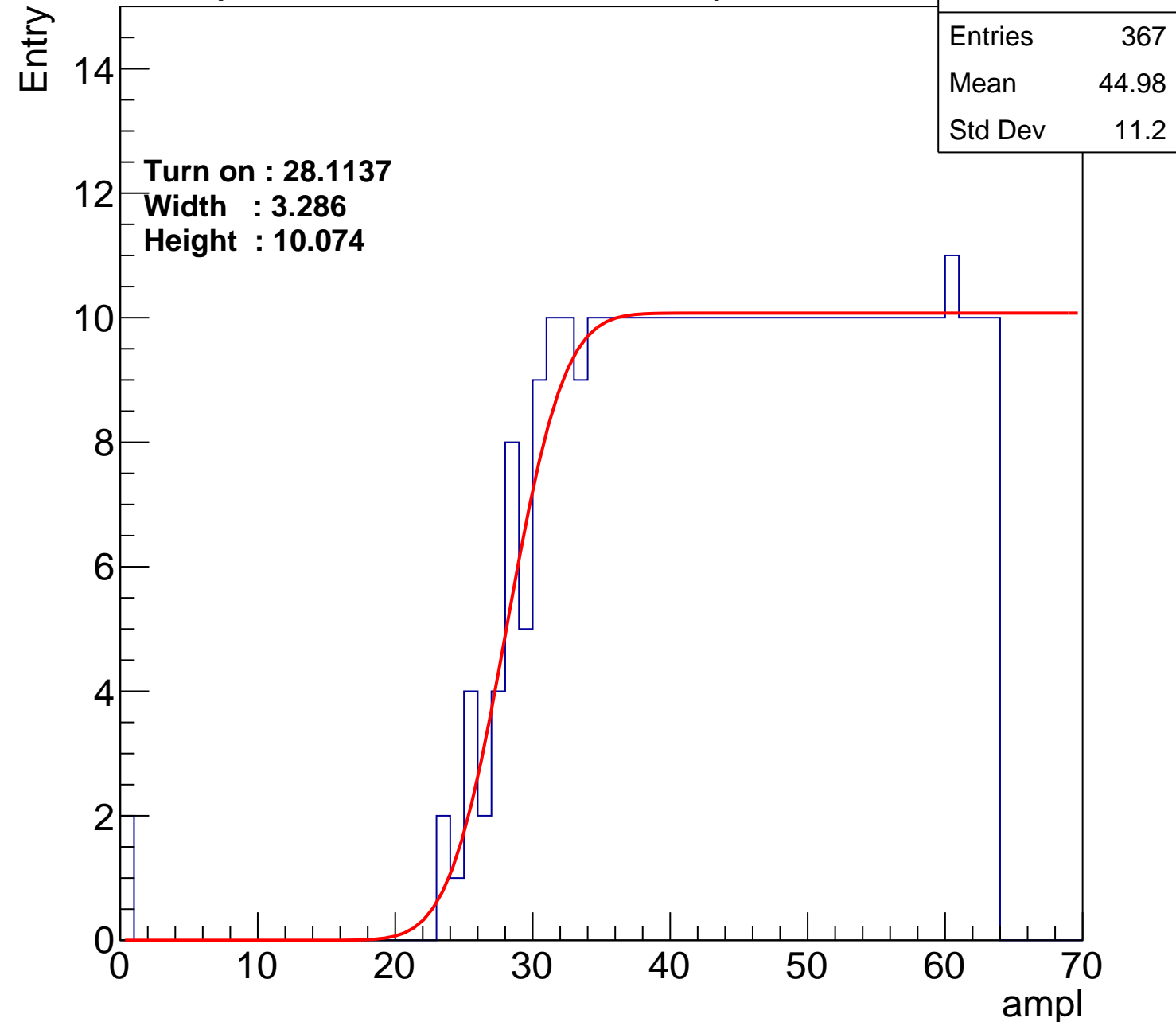
Width : 3.286

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.84
Std Dev	12.35

Turn on : 26.5186

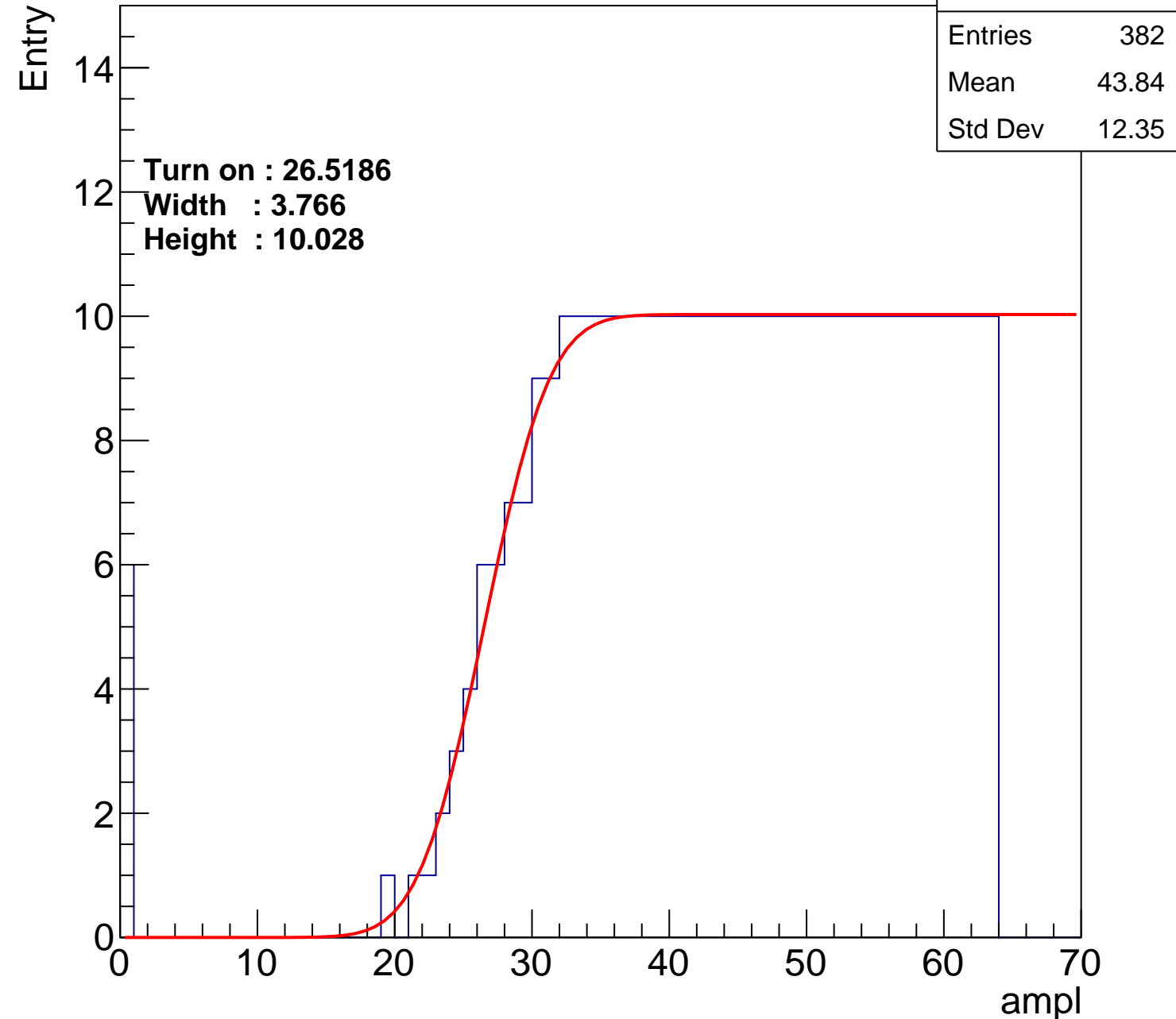
Width : 3.766

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch116

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.08
Std Dev	12.02

Turn on : 26.7794

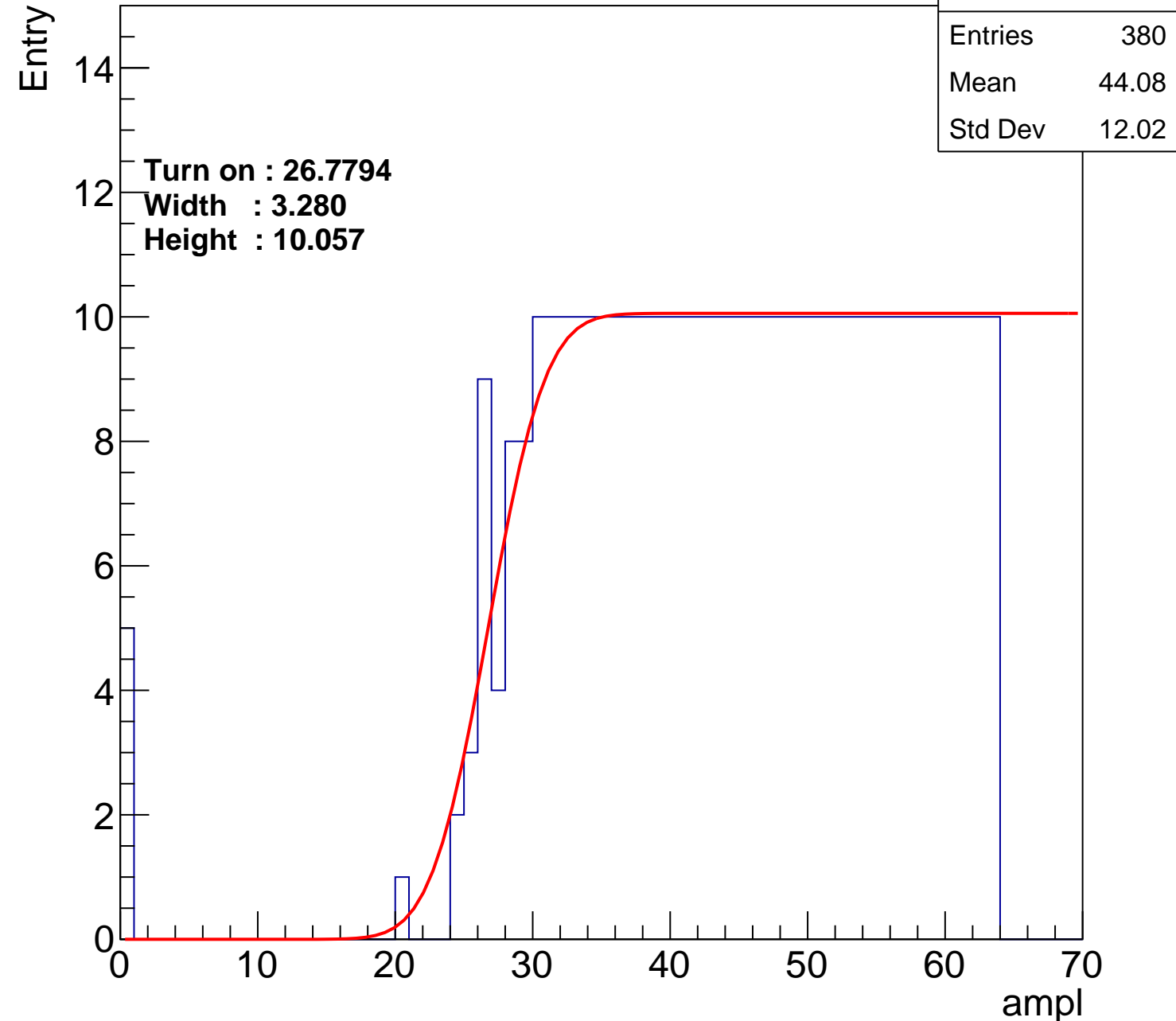
Width : 3.280

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch117

calib_packv5_042523_0143.root, FC#13, port D2

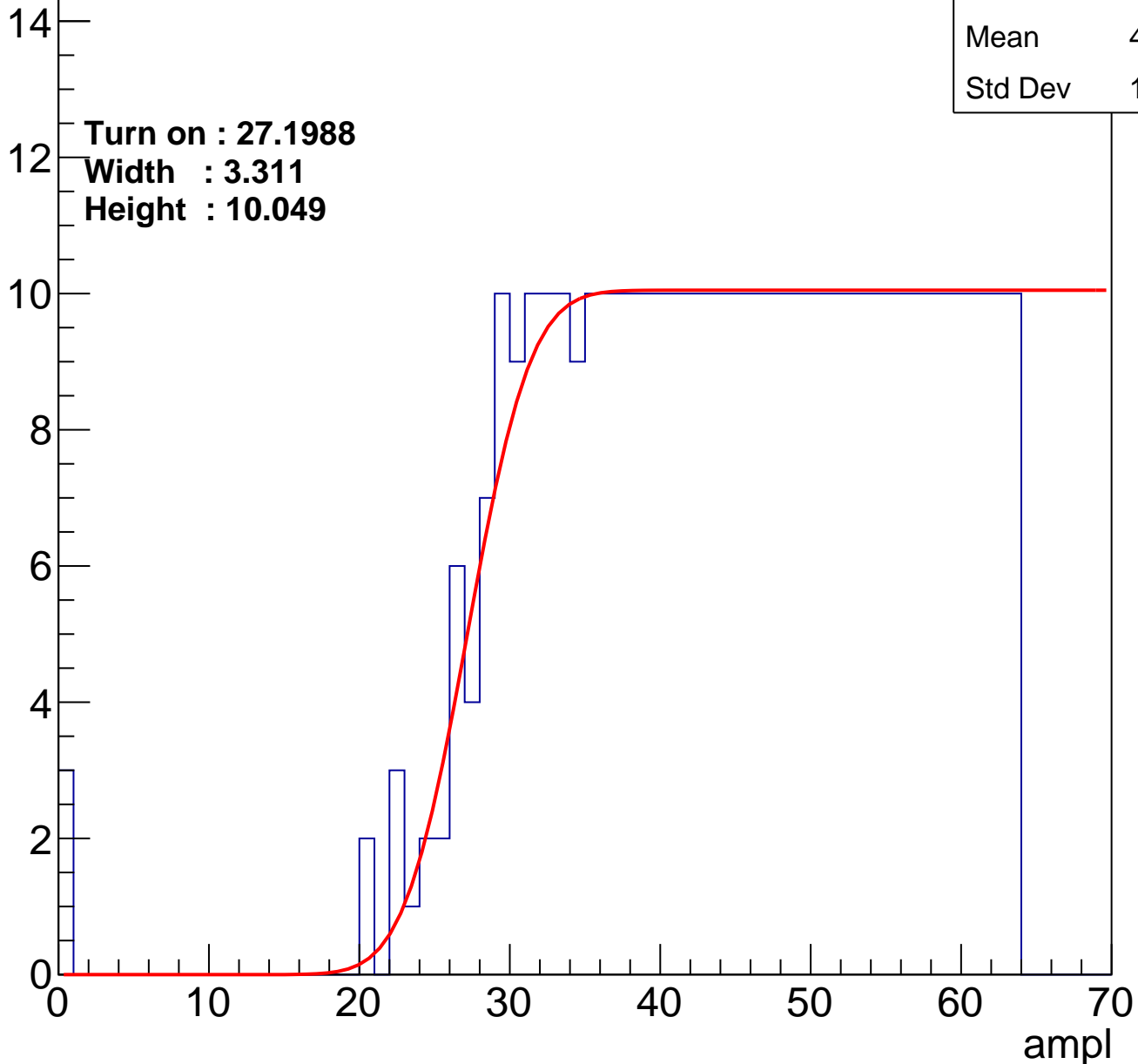
Entries	378
Mean	44.24
Std Dev	11.73

Turn on : 27.1988

Width : 3.311

Height : 10.049

Entry



B1L003S, U3-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 27.8298

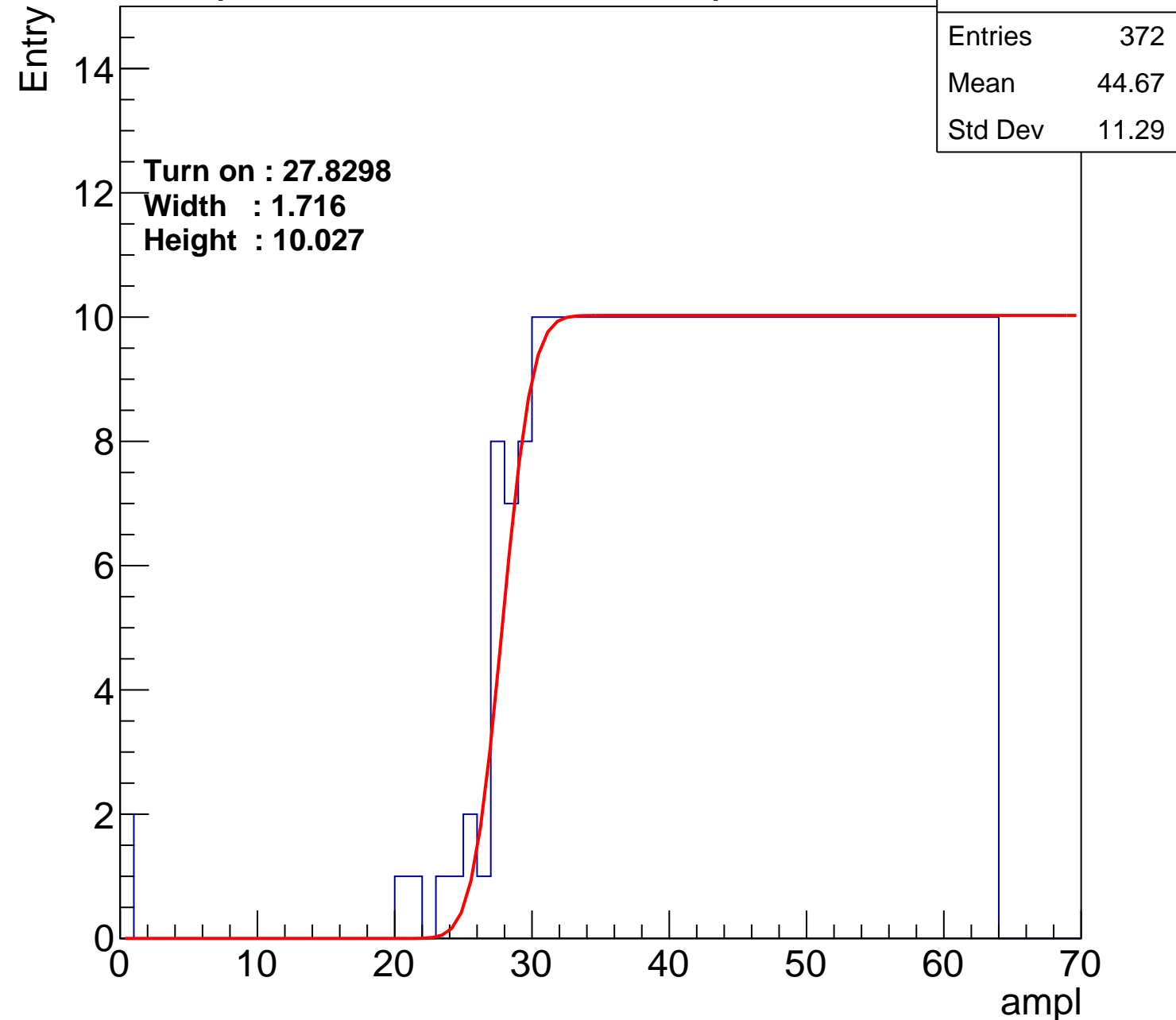
Width : 1.716

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch119

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.04
Std Dev	11.66

Turn on : 26.3483

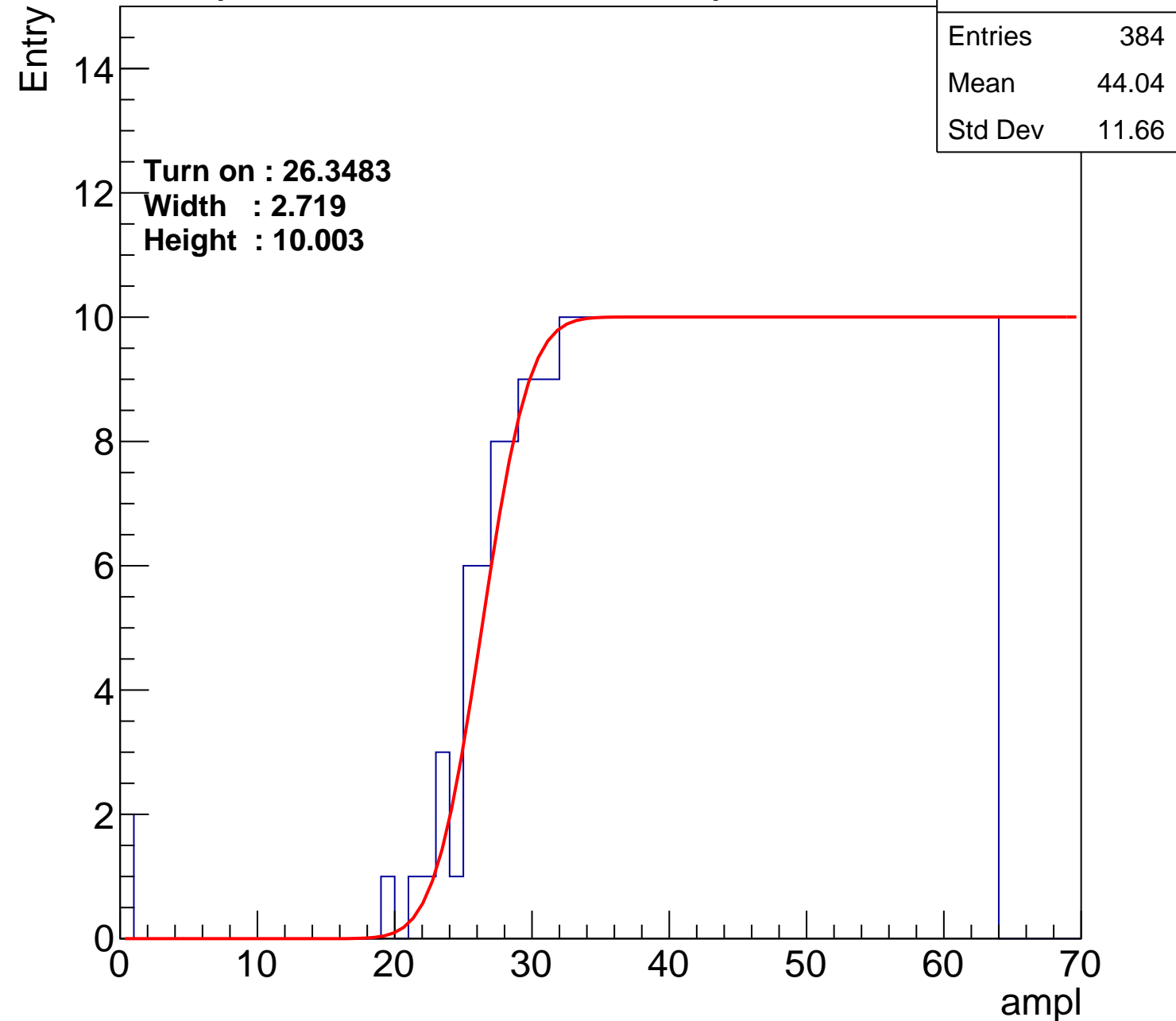
Width : 2.719

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch120

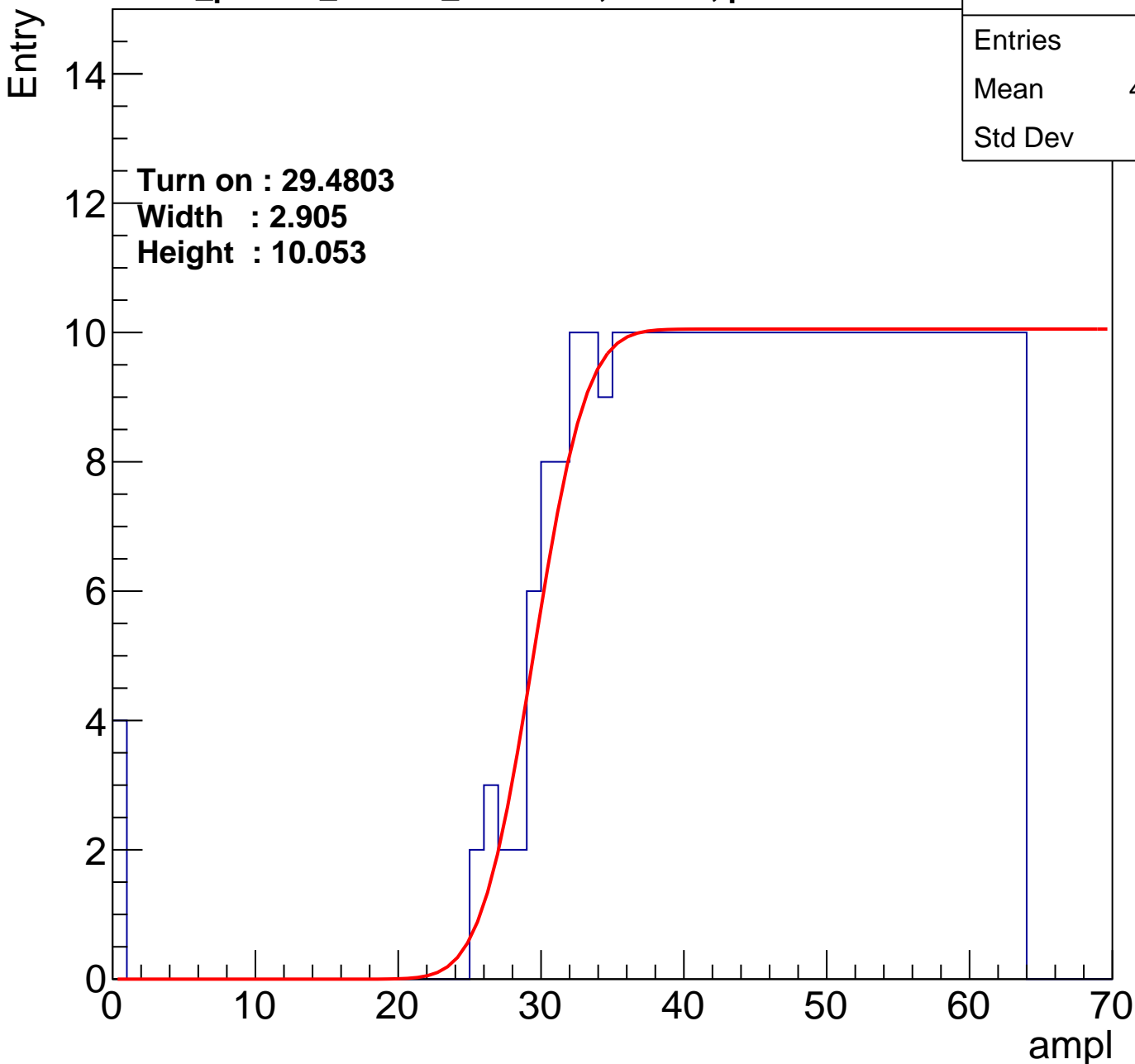
calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.38
Std Dev	11.3

Turn on : 29.4803

Width : 2.905

Height : 10.053



B1L003S, U3-ch121

calib_packv5_042523_0143.root, FC#13, port D2

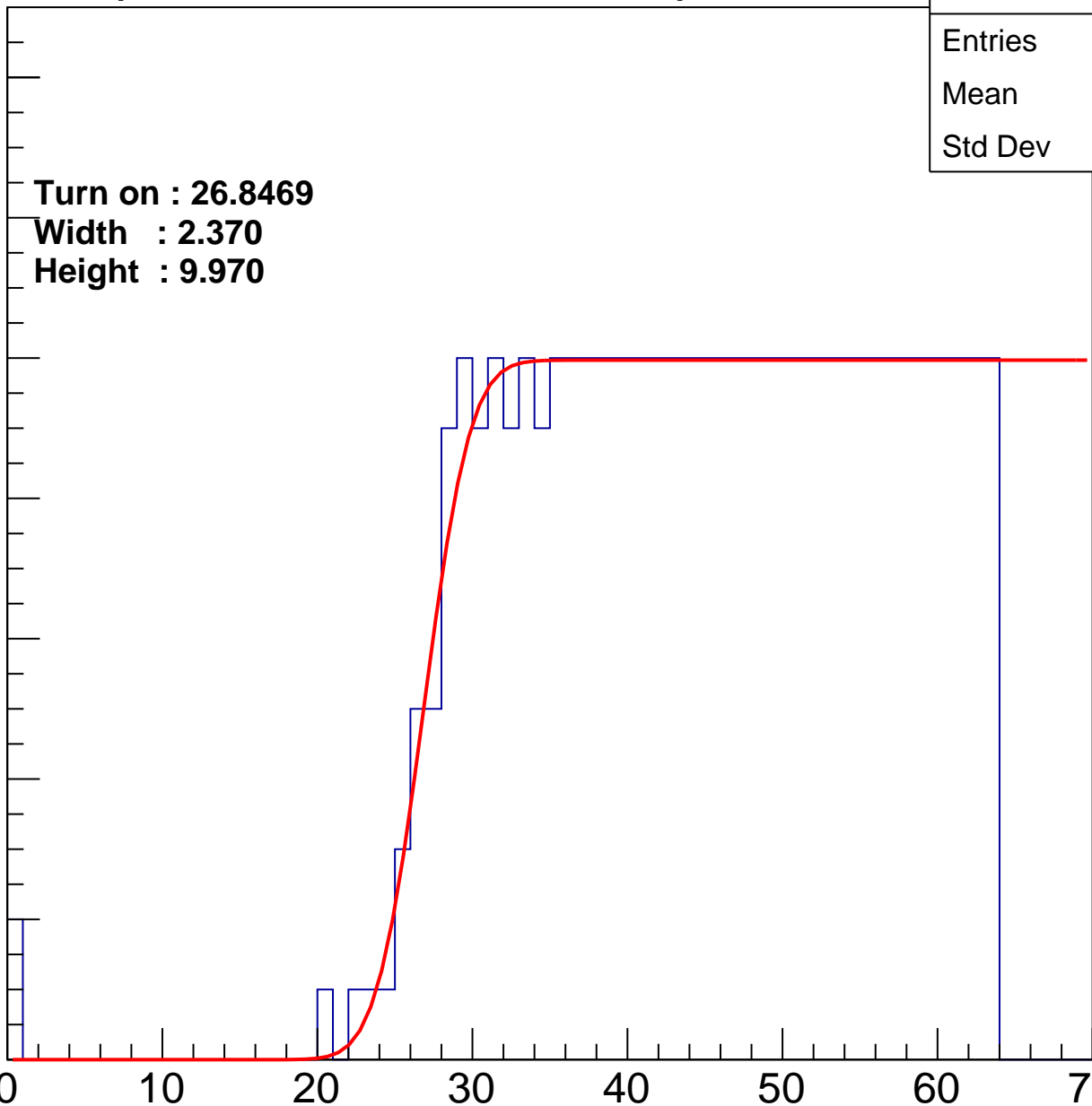
Entry

14
12
10
8
6
4
2
0

Turn on : 26.8469
Width : 2.370
Height : 9.970

Entries	375
Mean	44.49
Std Dev	11.41

ampl



B1L003S, U3-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.2
Std Dev	11.87

Turn on : 26.9910

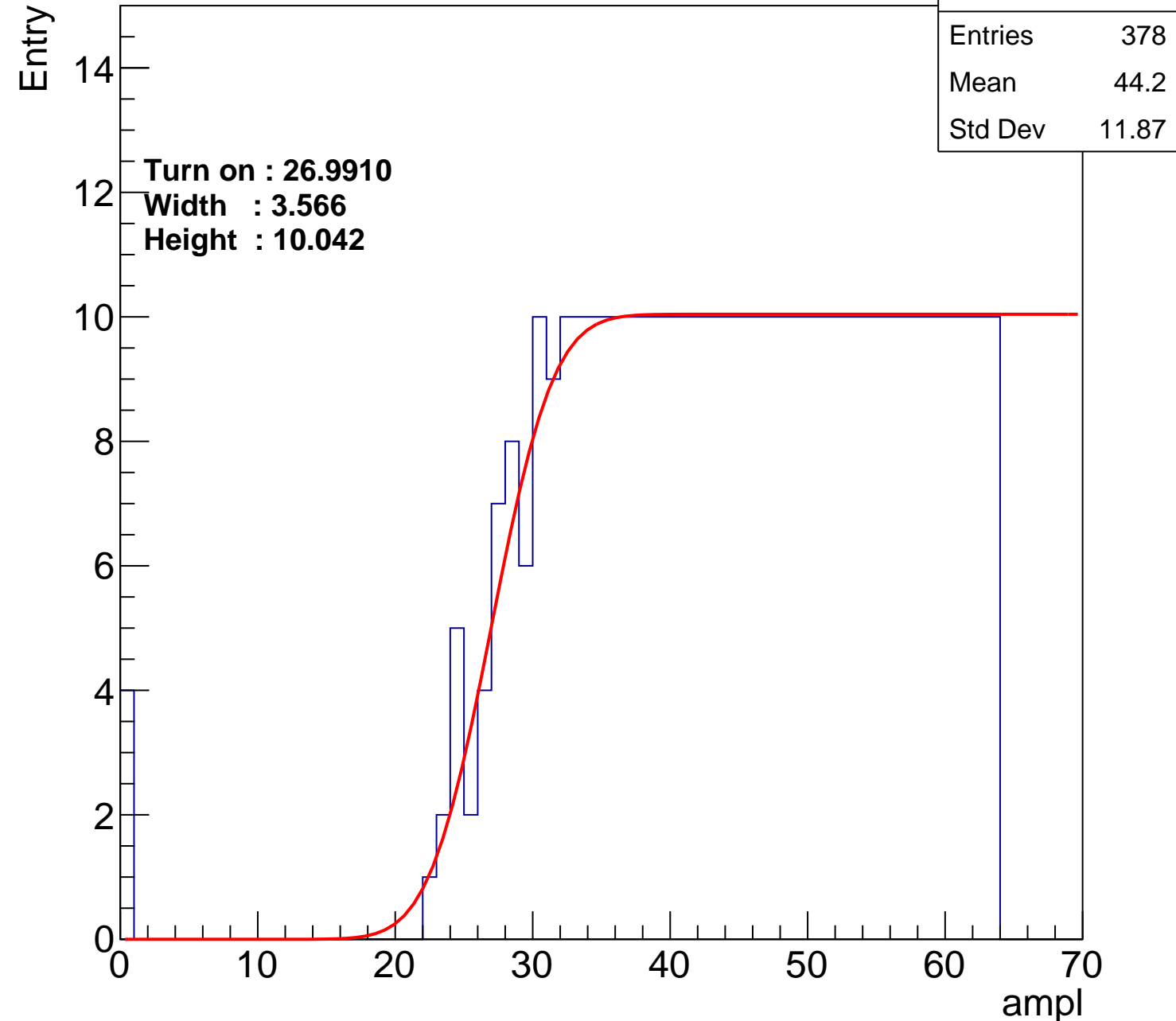
Width : 3.566

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch123

calib_packv5_042523_0143.root, FC#13, port D2

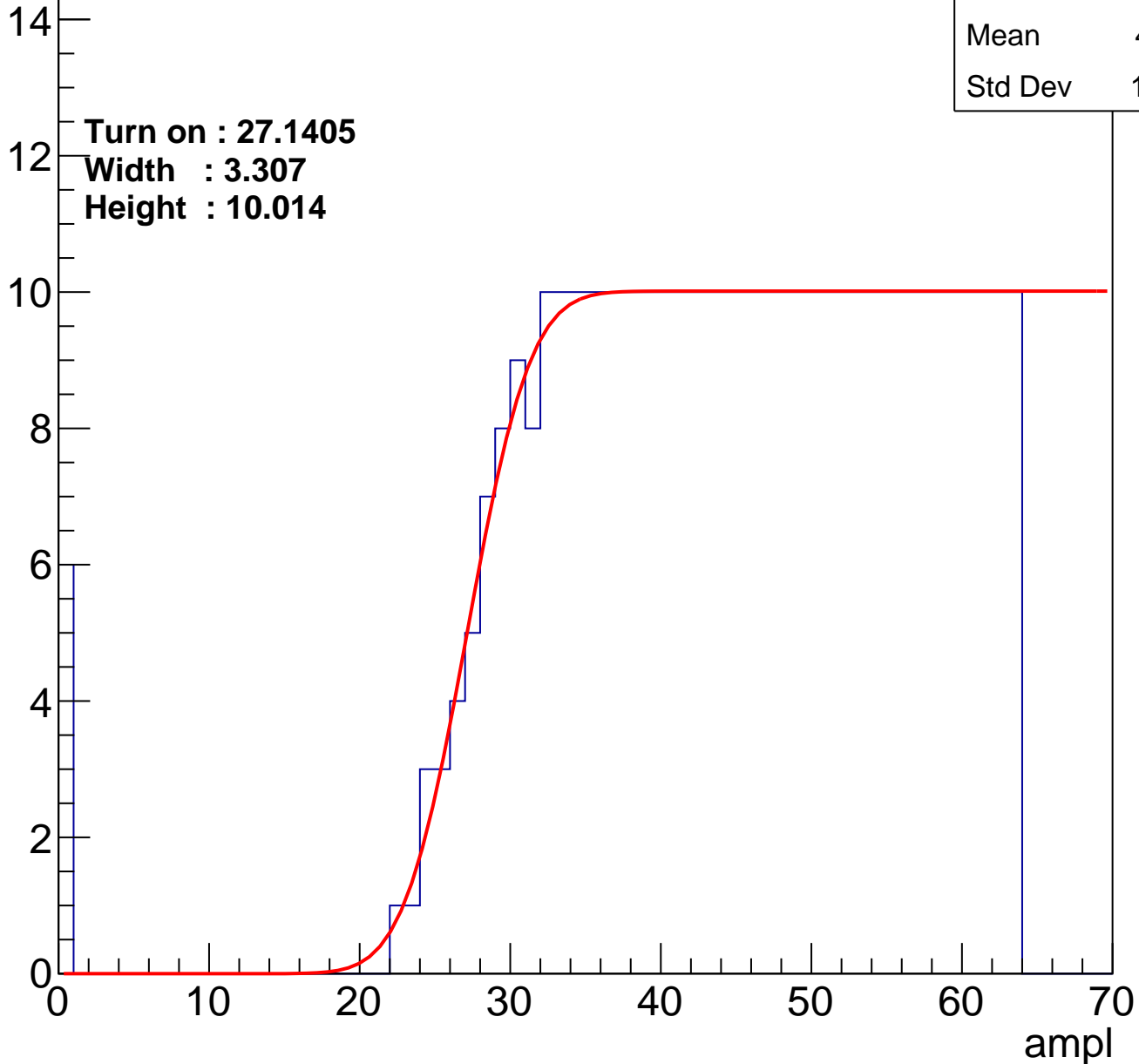
Entries	375
Mean	44.21
Std Dev	12.16

Turn on : 27.1405

Width : 3.307

Height : 10.014

Entry



B1L003S, U3-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	45.07
Std Dev	11.13

Turn on : 28.2234

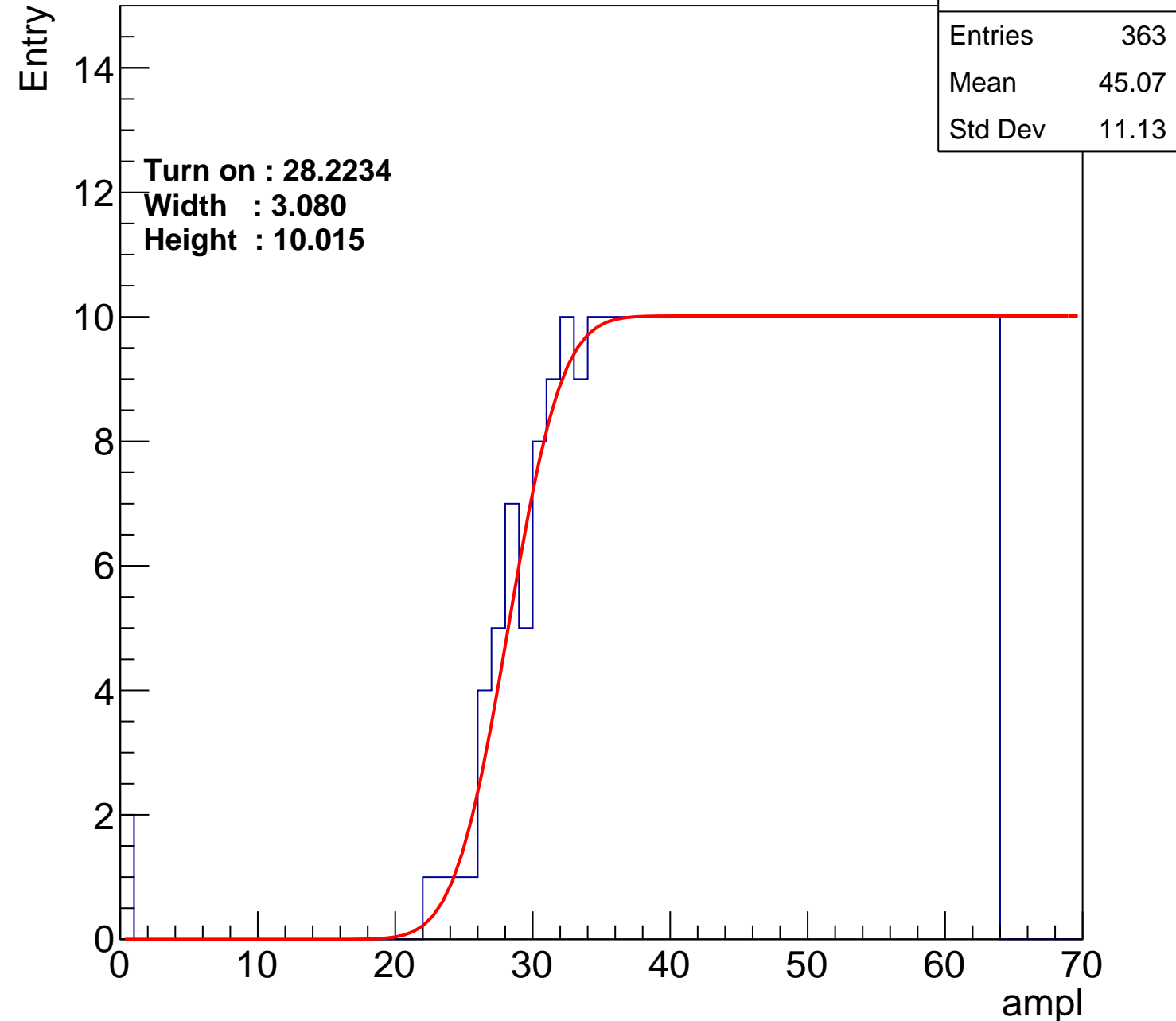
Width : 3.080

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch125

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.69
Std Dev	11.82

Turn on : 28.1006

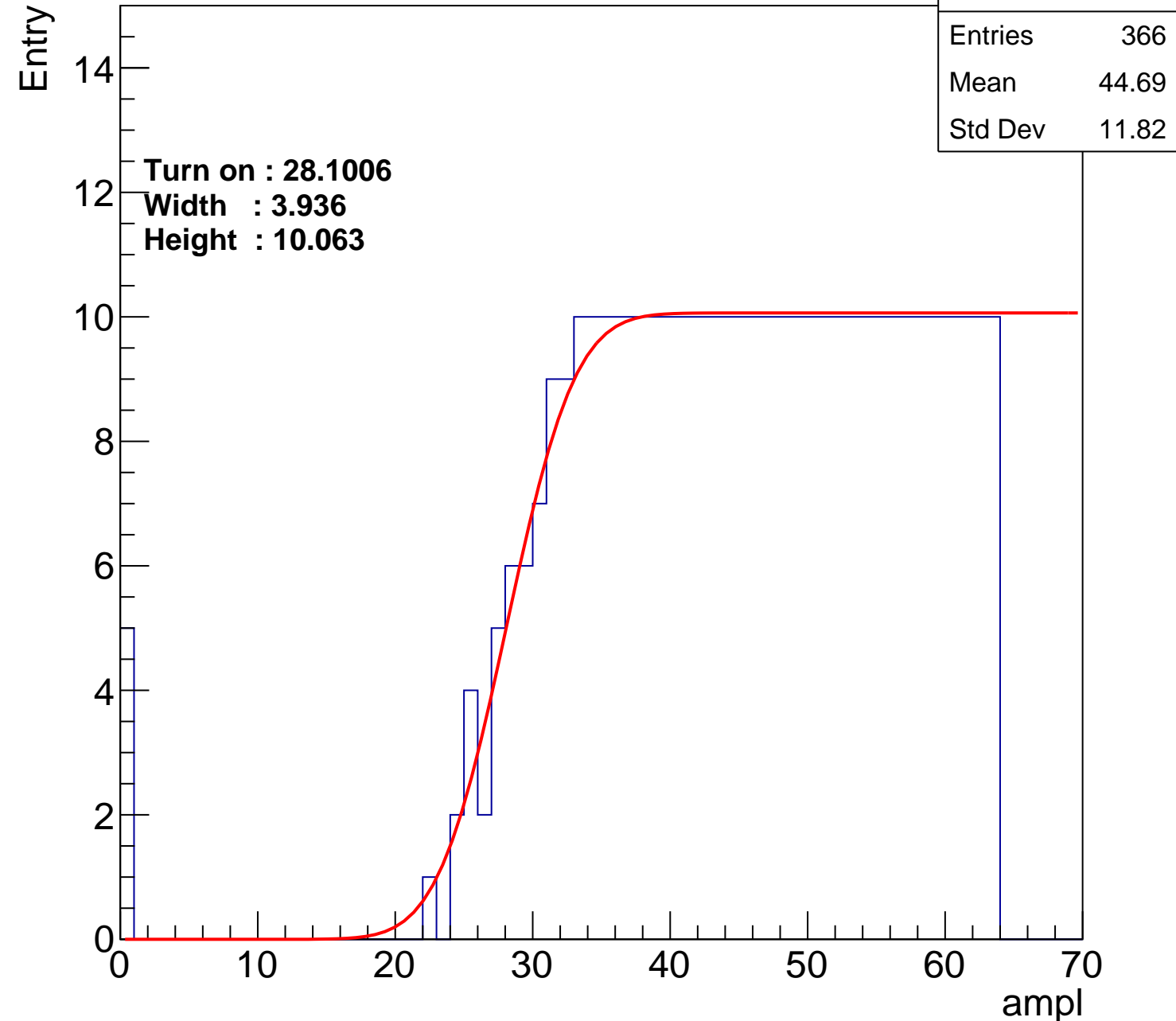
Width : 3.936

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.94
Std Dev	11.46

Turn on : 27.2765

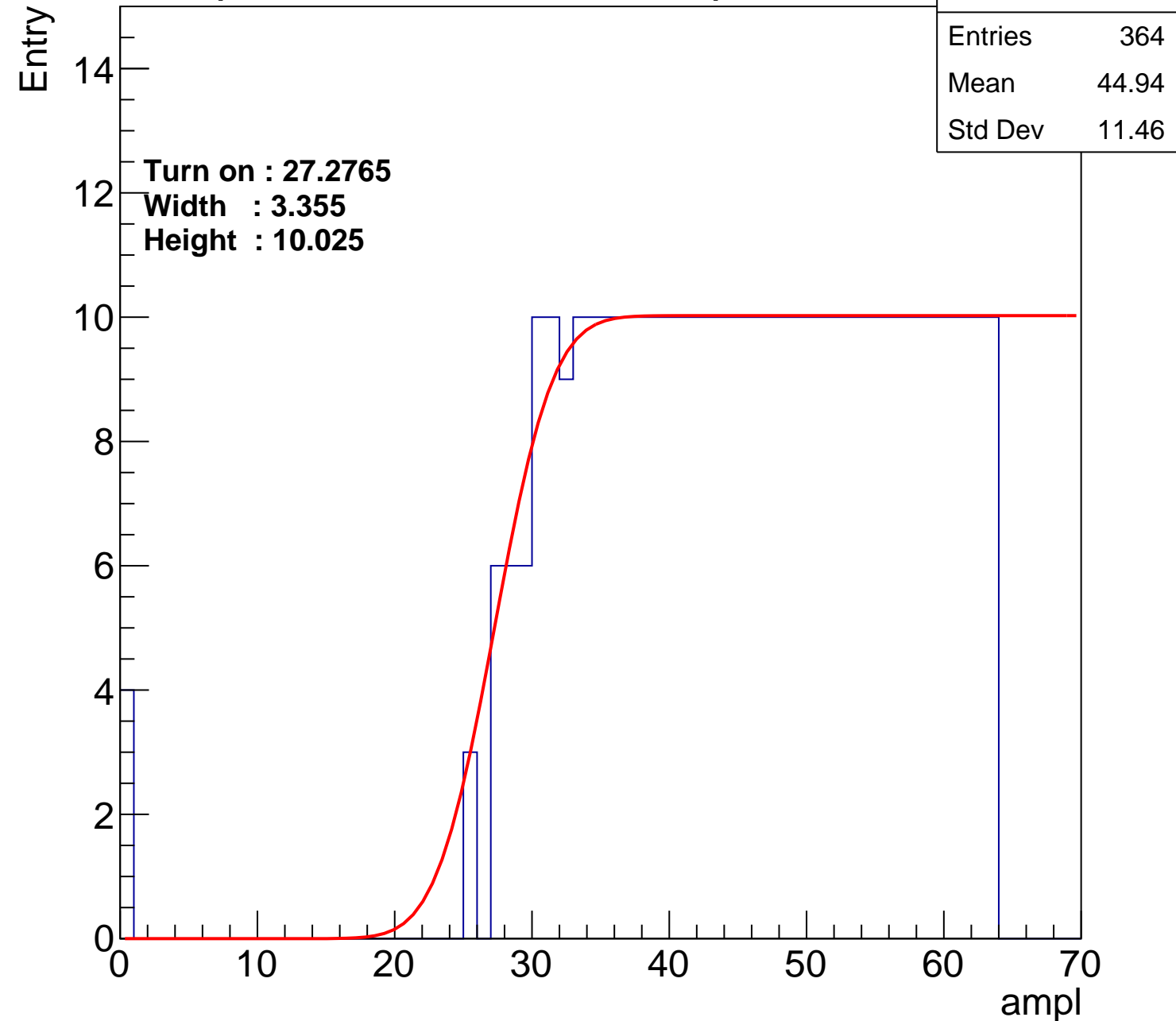
Width : 3.355

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U3-ch127

calib_packv5_042523_0143.root, FC#13, port D2

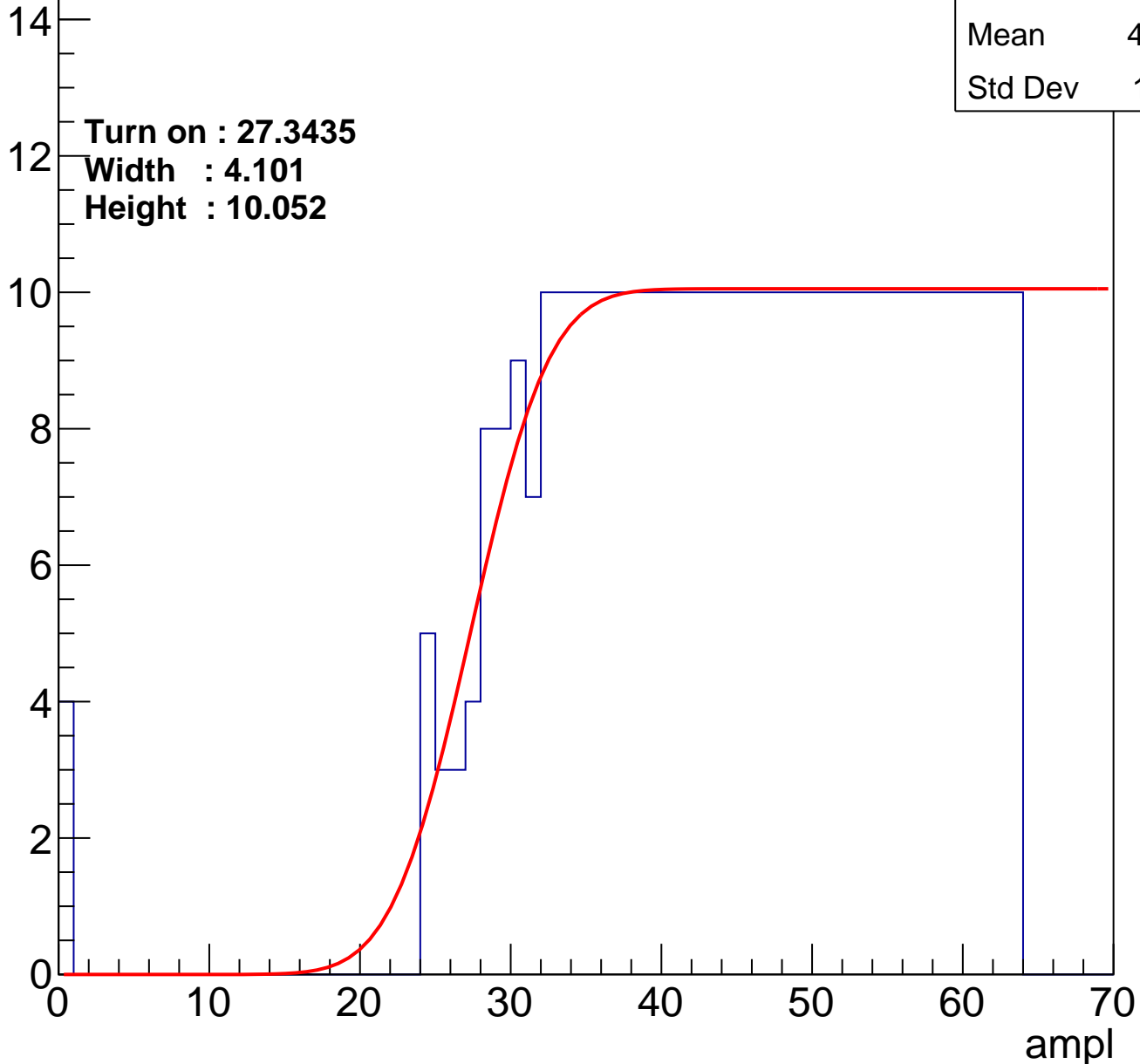
Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 27.3435

Width : 4.101

Height : 10.052

Entry



B1L003S, U3-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.54
Std Dev	11.71

Turn on : 27.3435

Width : 4.101

Height : 10.052

Entry

