



# B1L001S, U23-ch0

calib\_packv5\_042523\_0143.root, FC#2, port C2

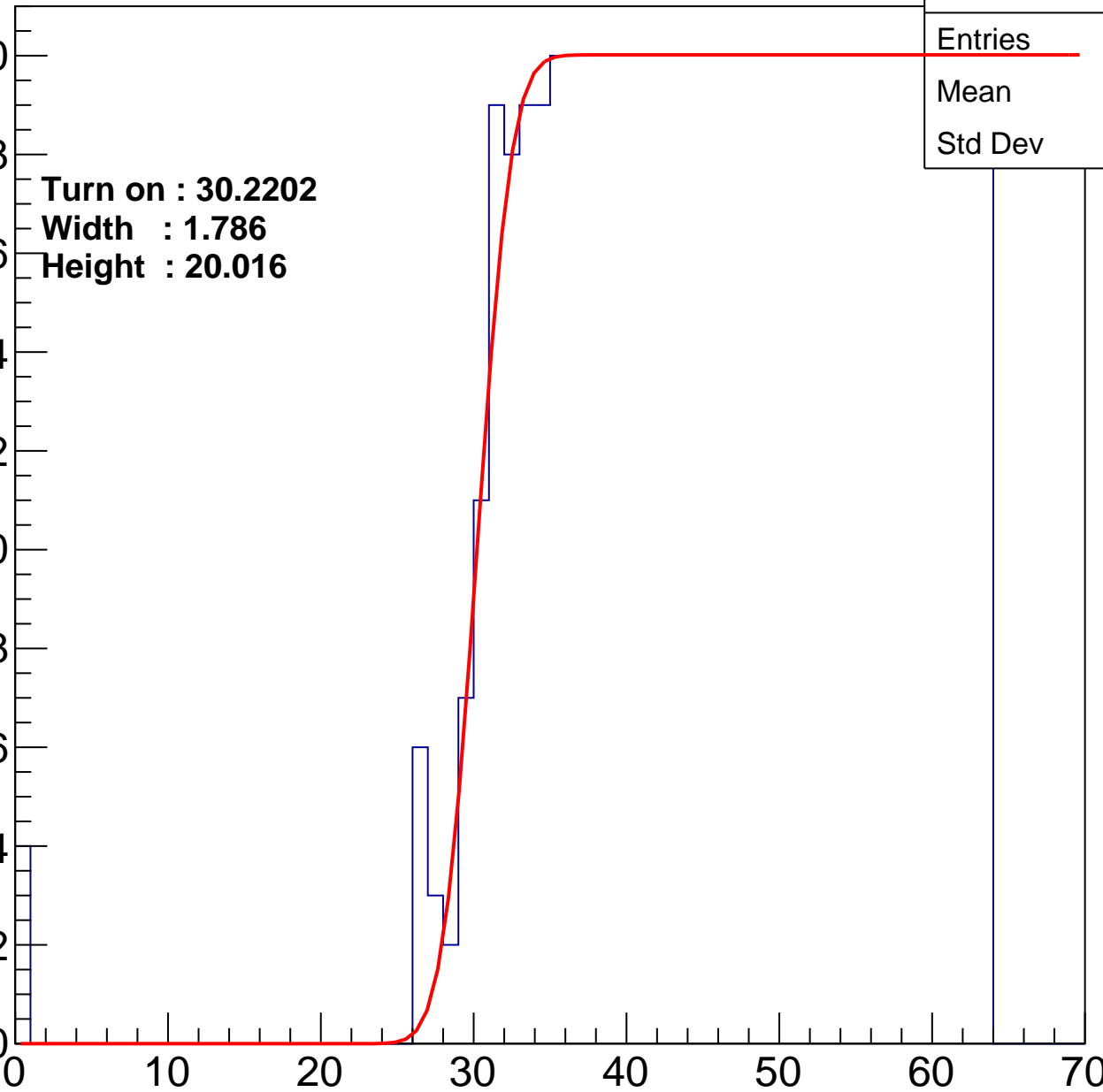
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 30.2202  
Width : 1.786  
Height : 20.016

|         |       |
|---------|-------|
| Entries | 688   |
| Mean    | 46.05 |
| Std Dev | 10.58 |

ampl



# B1L001S, U23-ch1

calib\_packv5\_042523\_0143.root, FC#2, port C2

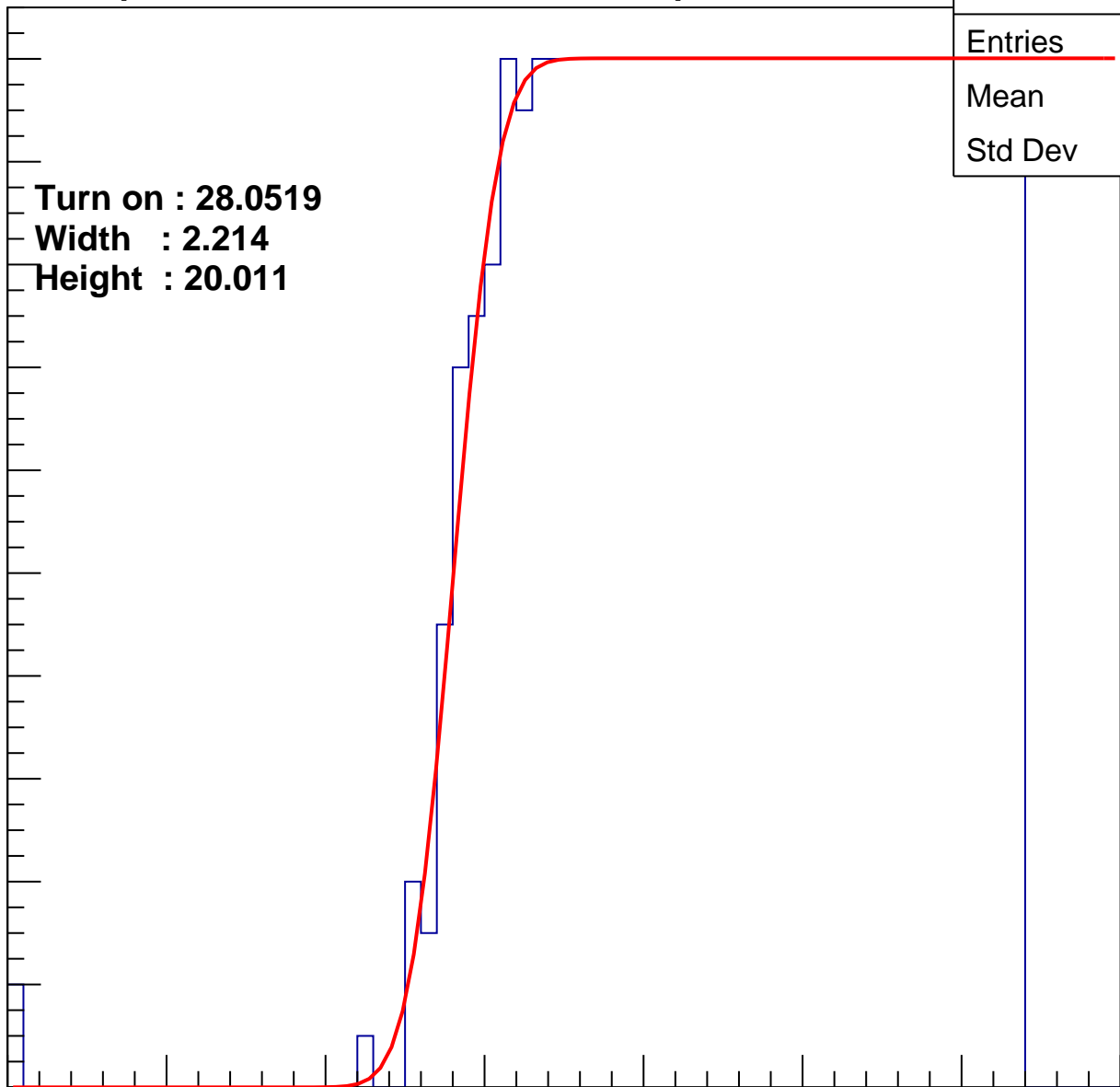
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.0519  
Width : 2.214  
Height : 20.011

|         |       |
|---------|-------|
| Entries | 723   |
| Mean    | 45.28 |
| Std Dev | 10.77 |

ampl



# B1L001S, U23-ch2

calib\_packv5\_042523\_0143.root, FC#2, port C2

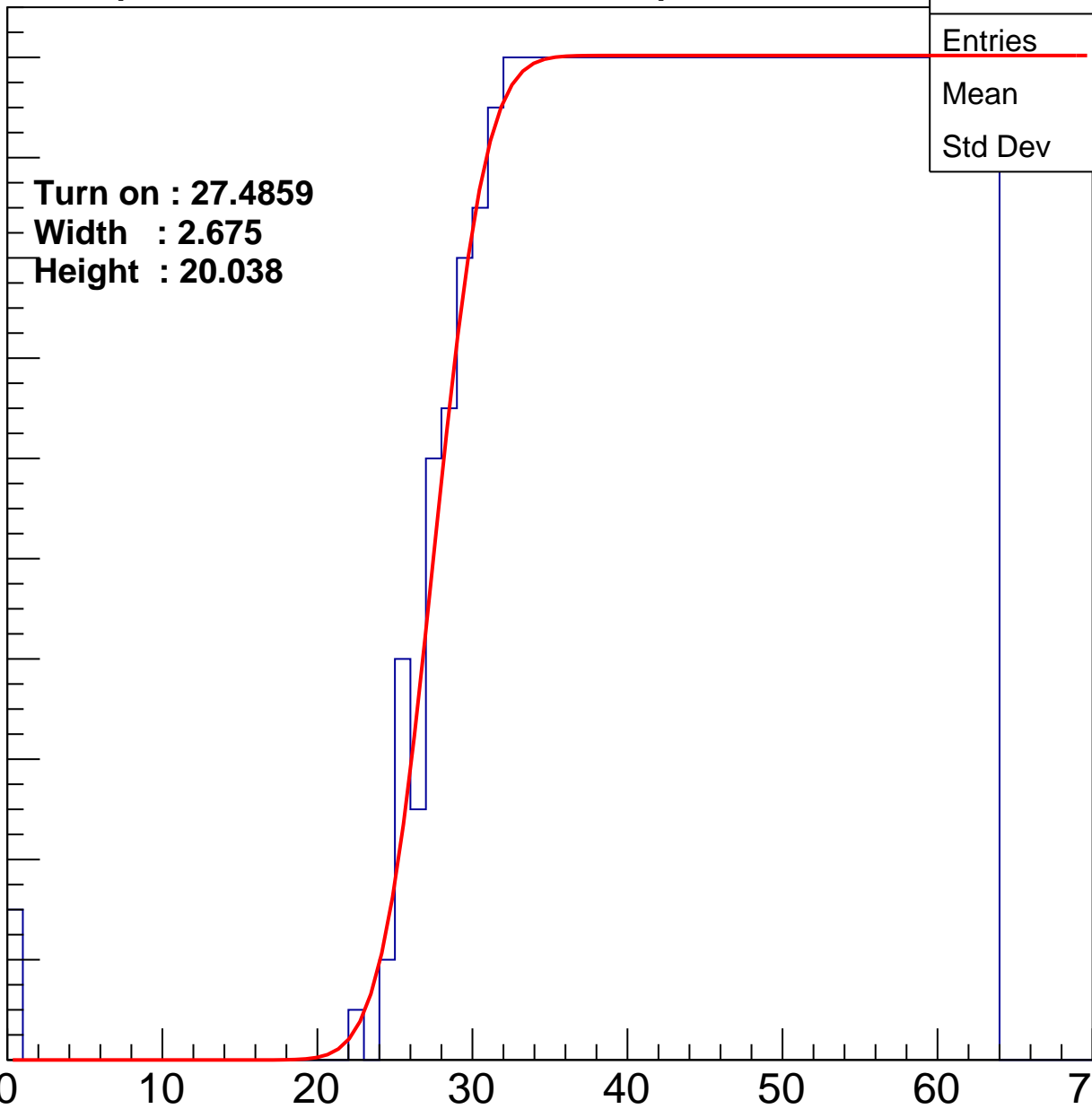
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4859  
Width : 2.675  
Height : 20.038

|         |       |
|---------|-------|
| Entries | 736   |
| Mean    | 44.91 |
| Std Dev | 11.08 |

ampl



# B1L001S, U23-ch3

calib\_packv5\_042523\_0143.root, FC#2, port C2

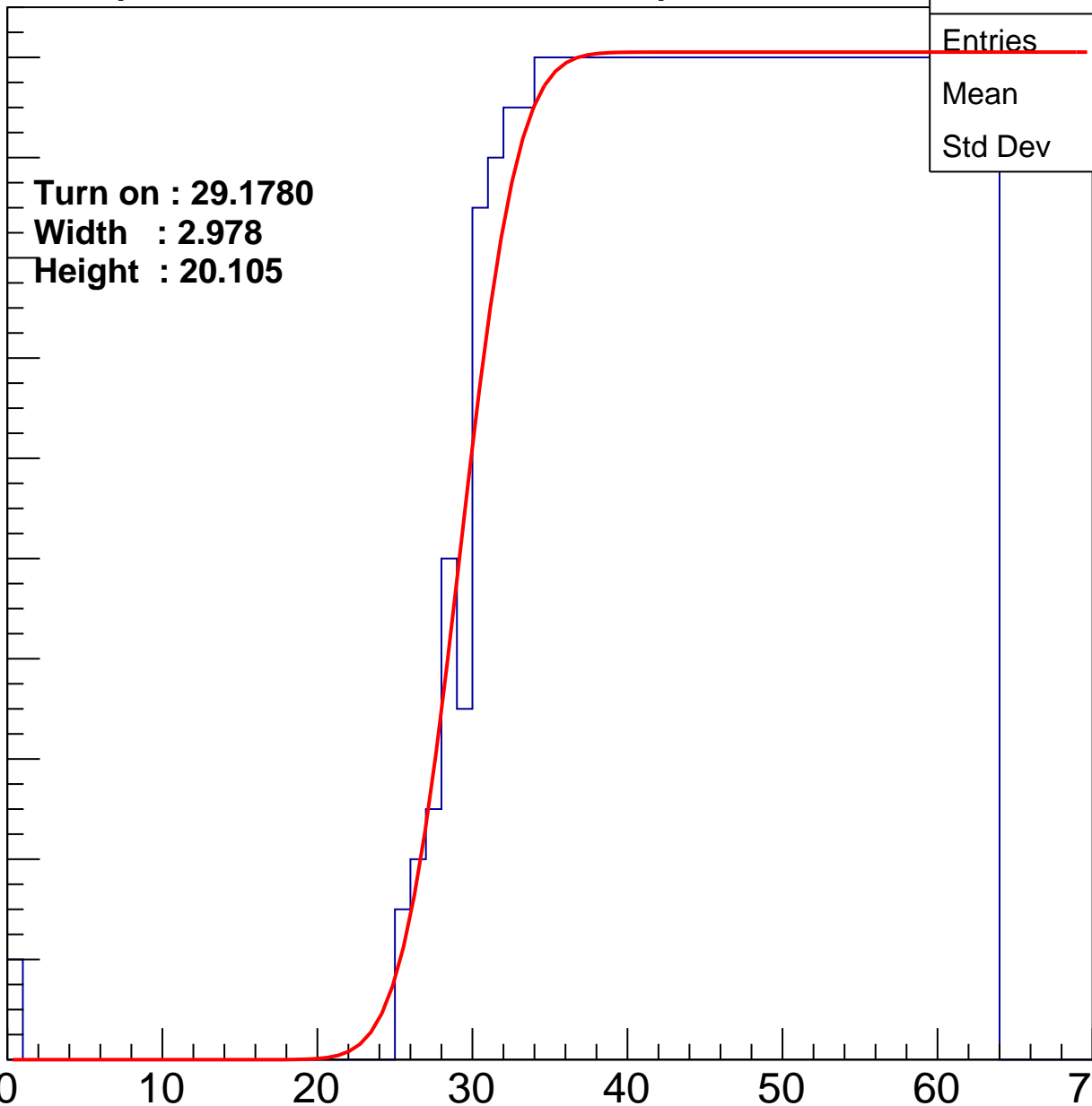
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.1780**  
**Width : 2.978**  
**Height : 20.105**

|         |       |
|---------|-------|
| Entries | 704   |
| Mean    | 45.74 |
| Std Dev | 10.54 |

ampl



# B1L001S, U23-ch4

calib\_packv5\_042523\_0143.root, FC#2, port C2

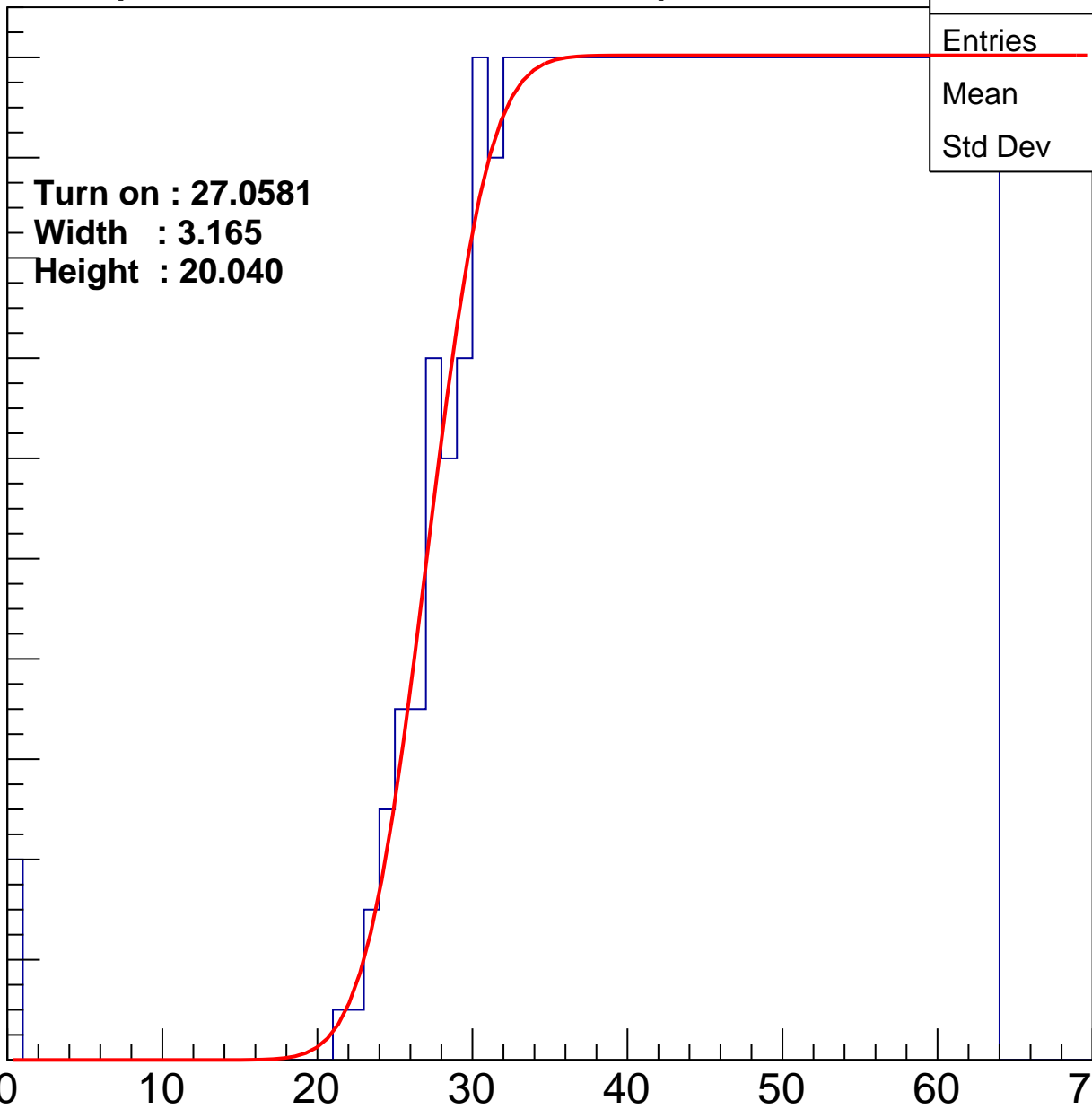
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.0581  
Width : 3.165  
Height : 20.040

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.59 |
| Std Dev | 11.36 |

ampl



# B1L001S, U23-ch5

calib\_packv5\_042523\_0143.root, FC#2, port C2

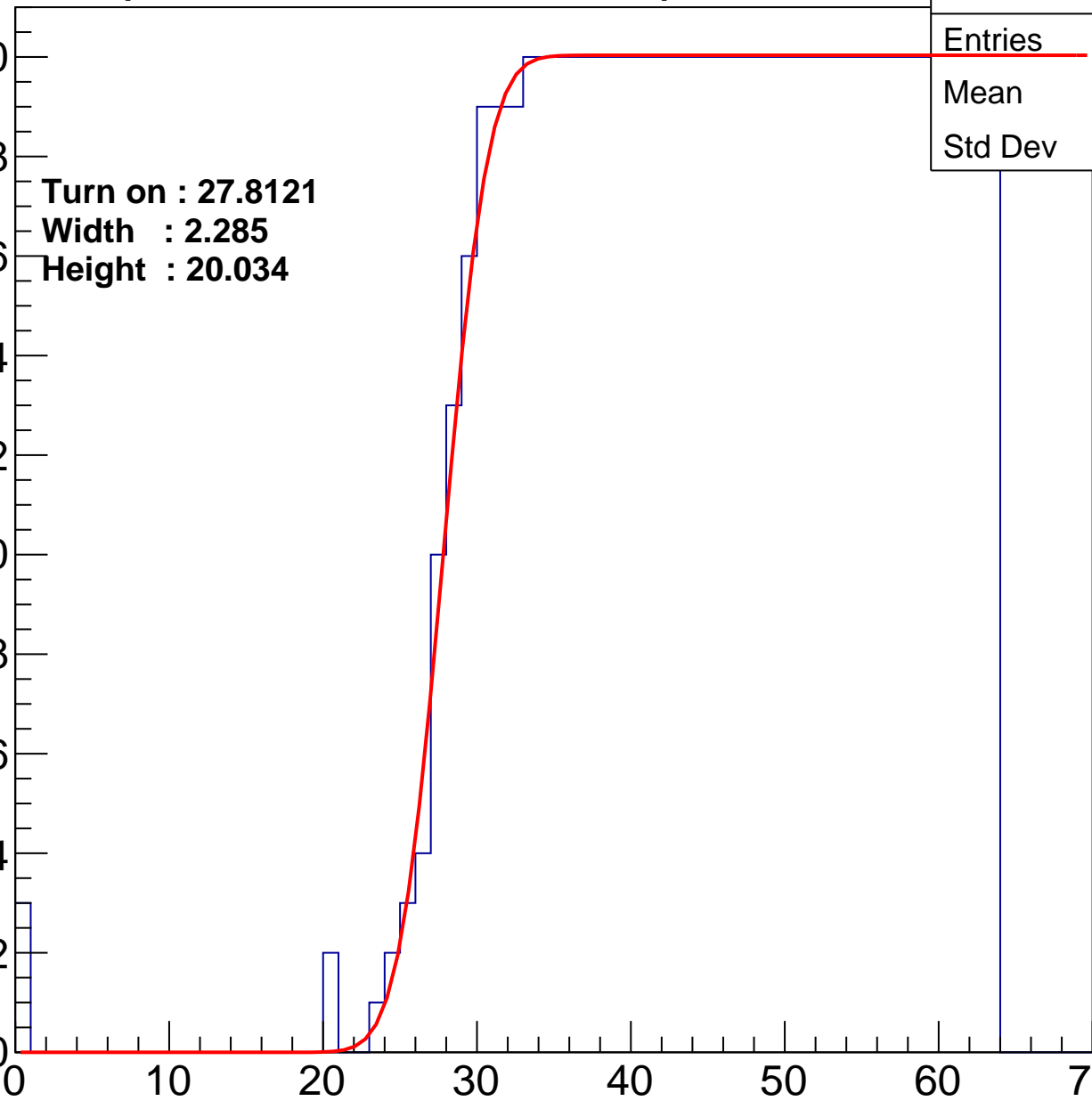
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.8121  
Width : 2.285  
Height : 20.034

|         |       |
|---------|-------|
| Entries | 731   |
| Mean    | 45.03 |
| Std Dev | 11.02 |

ampl



# B1L001S, U23-ch6

calib\_packv5\_042523\_0143.root, FC#2, port C2

|         |       |
|---------|-------|
| Entries | 710   |
| Mean    | 45.53 |
| Std Dev | 10.77 |

**Turn on : 28.8302**

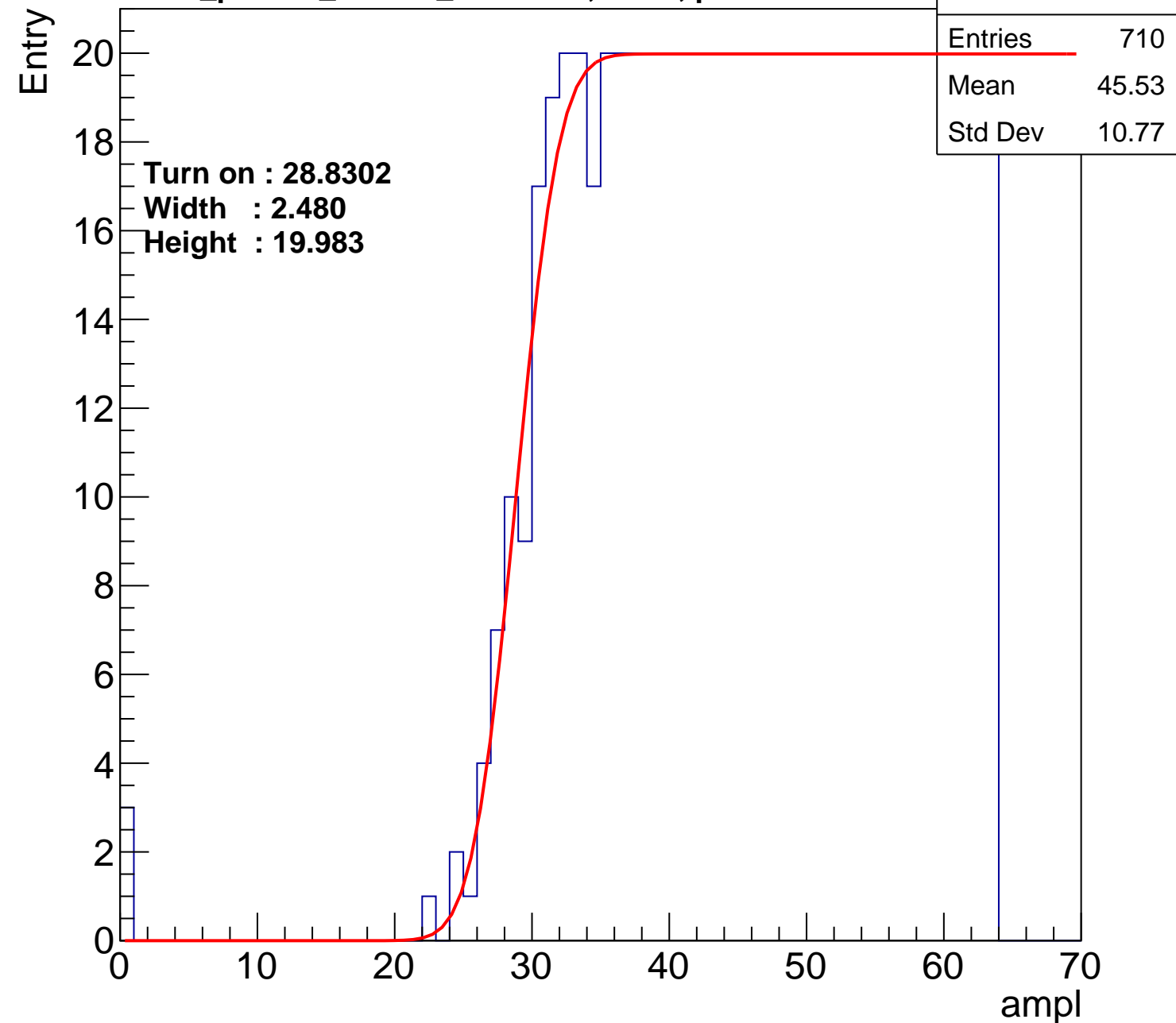
**Width : 2.480**

**Height : 19.983**

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L001S, U23-ch7

calib\_packv5\_042523\_0143.root, FC#2, port C2

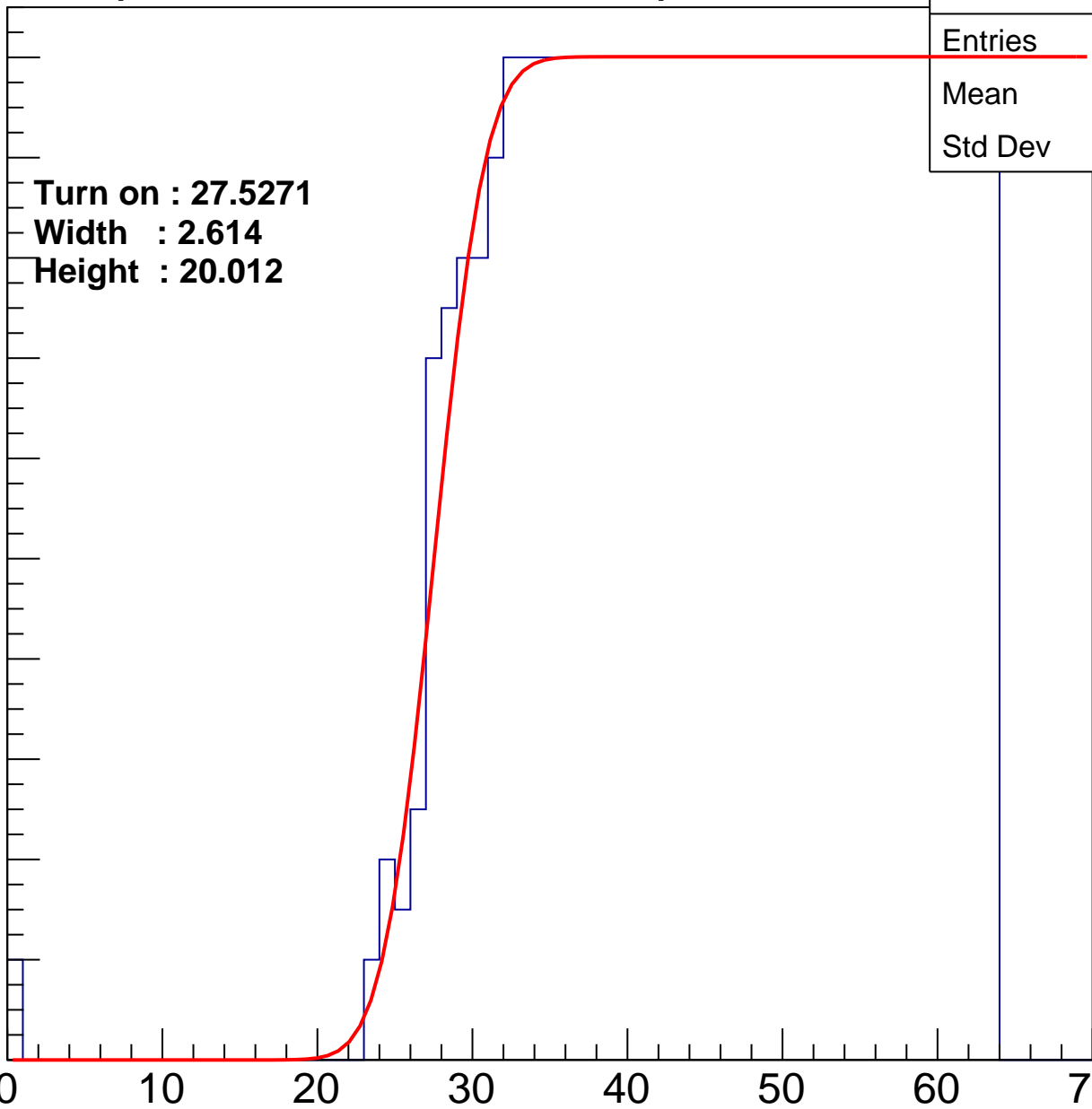
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.5271  
Width : 2.614  
Height : 20.012

|         |       |
|---------|-------|
| Entries | 735   |
| Mean    | 44.96 |
| Std Dev | 10.97 |

ampl



# B1L001S, U23-ch8

calib\_packv5\_042523\_0143.root, FC#2, port C2

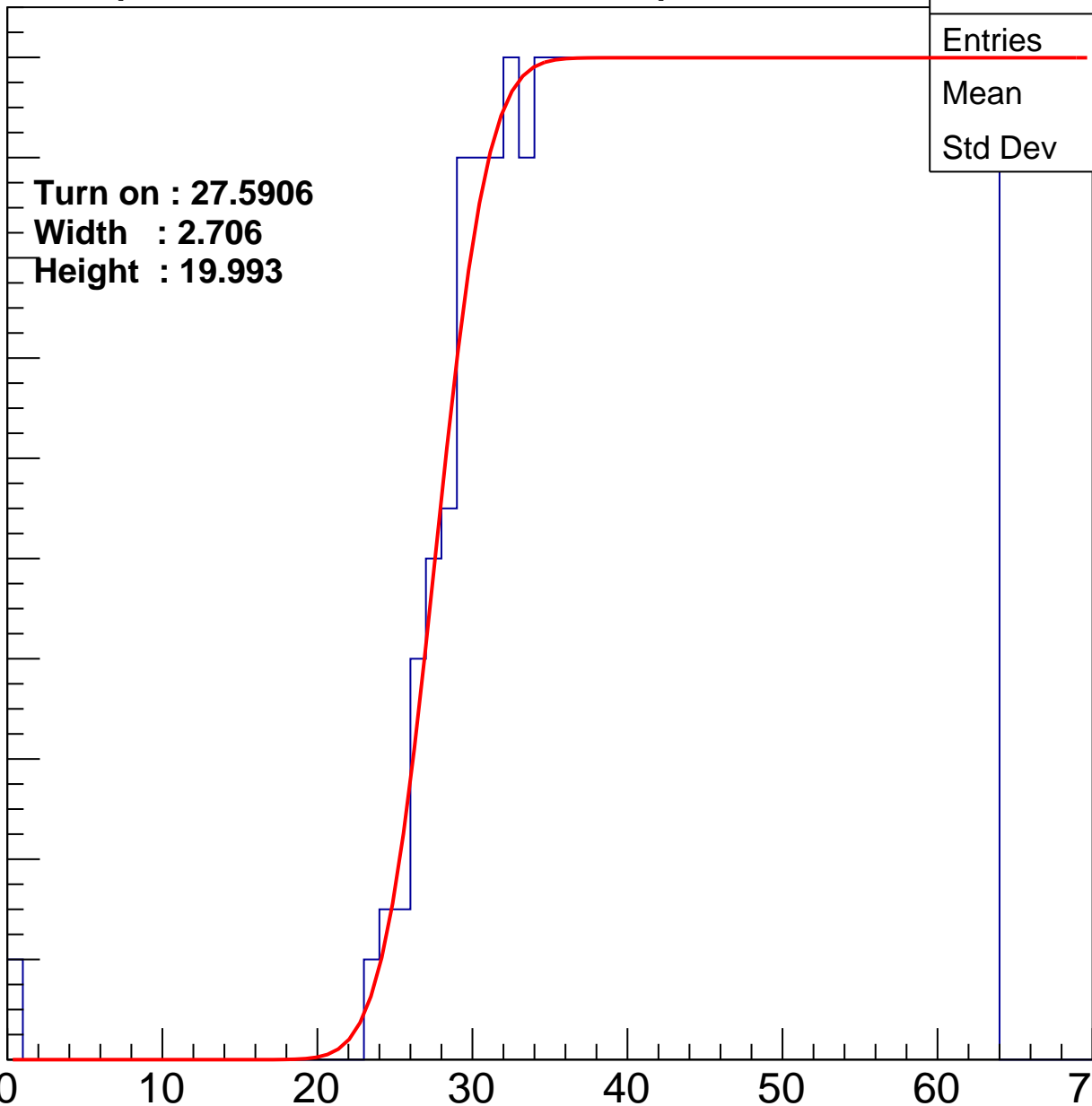
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.5906**  
**Width : 2.706**  
**Height : 19.993**

|         |       |
|---------|-------|
| Entries | 731   |
| Mean    | 45.05 |
| Std Dev | 10.93 |

ampl



# B1L001S, U23-ch9

calib\_packv5\_042523\_0143.root, FC#2, port C2

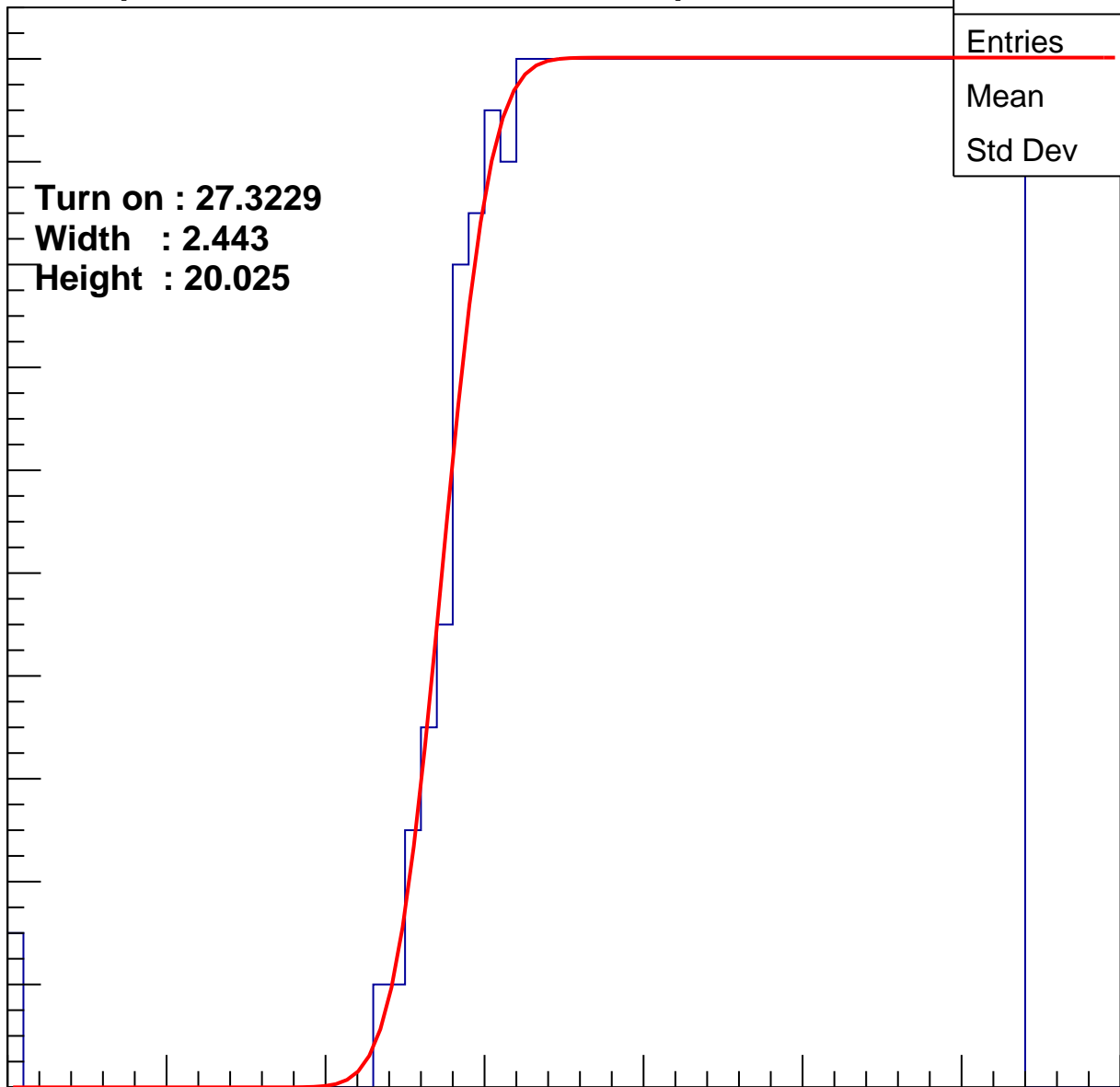
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.3229  
Width : 2.443  
Height : 20.025

|         |       |
|---------|-------|
| Entries | 738   |
| Mean    | 44.87 |
| Std Dev | 11.09 |

ampl



# B1L001S, U23-ch10

calib\_packv5\_042523\_0143.root, FC#2, port C2

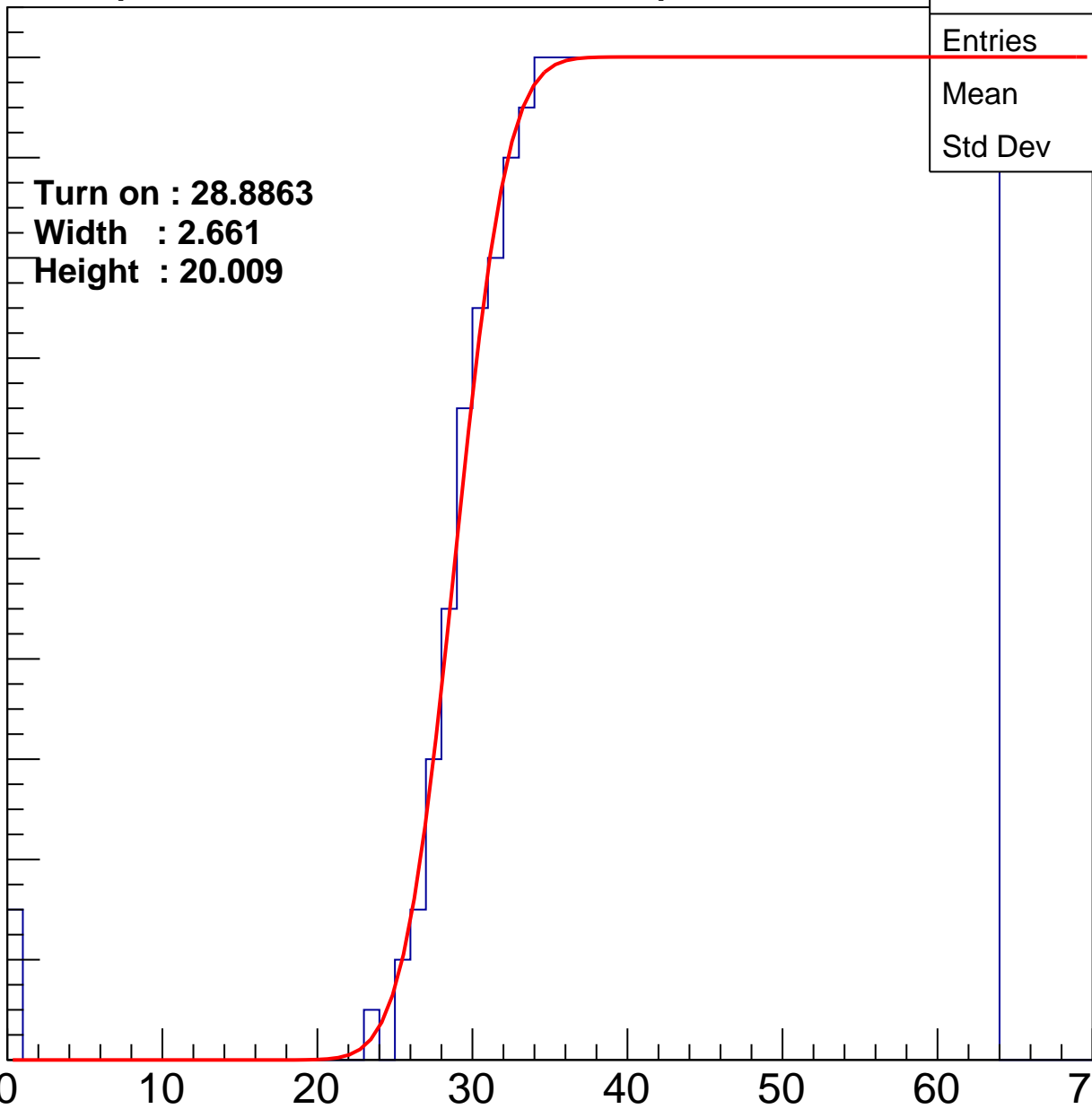
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.8863**  
**Width : 2.661**  
**Height : 20.009**

|         |       |
|---------|-------|
| Entries | 705   |
| Mean    | 45.66 |
| Std Dev | 10.69 |

ampl



# B1L001S, U23-ch11

calib\_packv5\_042523\_0143.root, FC#2, port C2

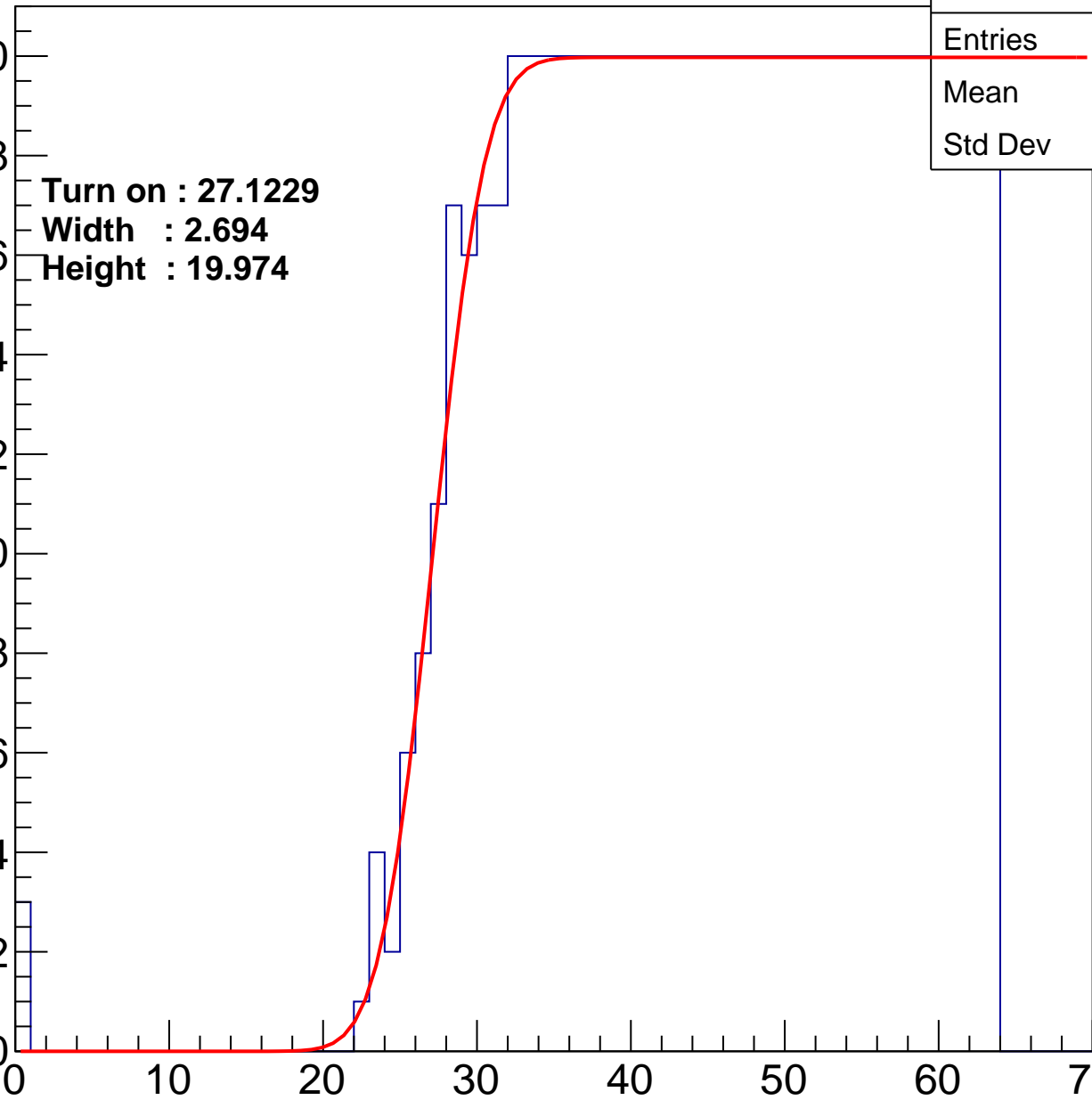
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.1229  
Width : 2.694  
Height : 19.974

|         |       |
|---------|-------|
| Entries | 742   |
| Mean    | 44.74 |
| Std Dev | 11.19 |

ampl



# B1L001S, U23-ch12

calib\_packv5\_042523\_0143.root, FC#2, port C2

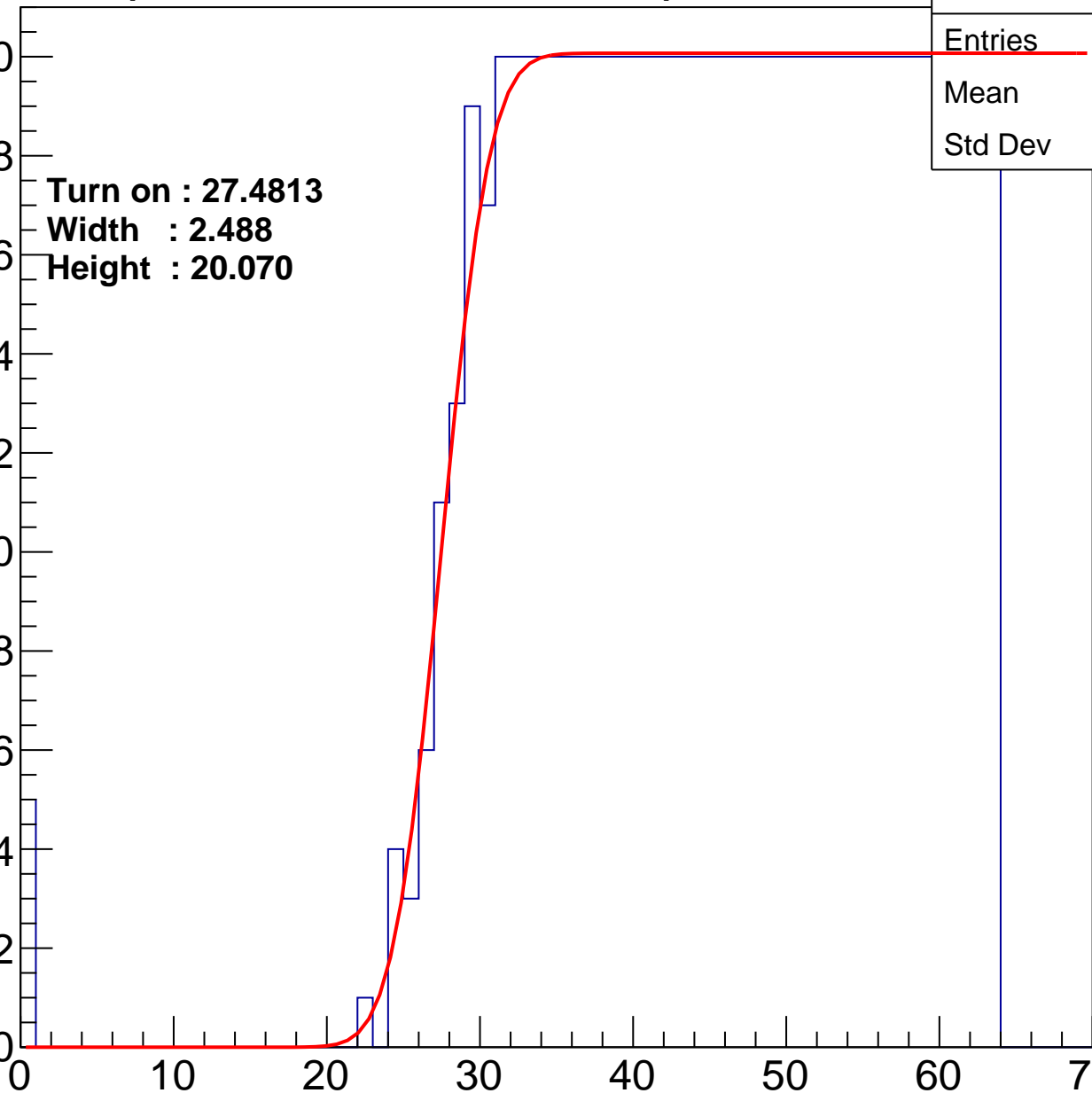
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4813  
Width : 2.488  
Height : 20.070

|         |       |
|---------|-------|
| Entries | 739   |
| Mean    | 44.78 |
| Std Dev | 11.29 |

ampl



# B1L001S, U23-ch13

calib\_packv5\_042523\_0143.root, FC#2, port C2

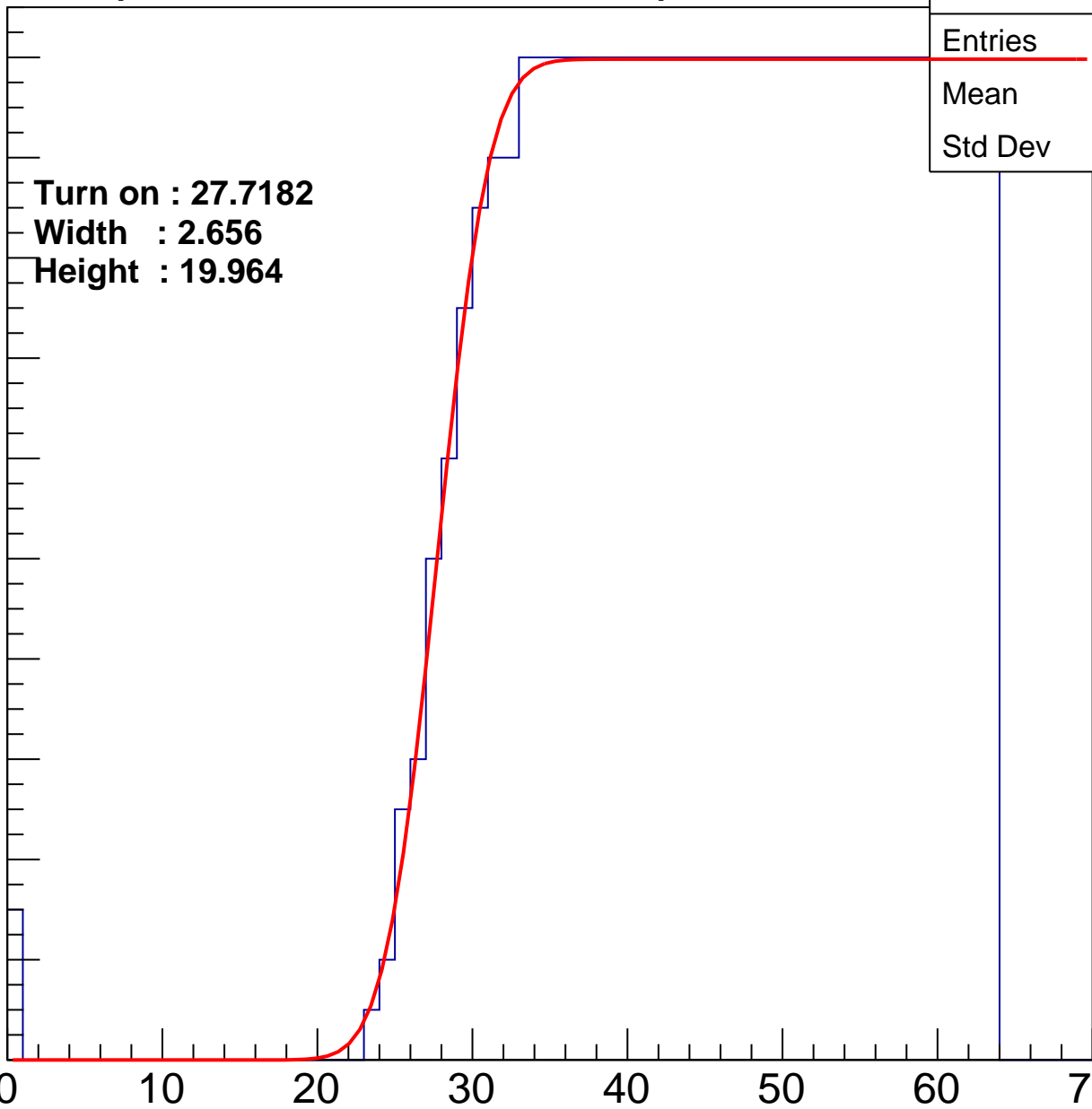
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.7182**  
**Width : 2.656**  
**Height : 19.964**

|         |       |
|---------|-------|
| Entries | 727   |
| Mean    | 45.11 |
| Std Dev | 10.98 |

ampl



# B1L001S, U23-ch14

calib\_packv5\_042523\_0143.root, FC#2, port C2

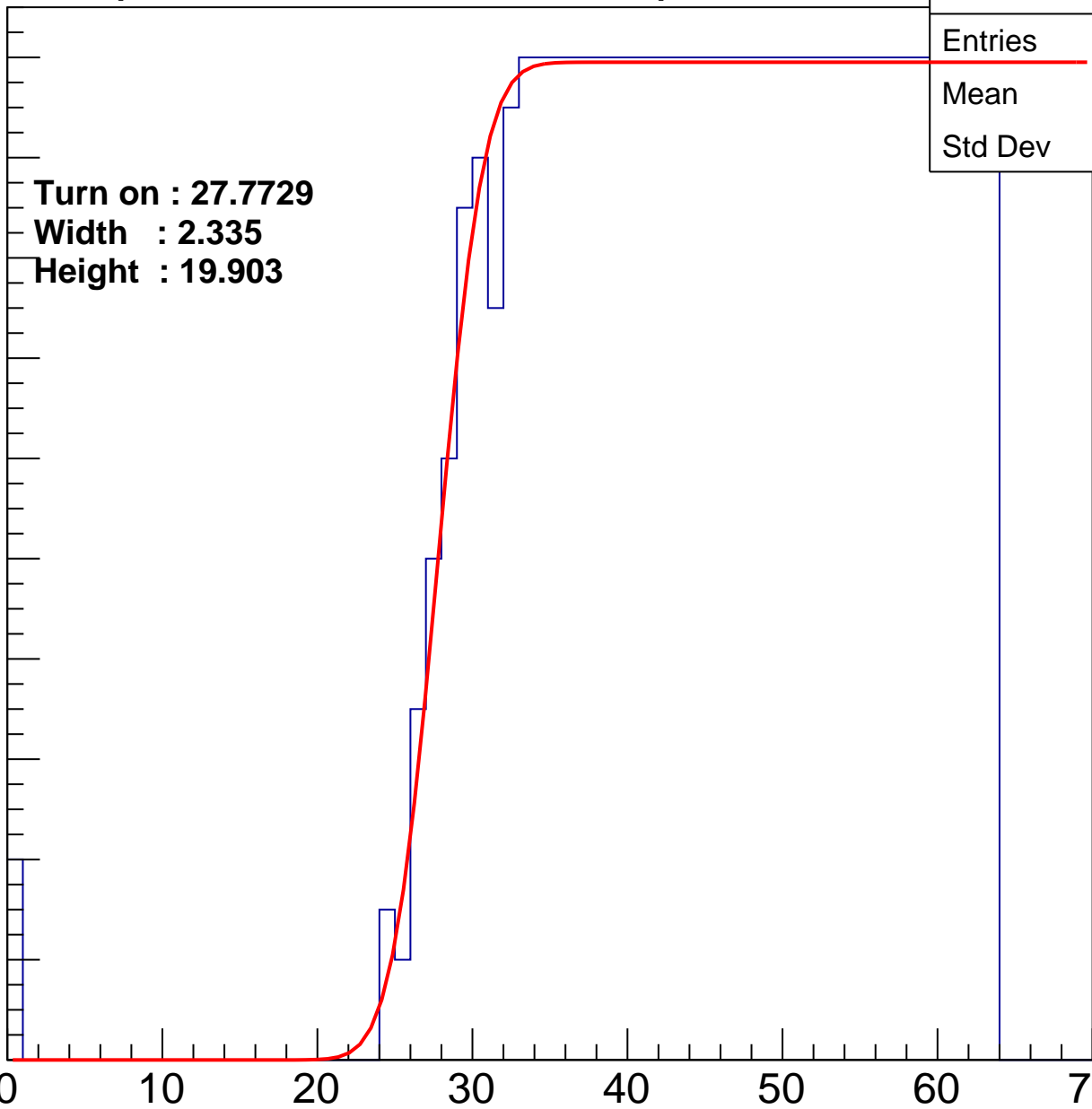
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.7729**  
**Width : 2.335**  
**Height : 19.903**

|         |       |
|---------|-------|
| Entries | 727   |
| Mean    | 45.08 |
| Std Dev | 11.08 |

ampl





# B1L001S, U23-ch15

calib\_packv5\_042523\_0143.root, FC#2, port C2

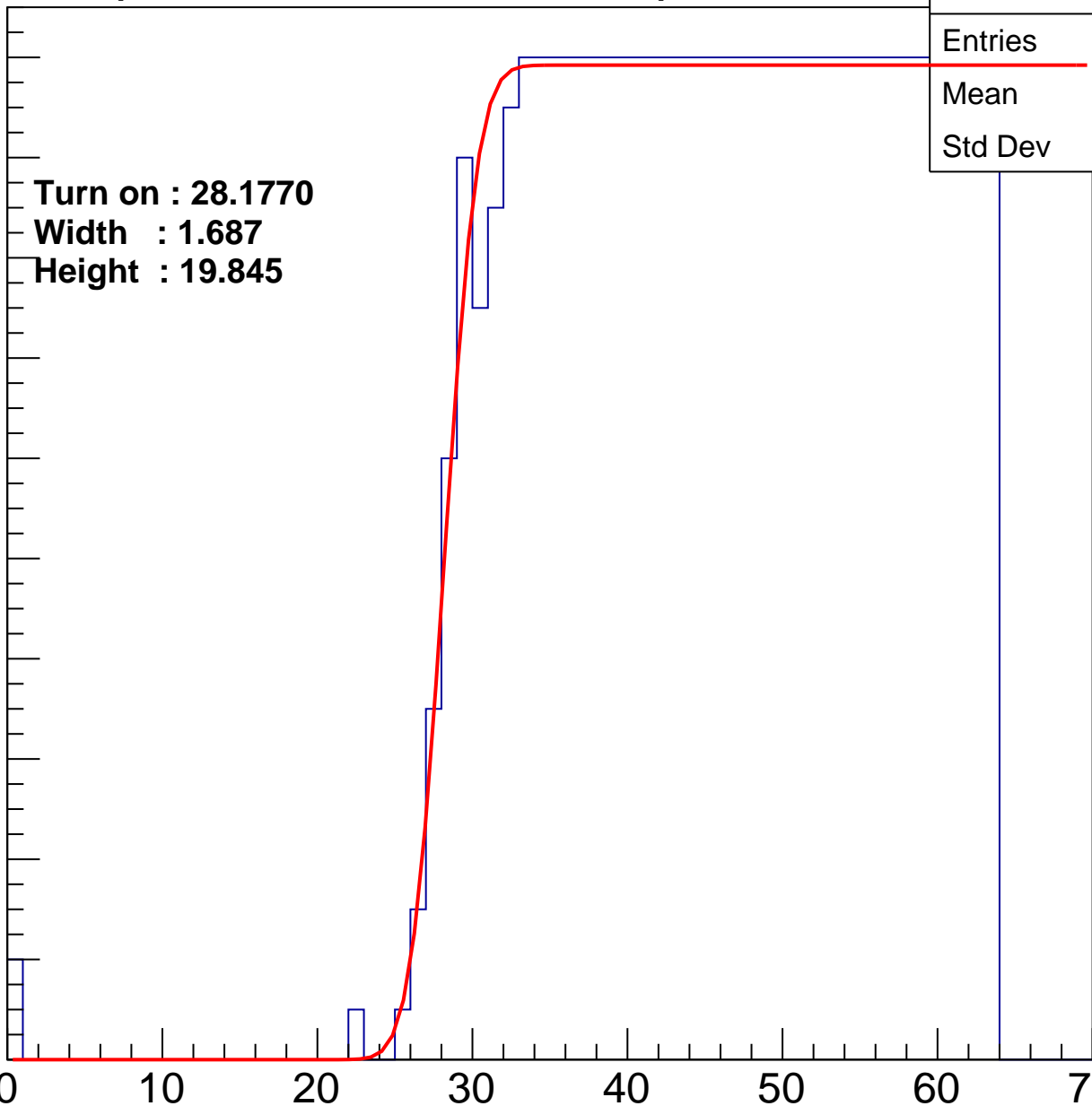
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.1770**  
**Width : 1.687**  
**Height : 19.845**

|         |       |
|---------|-------|
| Entries | 715   |
| Mean    | 45.48 |
| Std Dev | 10.67 |

ampl



# B1L001S, U23-ch16

calib\_packv5\_042523\_0143.root, FC#2, port C2

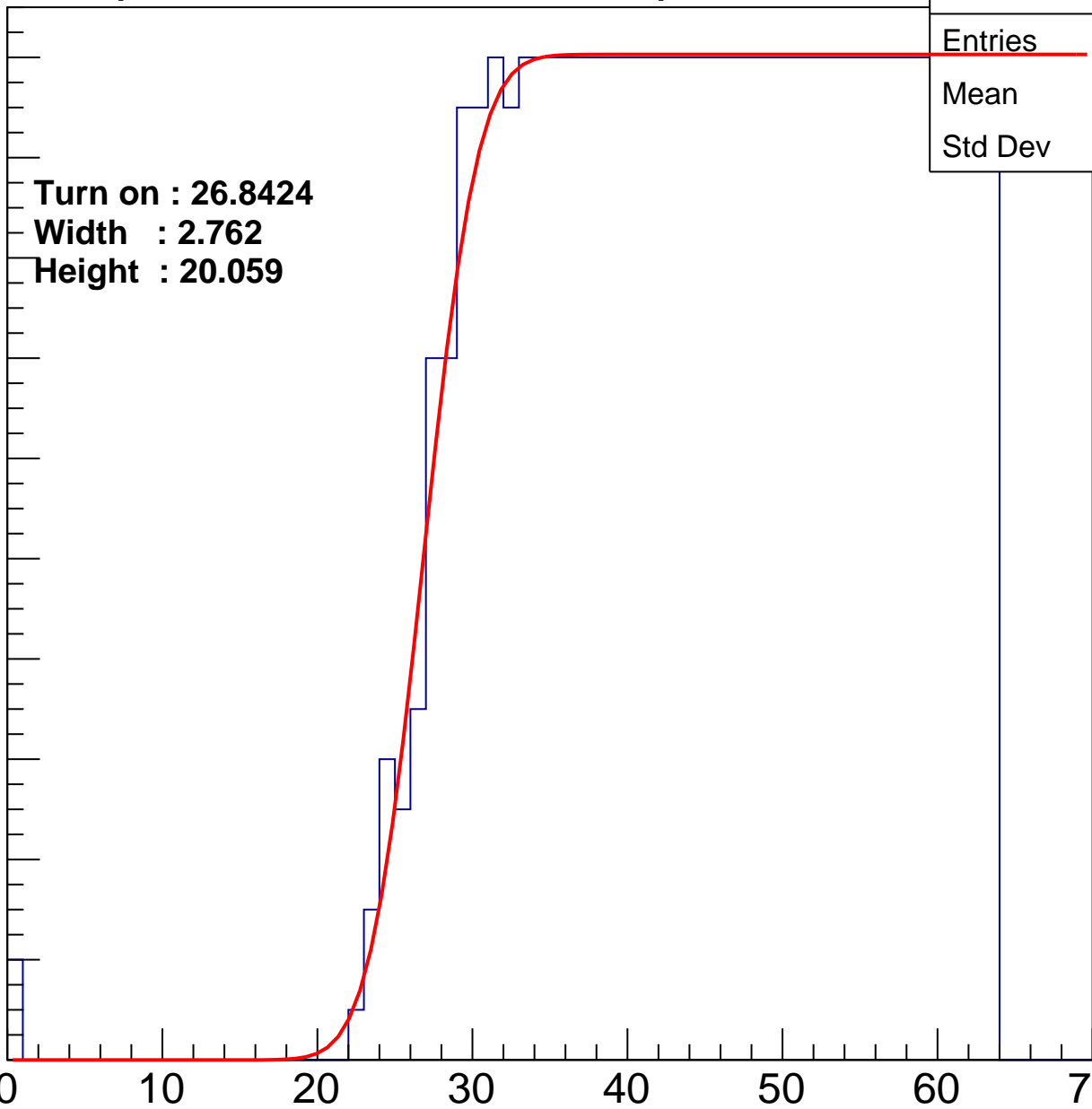
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.8424  
Width : 2.762  
Height : 20.059

|         |       |
|---------|-------|
| Entries | 749   |
| Mean    | 44.62 |
| Std Dev | 11.15 |

ampl



# B1L001S, U23-ch17

calib\_packv5\_042523\_0143.root, FC#2, port C2

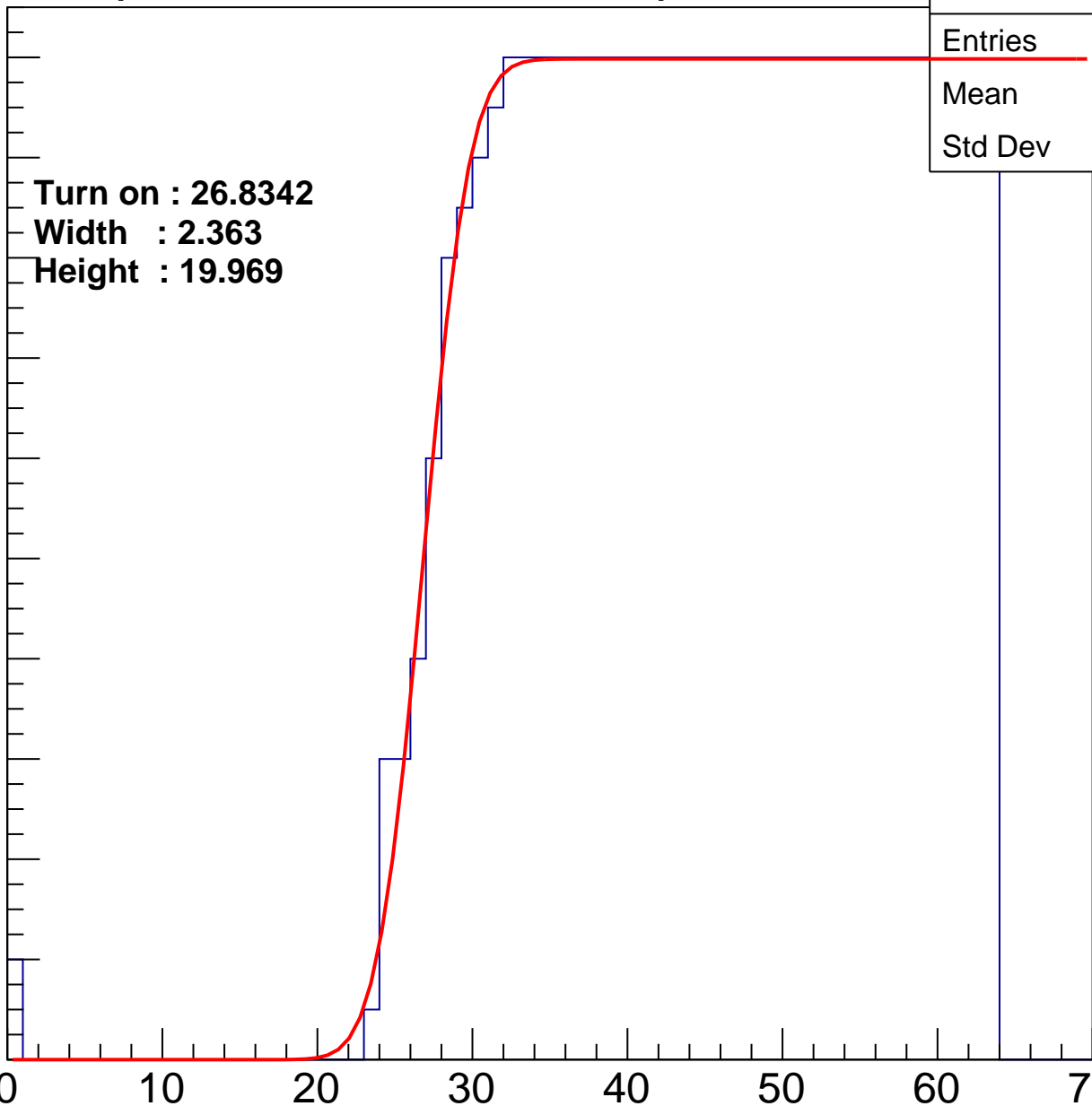
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.8342**  
**Width : 2.363**  
**Height : 19.969**

|         |       |
|---------|-------|
| Entries | 745   |
| Mean    | 44.72 |
| Std Dev | 11.09 |

ampl



# B1L001S, U23-ch18

calib\_packv5\_042523\_0143.root, FC#2, port C2

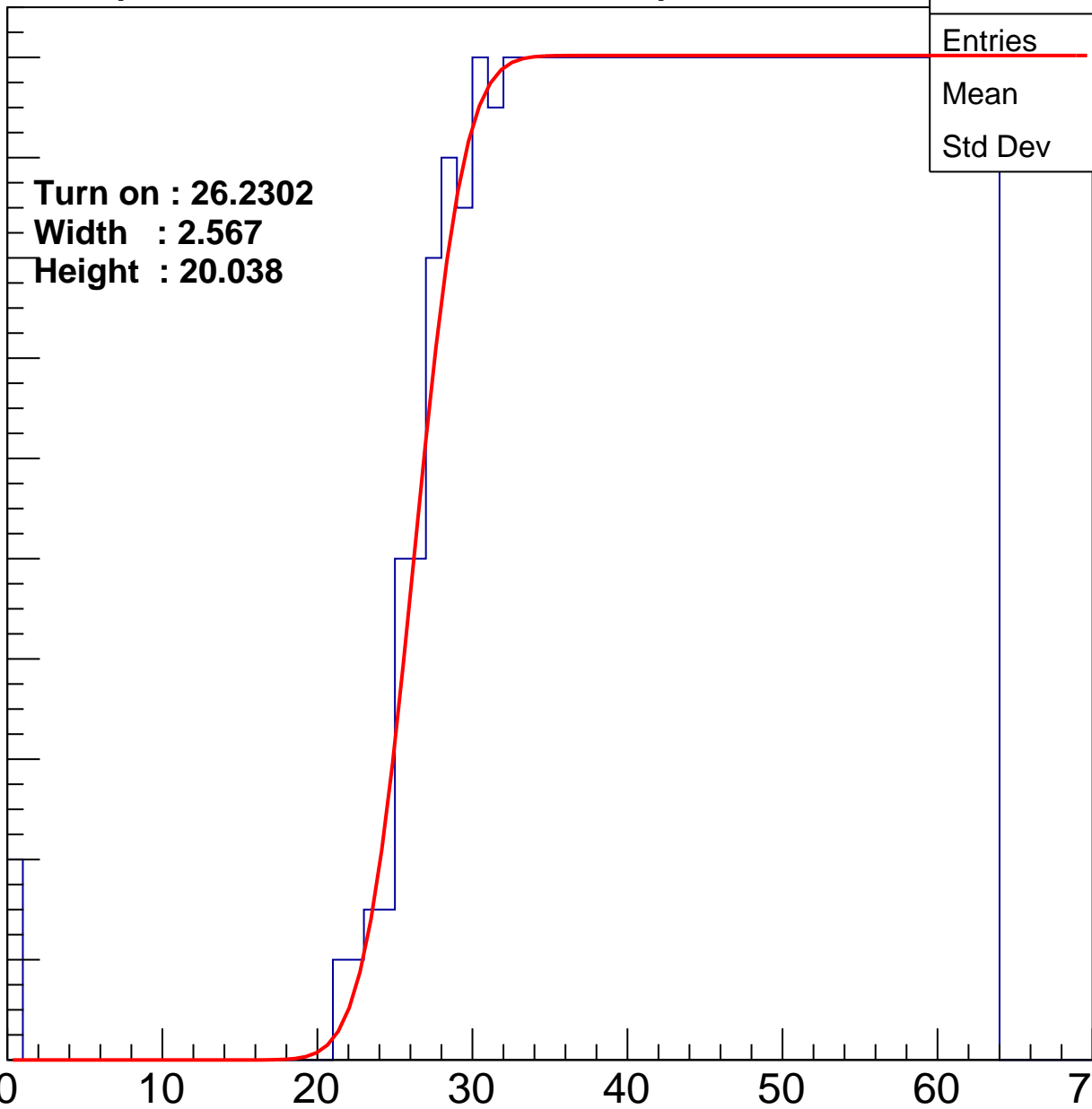
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.2302**  
**Width : 2.567**  
**Height : 20.038**

|         |       |
|---------|-------|
| Entries | 764   |
| Mean    | 44.18 |
| Std Dev | 11.54 |

ampl



# B1L001S, U23-ch19

calib\_packv5\_042523\_0143.root, FC#2, port C2

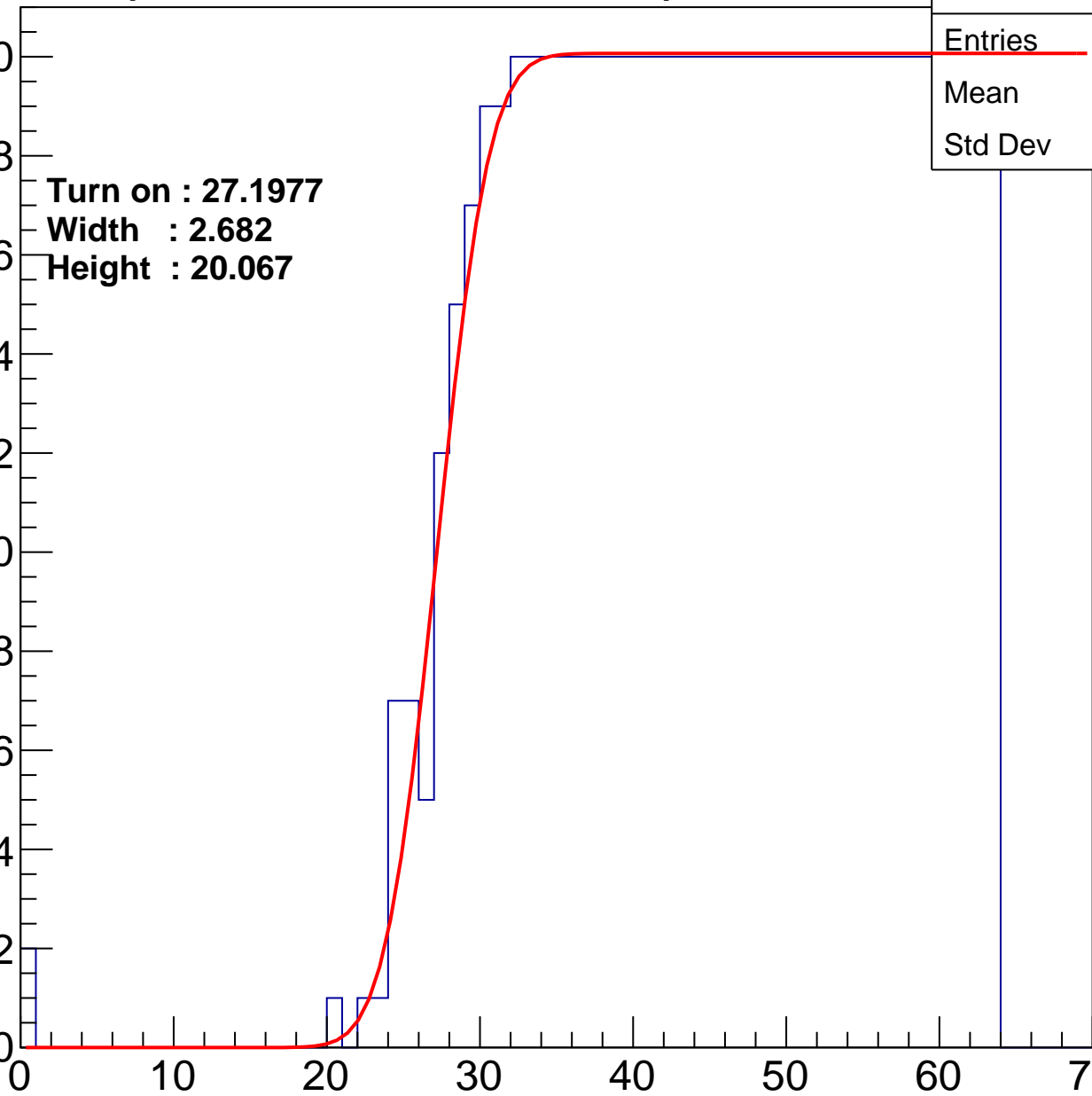
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.1977**  
**Width : 2.682**  
**Height : 20.067**

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.68 |
| Std Dev | 11.13 |

ampl



# B1L001S, U23-ch20

calib\_packv5\_042523\_0143.root, FC#2, port C2

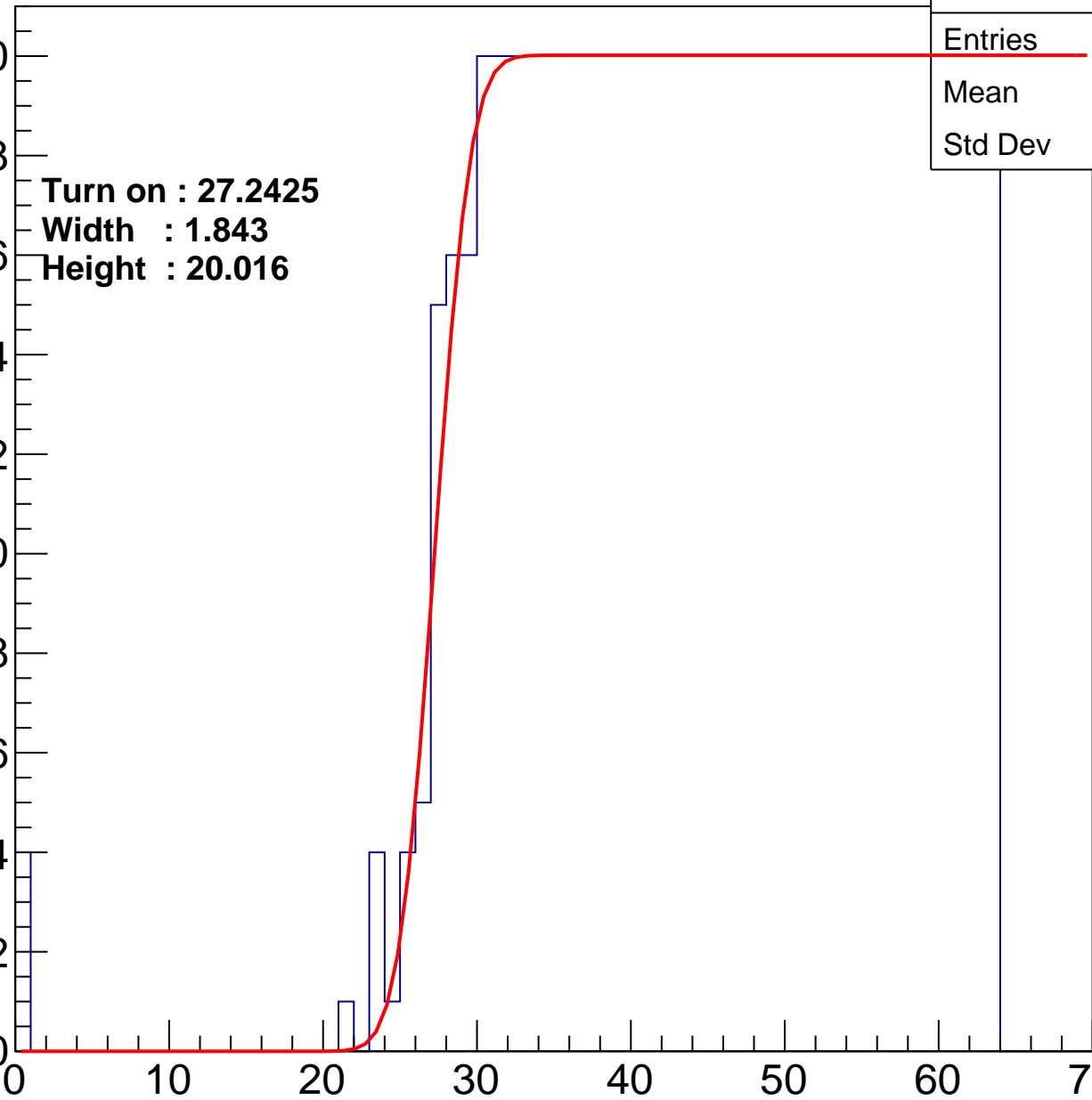
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2425**  
**Width : 1.843**  
**Height : 20.016**

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.64 |
| Std Dev | 11.28 |

ampl



# B1L001S, U23-ch21

calib\_packv5\_042523\_0143.root, FC#2, port C2

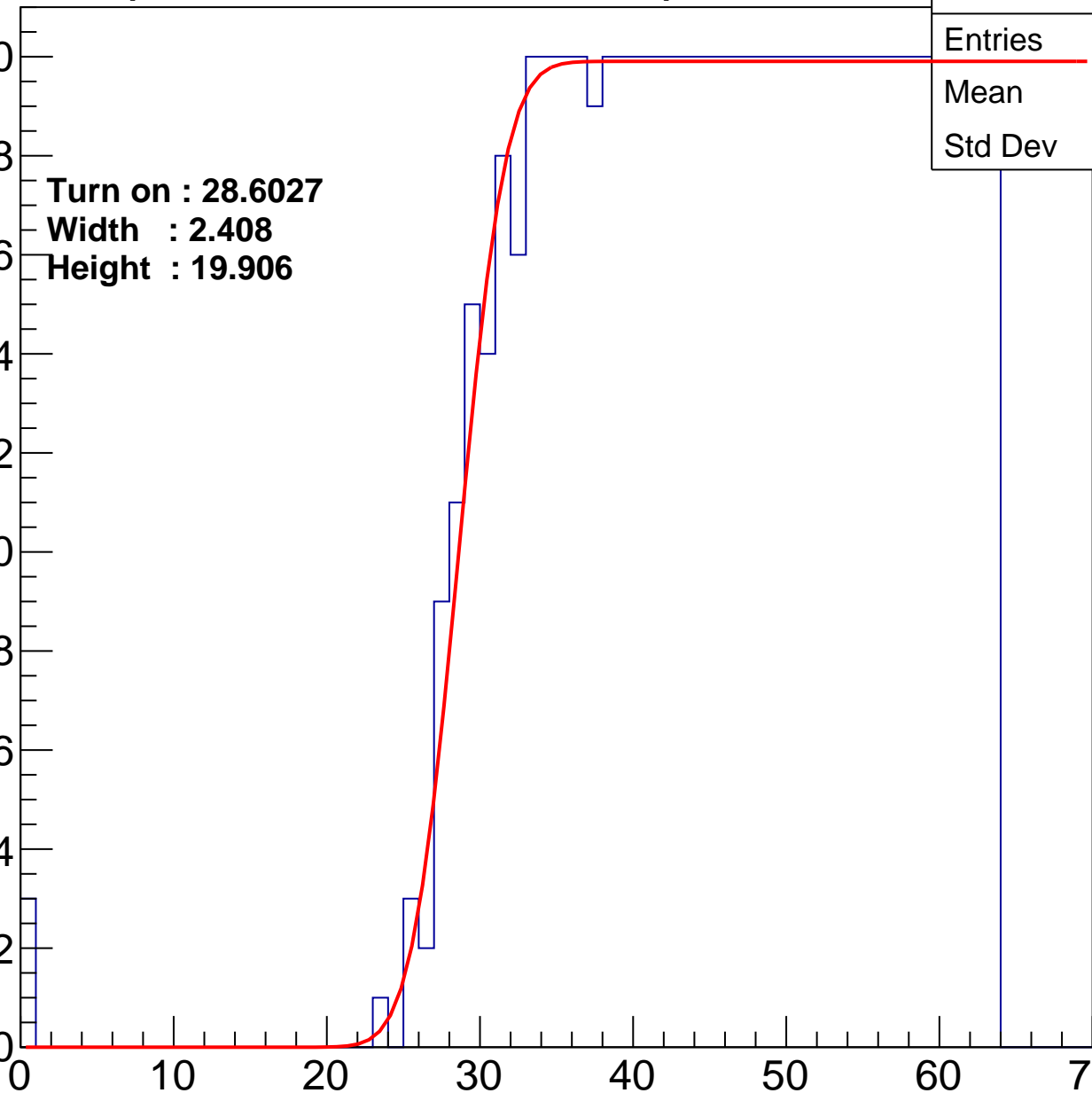
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.6027**  
**Width : 2.408**  
**Height : 19.906**

|         |       |
|---------|-------|
| Entries | 711   |
| Mean    | 45.5  |
| Std Dev | 10.79 |

ampl



# B1L001S, U23-ch22

calib\_packv5\_042523\_0143.root, FC#2, port C2

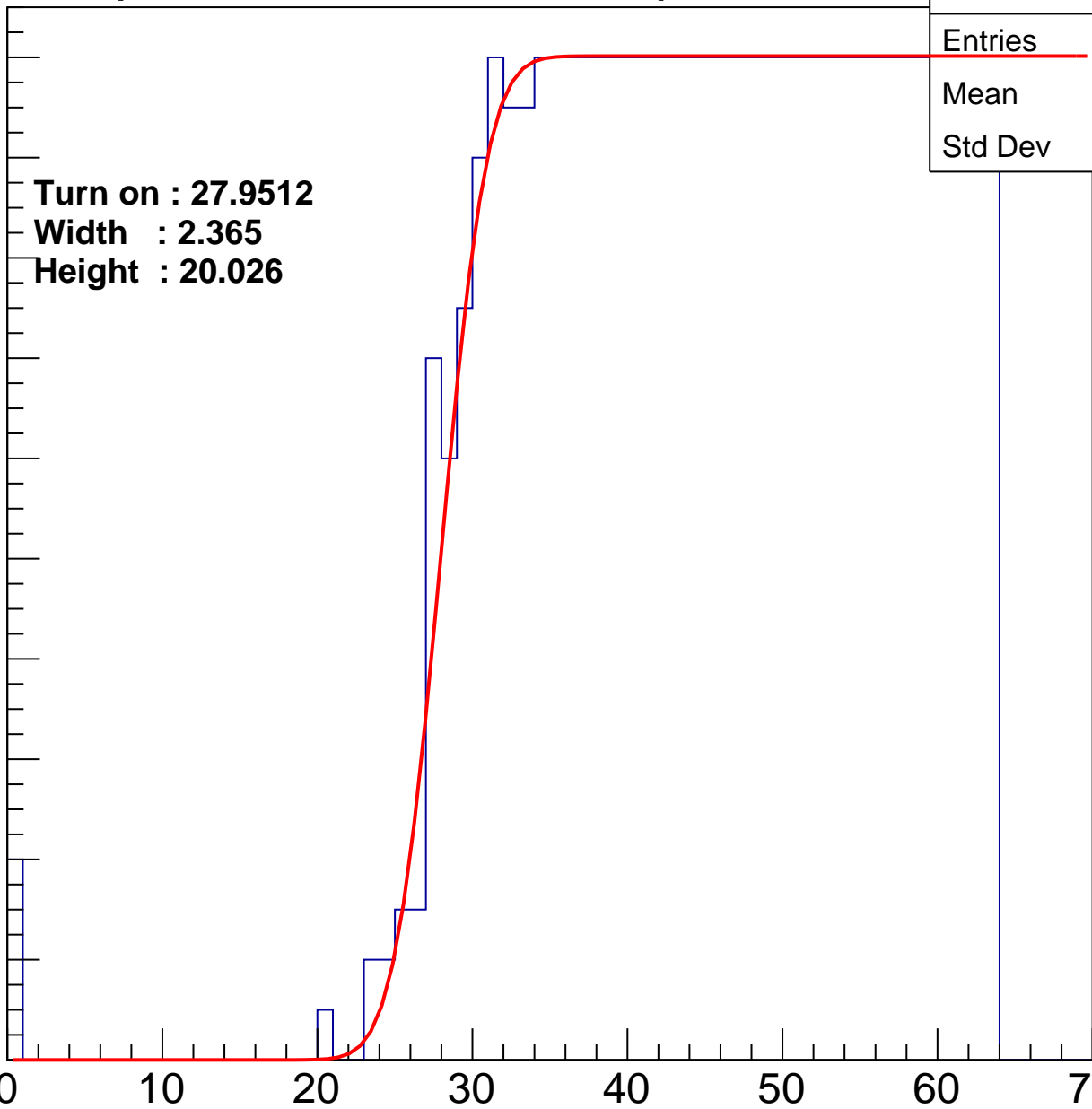
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.9512  
Width : 2.365  
Height : 20.026

|         |       |
|---------|-------|
| Entries | 732   |
| Mean    | 44.96 |
| Std Dev | 11.14 |

ampl





# B1L001S, U23-ch23

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry

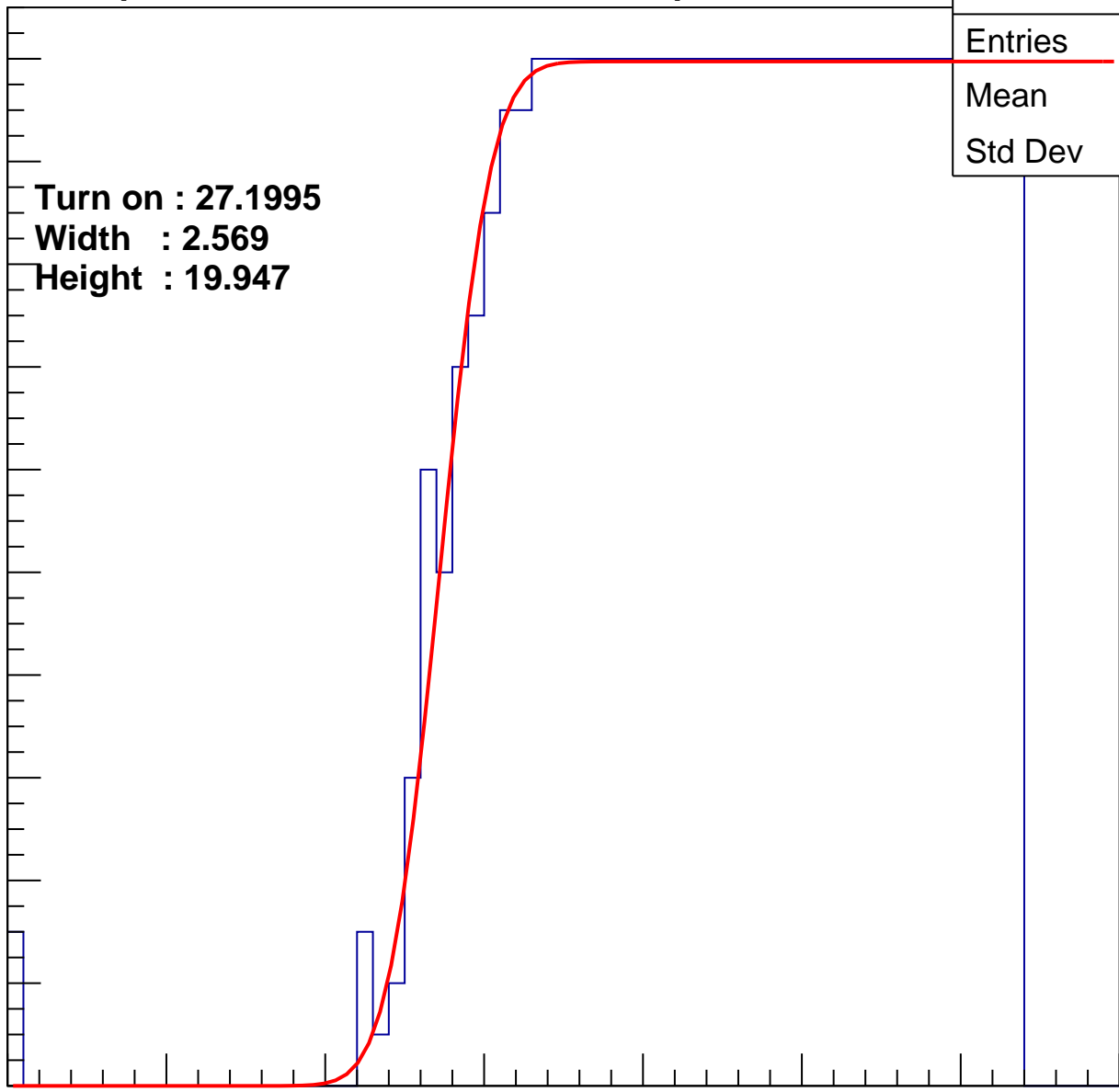
20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.1995**  
**Width : 2.569**  
**Height : 19.947**

|         |       |
|---------|-------|
| Entries | 741   |
| Mean    | 44.75 |
| Std Dev | 11.19 |

ampl

0 10 20 30 40 50 60 70



# B1L001S, U23-ch24

calib\_packv5\_042523\_0143.root, FC#2, port C2

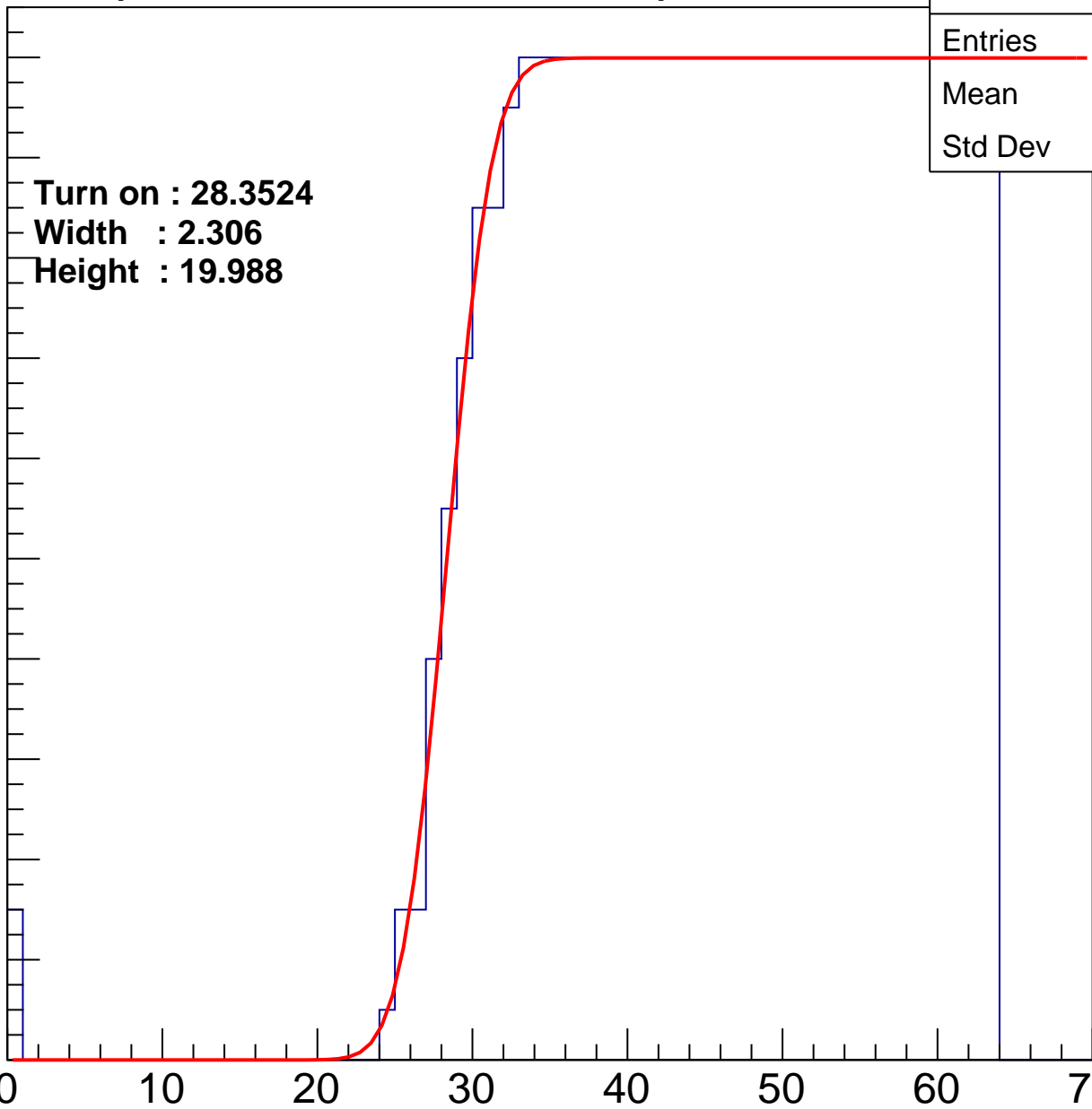
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.3524**  
**Width : 2.306**  
**Height : 19.988**

|         |       |
|---------|-------|
| Entries | 716   |
| Mean    | 45.41 |
| Std Dev | 10.8  |

ampl



# B1L001S, U23-ch25

calib\_packv5\_042523\_0143.root, FC#2, port C2

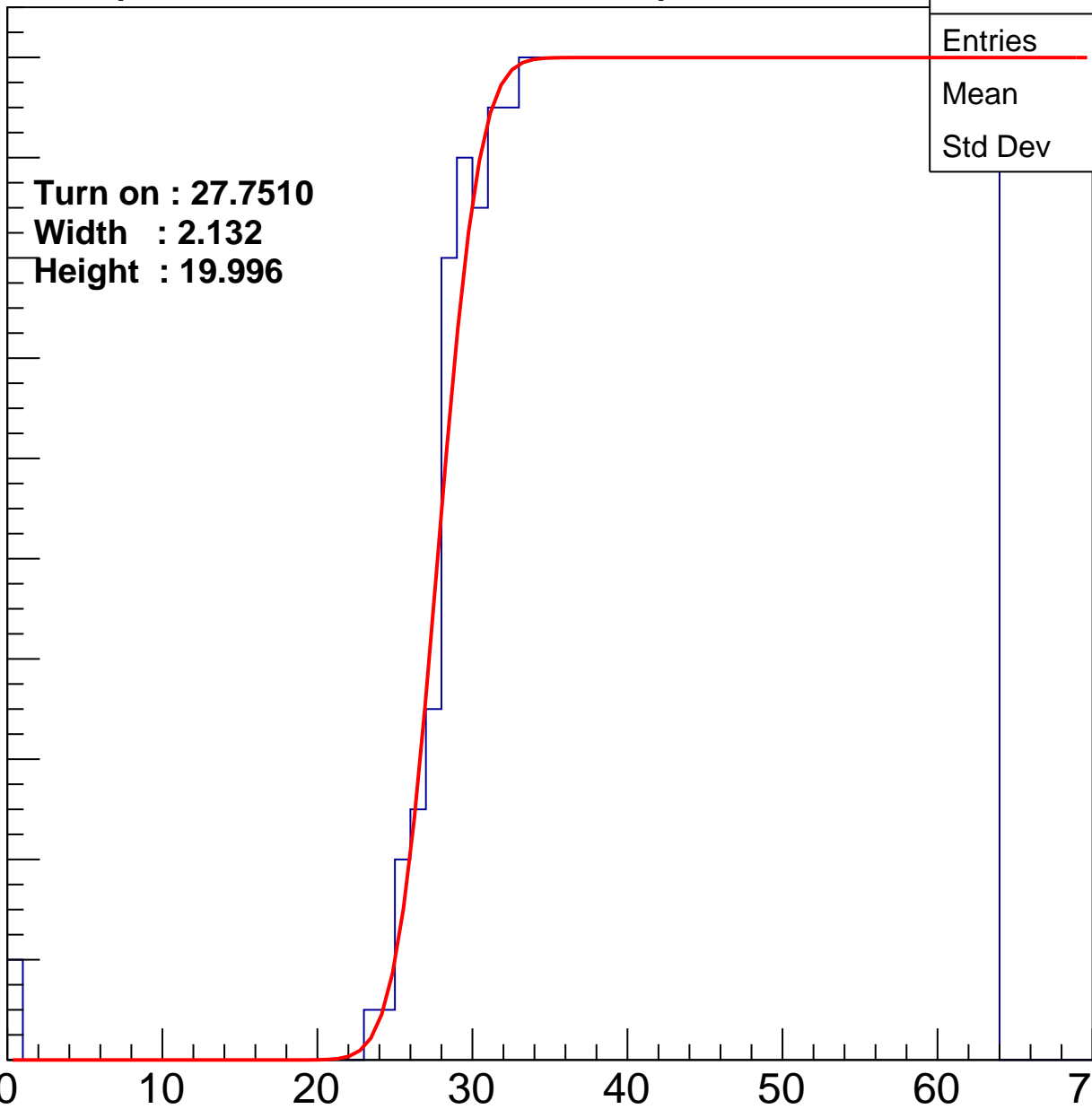
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.7510  
Width : 2.132  
Height : 19.996

|         |       |
|---------|-------|
| Entries | 729   |
| Mean    | 45.13 |
| Std Dev | 10.85 |

ampl



# B1L001S, U23-ch26

calib\_packv5\_042523\_0143.root, FC#2, port C2

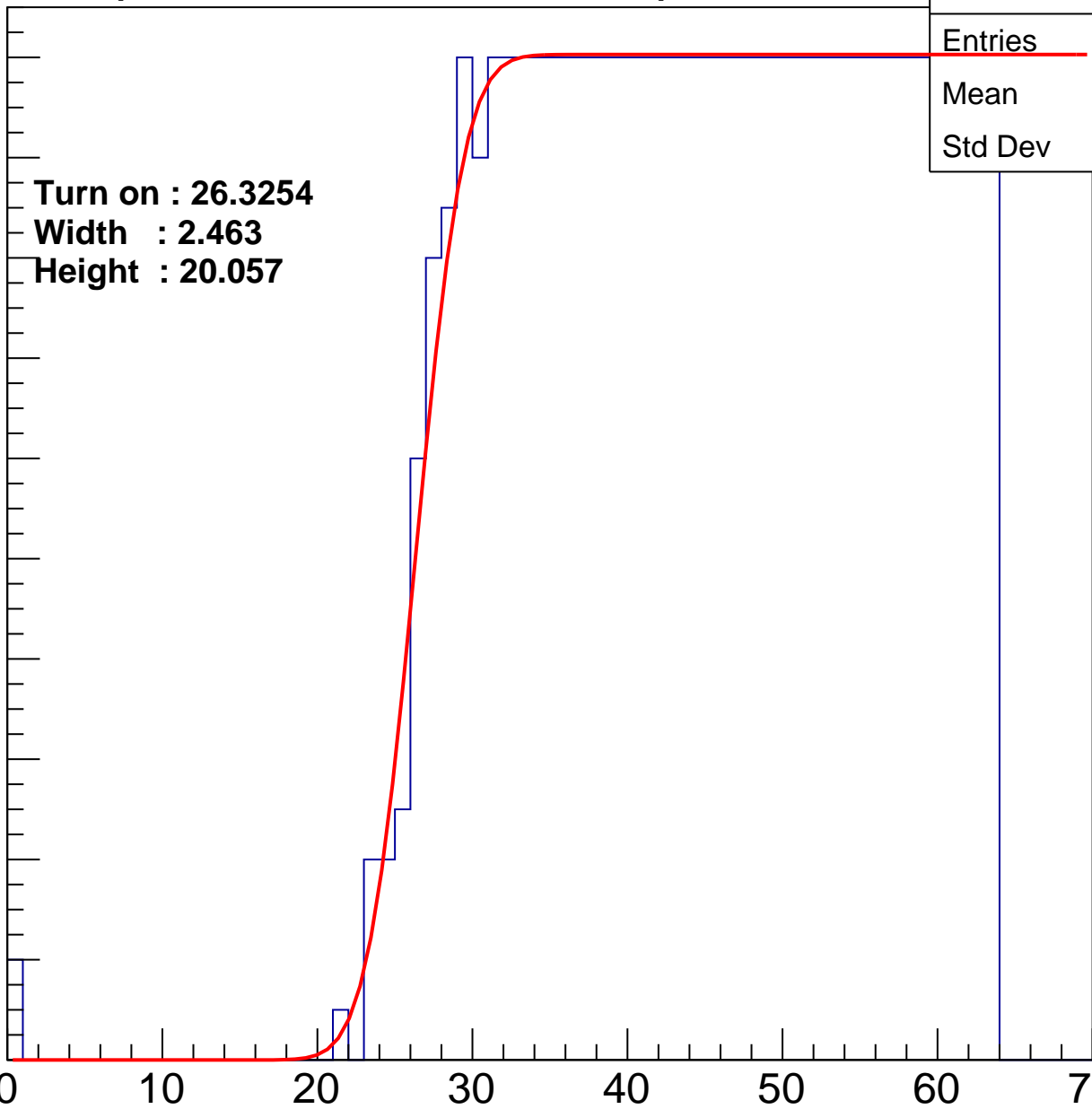
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.3254**  
**Width : 2.463**  
**Height : 20.057**

|         |       |
|---------|-------|
| Entries | 759   |
| Mean    | 44.39 |
| Std Dev | 11.25 |

ampl



# B1L001S, U23-ch27

calib\_packv5\_042523\_0143.root, FC#2, port C2

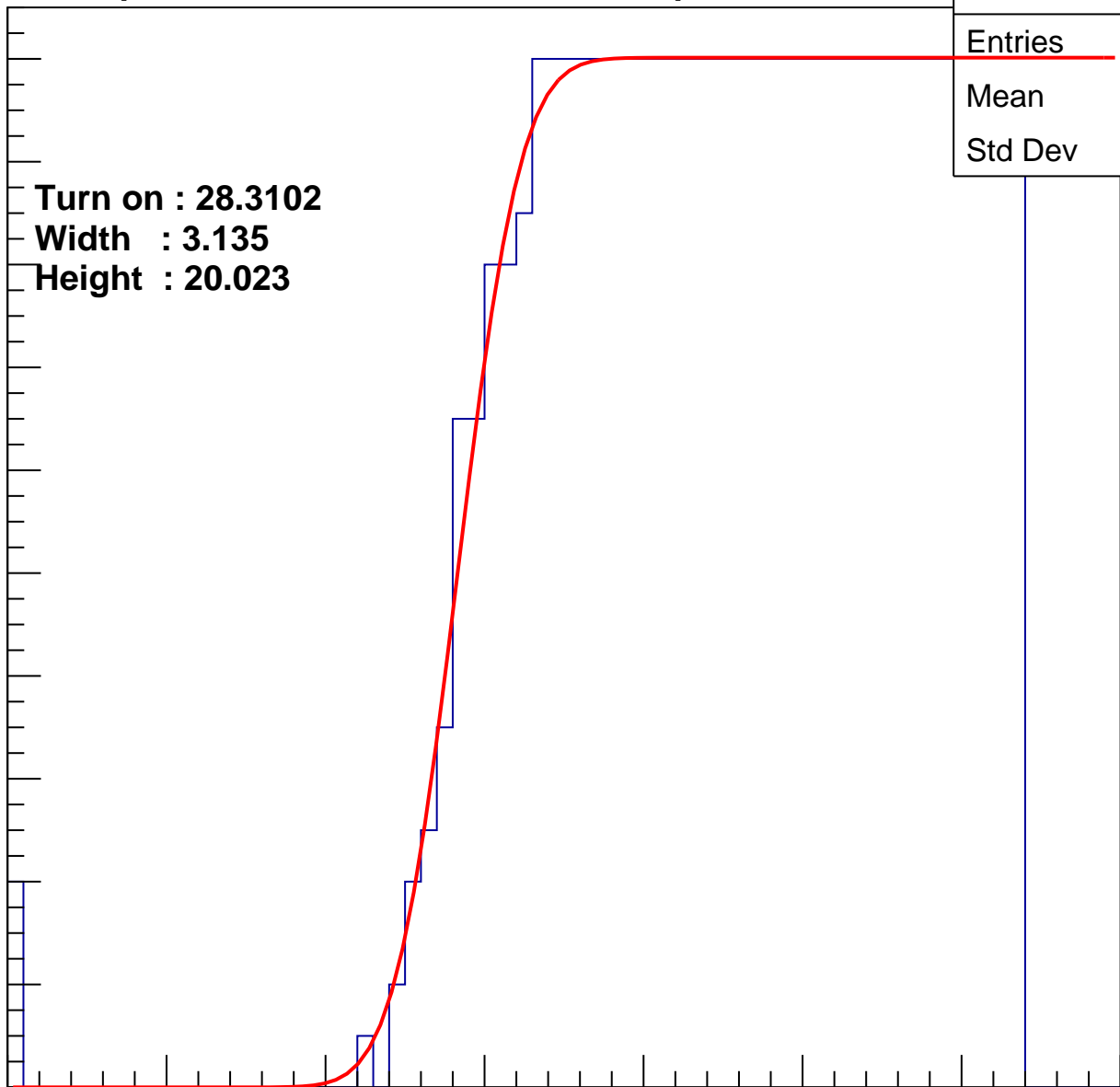
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.3102**  
**Width : 3.135**  
**Height : 20.023**

|         |       |
|---------|-------|
| Entries | 718   |
| Mean    | 45.28 |
| Std Dev | 11.01 |

ampl



# B1L001S, U23-ch28

calib\_packv5\_042523\_0143.root, FC#2, port C2

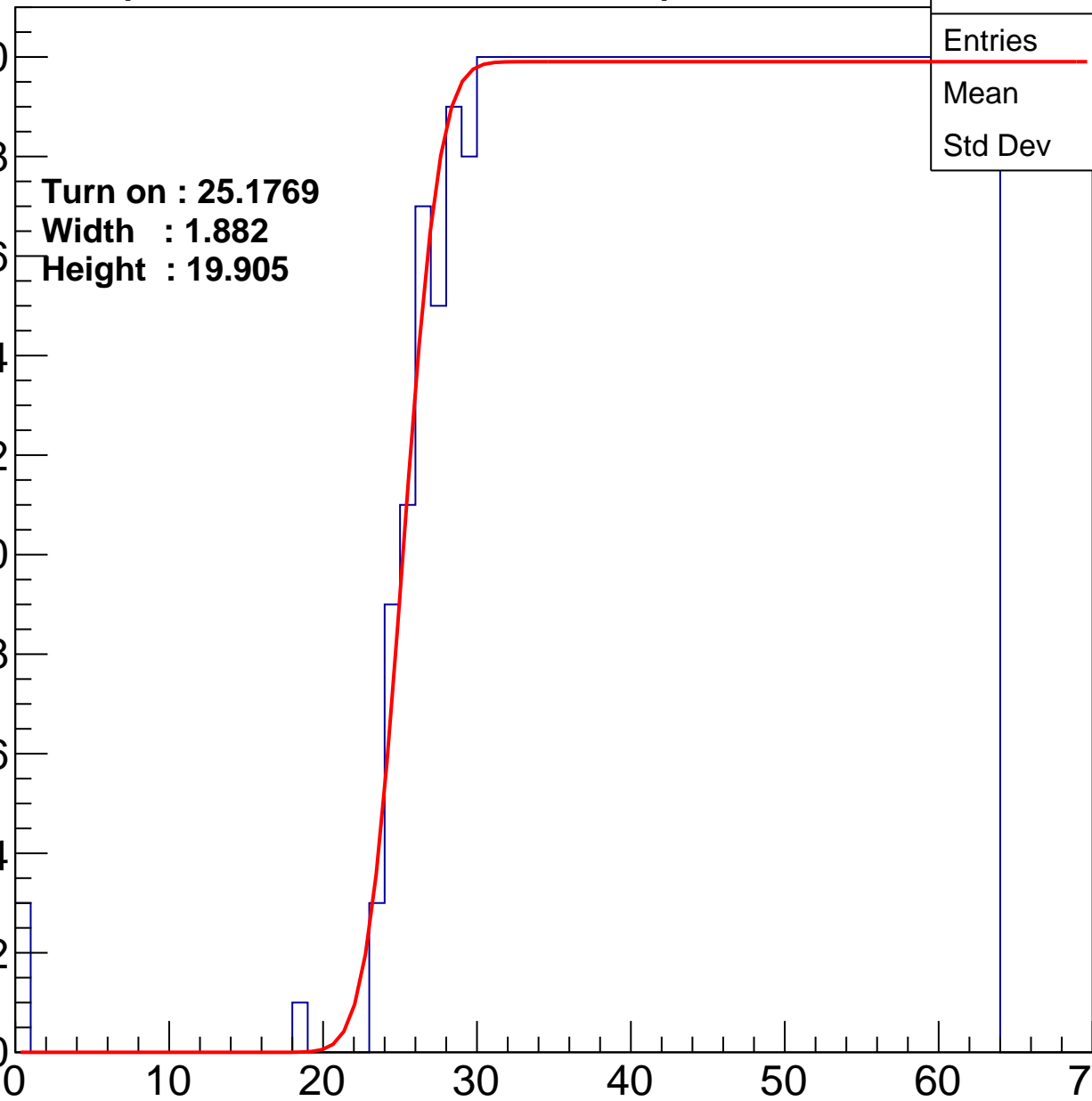
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 25.1769**  
**Width : 1.882**  
**Height : 19.905**

|         |       |
|---------|-------|
| Entries | 776   |
| Mean    | 43.94 |
| Std Dev | 11.57 |

ampl



# B1L001S, U23-ch29

calib\_packv5\_042523\_0143.root, FC#2, port C2

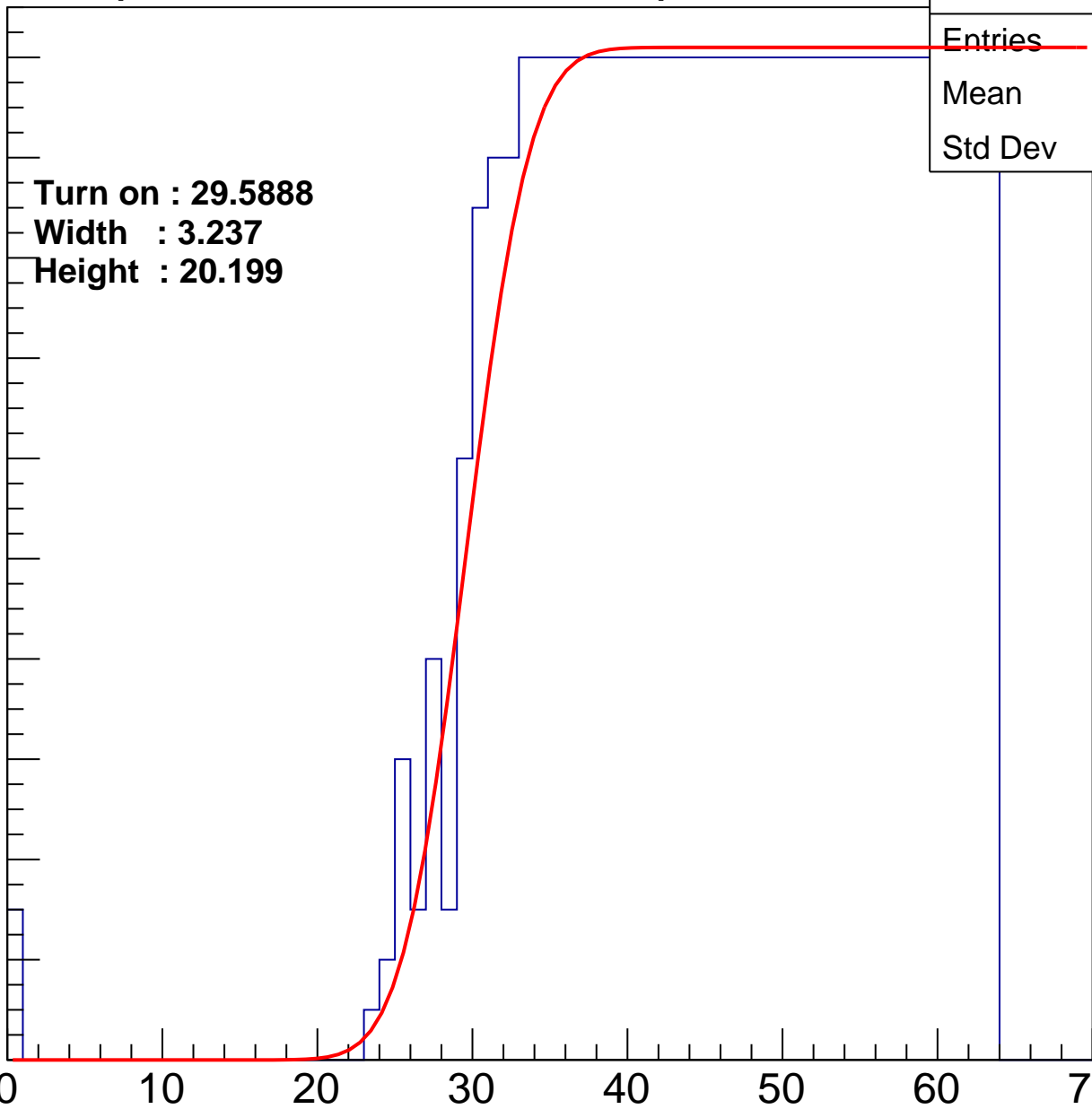
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.5888**  
**Width : 3.237**  
**Height : 20.199**

|         |       |
|---------|-------|
| Entries | 711   |
| Mean    | 45.5  |
| Std Dev | 10.79 |

ampl



# B1L001S, U23-ch30

calib\_packv5\_042523\_0143.root, FC#2, port C2

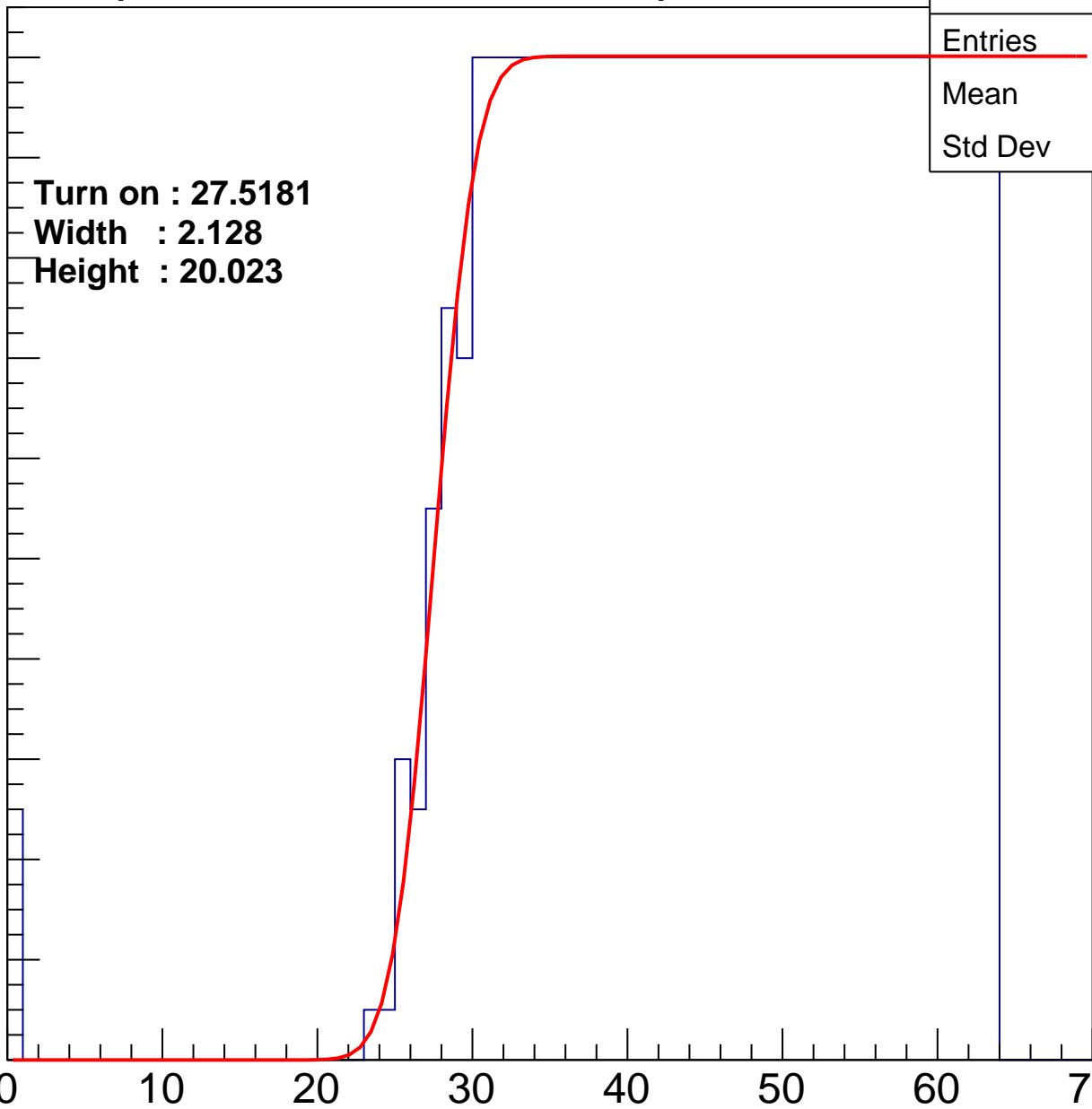
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.5181  
Width : 2.128  
Height : 20.023

|         |       |
|---------|-------|
| Entries | 738   |
| Mean    | 44.81 |
| Std Dev | 11.27 |

ampl





# B1L001S, U23-ch31

calib\_packv5\_042523\_0143.root, FC#2, port C2

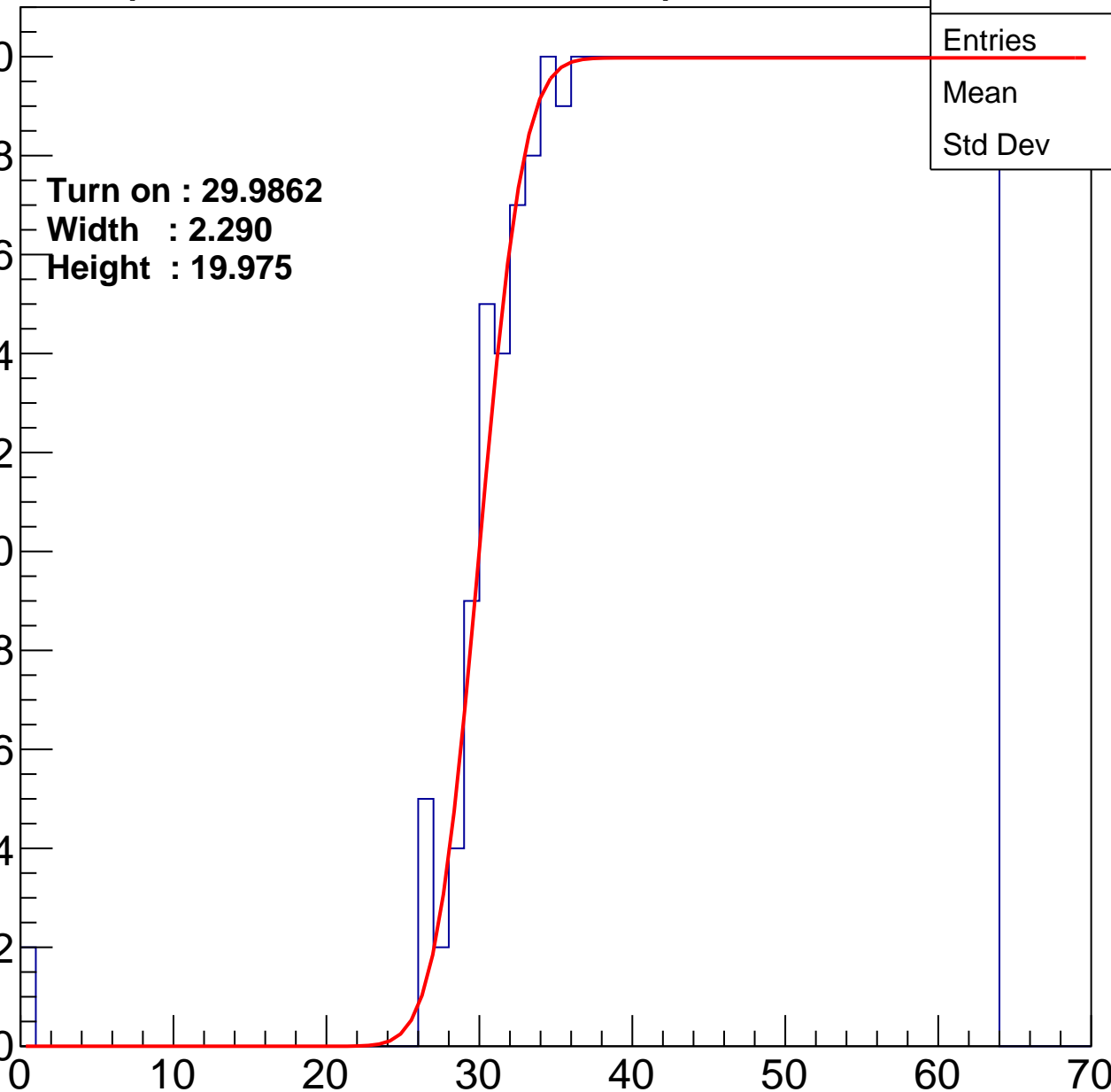
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.9862**  
**Width : 2.290**  
**Height : 19.975**

|         |       |
|---------|-------|
| Entries | 685   |
| Mean    | 46.2  |
| Std Dev | 10.31 |

ampl



# B1L001S, U23-ch32

calib\_packv5\_042523\_0143.root, FC#2, port C2

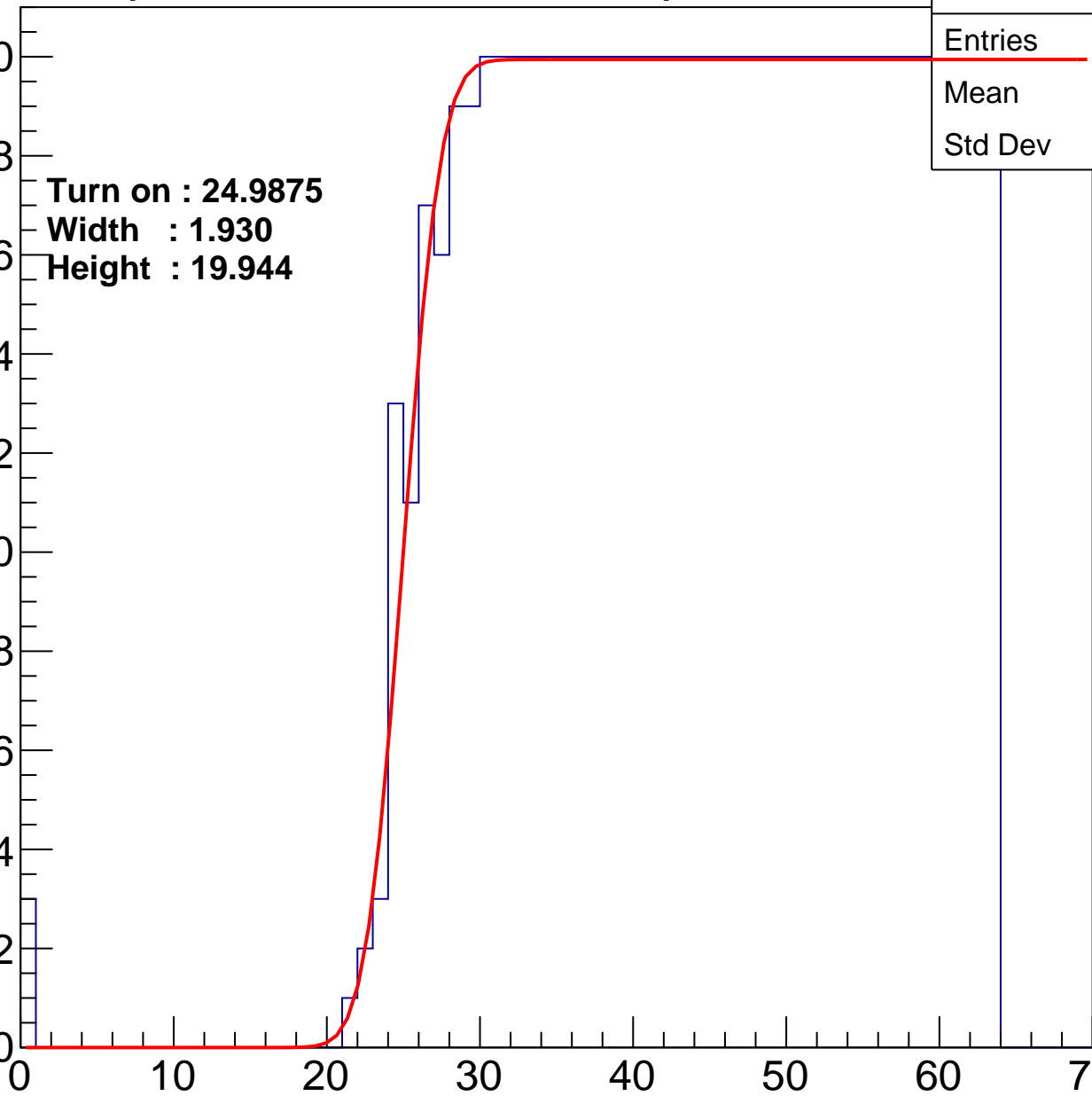
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 24.9875**  
**Width : 1.930**  
**Height : 19.944**

|         |       |
|---------|-------|
| Entries | 784   |
| Mean    | 43.75 |
| Std Dev | 11.67 |

ampl



# B1L001S, U23-ch33

calib\_packv5\_042523\_0143.root, FC#2, port C2

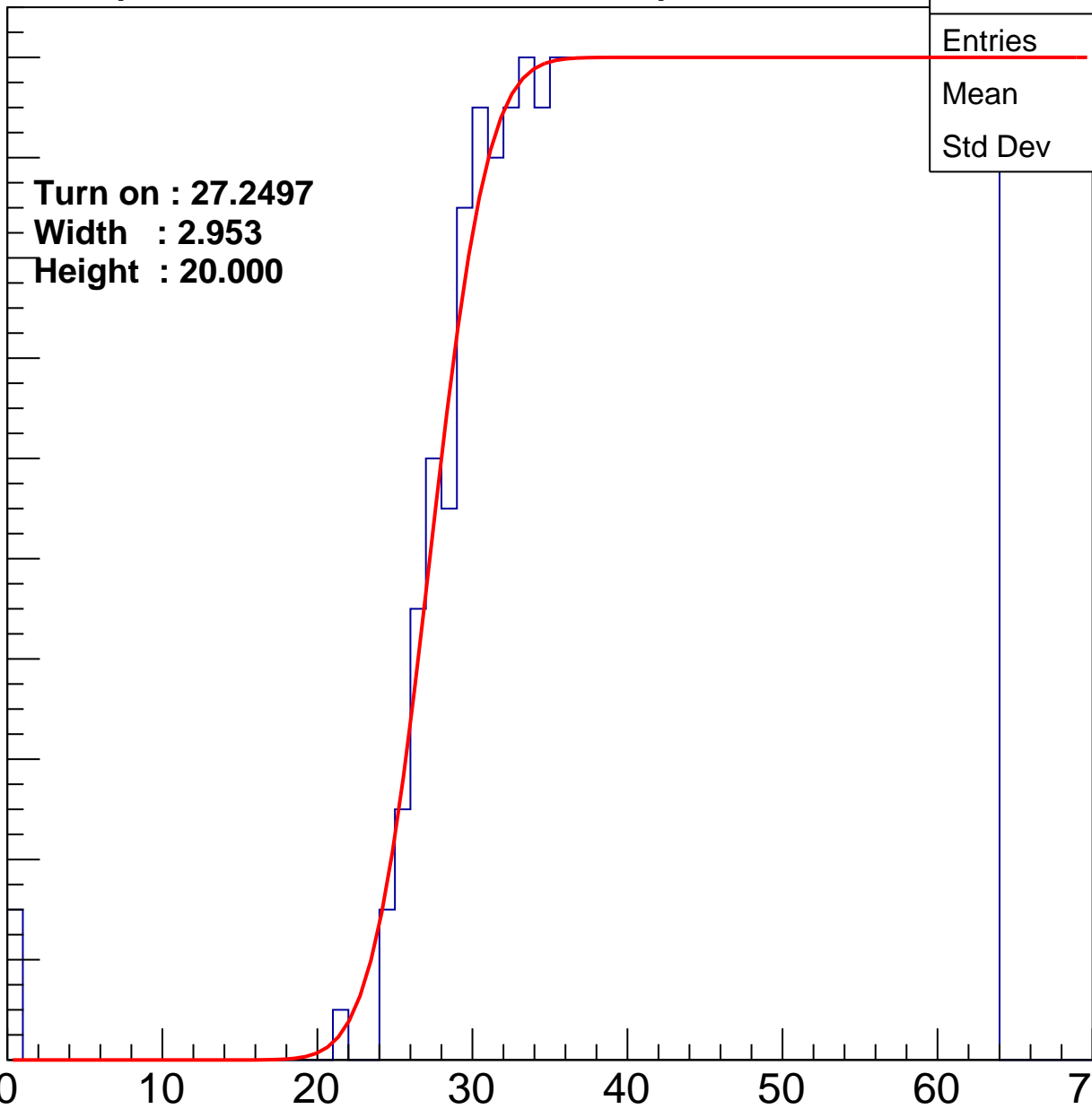
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.2497  
Width : 2.953  
Height : 20.000

|         |       |
|---------|-------|
| Entries | 736   |
| Mean    | 44.89 |
| Std Dev | 11.1  |

ampl



# B1L001S, U23-ch34

calib\_packv5\_042523\_0143.root, FC#2, port C2

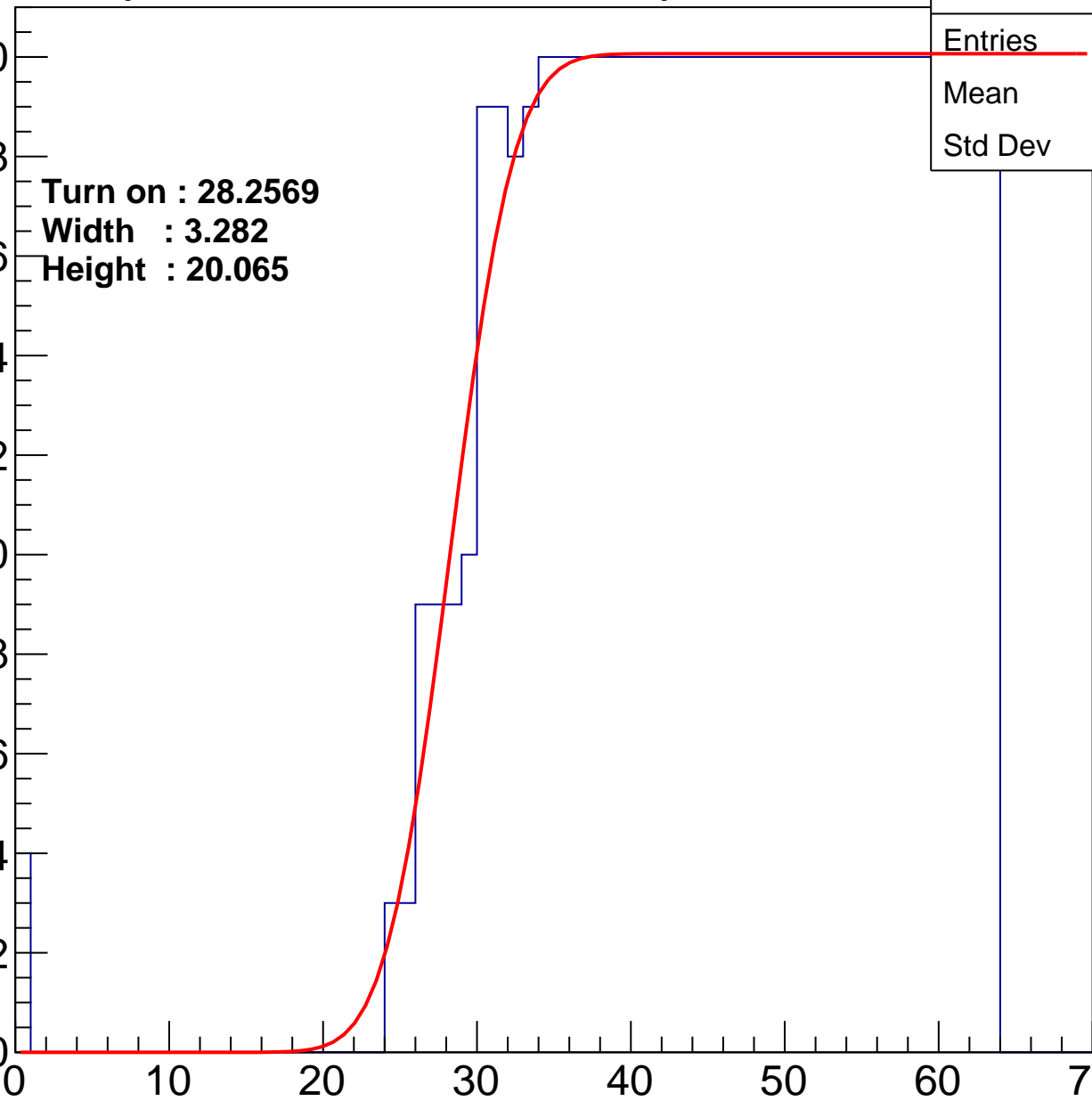
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.2569**  
**Width : 3.282**  
**Height : 20.065**

|         |       |
|---------|-------|
| Entries | 722   |
| Mean    | 45.19 |
| Std Dev | 11.04 |

ampl



# B1L001S, U23-ch35

calib\_packv5\_042523\_0143.root, FC#2, port C2

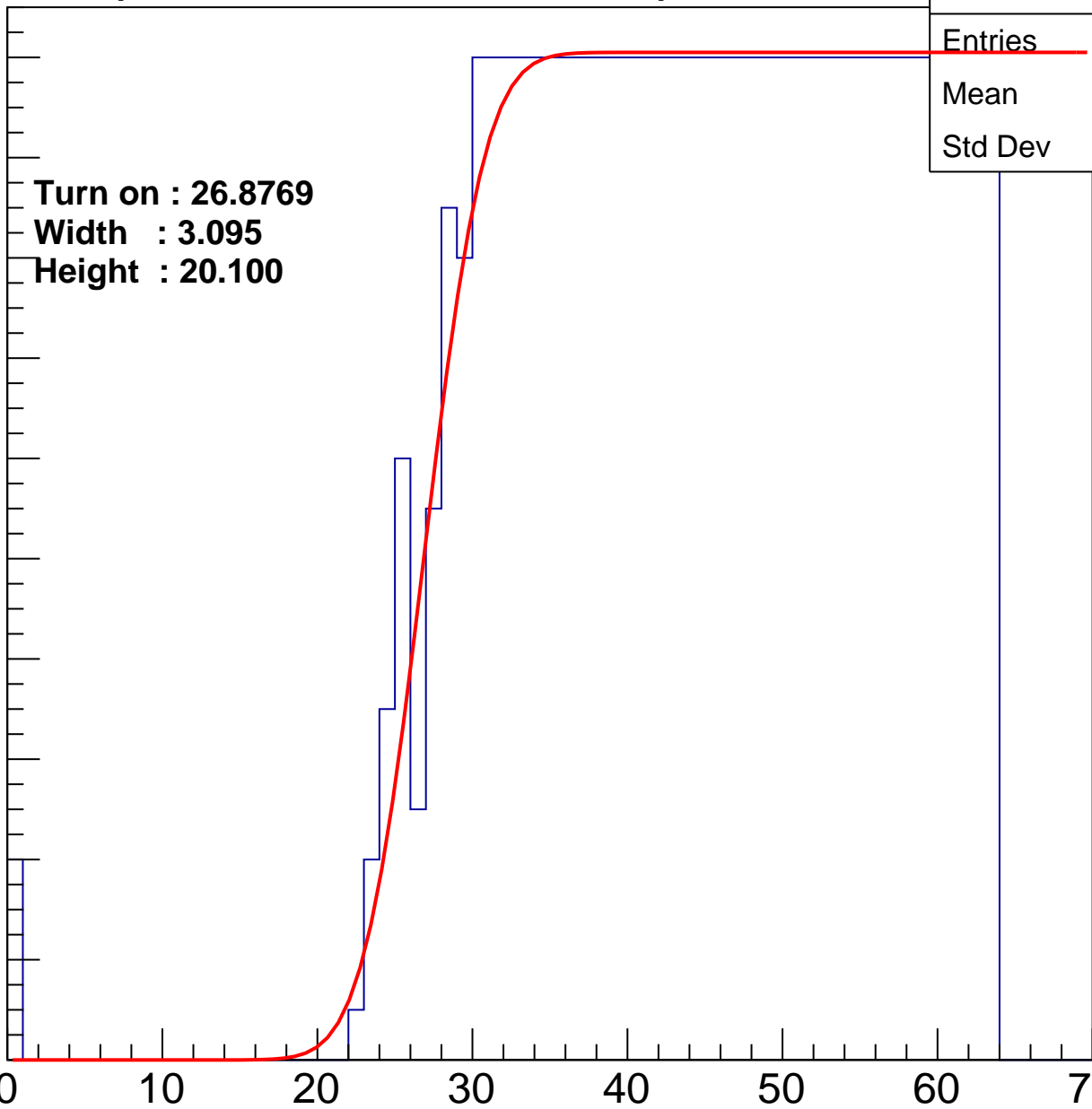
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.8769**  
**Width : 3.095**  
**Height : 20.100**

|         |       |
|---------|-------|
| Entries | 757   |
| Mean    | 44.34 |
| Std Dev | 11.46 |

ampl



# B1L001S, U23-ch36

calib\_packv5\_042523\_0143.root, FC#2, port C2

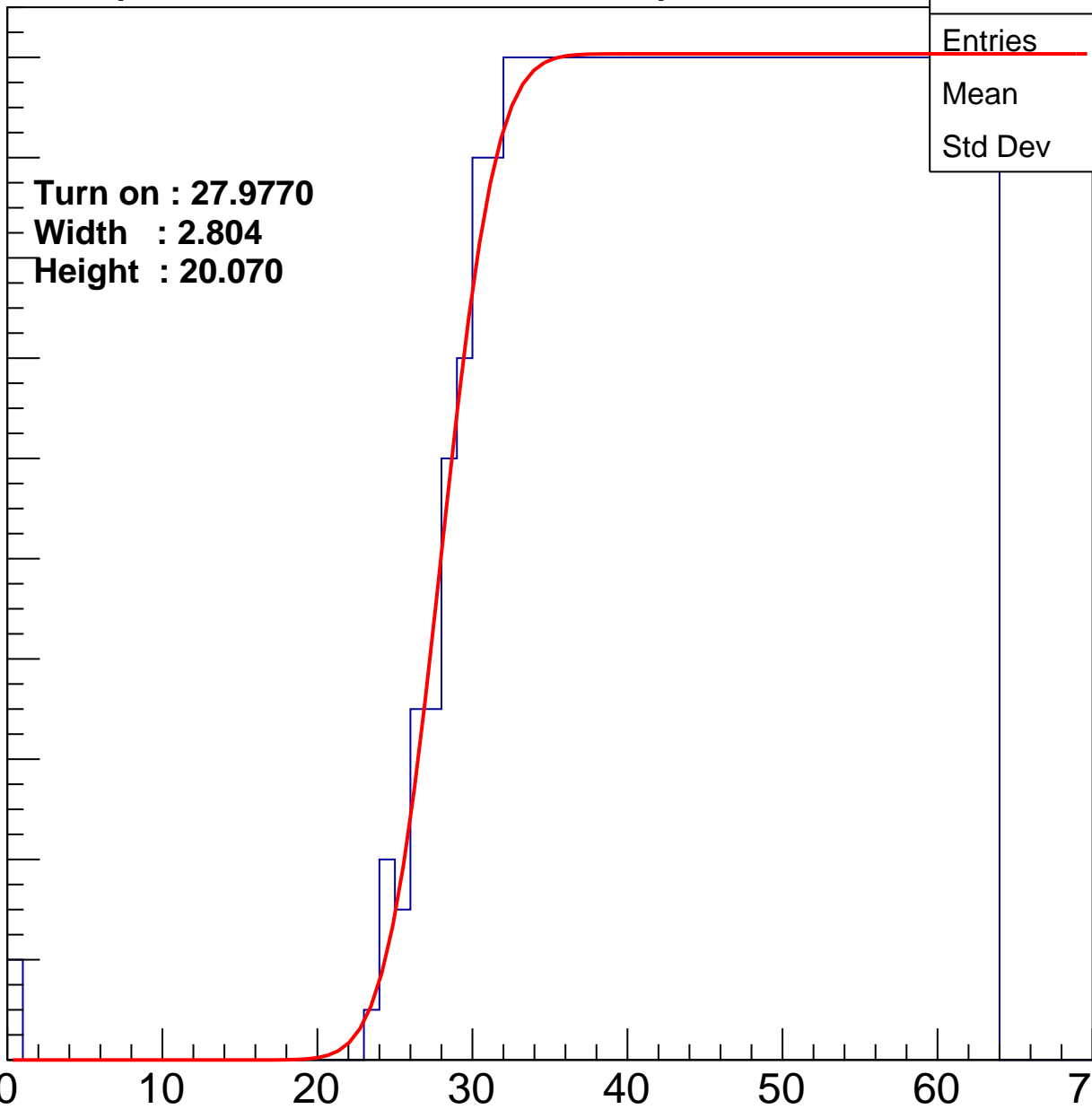
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.9770**  
**Width : 2.804**  
**Height : 20.070**

|         |       |
|---------|-------|
| Entries | 726   |
| Mean    | 45.19 |
| Std Dev | 10.85 |

ampl



# B1L001S, U23-ch37

calib\_packv5\_042523\_0143.root, FC#2, port C2

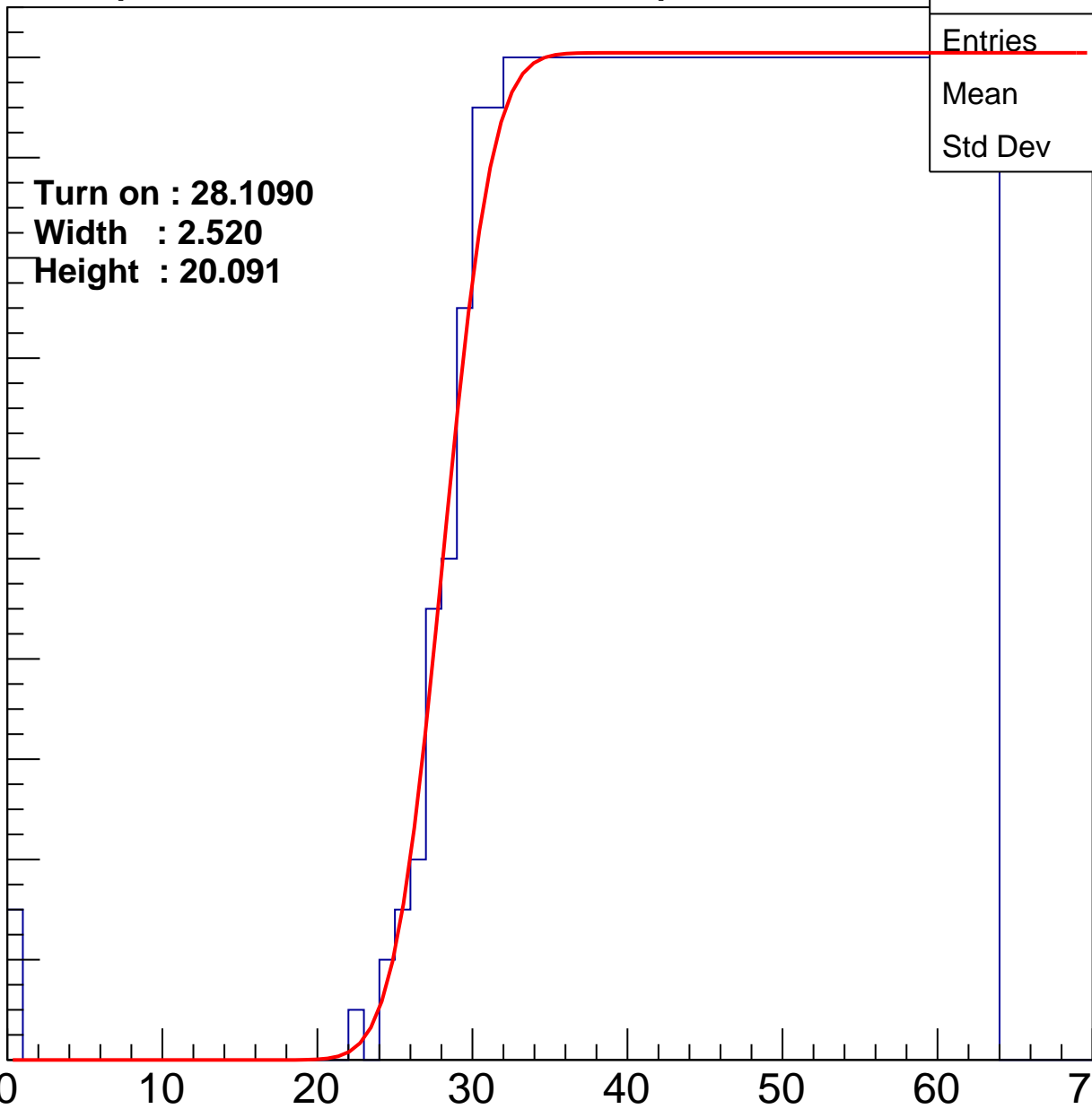
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.1090**  
**Width : 2.520**  
**Height : 20.091**

|         |       |
|---------|-------|
| Entries | 725   |
| Mean    | 45.19 |
| Std Dev | 10.91 |

ampl



# B1L001S, U23-ch38

calib\_packv5\_042523\_0143.root, FC#2, port C2

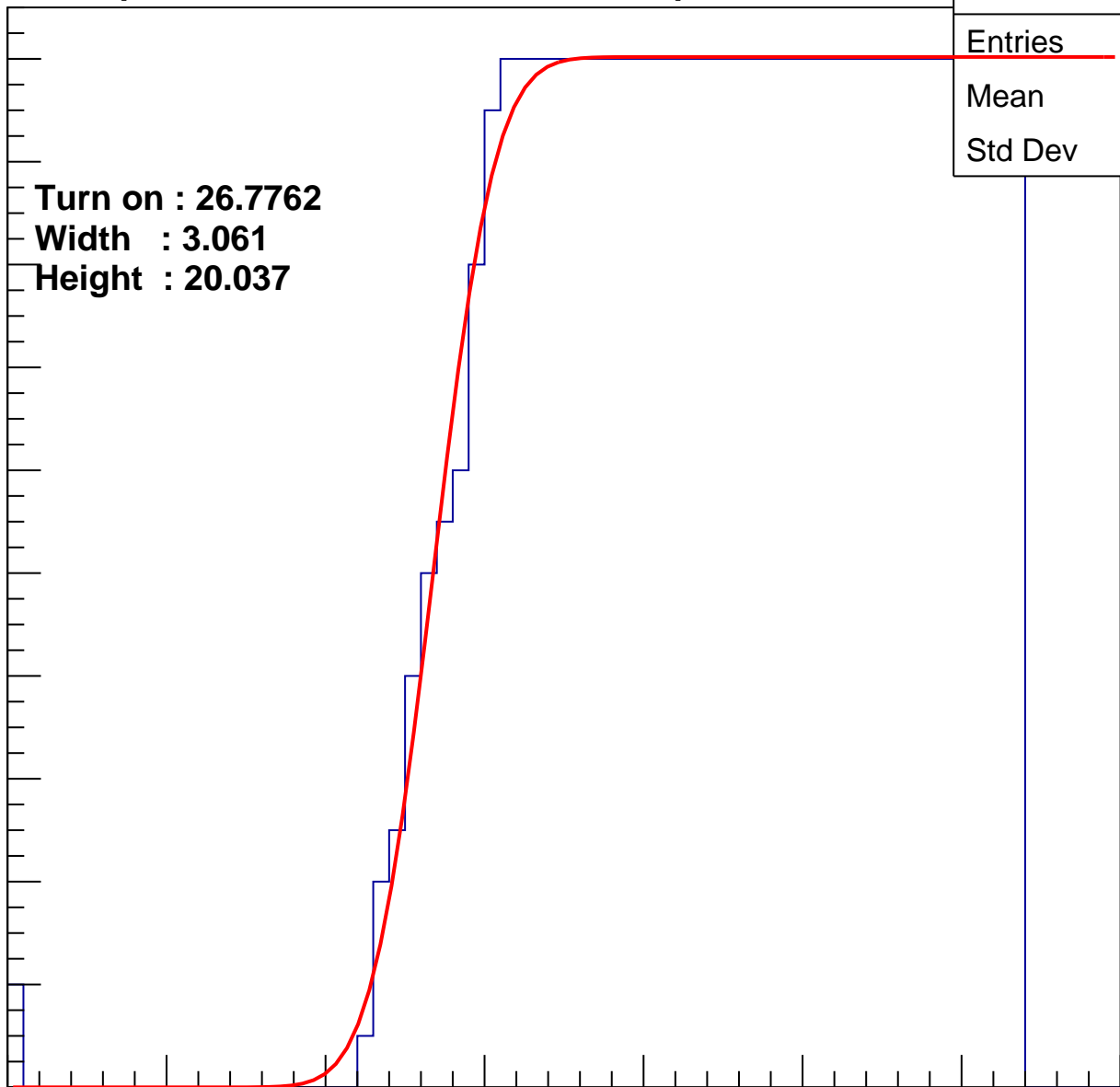
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.7762**  
**Width : 3.061**  
**Height : 20.037**

|         |       |
|---------|-------|
| Entries | 748   |
| Mean    | 44.63 |
| Std Dev | 11.17 |

ampl





# B1L001S, U23-ch39

calib\_packv5\_042523\_0143.root, FC#2, port C2

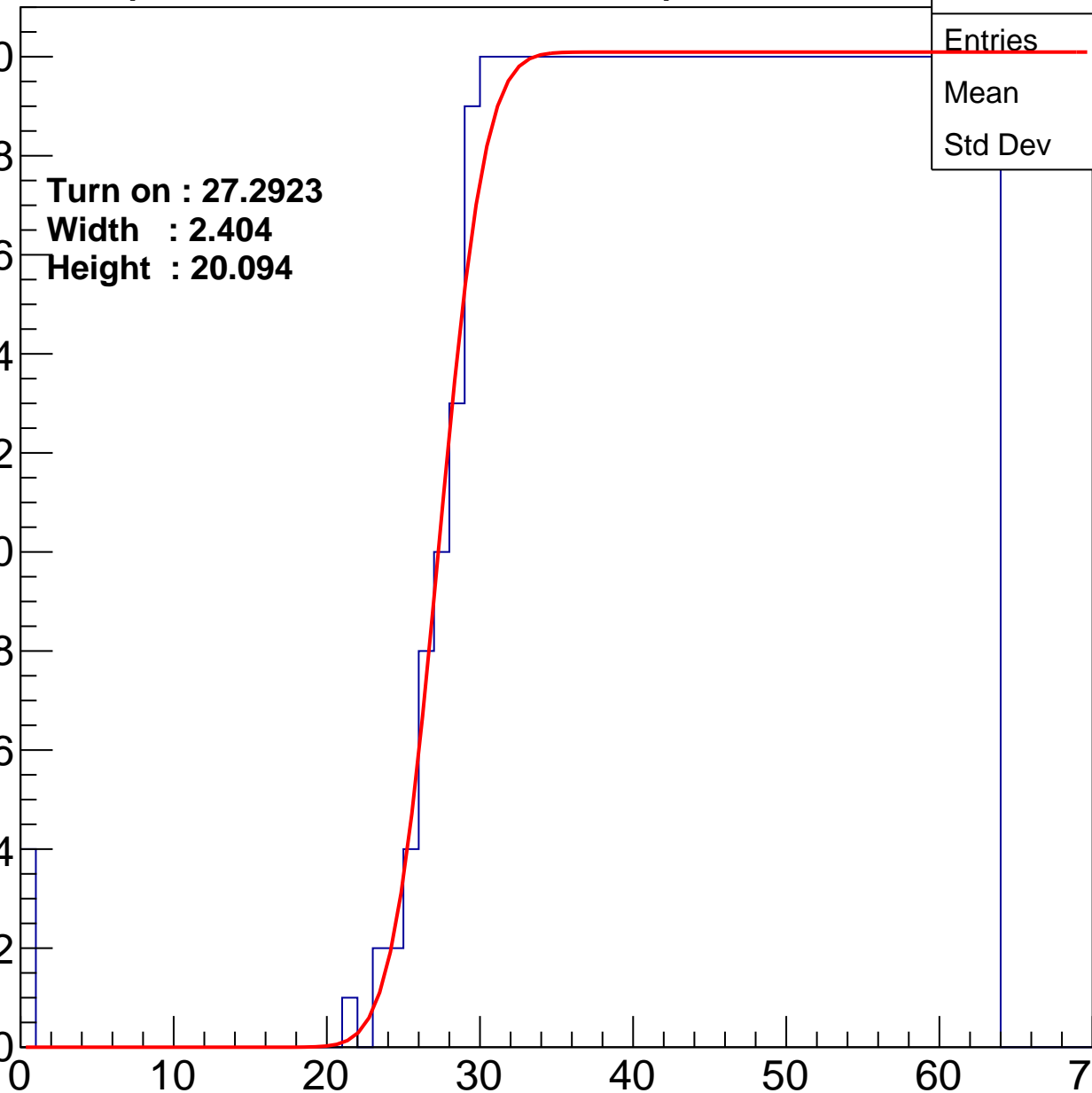
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2923**  
**Width : 2.404**  
**Height : 20.094**

|         |       |
|---------|-------|
| Entries | 743   |
| Mean    | 44.72 |
| Std Dev | 11.24 |

ampl



# B1L001S, U23-ch40

calib\_packv5\_042523\_0143.root, FC#2, port C2

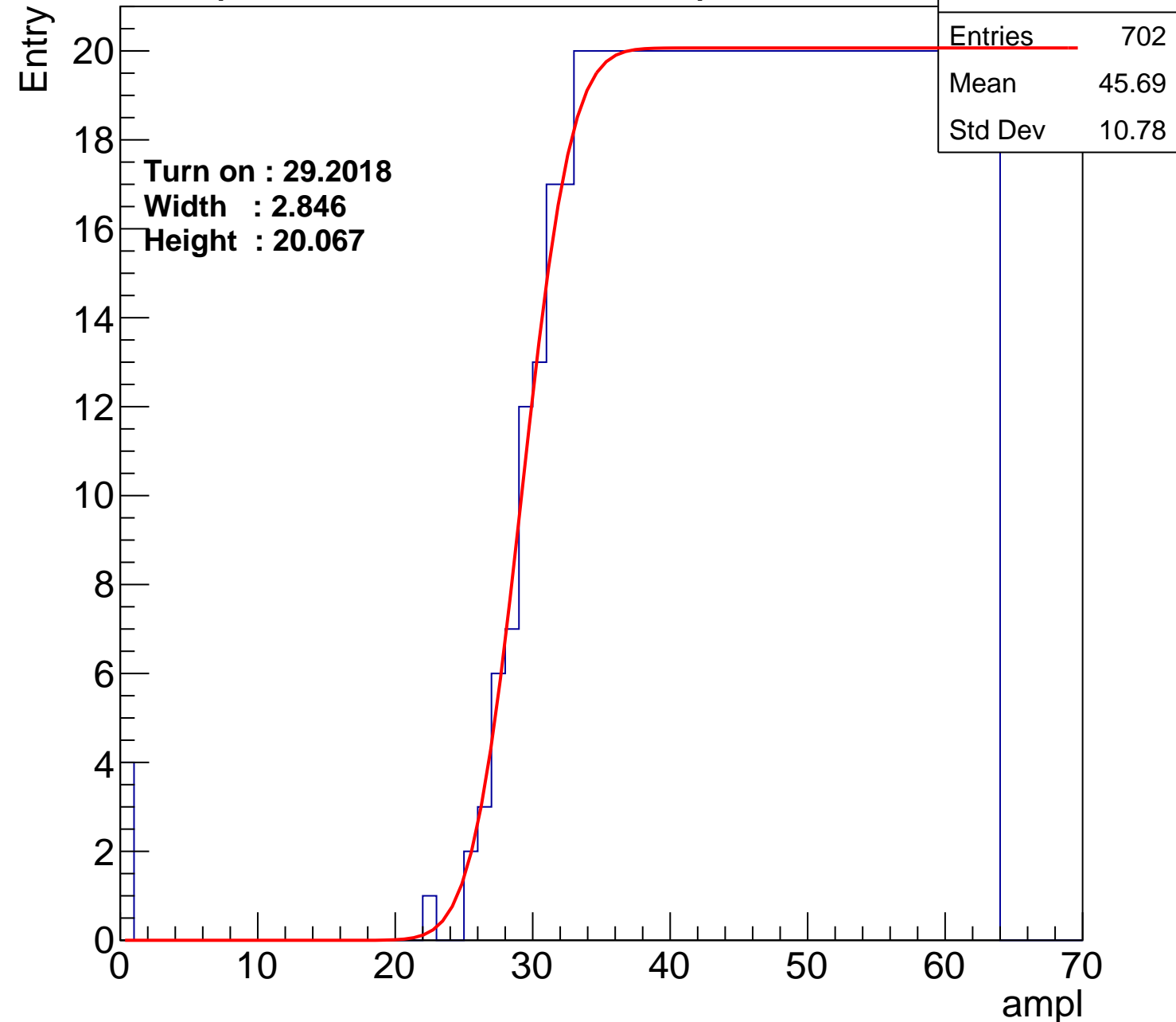
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.2018**  
**Width : 2.846**  
**Height : 20.067**

|         |       |
|---------|-------|
| Entries | 702   |
| Mean    | 45.69 |
| Std Dev | 10.78 |

ampl



# B1L001S, U23-ch41

calib\_packv5\_042523\_0143.root, FC#2, port C2

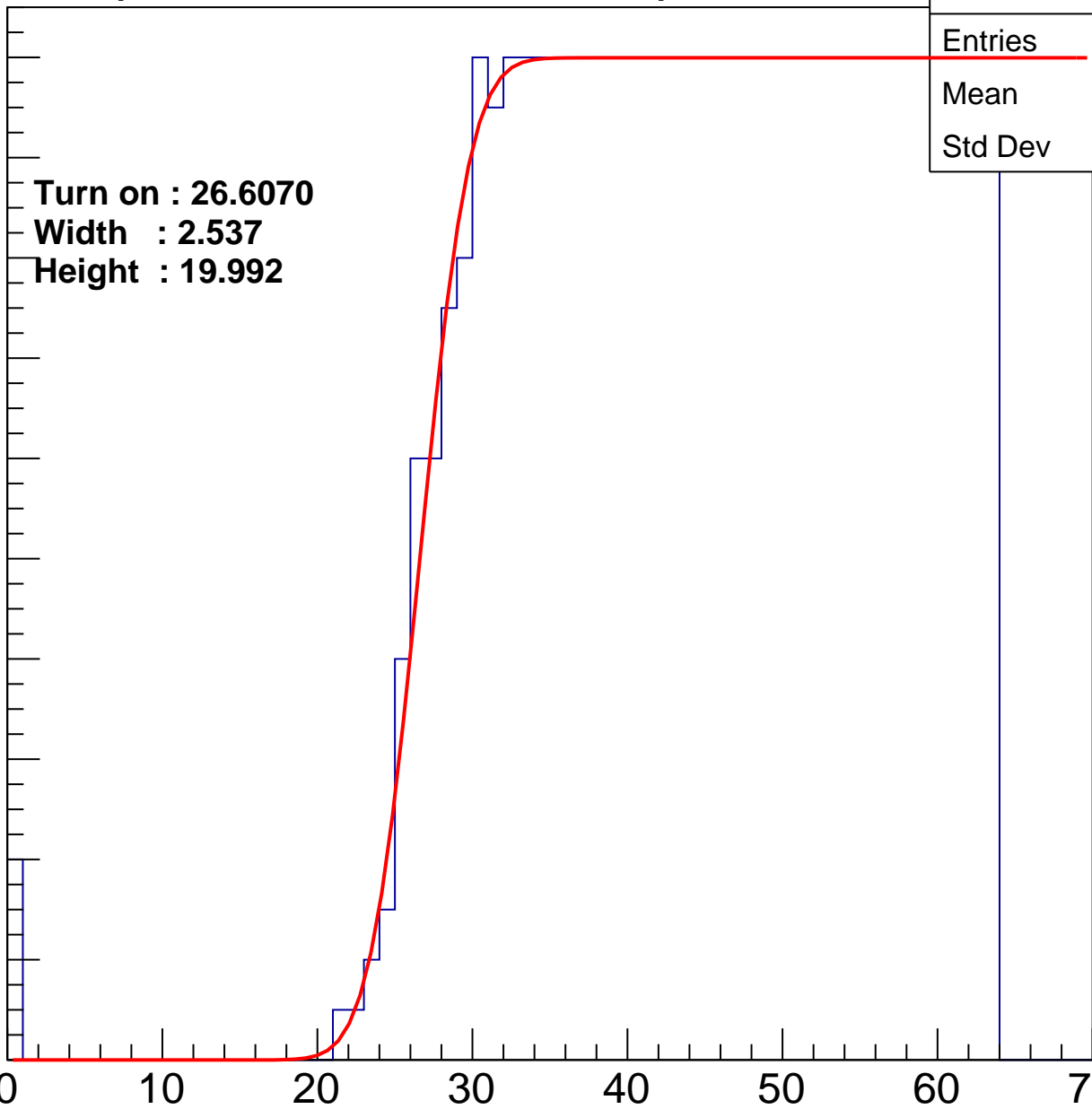
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.6070**  
**Width : 2.537**  
**Height : 19.992**

|         |       |
|---------|-------|
| Entries | 753   |
| Mean    | 44.45 |
| Std Dev | 11.4  |

ampl



# B1L001S, U23-ch42

calib\_packv5\_042523\_0143.root, FC#2, port C2

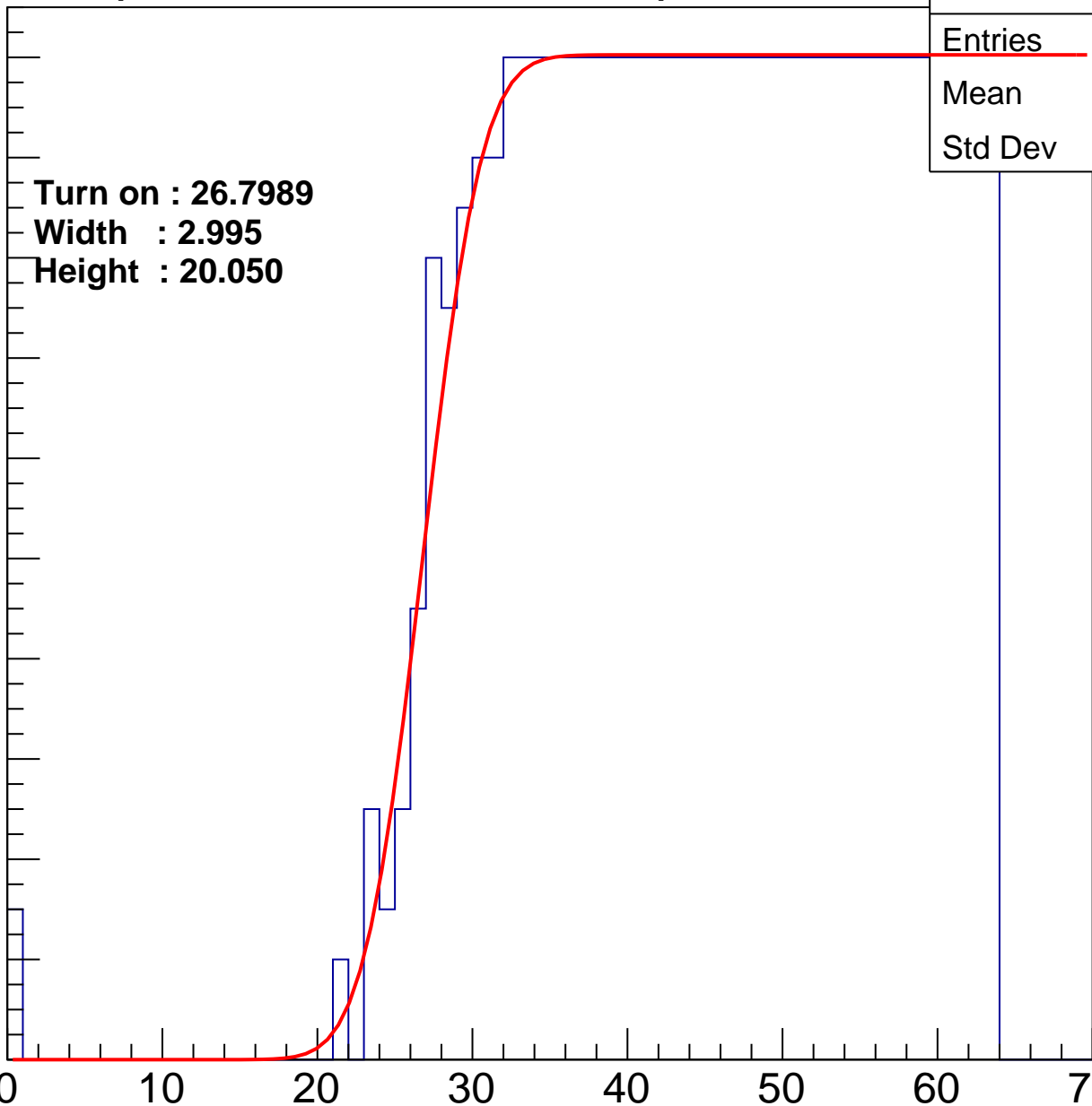
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.7989**  
**Width : 2.995**  
**Height : 20.050**

|         |       |
|---------|-------|
| Entries | 751   |
| Mean    | 44.52 |
| Std Dev | 11.31 |

ampl



# B1L001S, U23-ch43

calib\_packv5\_042523\_0143.root, FC#2, port C2

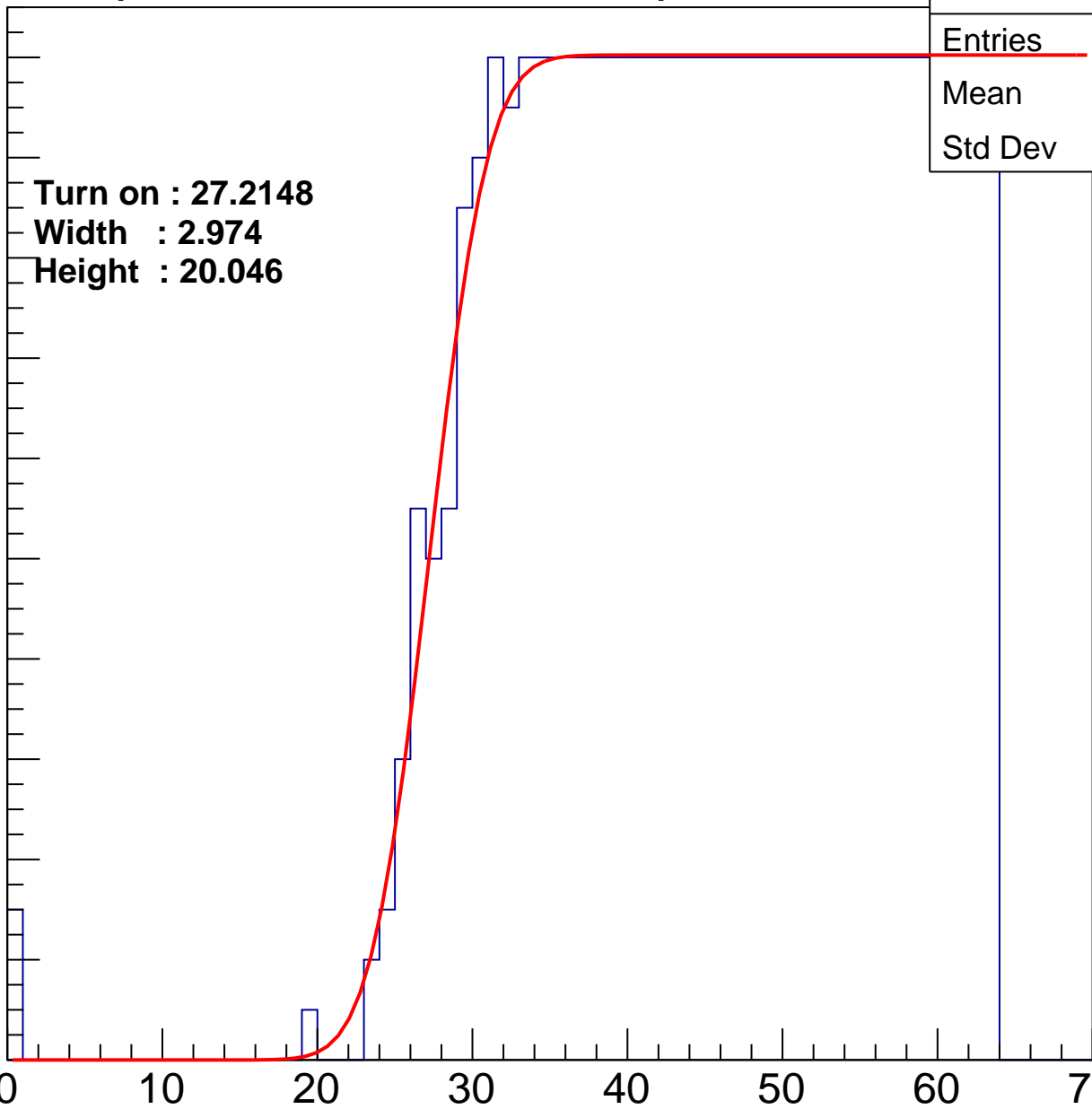
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2148**  
**Width : 2.974**  
**Height : 20.046**

|         |       |
|---------|-------|
| Entries | 741   |
| Mean    | 44.77 |
| Std Dev | 11.17 |

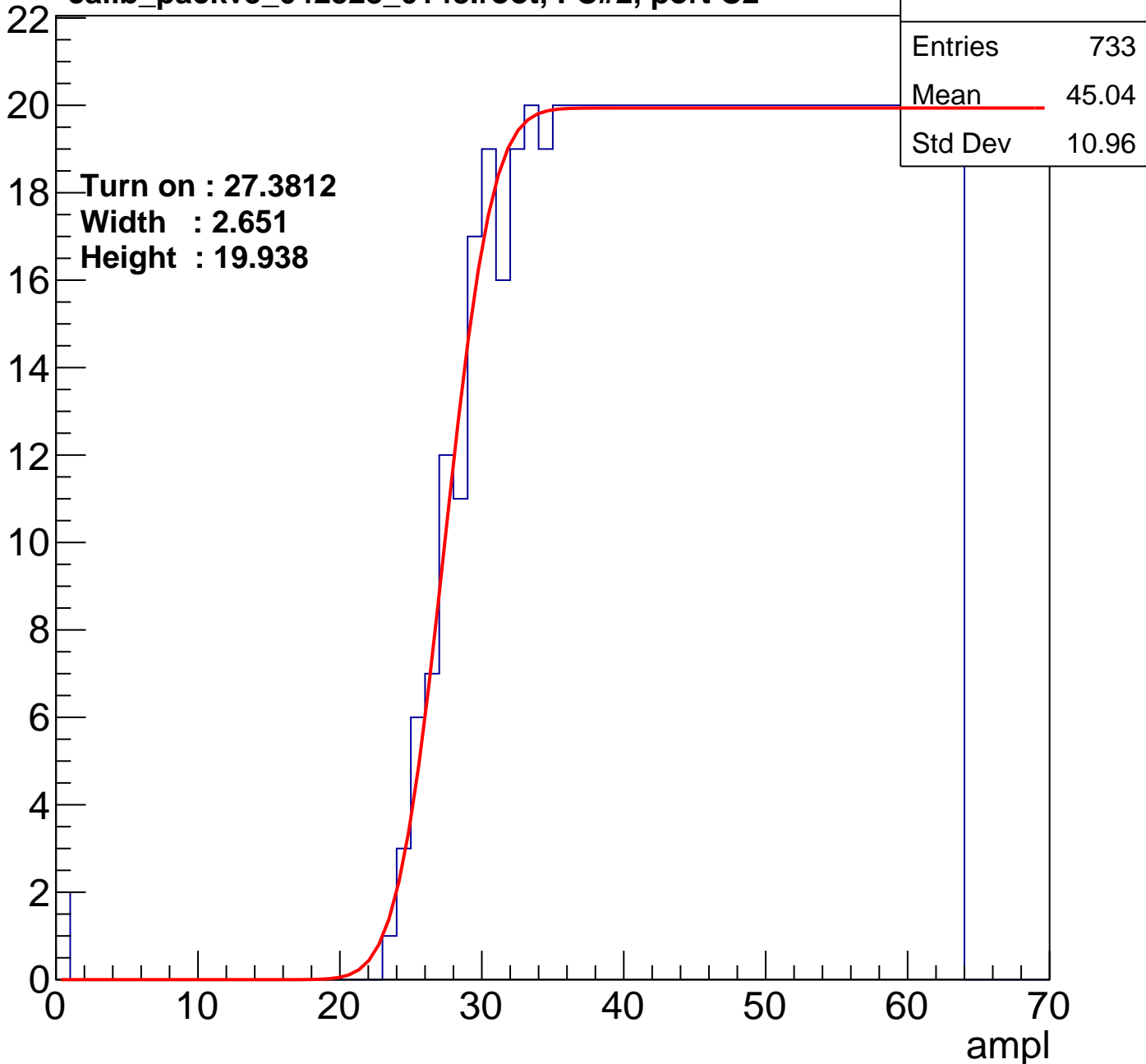
ampl



# B1L001S, U23-ch44

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry



# B1L001S, U23-ch45

calib\_packv5\_042523\_0143.root, FC#2, port C2

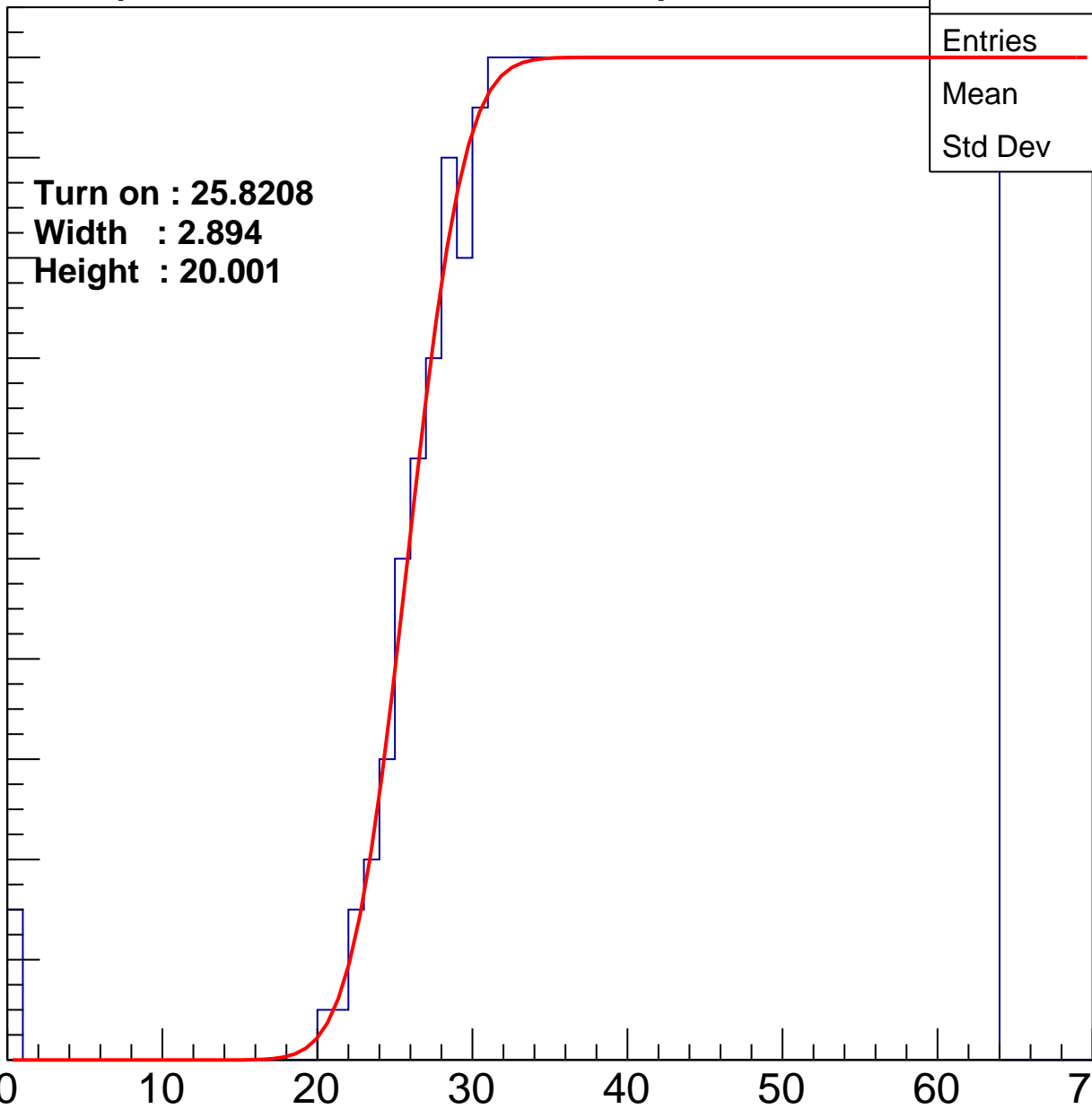
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.8208  
Width : 2.894  
Height : 20.001

|         |       |
|---------|-------|
| Entries | 767   |
| Mean    | 44.12 |
| Std Dev | 11.52 |

ampl



# B1L001S, U23-ch46

calib\_packv5\_042523\_0143.root, FC#2, port C2

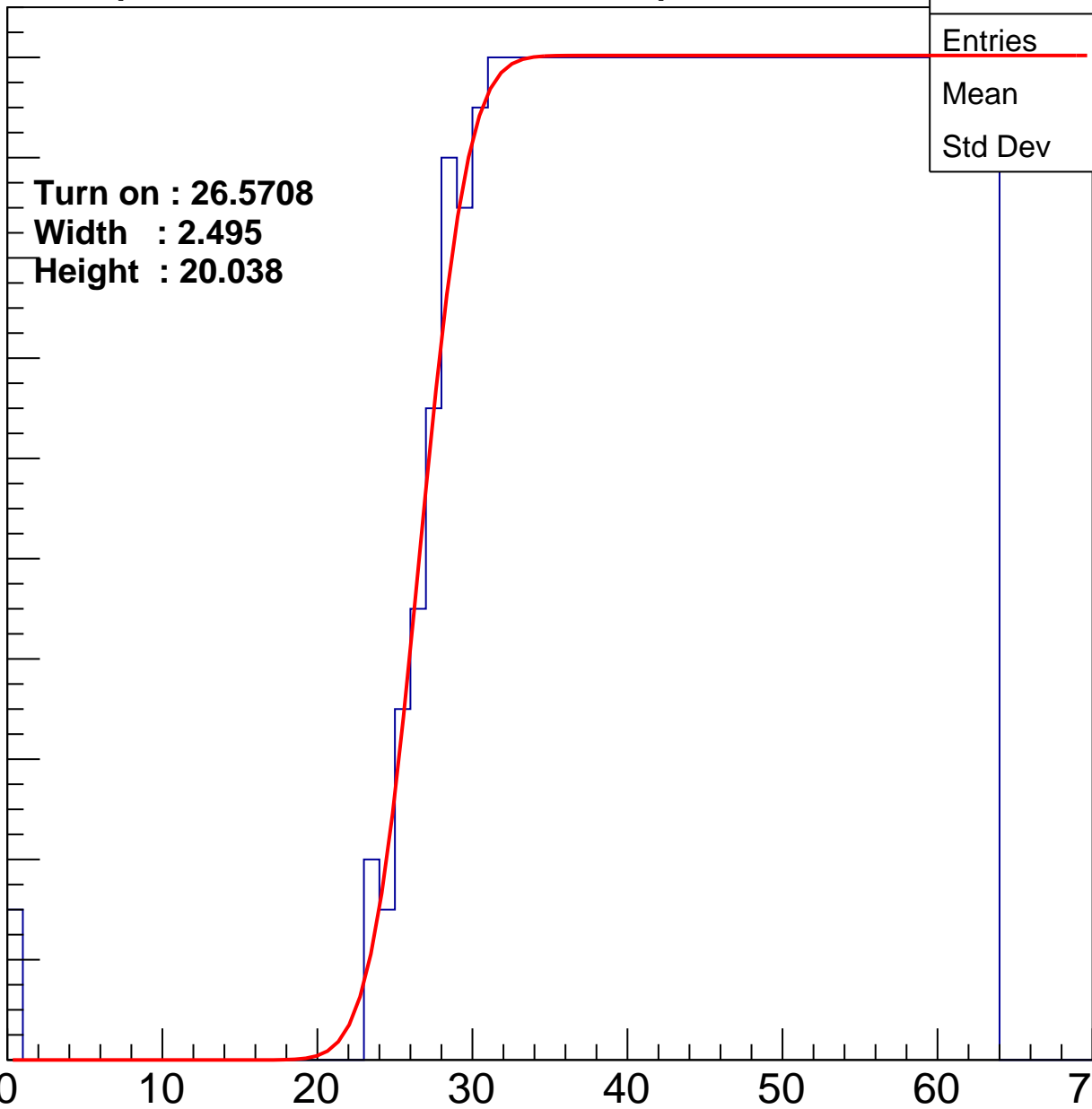
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.5708**  
**Width : 2.495**  
**Height : 20.038**

|         |       |
|---------|-------|
| Entries | 753   |
| Mean    | 44.5  |
| Std Dev | 11.27 |

ampl





# B1L001S, U23-ch47

calib\_packv5\_042523\_0143.root, FC#2, port C2

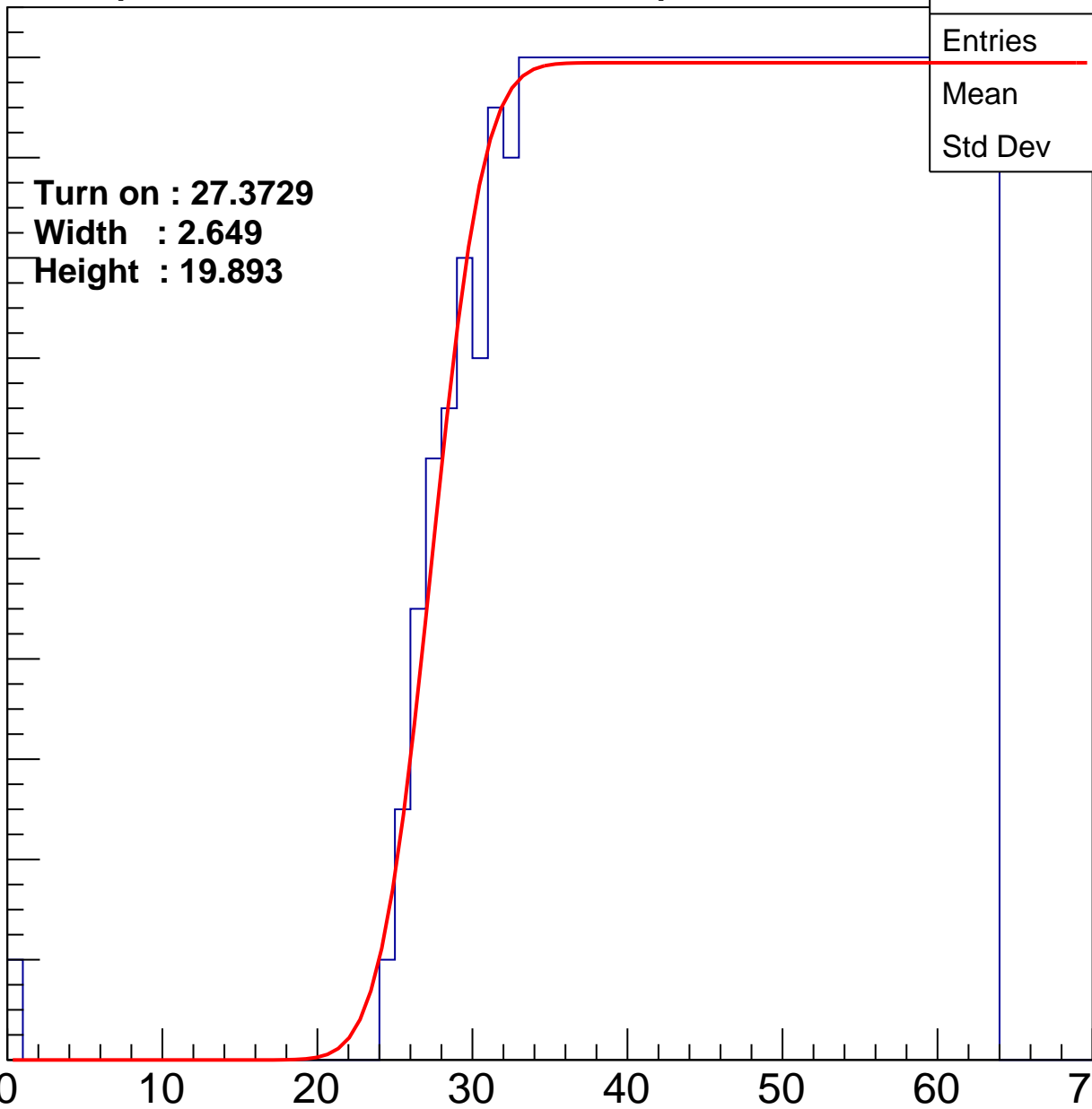
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.3729**  
**Width : 2.649**  
**Height : 19.893**

|         |       |
|---------|-------|
| Entries | 730   |
| Mean    | 45.07 |
| Std Dev | 10.92 |

ampl



# B1L001S, U23-ch48

calib\_packv5\_042523\_0143.root, FC#2, port C2

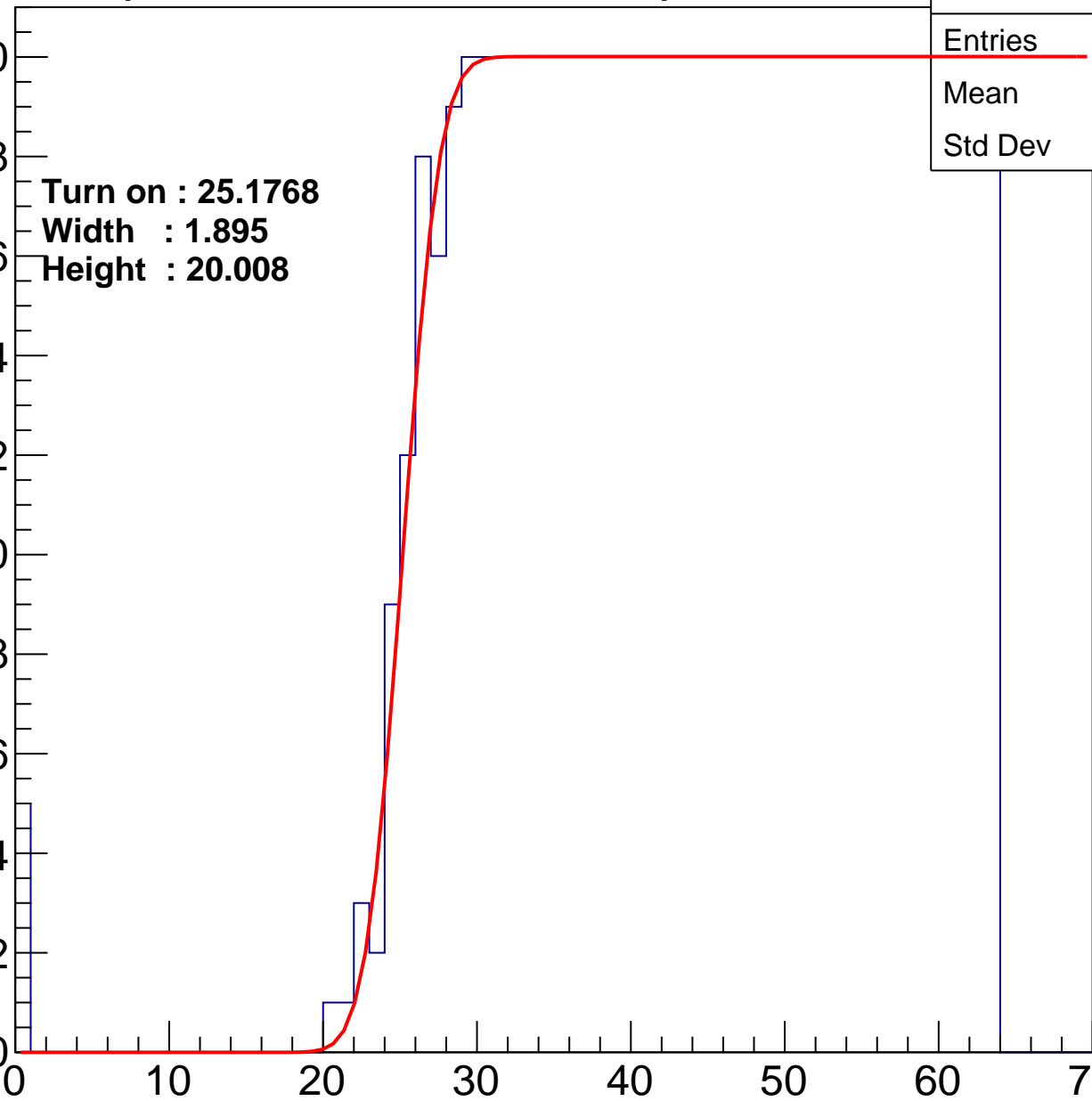
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 25.1768  
Width : 1.895  
Height : 20.008

|         |       |
|---------|-------|
| Entries | 786   |
| Mean    | 43.64 |
| Std Dev | 11.86 |

ampl



# B1L001S, U23-ch49

calib\_packv5\_042523\_0143.root, FC#2, port C2

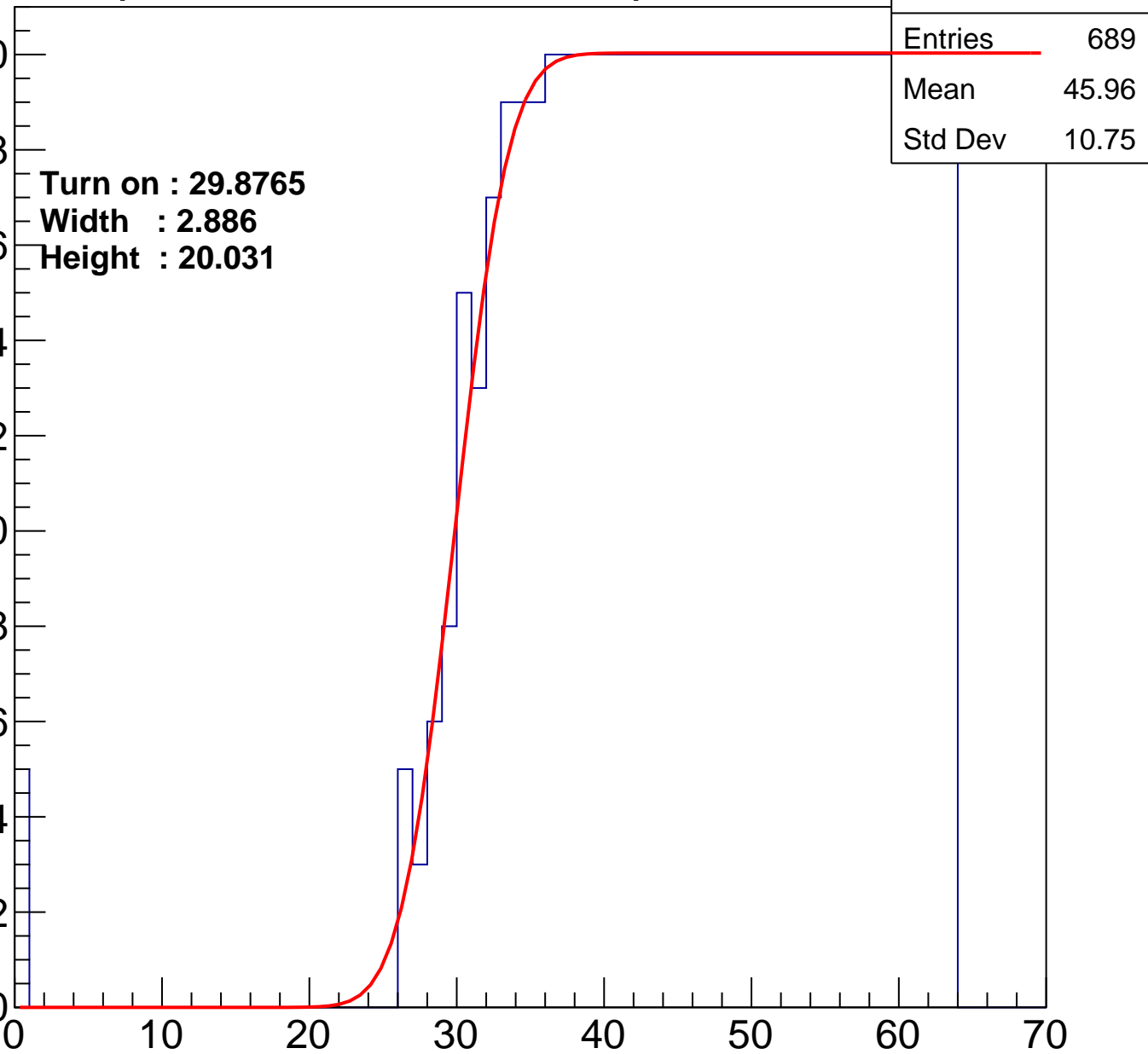
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.8765**  
**Width : 2.886**  
**Height : 20.031**

|         |       |
|---------|-------|
| Entries | 689   |
| Mean    | 45.96 |
| Std Dev | 10.75 |

ampl



# B1L001S, U23-ch50

calib\_packv5\_042523\_0143.root, FC#2, port C2

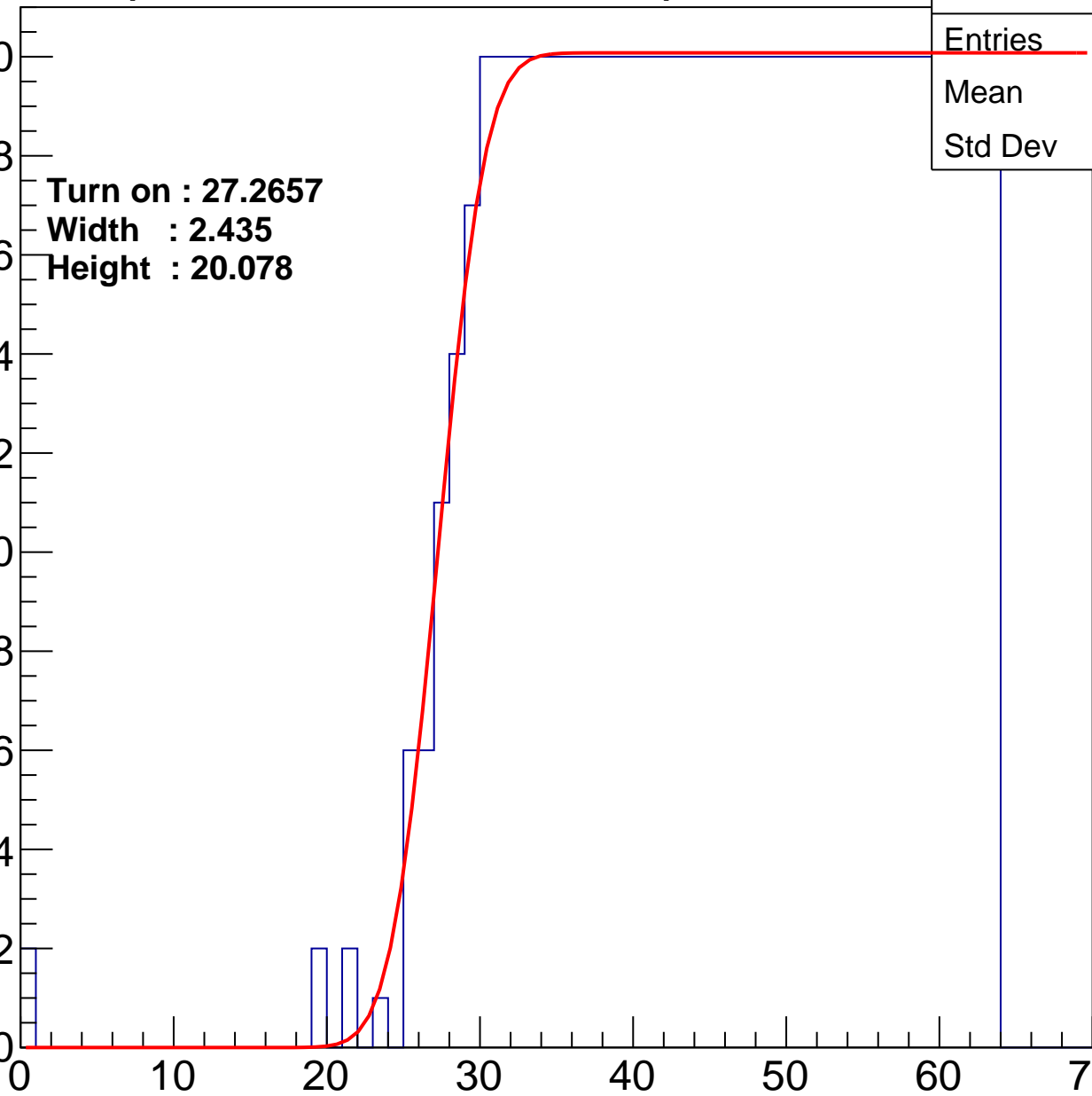
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2657**  
**Width : 2.435**  
**Height : 20.078**

|         |       |
|---------|-------|
| Entries | 741   |
| Mean    | 44.82 |
| Std Dev | 11.05 |

ampl



# B1L001S, U23-ch51

calib\_packv5\_042523\_0143.root, FC#2, port C2

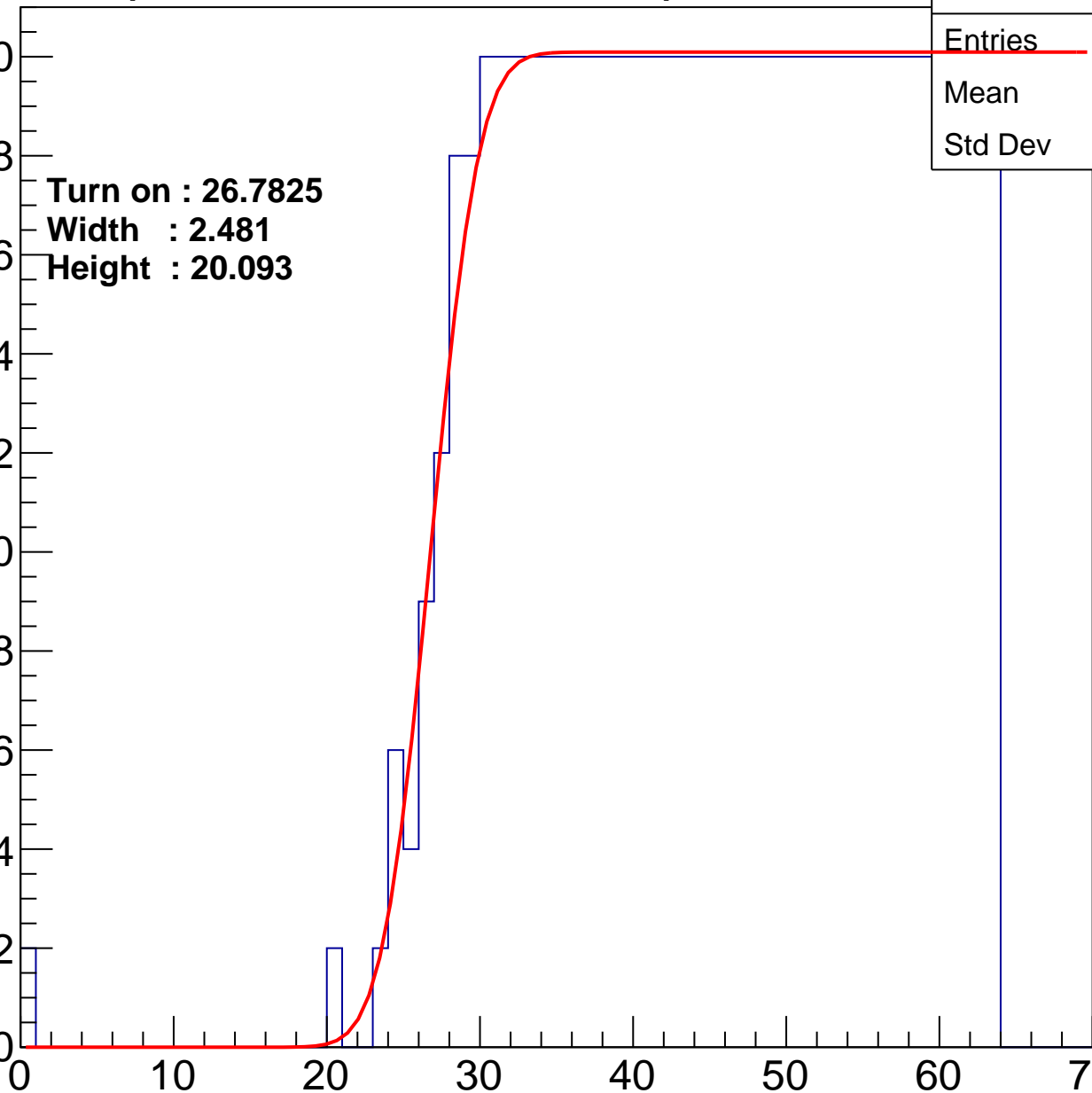
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.7825  
Width : 2.481  
Height : 20.093

|         |       |
|---------|-------|
| Entries | 753   |
| Mean    | 44.53 |
| Std Dev | 11.19 |

ampl



# B1L001S, U23-ch52

calib\_packv5\_042523\_0143.root, FC#2, port C2

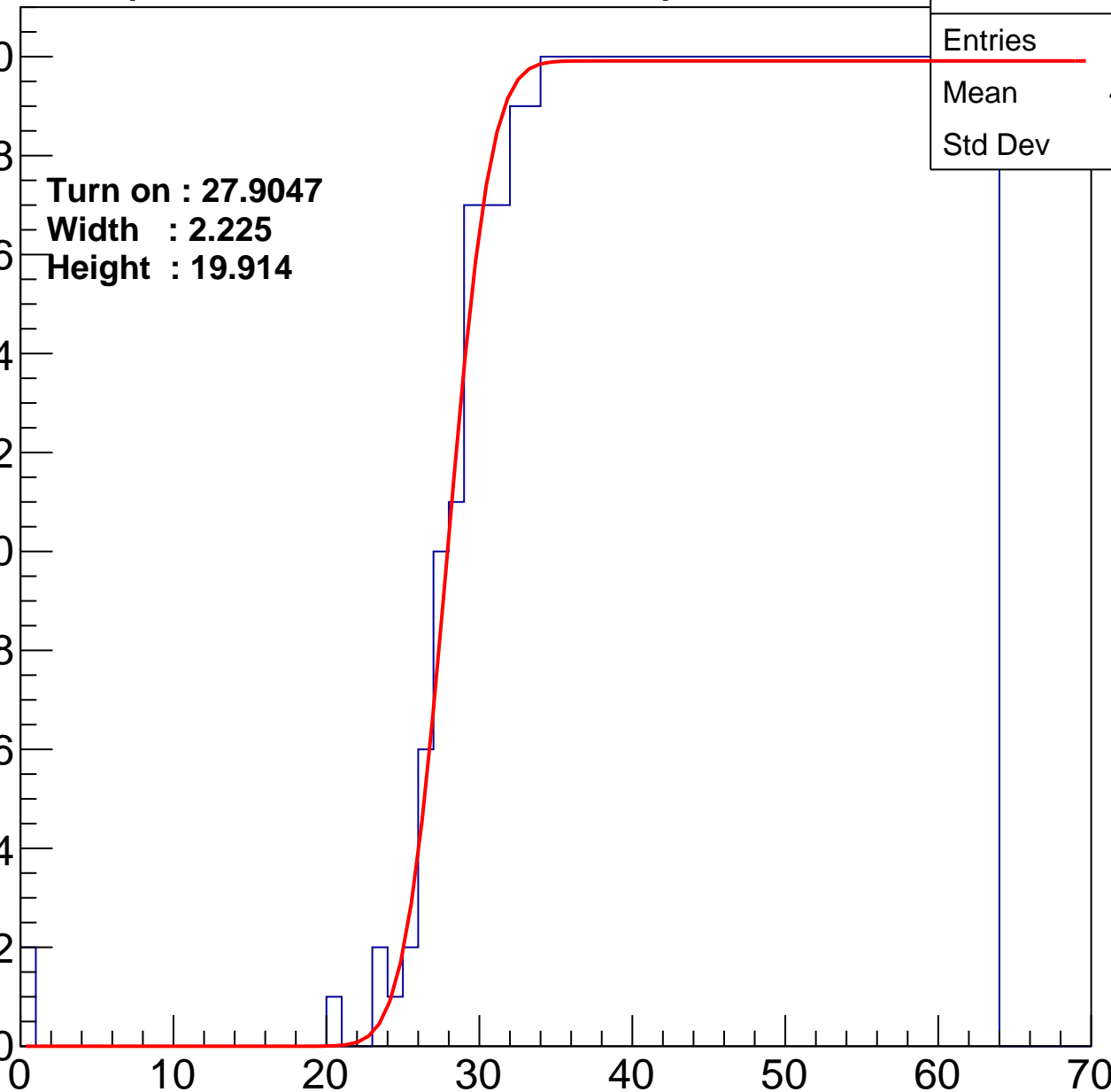
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.9047**  
**Width : 2.225**  
**Height : 19.914**

|         |       |
|---------|-------|
| Entries | 724   |
| Mean    | 45.22 |
| Std Dev | 10.85 |

ampl



# B1L001S, U23-ch53

calib\_packv5\_042523\_0143.root, FC#2, port C2

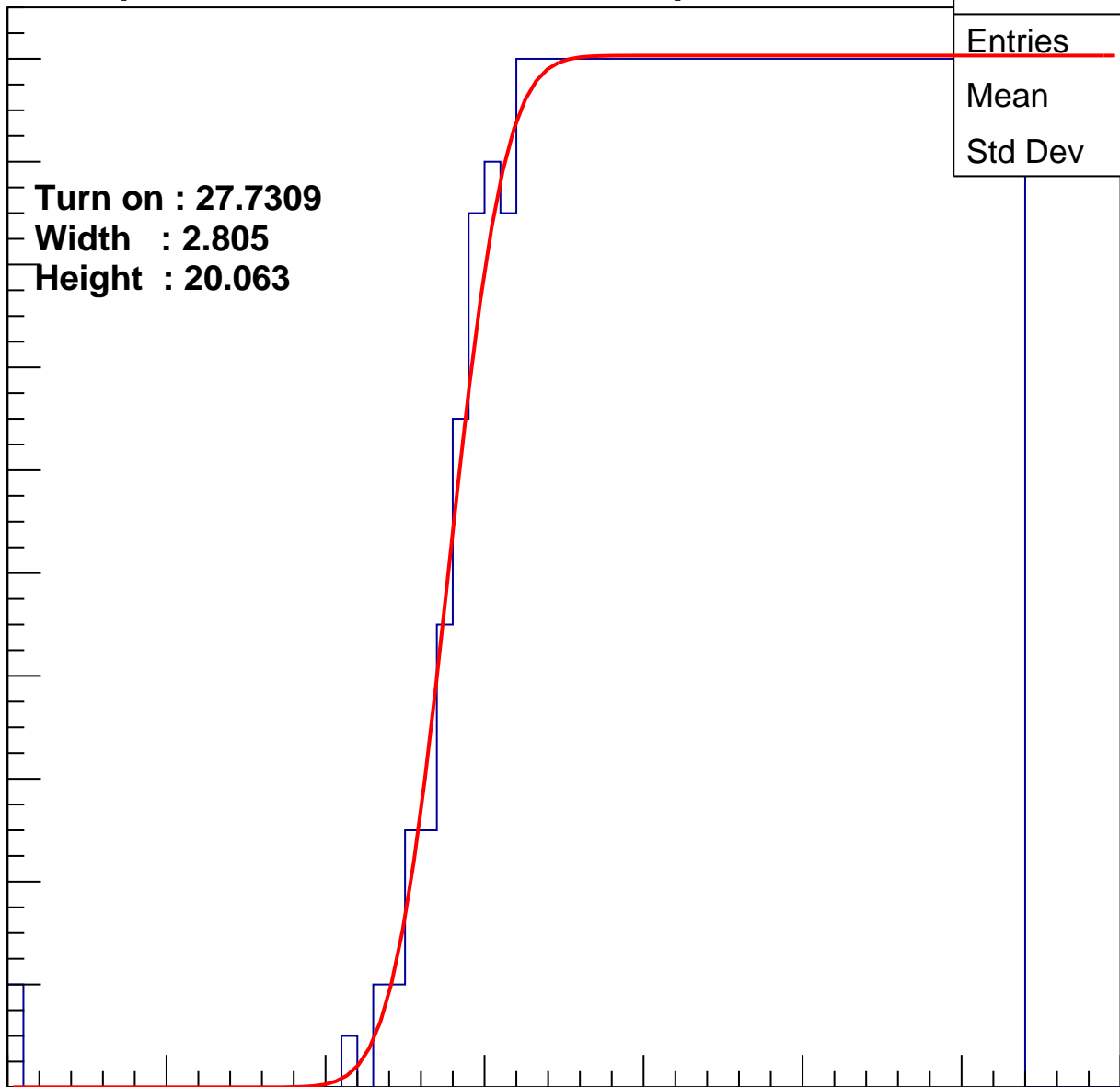
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.7309  
Width : 2.805  
Height : 20.063

|         |       |
|---------|-------|
| Entries | 731   |
| Mean    | 45.06 |
| Std Dev | 10.92 |

ampl



# B1L001S, U23-ch54

calib\_packv5\_042523\_0143.root, FC#2, port C2

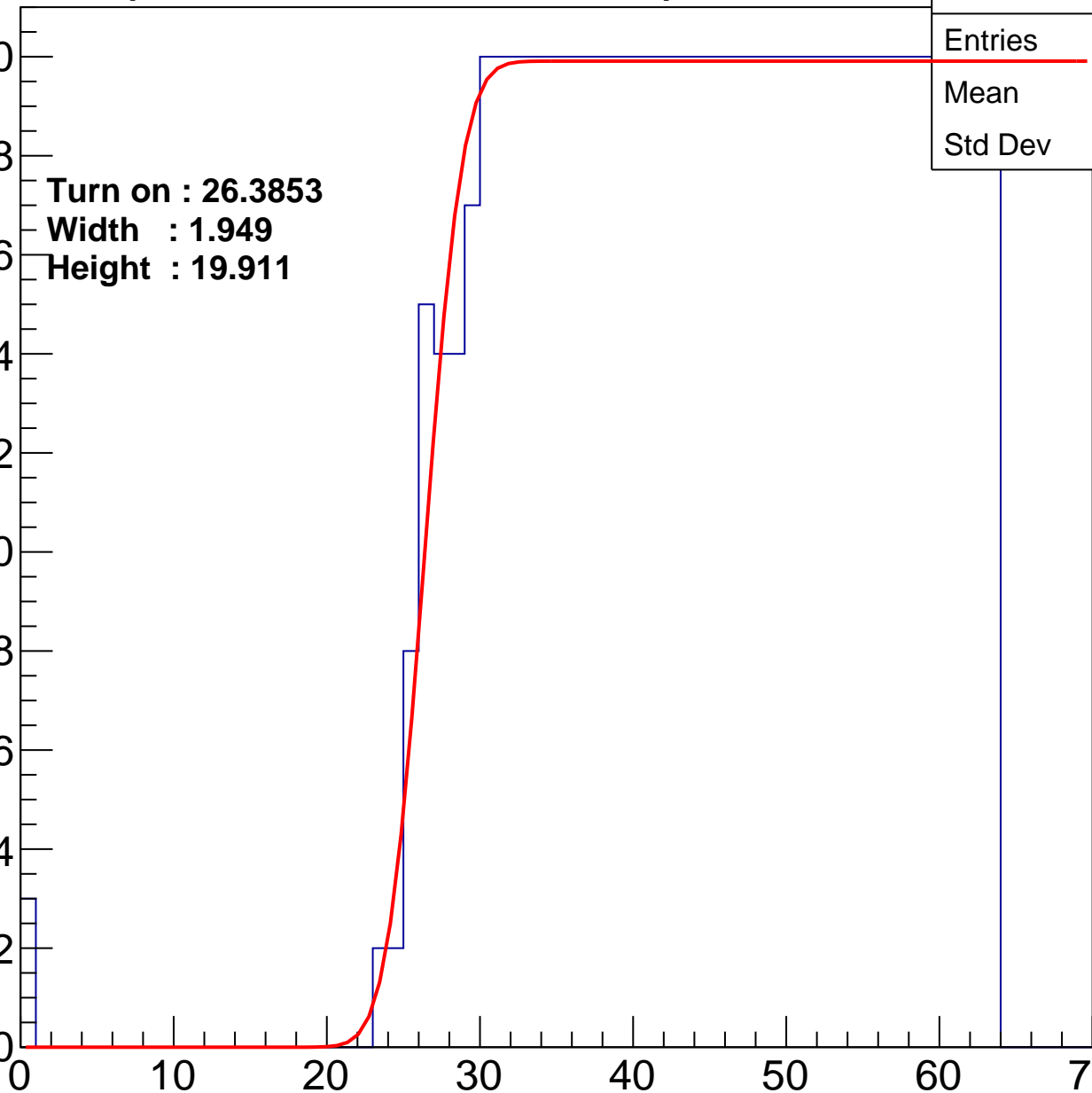
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.3853**  
**Width : 1.949**  
**Height : 19.911**

|         |       |
|---------|-------|
| Entries | 755   |
| Mean    | 44.46 |
| Std Dev | 11.29 |

ampl





# B1L001S, U23-ch55

calib\_packv5\_042523\_0143.root, FC#2, port C2

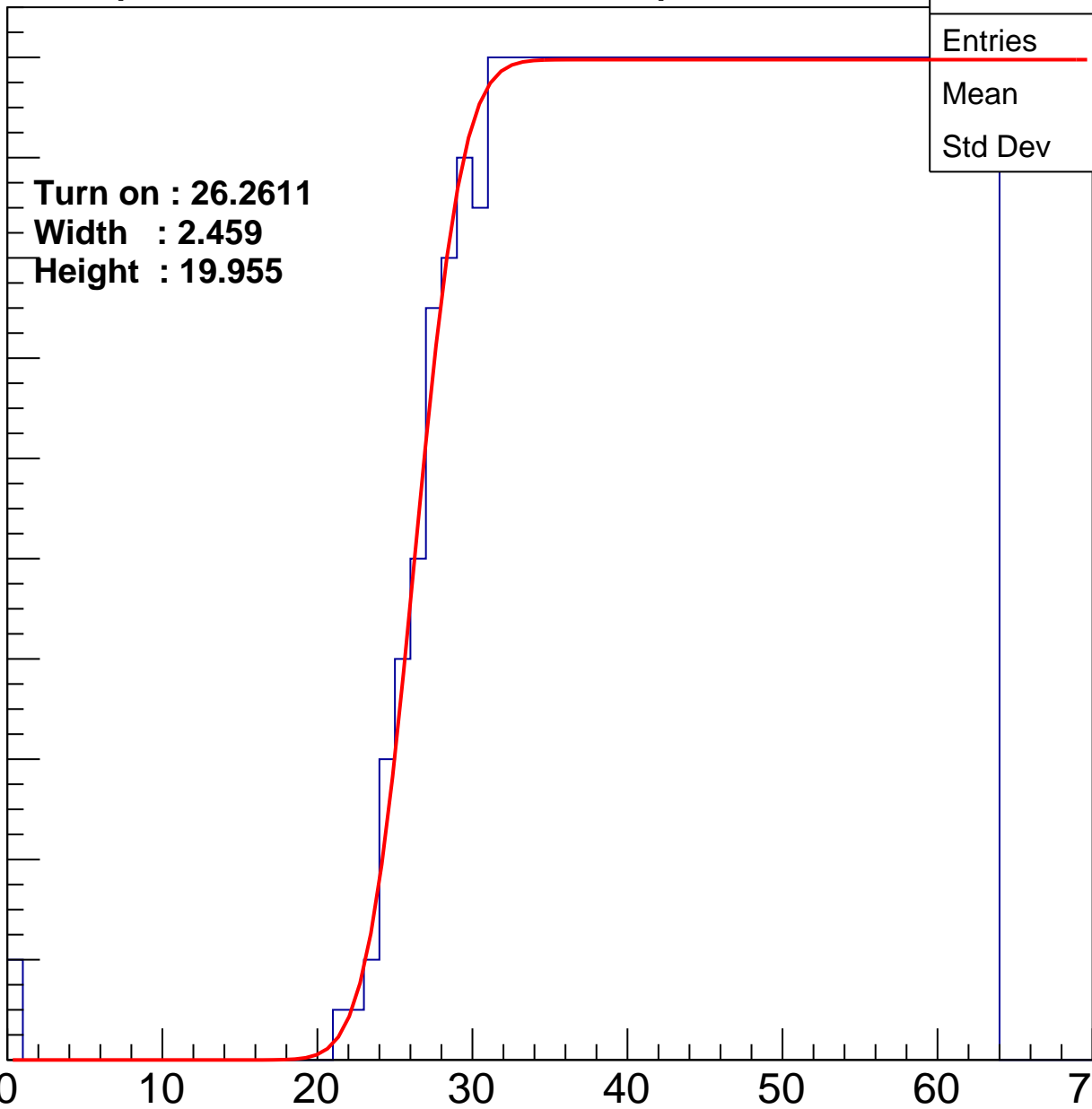
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.2611  
Width : 2.459  
Height : 19.955

|         |       |
|---------|-------|
| Entries | 756   |
| Mean    | 44.44 |
| Std Dev | 11.25 |

ampl



# B1L001S, U23-ch56

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry

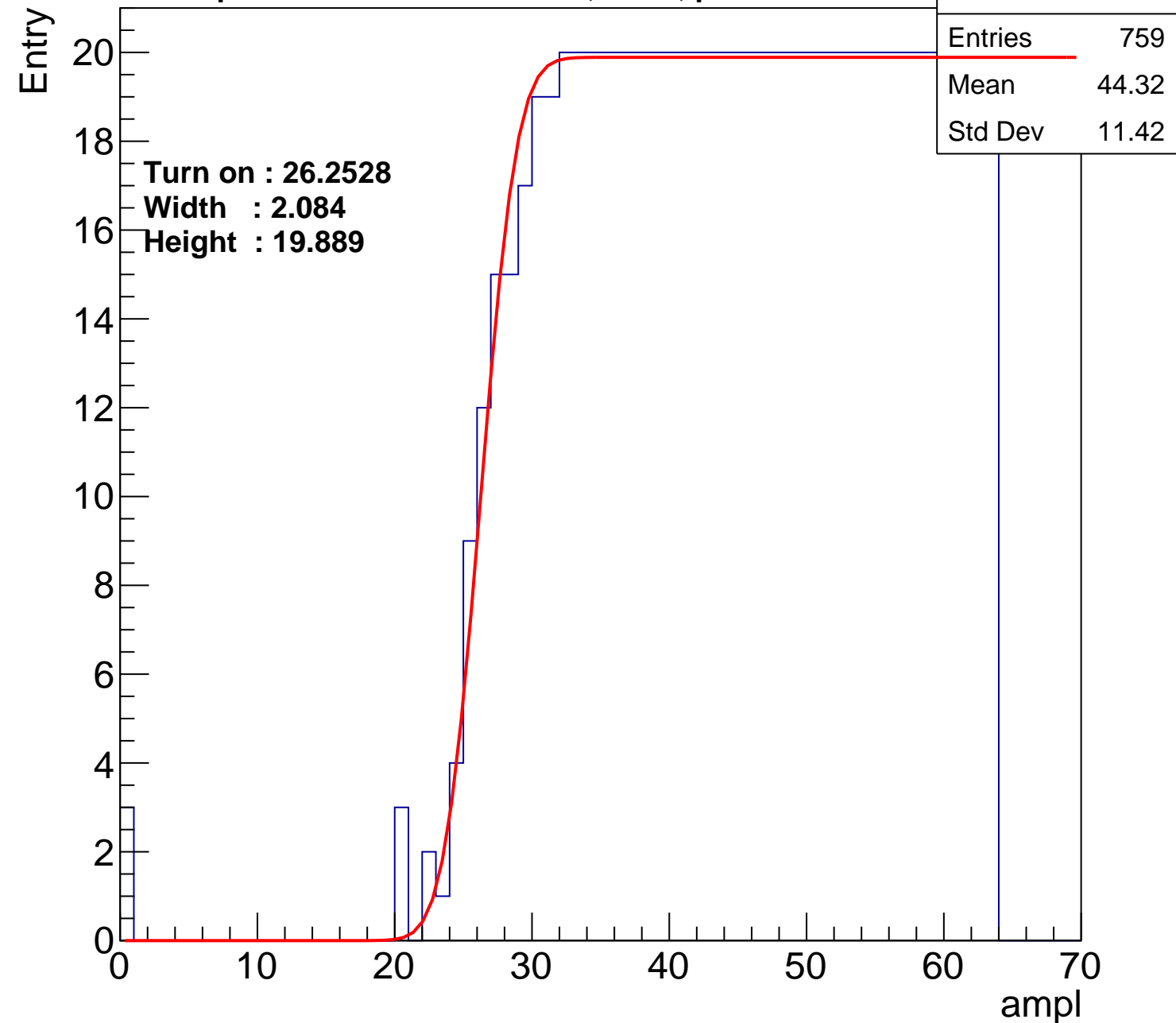
20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.2528**  
**Width : 2.084**  
**Height : 19.889**

|         |       |
|---------|-------|
| Entries | 759   |
| Mean    | 44.32 |
| Std Dev | 11.42 |

ampl

0 10 20 30 40 50 60 70



# B1L001S, U23-ch57

calib\_packv5\_042523\_0143.root, FC#2, port C2

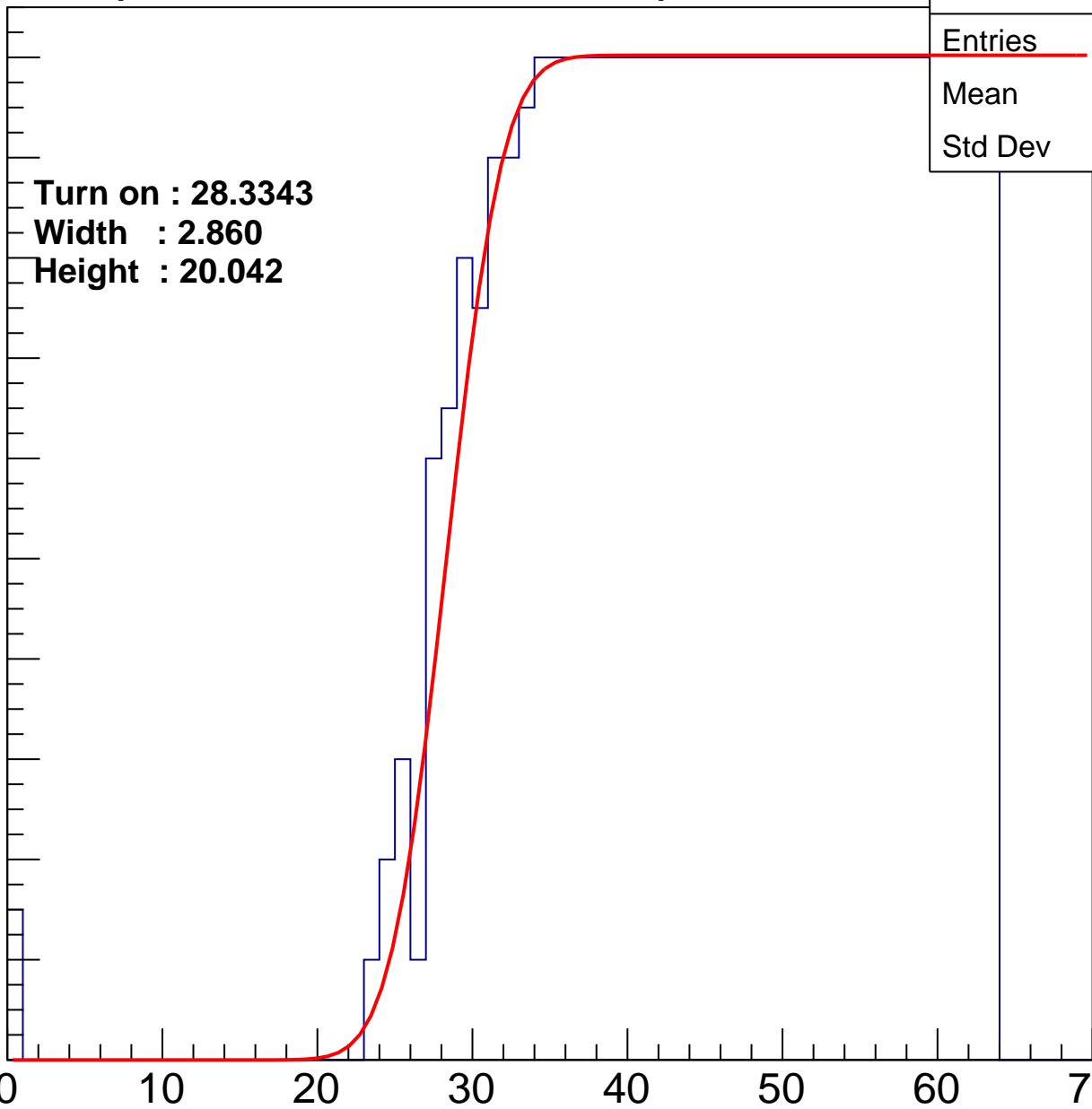
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.3343**  
**Width : 2.860**  
**Height : 20.042**

|         |       |
|---------|-------|
| Entries | 728   |
| Mean    | 45.06 |
| Std Dev | 11.03 |

ampl



# B1L001S, U23-ch58

calib\_packv5\_042523\_0143.root, FC#2, port C2

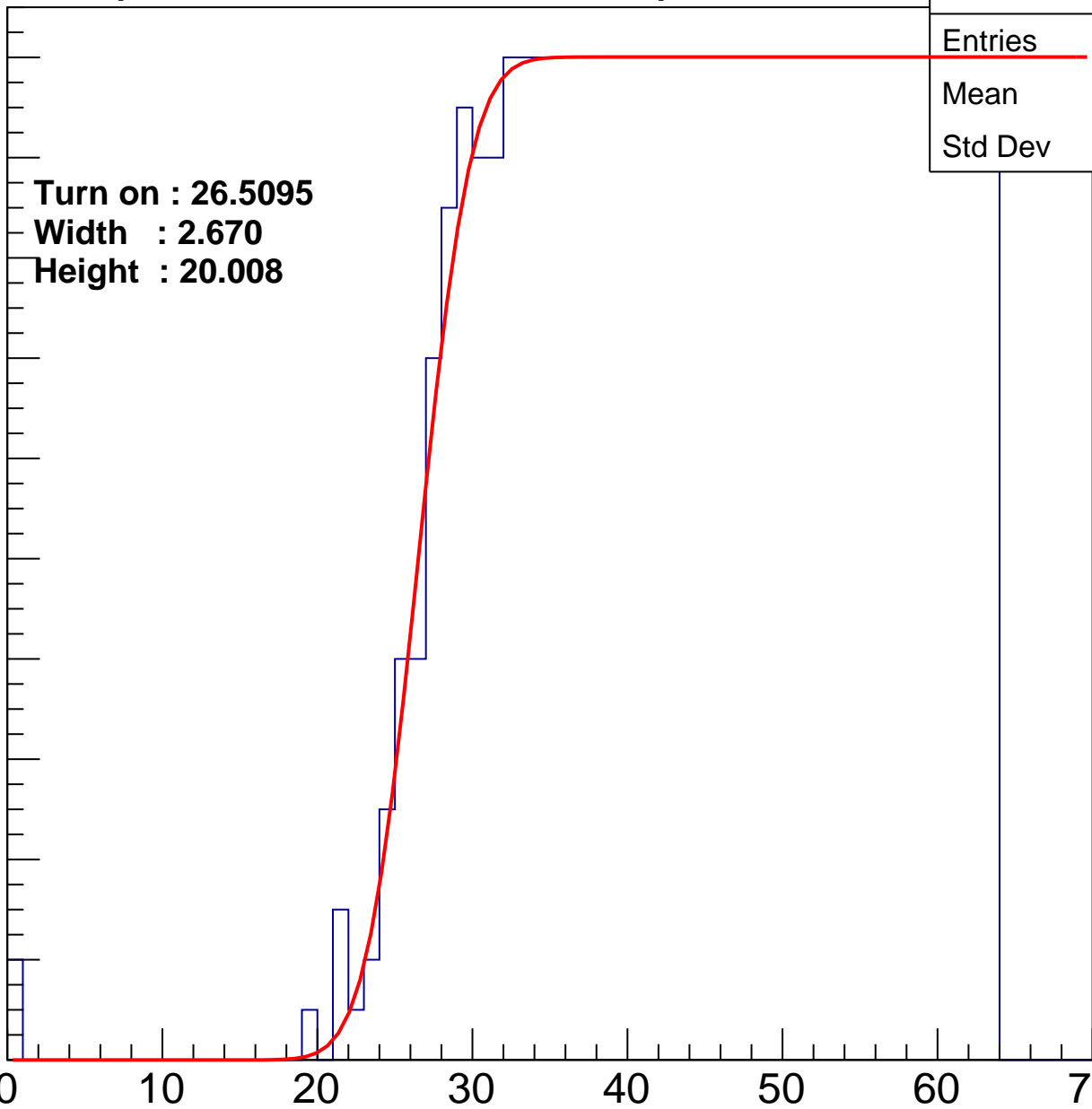
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.5095**  
**Width : 2.670**  
**Height : 20.008**

|         |       |
|---------|-------|
| Entries | 756   |
| Mean    | 44.42 |
| Std Dev | 11.29 |

ampl



# B1L001S, U23-ch59

calib\_packv5\_042523\_0143.root, FC#2, port C2

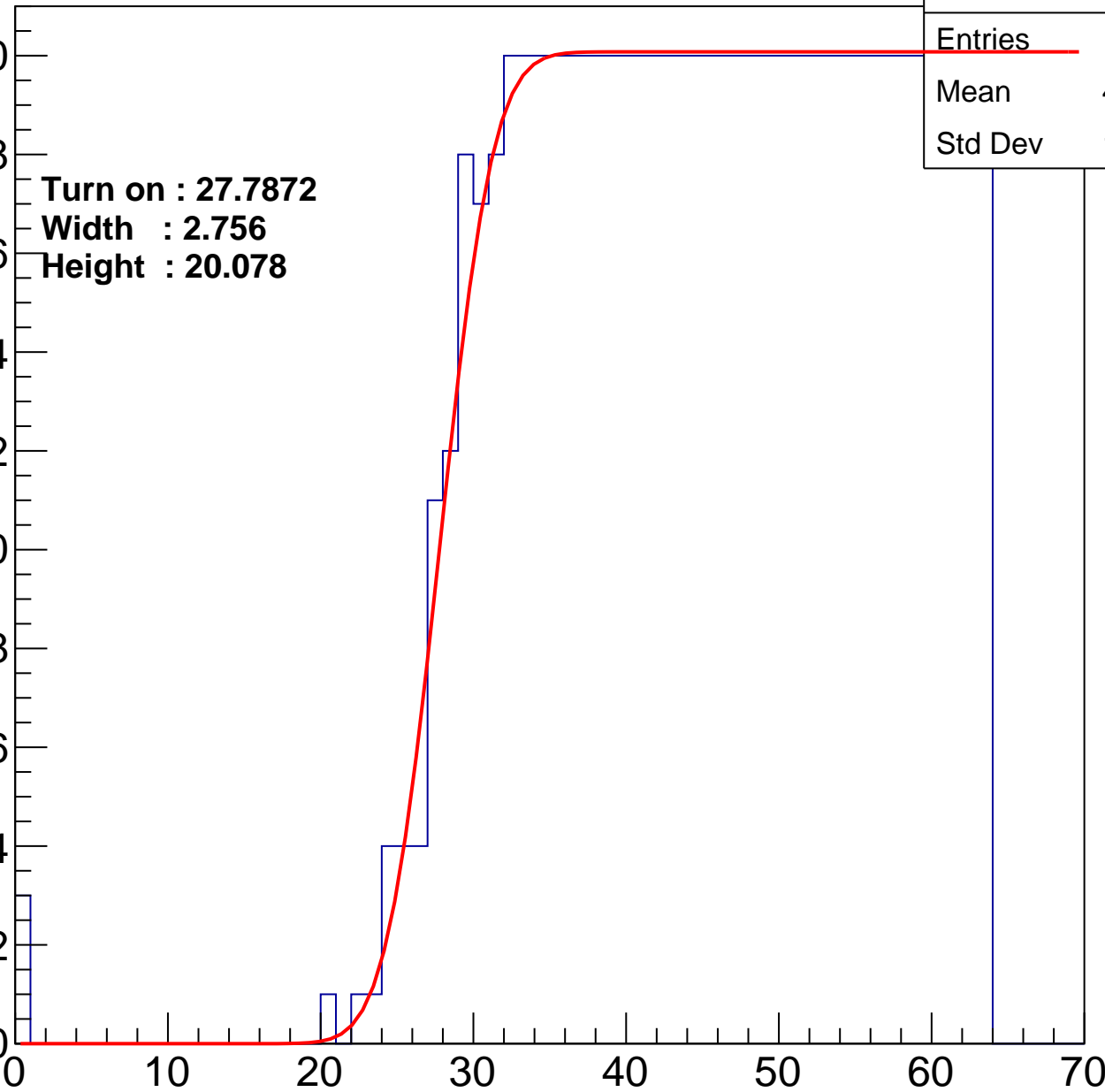
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.7872  
Width : 2.756  
Height : 20.078

|         |       |
|---------|-------|
| Entries | 734   |
| Mean    | 44.94 |
| Std Dev | 11.08 |

ampl



# B1L001S, U23-ch60

calib\_packv5\_042523\_0143.root, FC#2, port C2

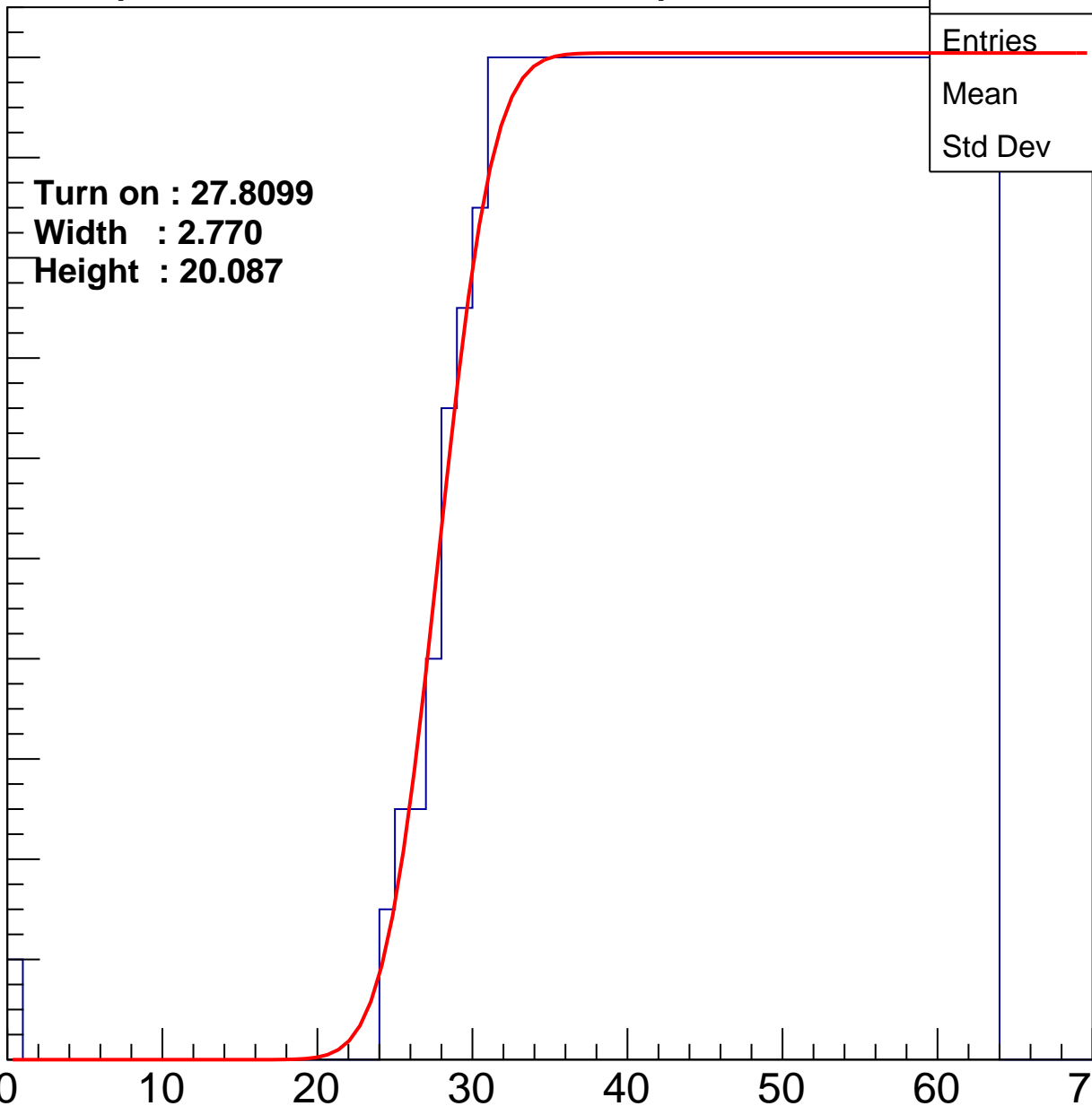
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.8099**  
**Width : 2.770**  
**Height : 20.087**

|         |       |
|---------|-------|
| Entries | 728   |
| Mean    | 45.15 |
| Std Dev | 10.85 |

ampl



# B1L001S, U23-ch61

calib\_packv5\_042523\_0143.root, FC#2, port C2

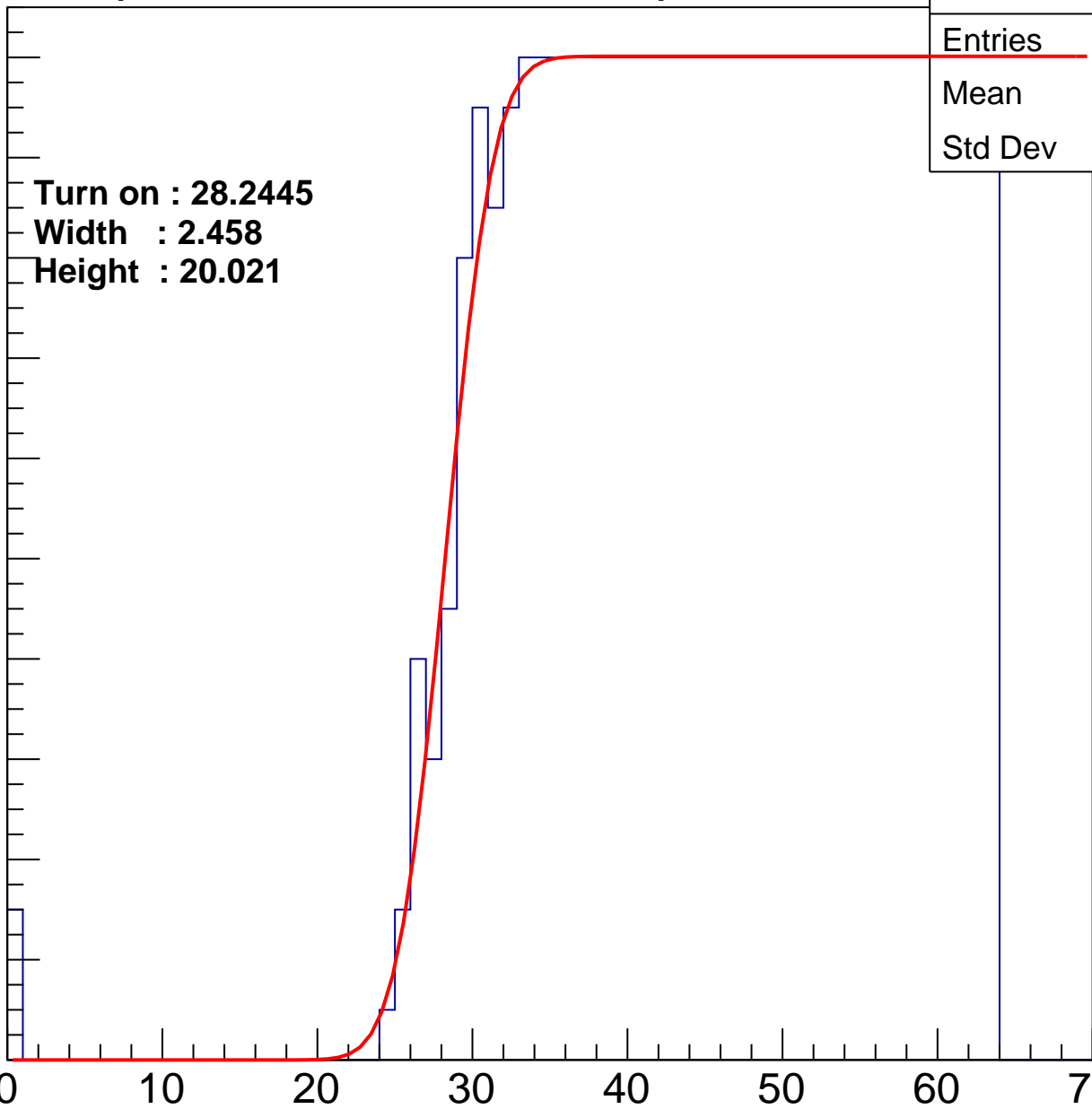
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.2445  
Width : 2.458  
Height : 20.021

|         |       |
|---------|-------|
| Entries | 721   |
| Mean    | 45.28 |
| Std Dev | 10.87 |

ampl



# B1L001S, U23-ch62

calib\_packv5\_042523\_0143.root, FC#2, port C2

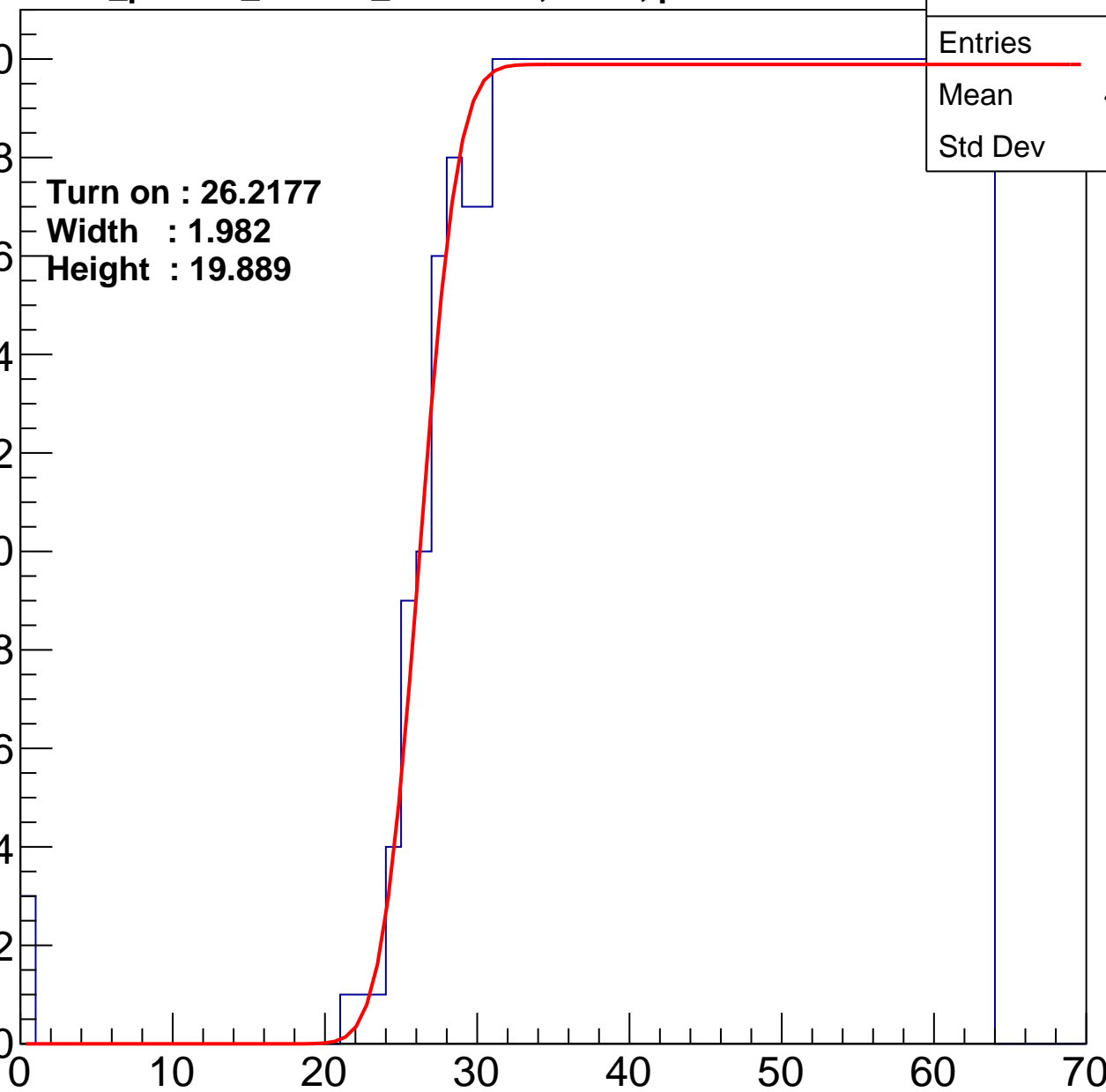
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.2177**  
**Width : 1.982**  
**Height : 19.889**

|         |       |
|---------|-------|
| Entries | 757   |
| Mean    | 44.39 |
| Std Dev | 11.34 |

ampl





# B1L001S, U23-ch63

calib\_packv5\_042523\_0143.root, FC#2, port C2

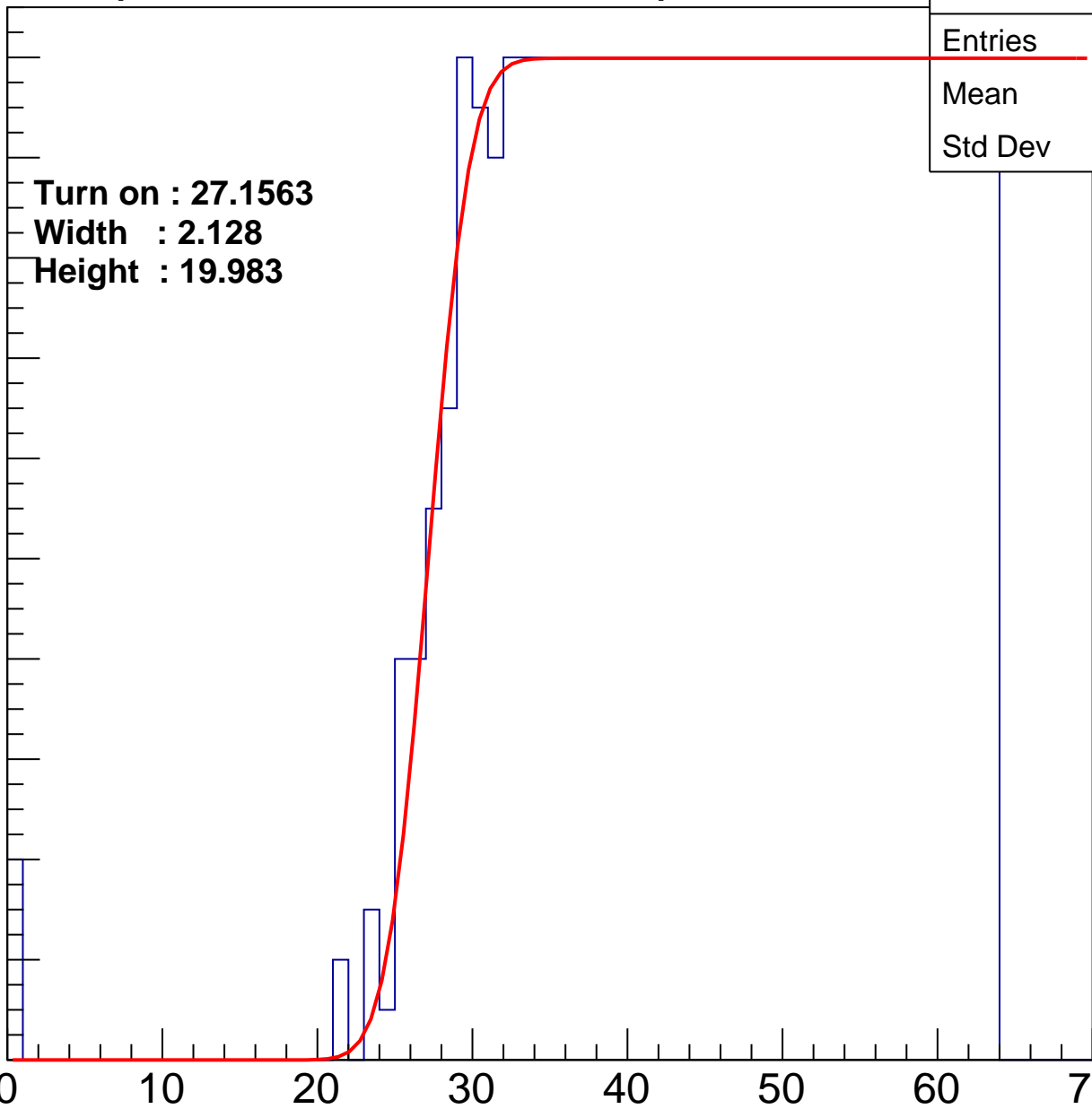
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.1563**  
**Width : 2.128**  
**Height : 19.983**

|         |       |
|---------|-------|
| Entries | 747   |
| Mean    | 44.59 |
| Std Dev | 11.33 |

ampl



# B1L001S, U23-ch64

calib\_packv5\_042523\_0143.root, FC#2, port C2

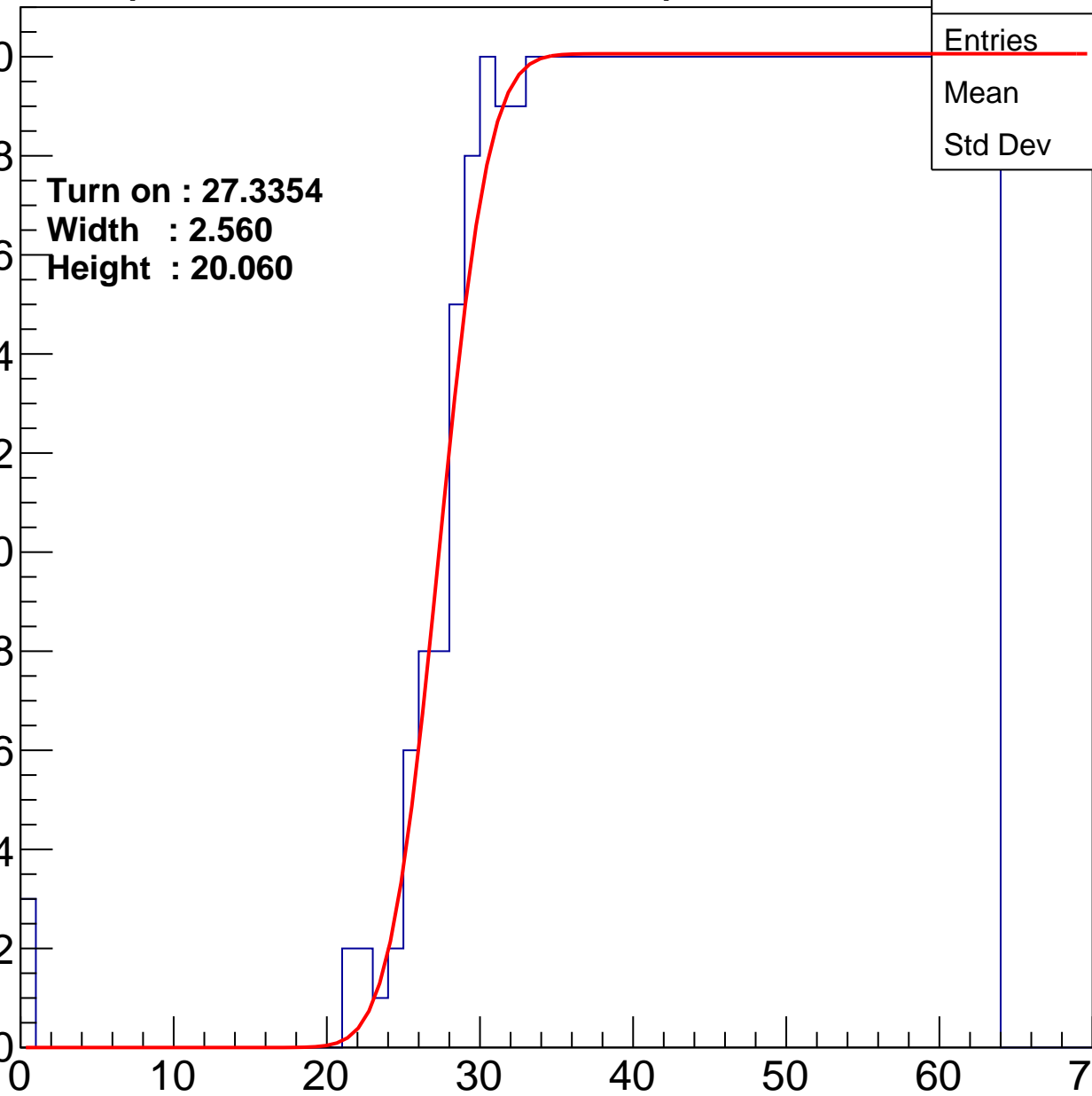
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.3354  
Width : 2.560  
Height : 20.060

|         |       |
|---------|-------|
| Entries | 743   |
| Mean    | 44.72 |
| Std Dev | 11.19 |

ampl



# B1L001S, U23-ch65

calib\_packv5\_042523\_0143.root, FC#2, port C2

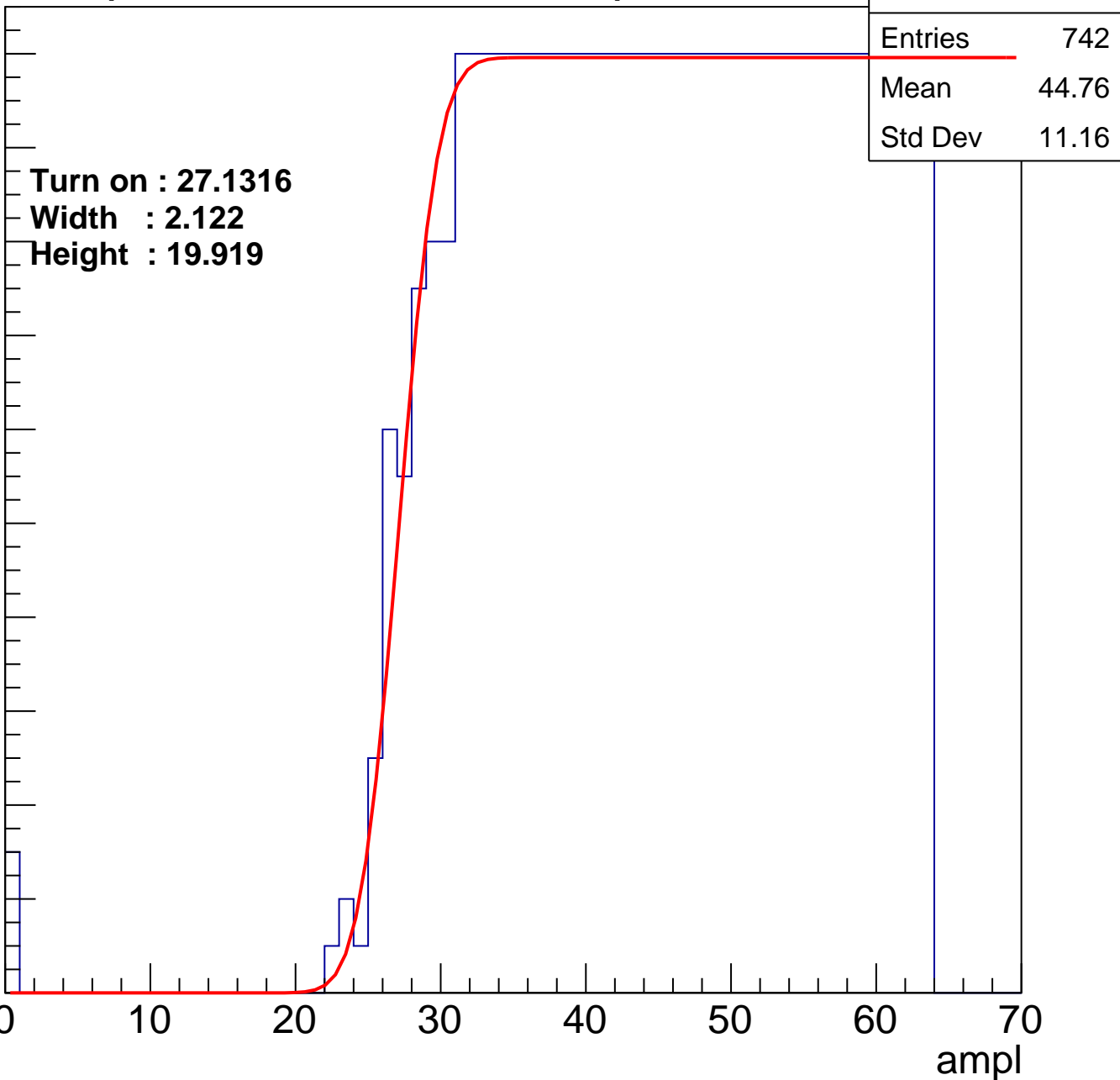
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.1316  
Width : 2.122  
Height : 19.919

|         |       |
|---------|-------|
| Entries | 742   |
| Mean    | 44.76 |
| Std Dev | 11.16 |

ampl



# B1L001S, U23-ch66

calib\_packv5\_042523\_0143.root, FC#2, port C2

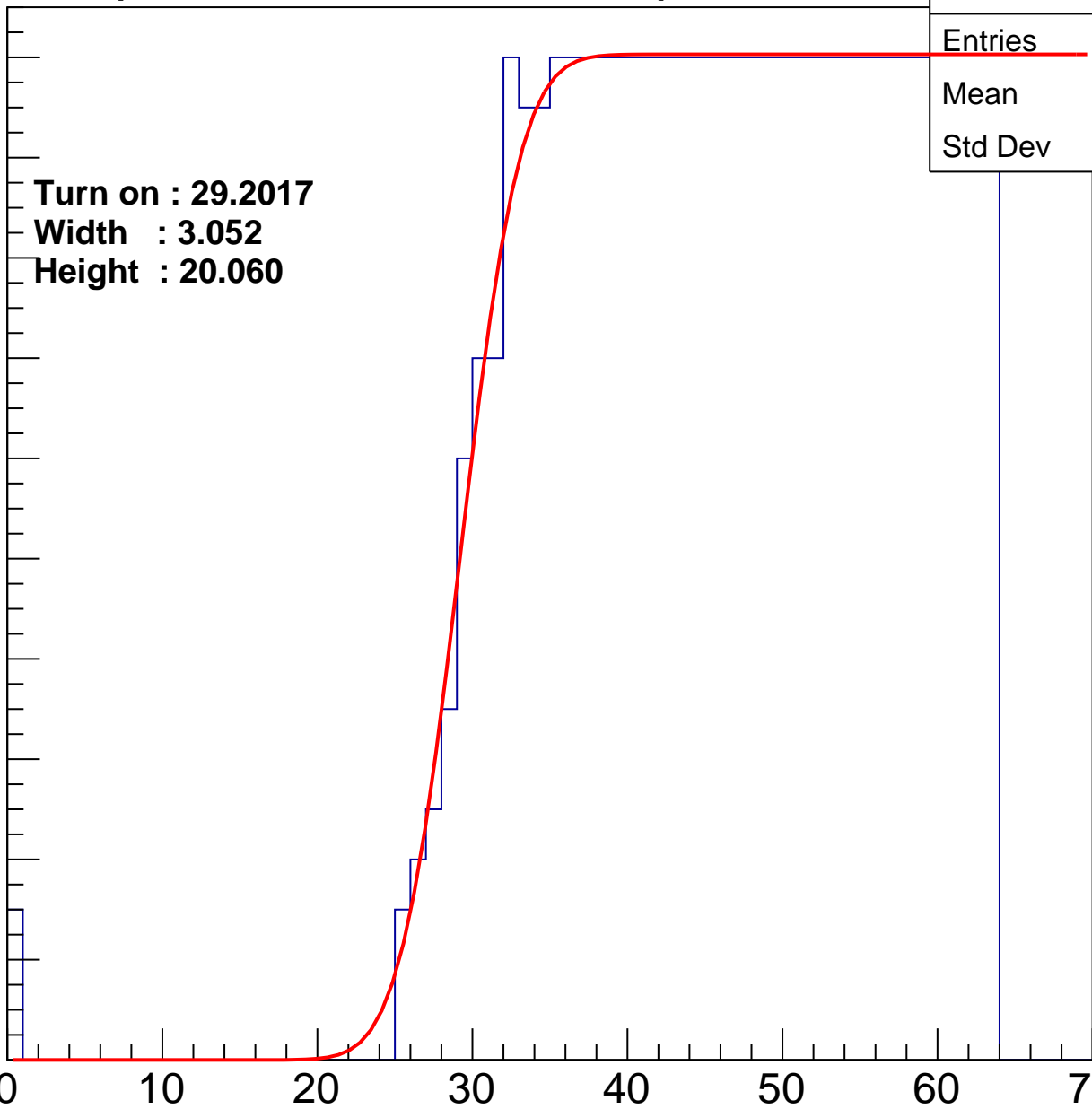
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 29.2017  
Width : 3.052  
Height : 20.060

|         |       |
|---------|-------|
| Entries | 700   |
| Mean    | 45.78 |
| Std Dev | 10.64 |

ampl



# B1L001S, U23-ch67

calib\_packv5\_042523\_0143.root, FC#2, port C2

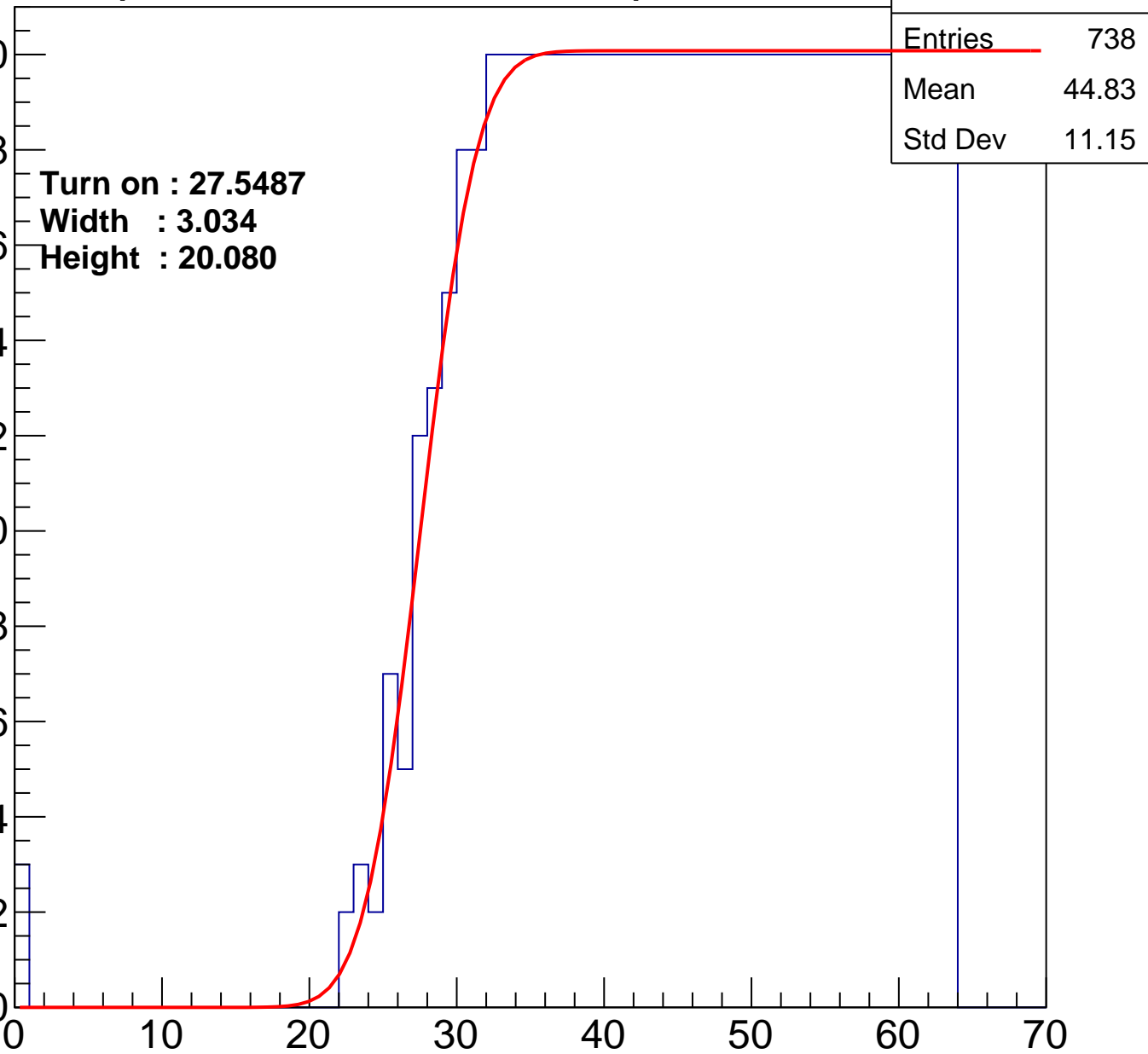
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.5487**  
**Width : 3.034**  
**Height : 20.080**

|         |       |
|---------|-------|
| Entries | 738   |
| Mean    | 44.83 |
| Std Dev | 11.15 |

ampl



# B1L001S, U23-ch68

calib\_packv5\_042523\_0143.root, FC#2, port C2

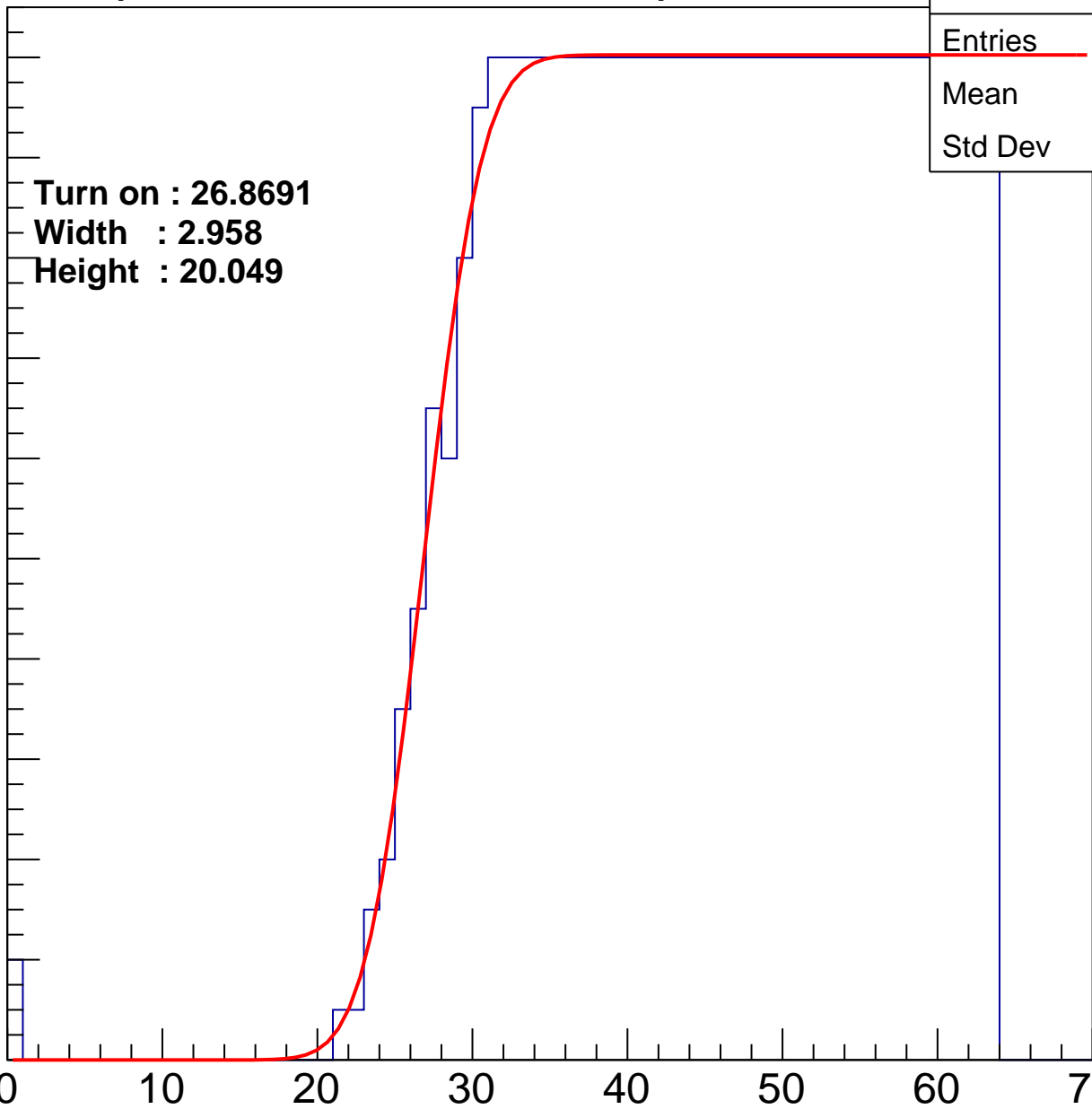
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.8691**  
**Width : 2.958**  
**Height : 20.049**

|         |       |
|---------|-------|
| Entries | 747   |
| Mean    | 44.66 |
| Std Dev | 11.15 |

ampl



# B1L001S, U23-ch69

calib\_packv5\_042523\_0143.root, FC#2, port C2

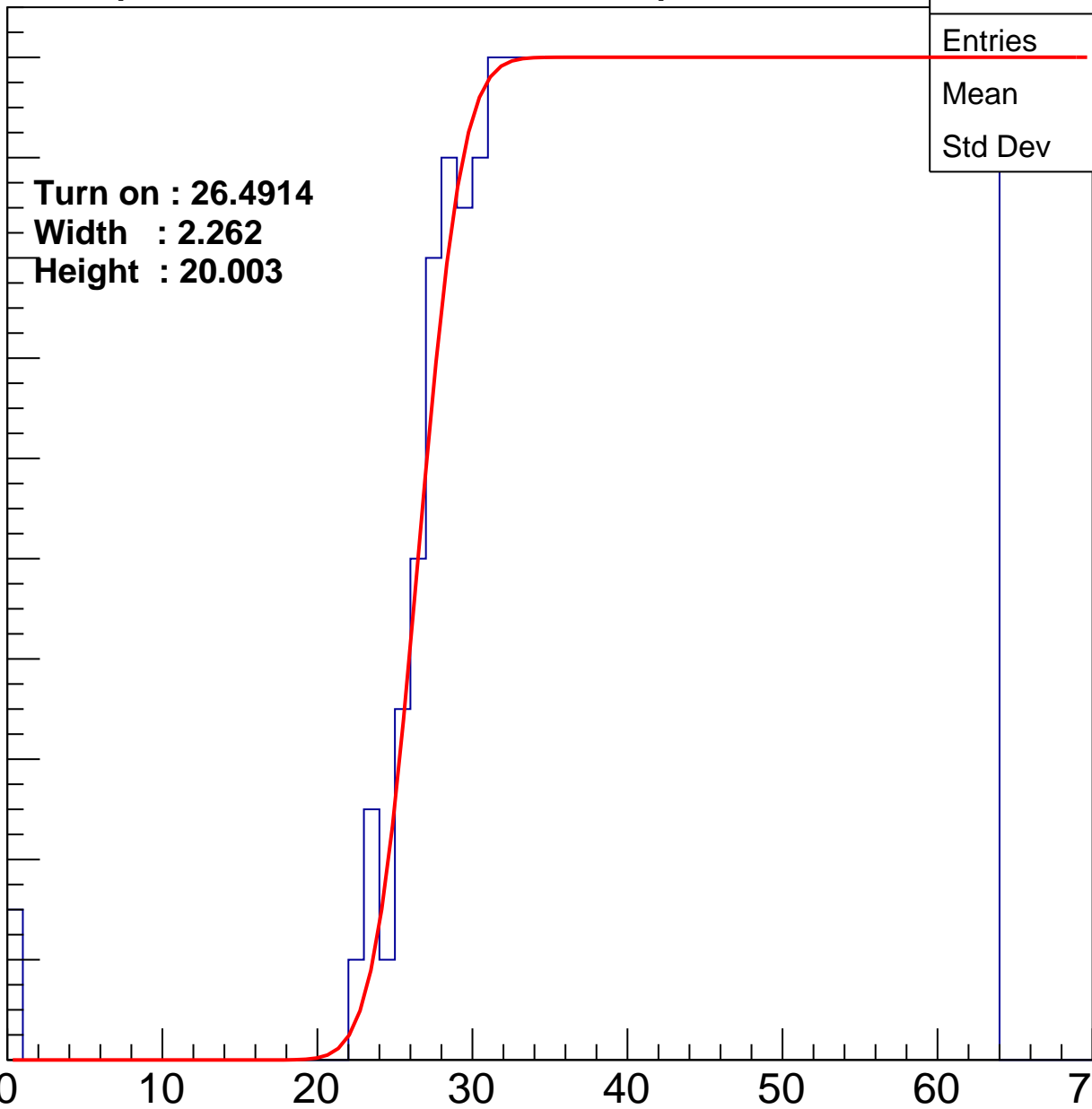
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.4914**  
**Width : 2.262**  
**Height : 20.003**

|         |       |
|---------|-------|
| Entries | 758   |
| Mean    | 44.37 |
| Std Dev | 11.36 |

ampl



# B1L001S, U23-ch70

calib\_packv5\_042523\_0143.root, FC#2, port C2

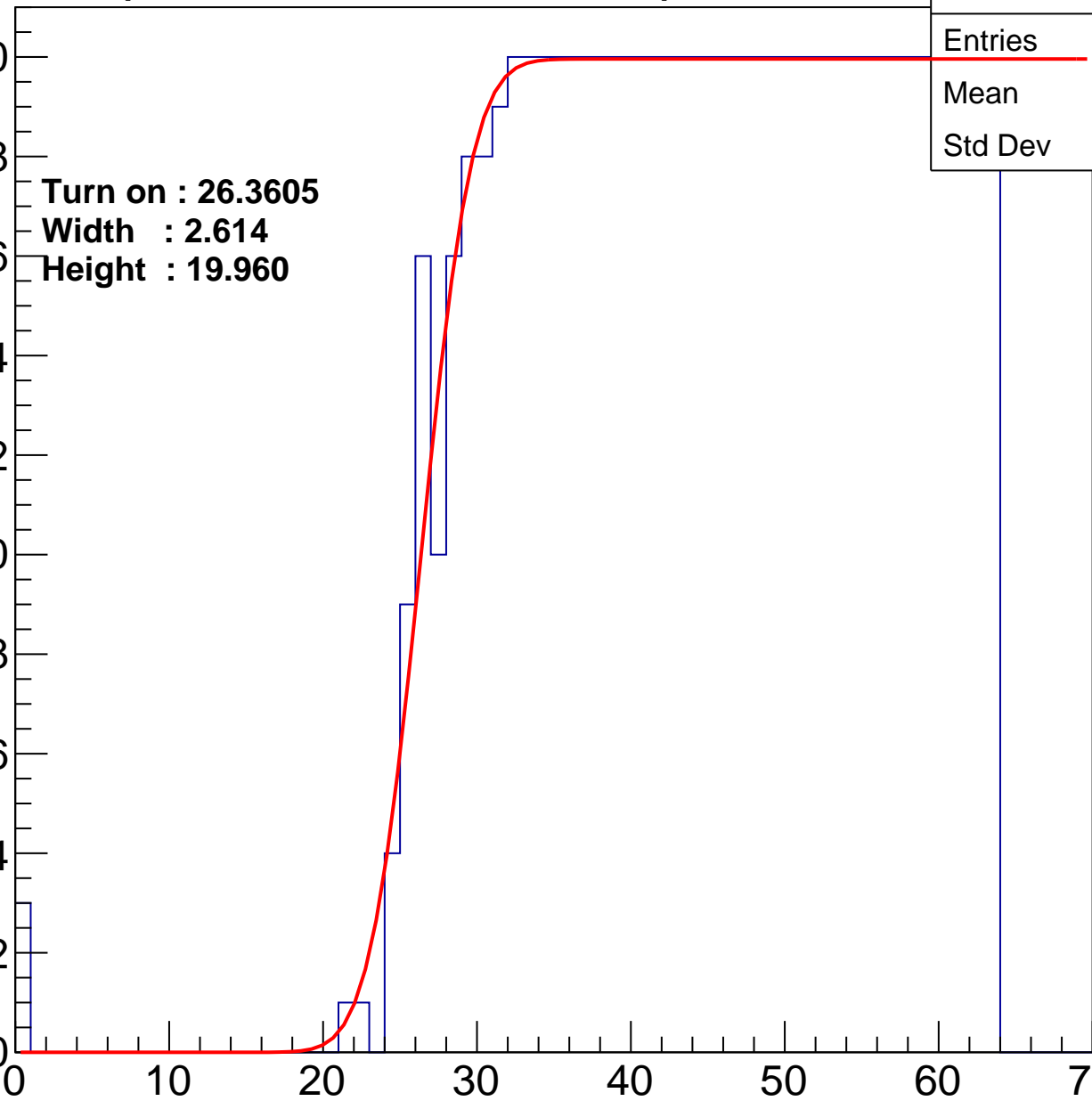
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.3605**  
**Width : 2.614**  
**Height : 19.960**

|         |       |
|---------|-------|
| Entries | 755   |
| Mean    | 44.44 |
| Std Dev | 11.33 |

ampl





# B1L001S, U23-ch71

calib\_packv5\_042523\_0143.root, FC#2, port C2

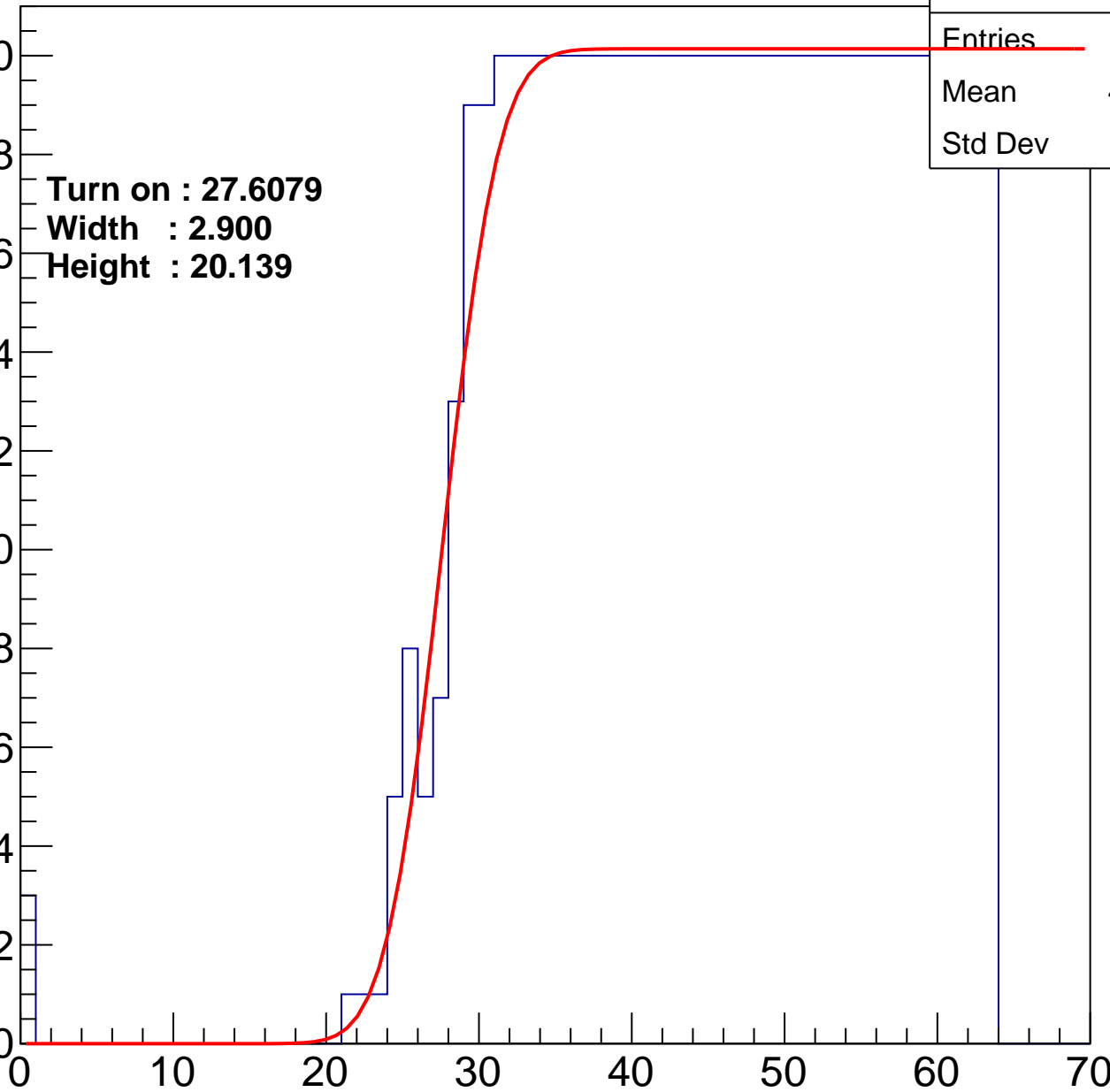
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.6079  
Width : 2.900  
Height : 20.139

|         |       |
|---------|-------|
| Entries | 742   |
| Mean    | 44.76 |
| Std Dev | 11.16 |

ampl



# B1L001S, U23-ch72

calib\_packv5\_042523\_0143.root, FC#2, port C2

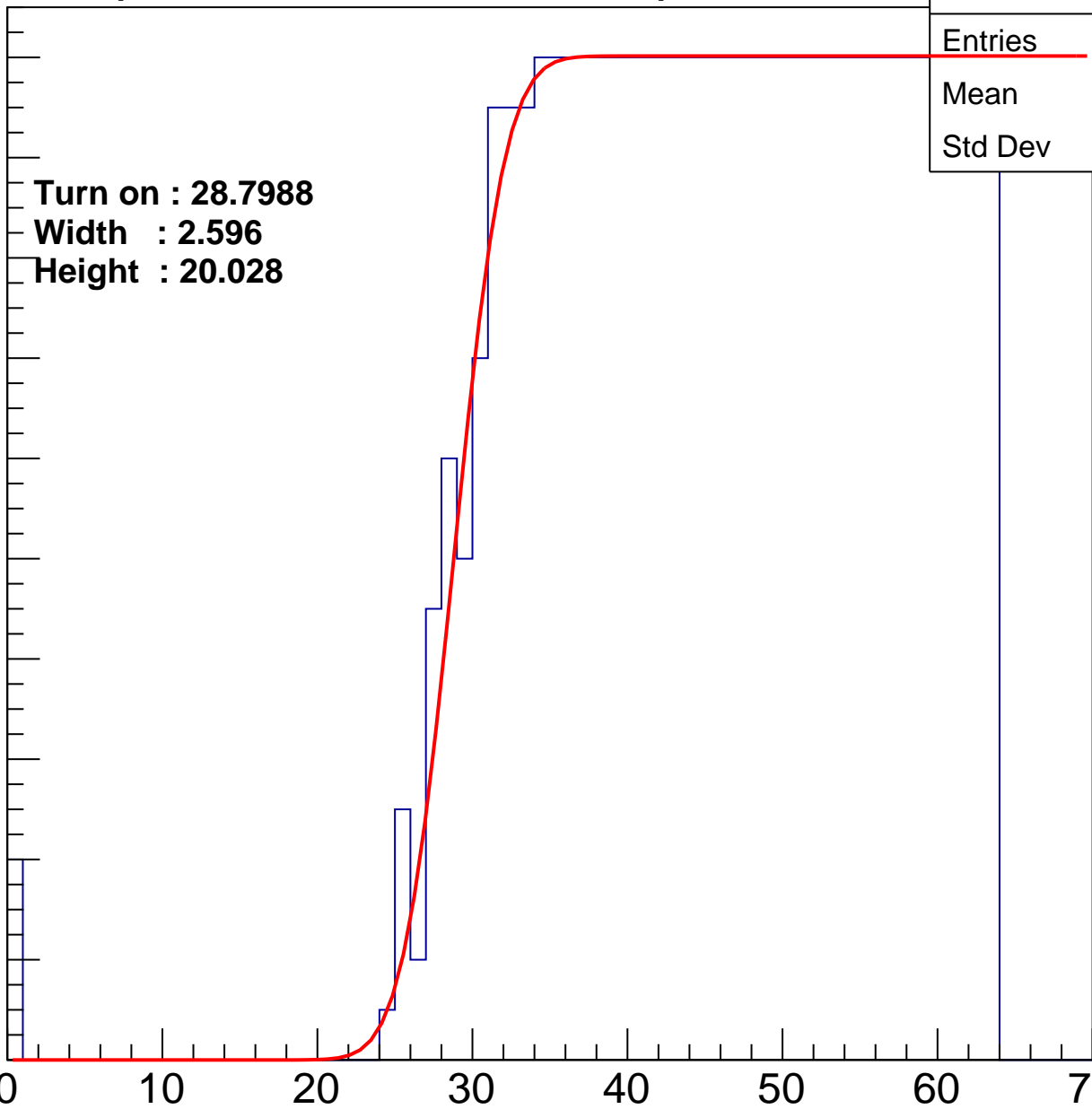
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.7988**  
**Width : 2.596**  
**Height : 20.028**

|         |       |
|---------|-------|
| Entries | 714   |
| Mean    | 45.4  |
| Std Dev | 10.92 |

ampl



# B1L001S, U23-ch73

calib\_packv5\_042523\_0143.root, FC#2, port C2

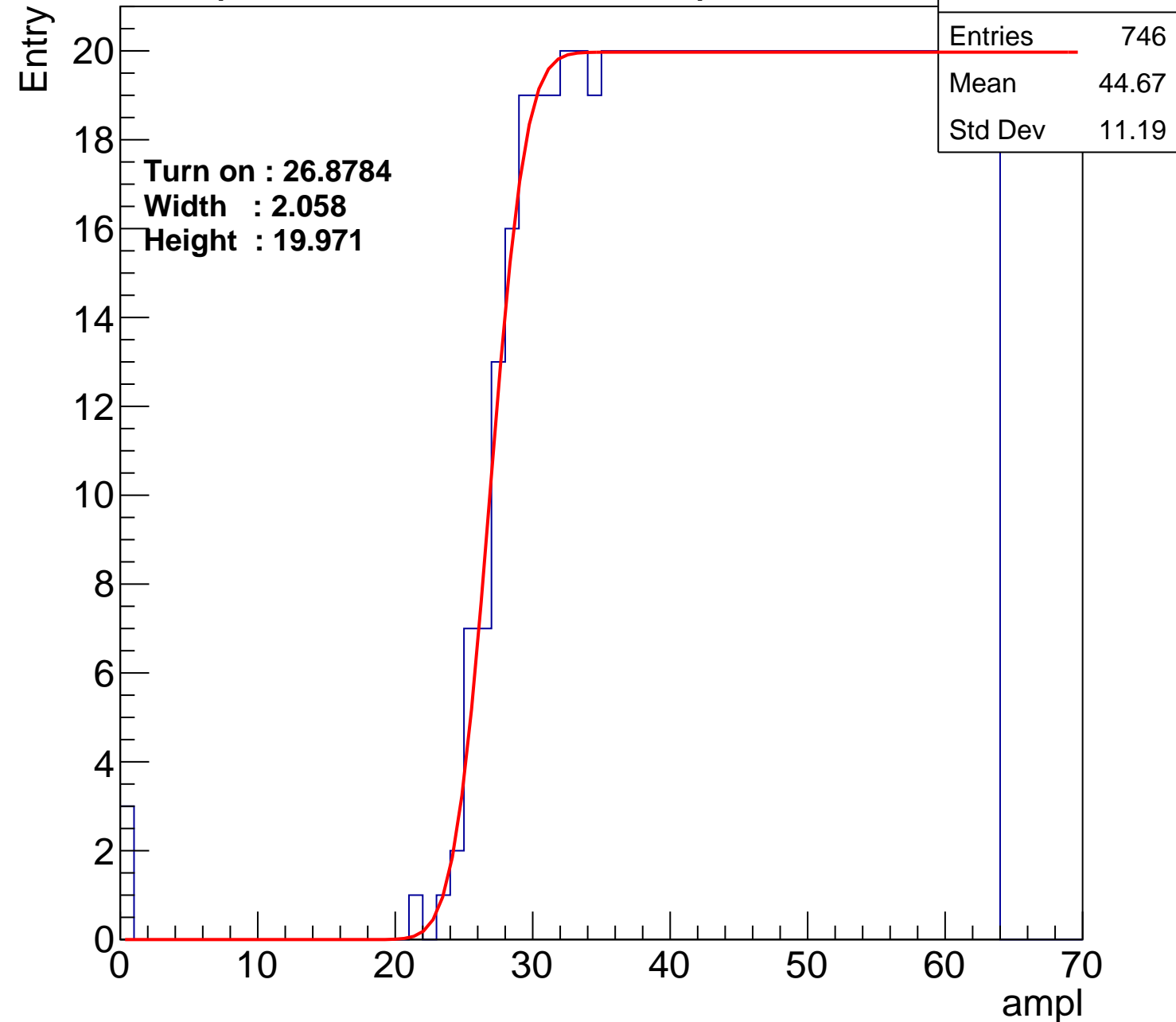
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.8784**  
**Width : 2.058**  
**Height : 19.971**

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.67 |
| Std Dev | 11.19 |

ampl



# B1L001S, U23-ch74

calib\_packv5\_042523\_0143.root, FC#2, port C2

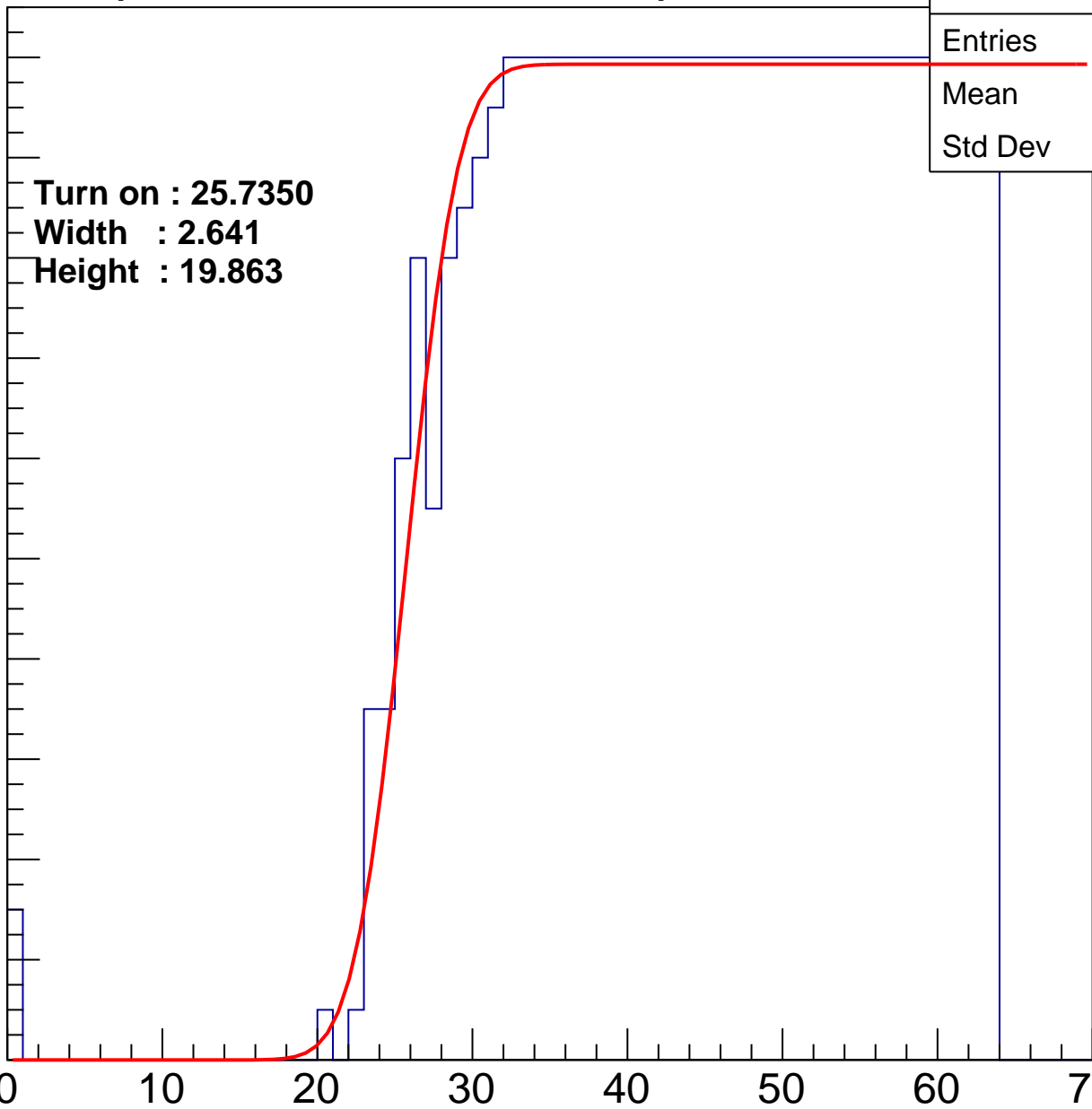
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 25.7350**  
**Width : 2.641**  
**Height : 19.863**

|         |       |
|---------|-------|
| Entries | 768   |
| Mean    | 44.08 |
| Std Dev | 11.55 |

ampl



# B1L001S, U23-ch75

calib\_packv5\_042523\_0143.root, FC#2, port C2

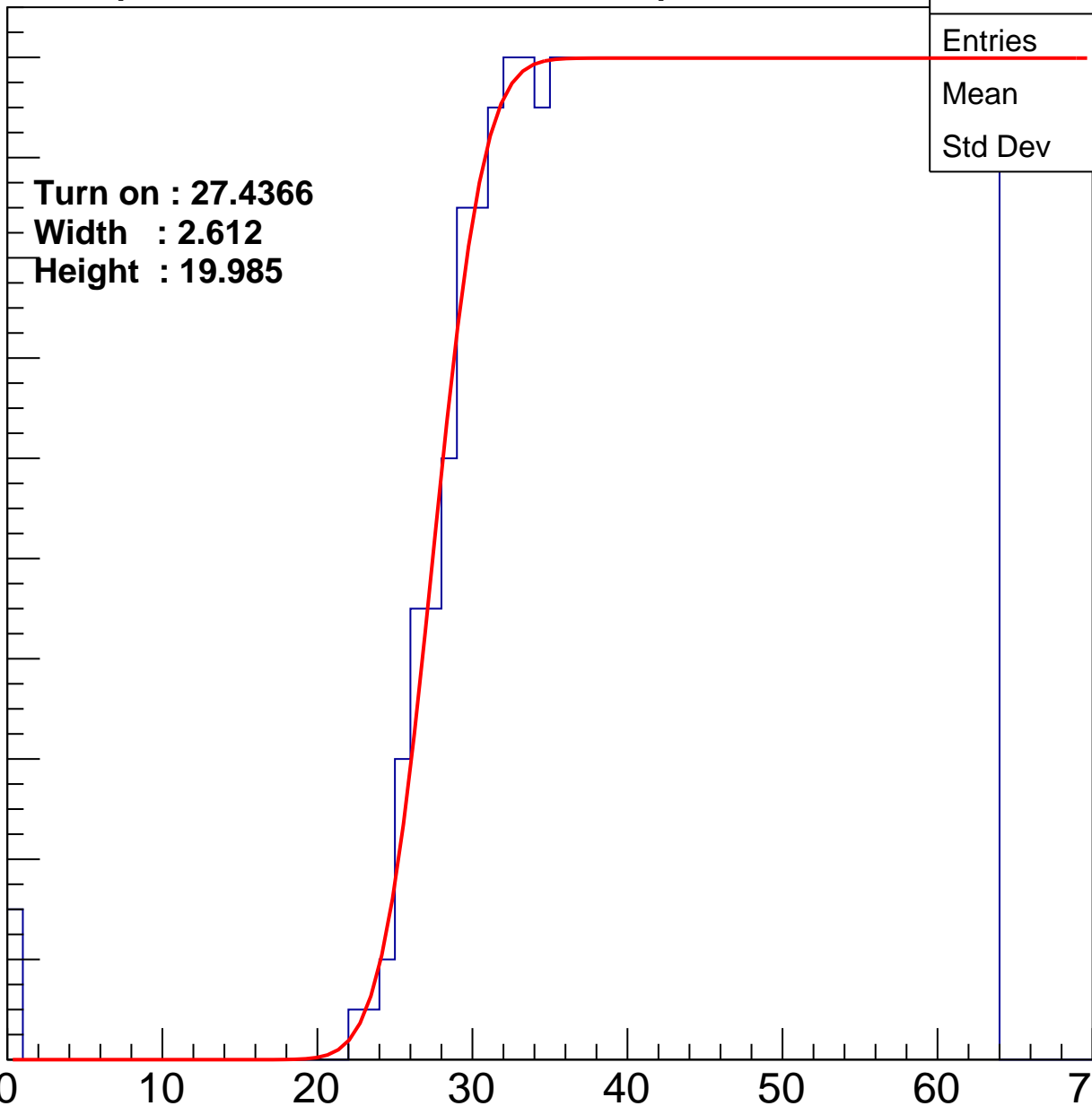
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.4366**  
**Width : 2.612**  
**Height : 19.985**

|         |       |
|---------|-------|
| Entries | 735   |
| Mean    | 44.92 |
| Std Dev | 11.09 |

ampl



# B1L001S, U23-ch76

calib\_packv5\_042523\_0143.root, FC#2, port C2

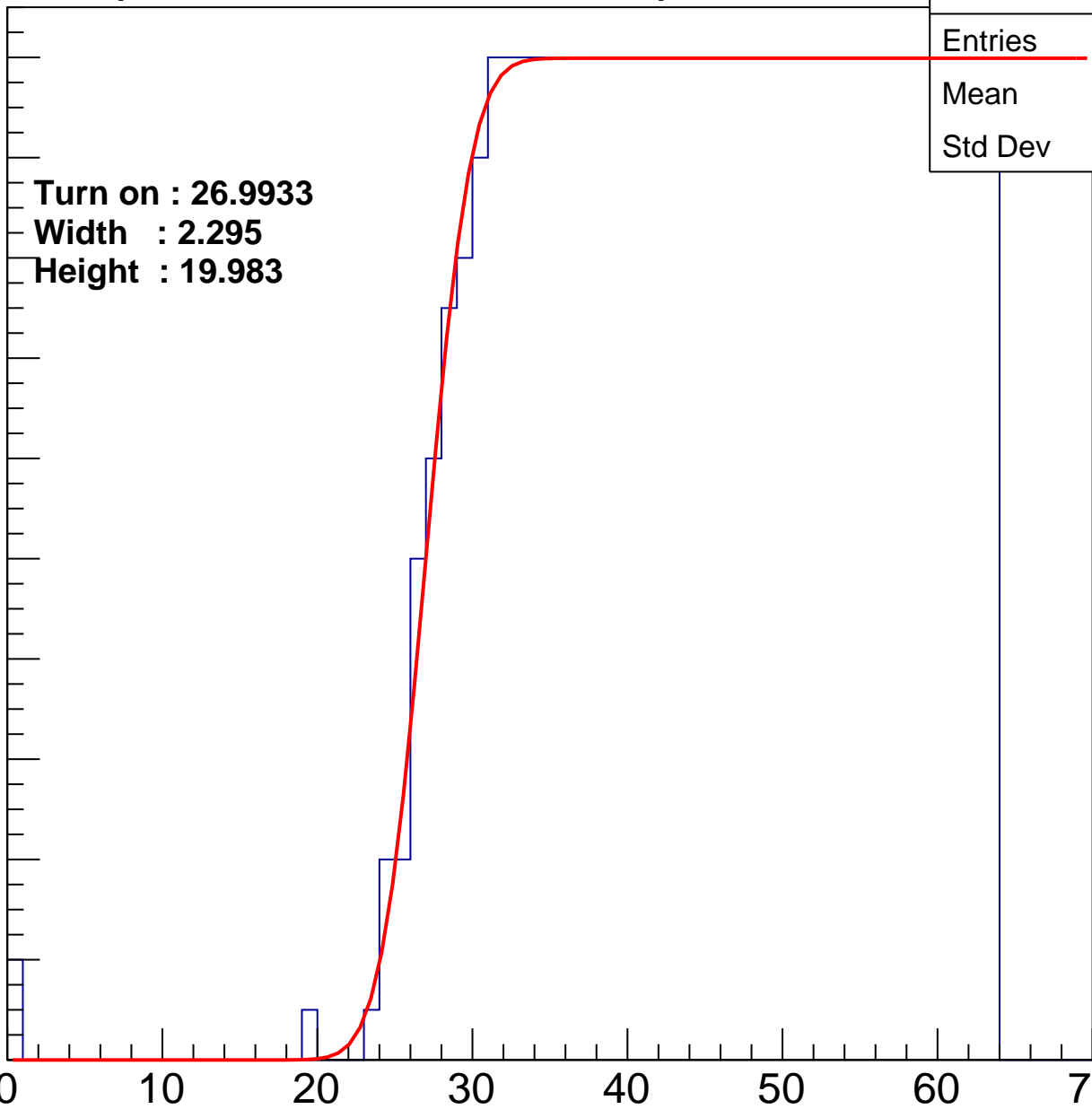
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.9933**  
**Width : 2.295**  
**Height : 19.983**

|         |       |
|---------|-------|
| Entries | 743   |
| Mean    | 44.77 |
| Std Dev | 11.07 |

ampl



# B1L001S, U23-ch77

calib\_packv5\_042523\_0143.root, FC#2, port C2

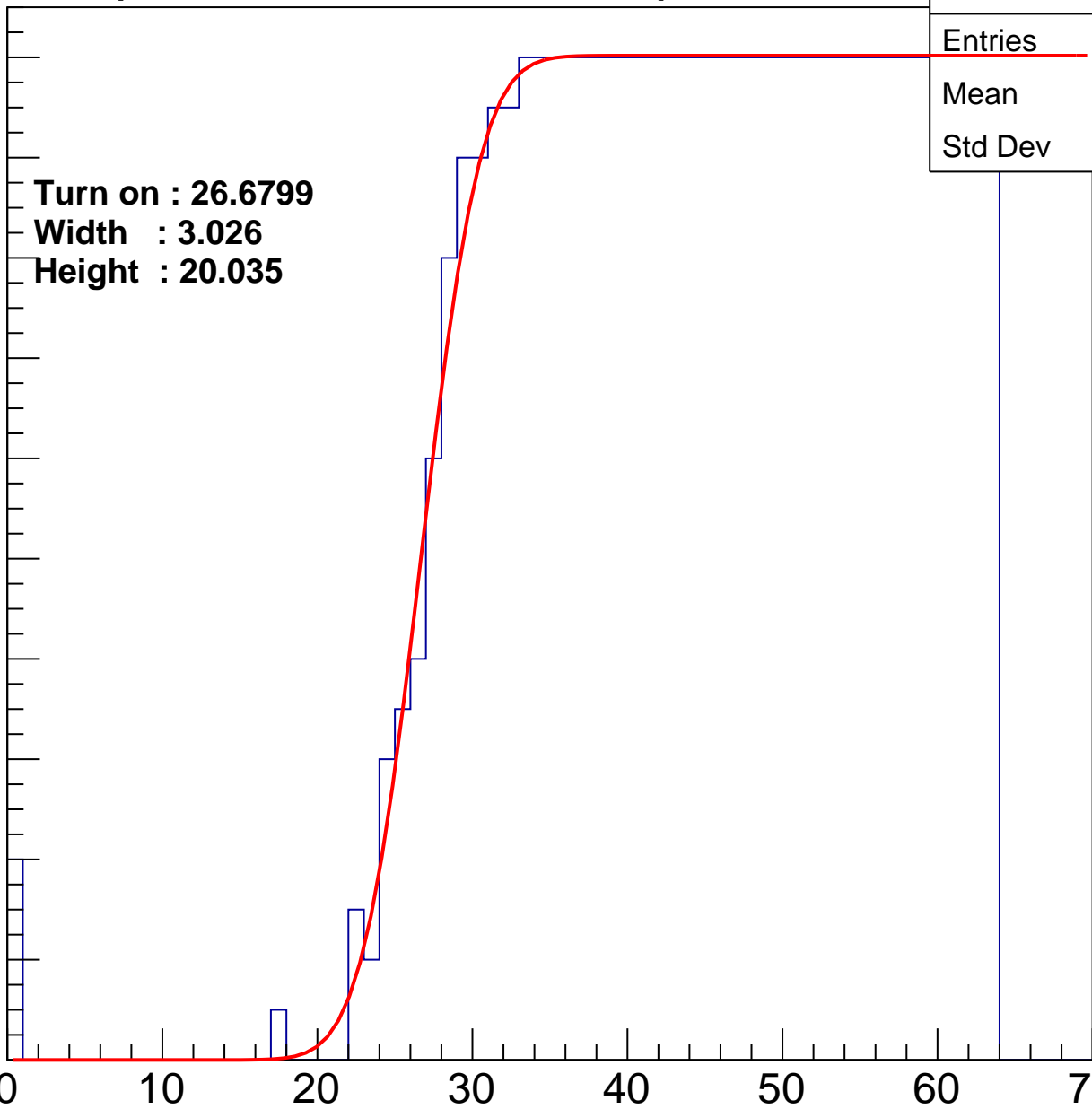
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.6799**  
**Width : 3.026**  
**Height : 20.035**

|         |       |
|---------|-------|
| Entries | 753   |
| Mean    | 44.42 |
| Std Dev | 11.45 |

ampl



# B1L001S, U23-ch78

calib\_packv5\_042523\_0143.root, FC#2, port C2

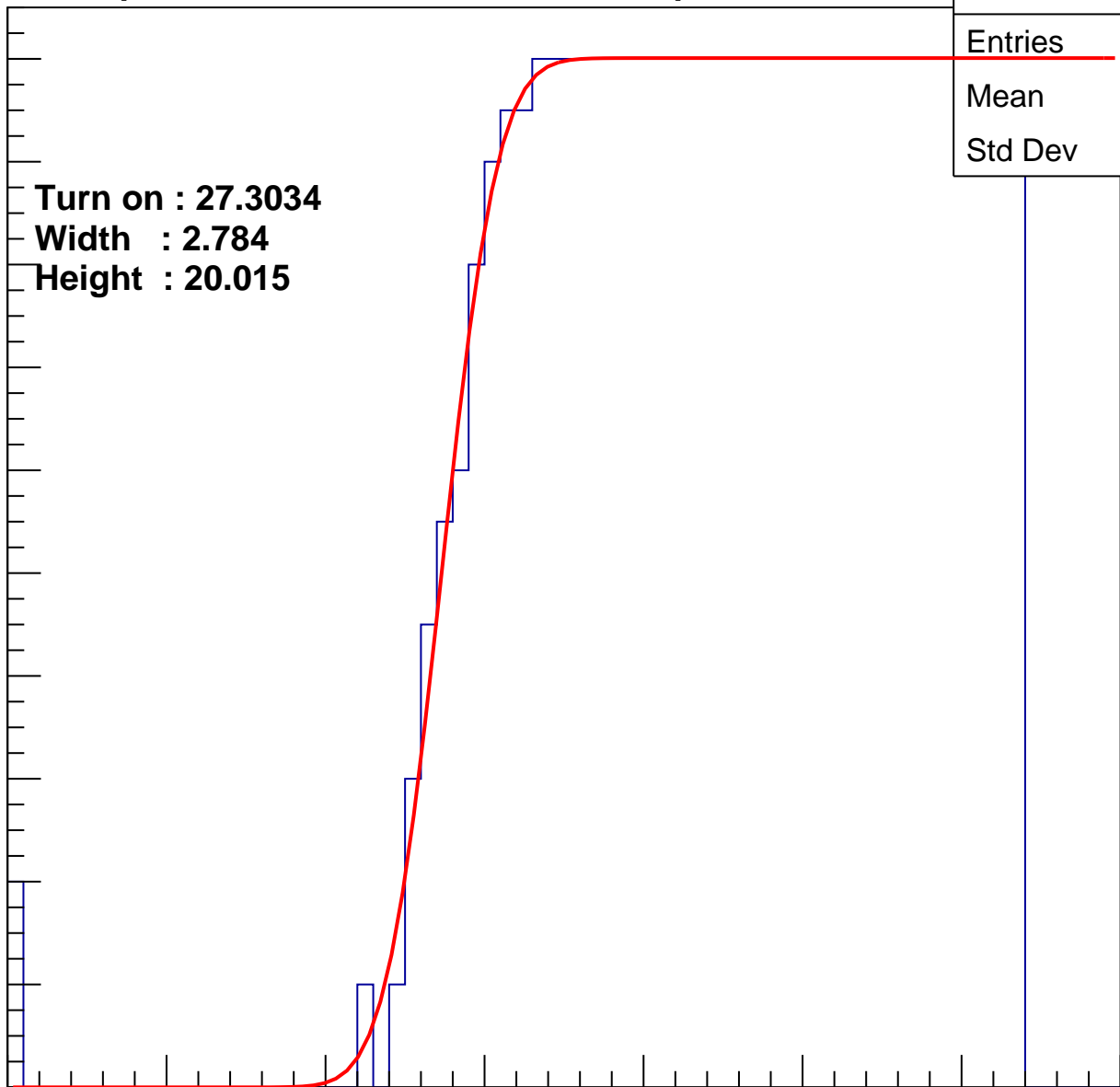
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.3034**  
**Width : 2.784**  
**Height : 20.015**

|         |       |
|---------|-------|
| Entries | 738   |
| Mean    | 44.81 |
| Std Dev | 11.22 |

ampl





# B1L001S, U23-ch79

calib\_packv5\_042523\_0143.root, FC#2, port C2

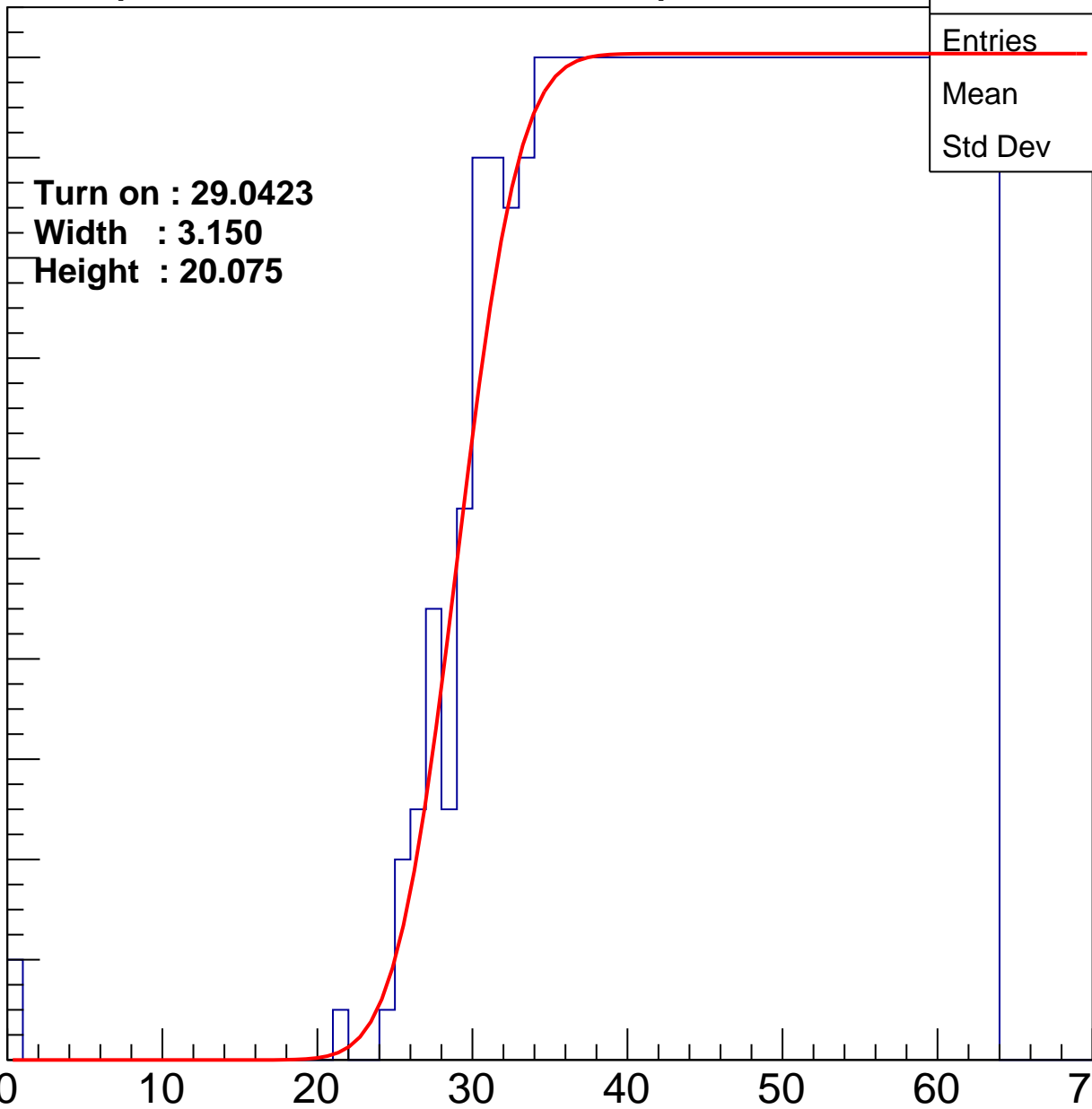
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.0423**  
**Width : 3.150**  
**Height : 20.075**

|         |       |
|---------|-------|
| Entries | 709   |
| Mean    | 45.58 |
| Std Dev | 10.67 |

ampl



# B1L001S, U23-ch80

calib\_packv5\_042523\_0143.root, FC#2, port C2

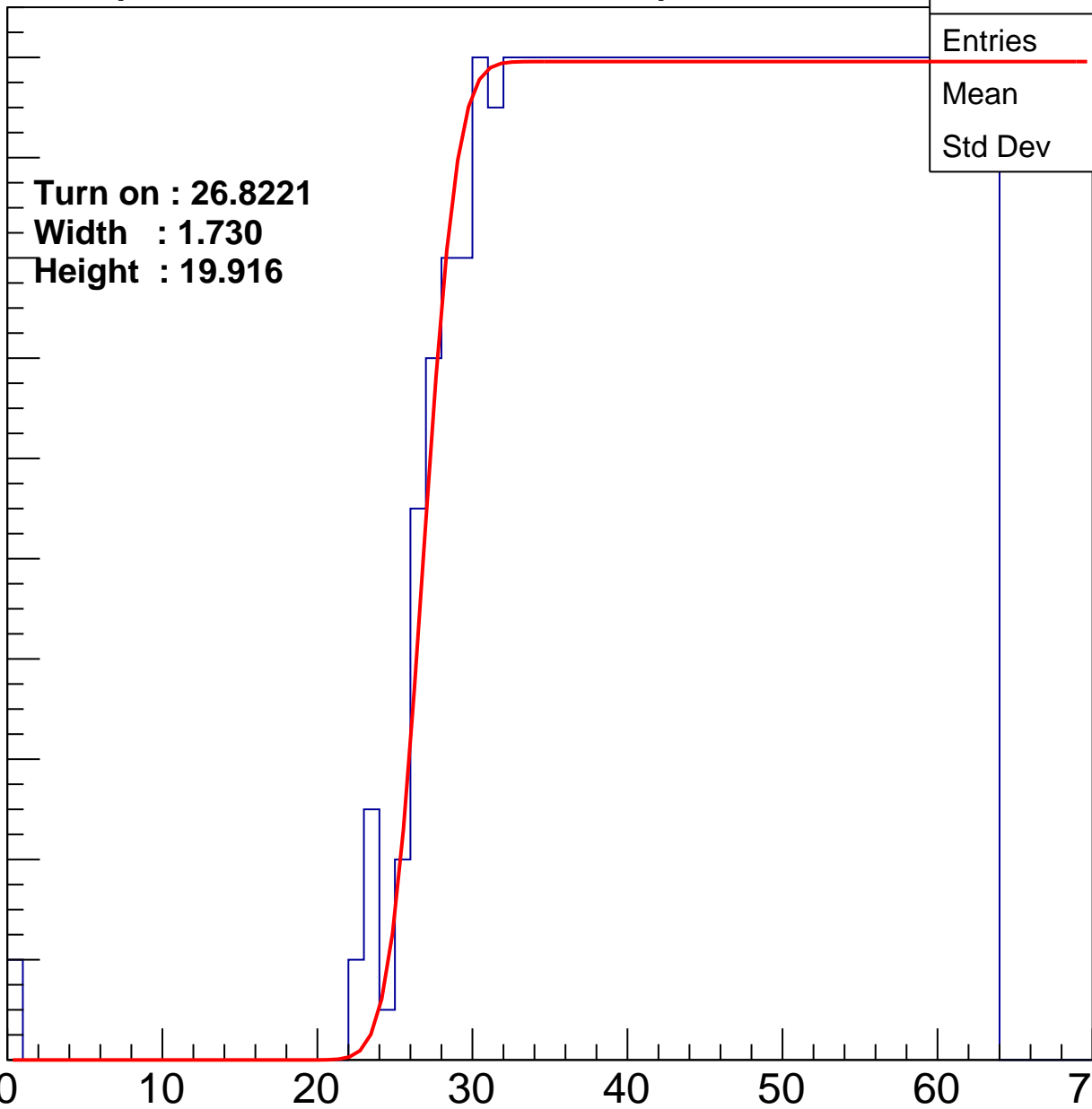
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.8221**  
**Width : 1.730**  
**Height : 19.916**

|         |       |
|---------|-------|
| Entries | 750   |
| Mean    | 44.6  |
| Std Dev | 11.16 |

ampl



# B1L001S, U23-ch81

calib\_packv5\_042523\_0143.root, FC#2, port C2

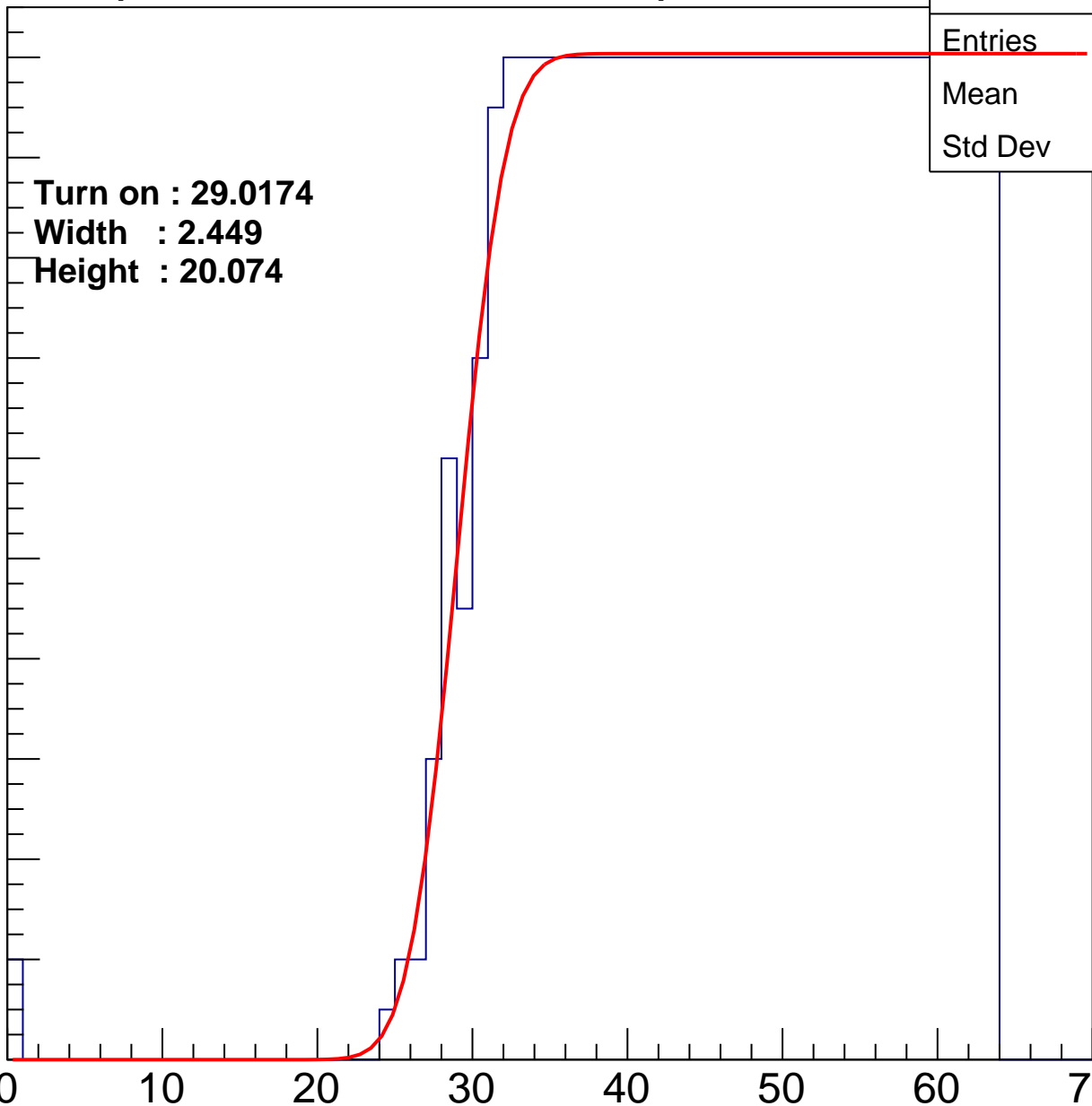
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.0174**  
**Width : 2.449**  
**Height : 20.074**

|         |       |
|---------|-------|
| Entries | 707   |
| Mean    | 45.68 |
| Std Dev | 10.56 |

ampl



# B1L001S, U23-ch82

calib\_packv5\_042523\_0143.root, FC#2, port C2

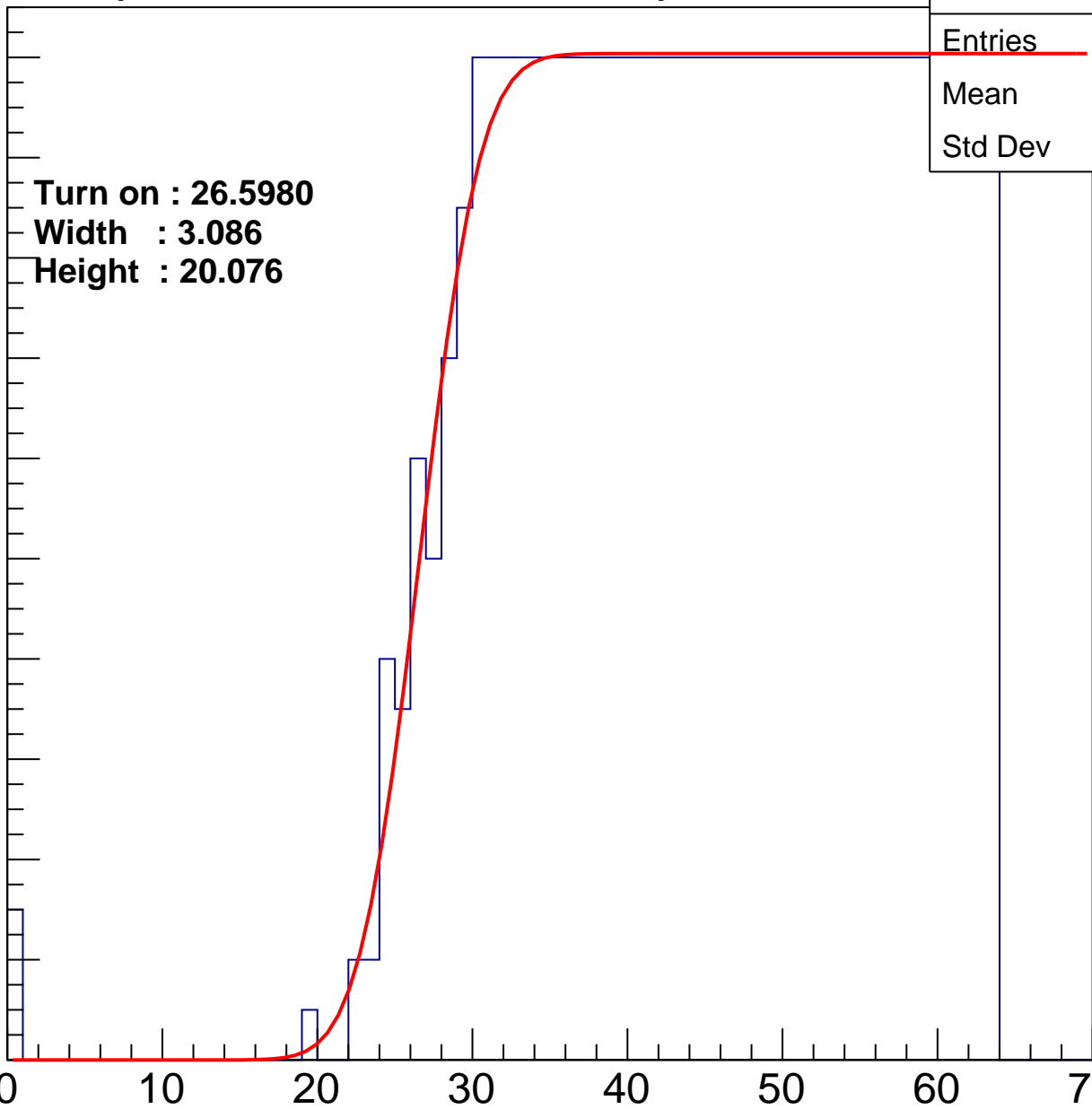
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.5980**  
**Width : 3.086**  
**Height : 20.076**

|         |       |
|---------|-------|
| Entries | 756   |
| Mean    | 44.4  |
| Std Dev | 11.37 |

ampl



# B1L001S, U23-ch83

calib\_packv5\_042523\_0143.root, FC#2, port C2

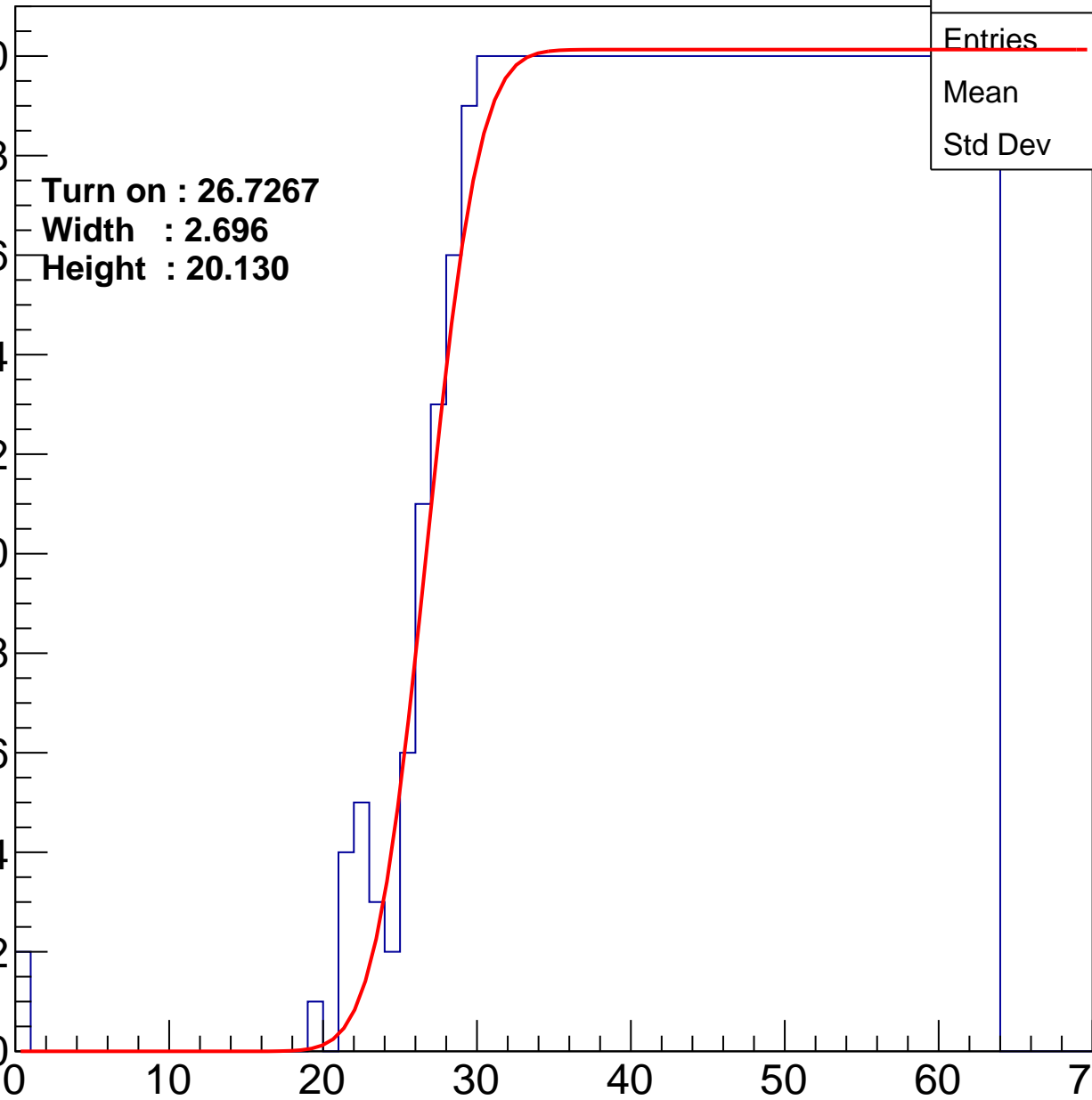
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.7267**  
**Width : 2.696**  
**Height : 20.130**

|         |       |
|---------|-------|
| Entries | 762   |
| Mean    | 44.27 |
| Std Dev | 11.38 |

ampl



# B1L001S, U23-ch84

calib\_packv5\_042523\_0143.root, FC#2, port C2

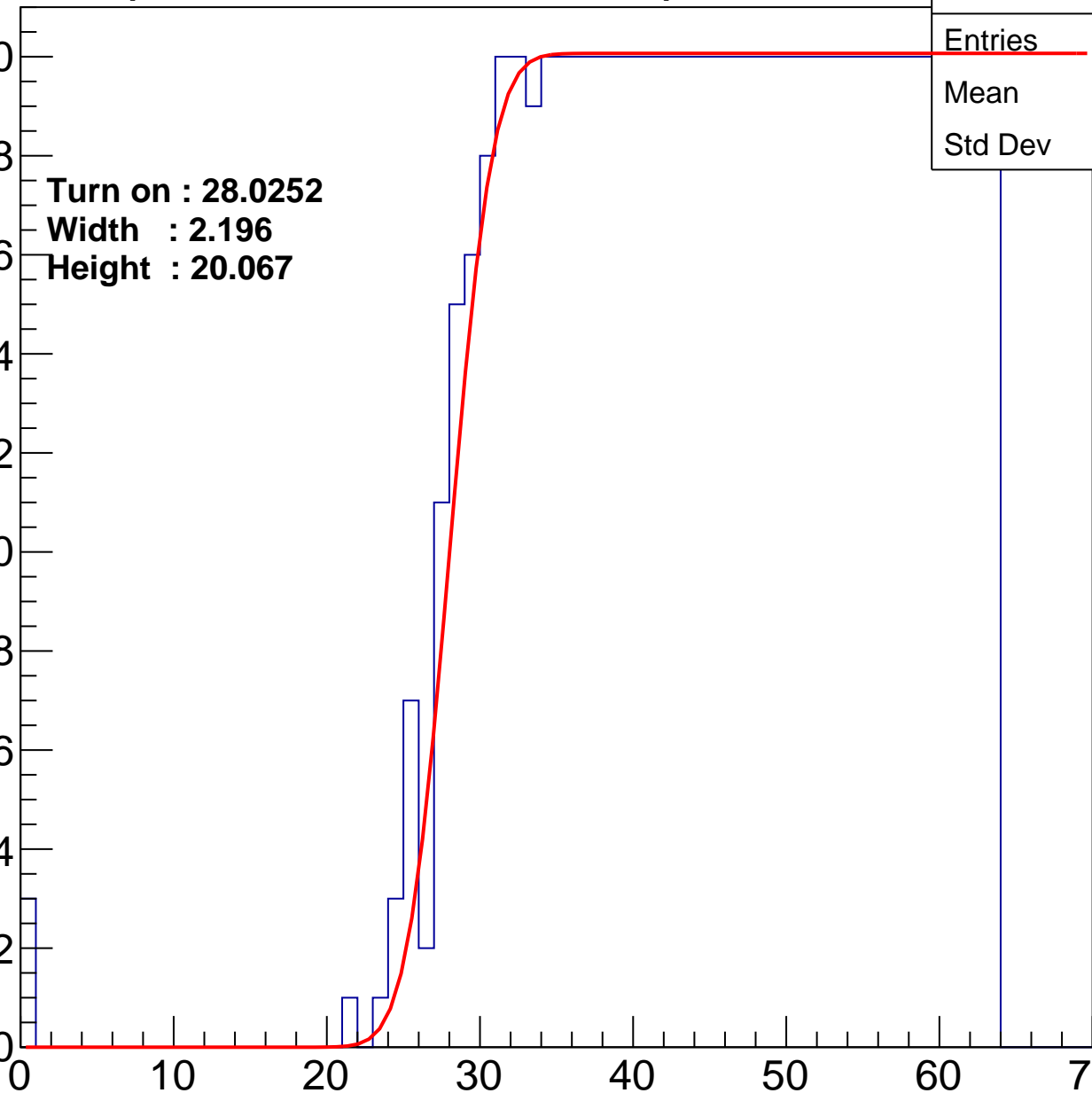
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.0252**  
**Width : 2.196**  
**Height : 20.067**

|         |       |
|---------|-------|
| Entries | 736   |
| Mean    | 44.91 |
| Std Dev | 11.08 |

ampl



# B1L001S, U23-ch85

calib\_packv5\_042523\_0143.root, FC#2, port C2

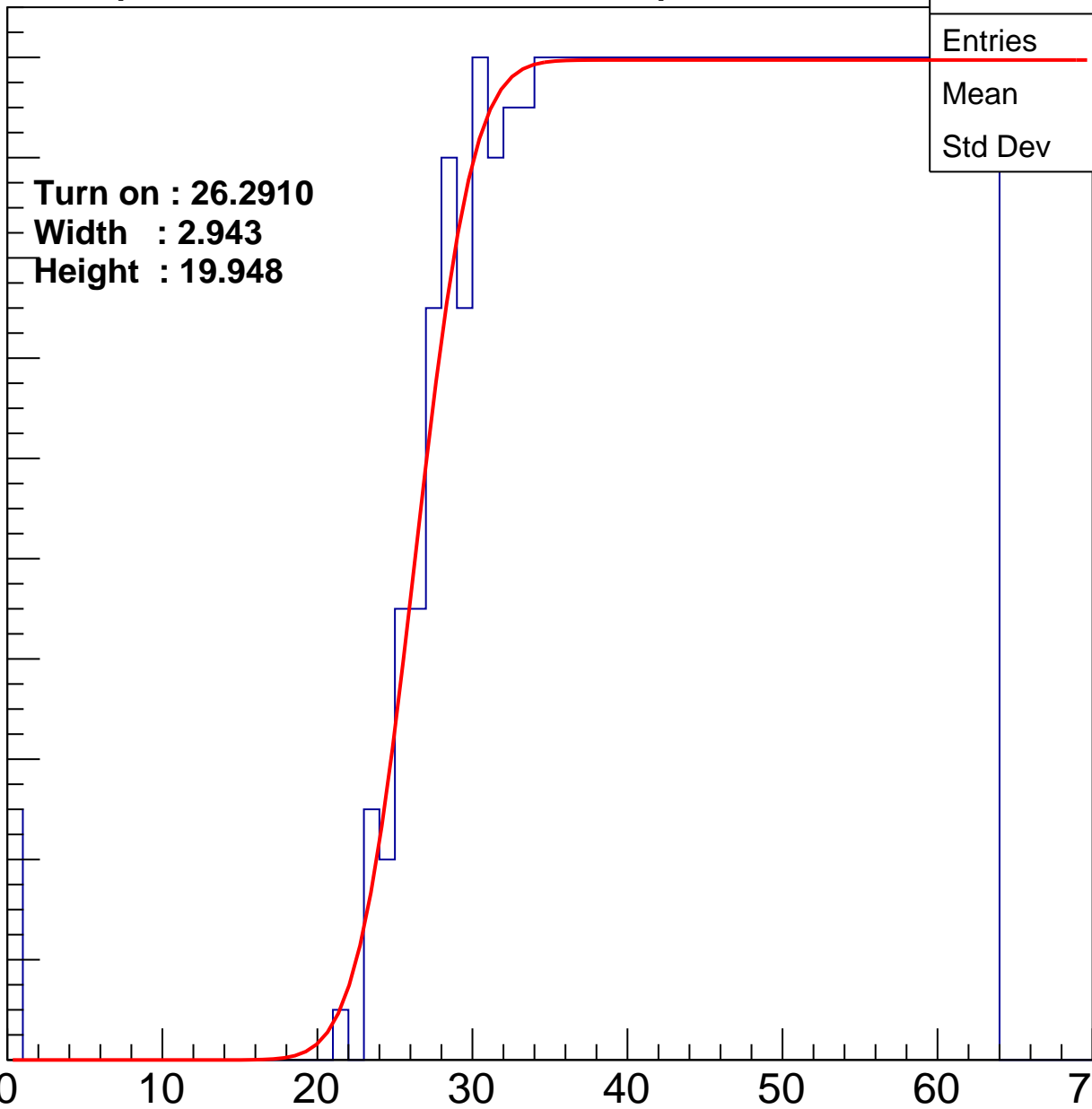
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.2910**  
**Width : 2.943**  
**Height : 19.948**

|         |       |
|---------|-------|
| Entries | 757   |
| Mean    | 44.29 |
| Std Dev | 11.58 |

ampl



# B1L001S, U23-ch86

calib\_packv5\_042523\_0143.root, FC#2, port C2

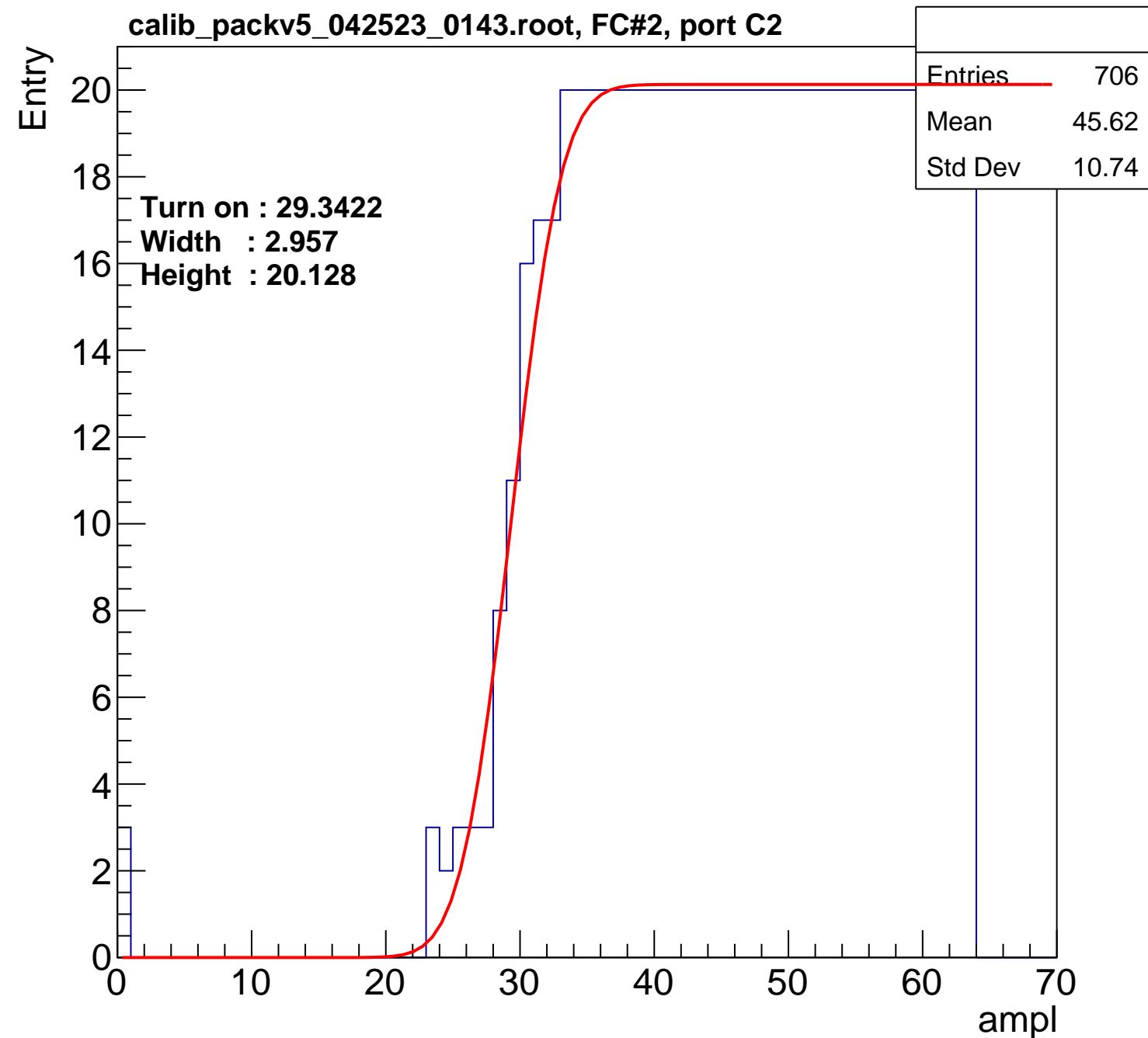
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.3422**  
**Width : 2.957**  
**Height : 20.128**

|         |       |
|---------|-------|
| Entries | 706   |
| Mean    | 45.62 |
| Std Dev | 10.74 |

ampl





# B1L001S, U23-ch87

calib\_packv5\_042523\_0143.root, FC#2, port C2

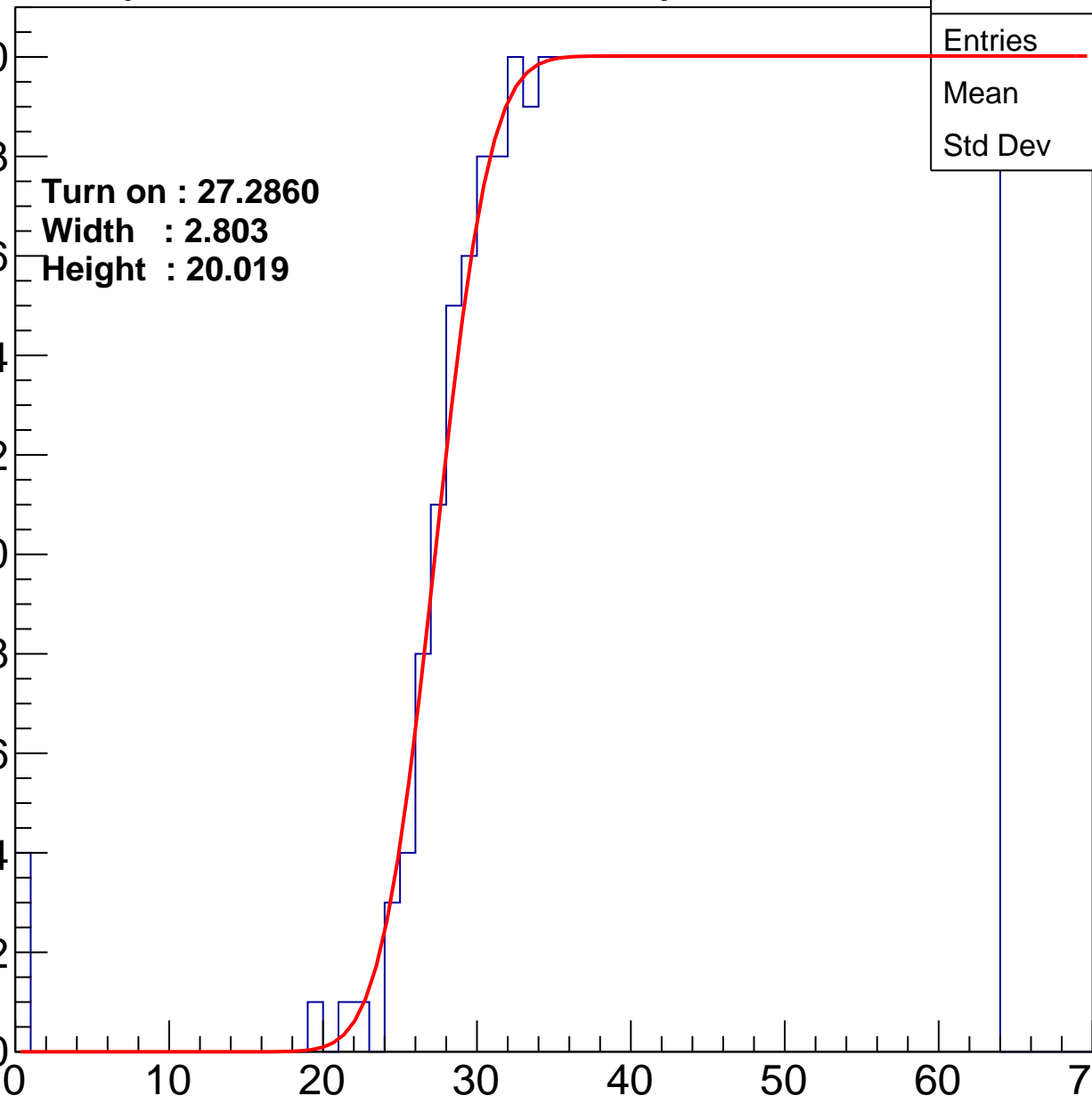
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2860**  
**Width : 2.803**  
**Height : 20.019**

|         |       |
|---------|-------|
| Entries | 739   |
| Mean    | 44.77 |
| Std Dev | 11.26 |

ampl



# B1L001S, U23-ch88

calib\_packv5\_042523\_0143.root, FC#2, port C2

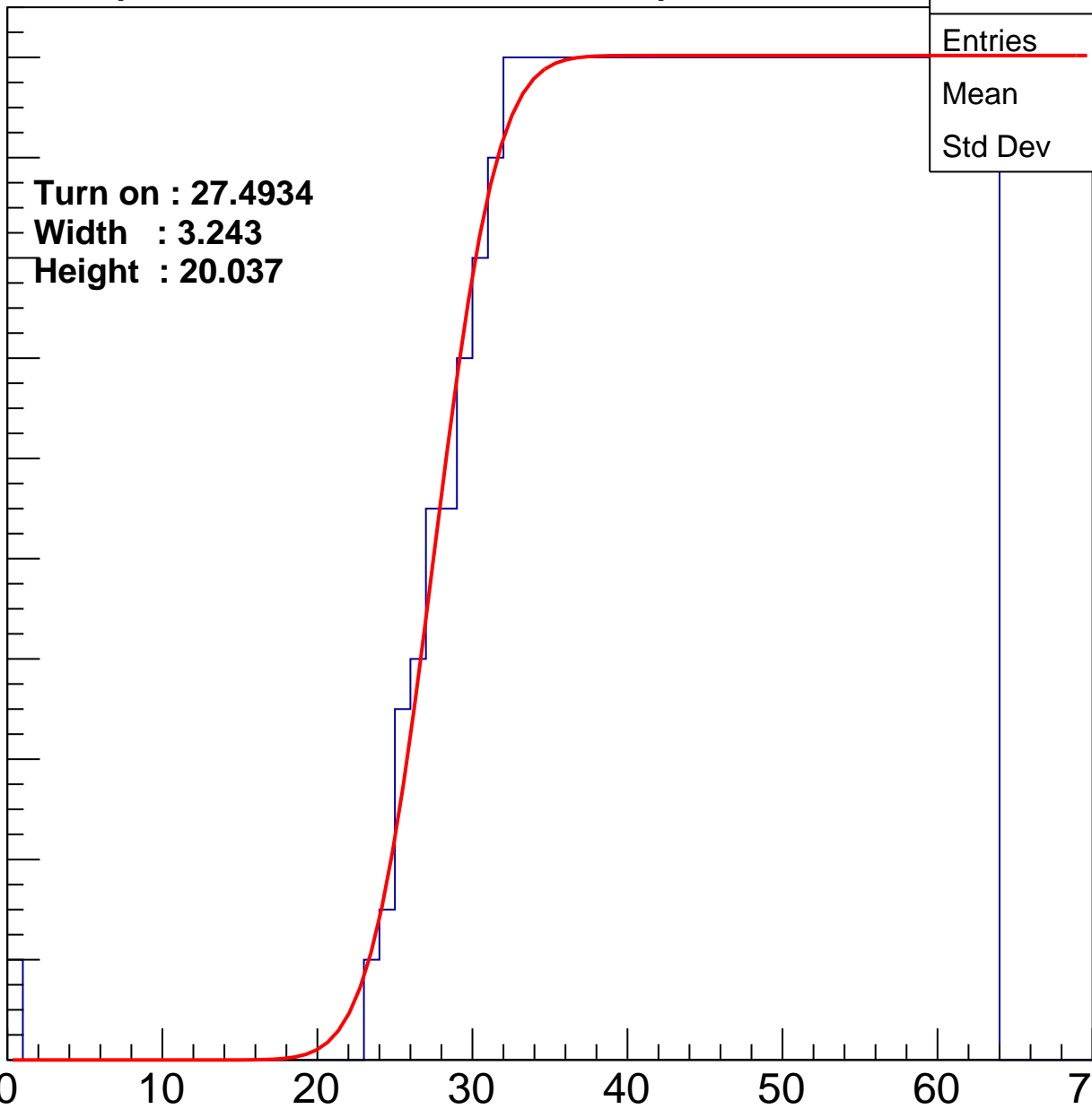
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.4934**  
**Width : 3.243**  
**Height : 20.037**

|         |       |
|---------|-------|
| Entries | 732   |
| Mean    | 45.01 |
| Std Dev | 10.97 |

ampl



# B1L001S, U23-ch89

calib\_packv5\_042523\_0143.root, FC#2, port C2

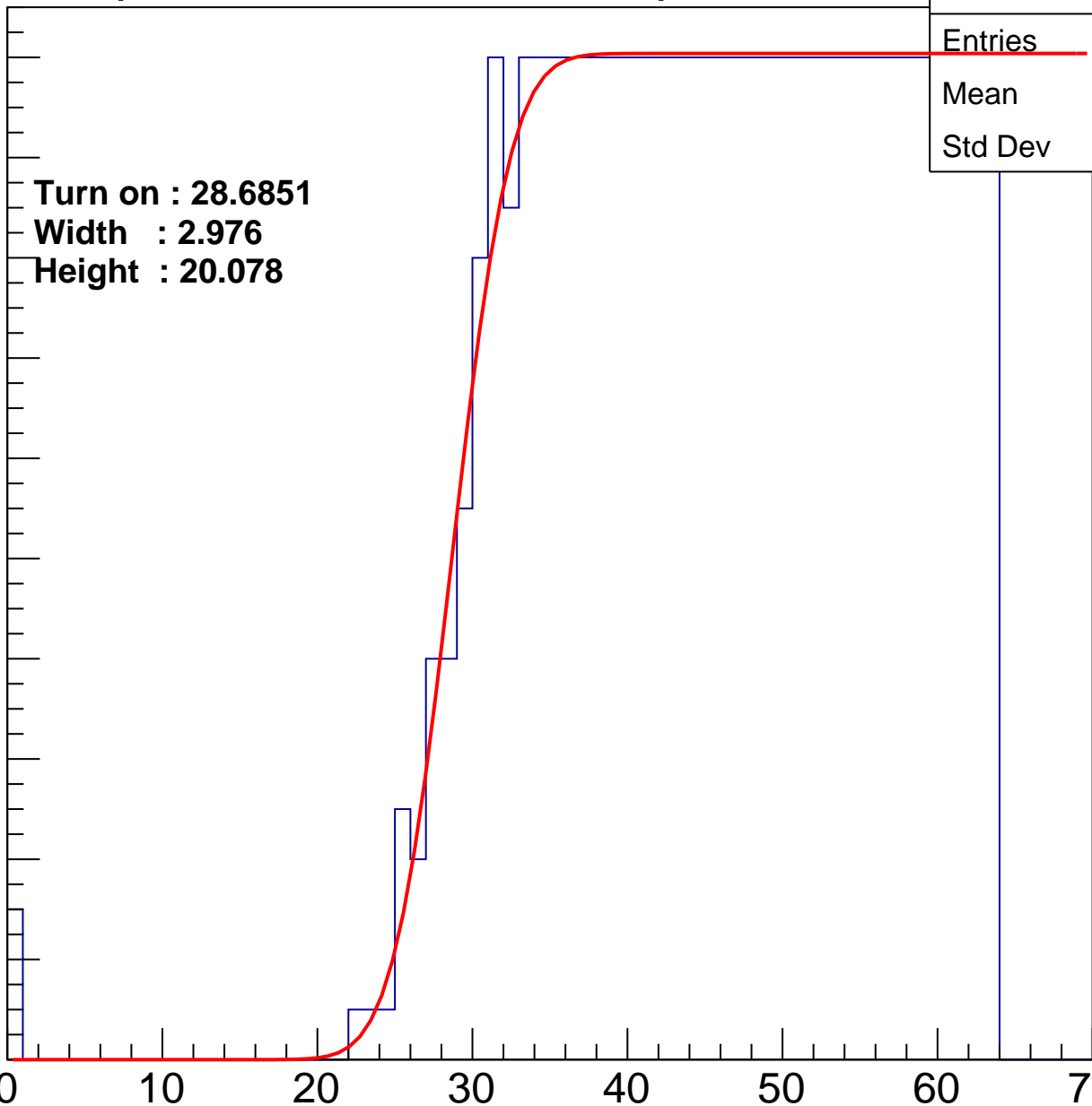
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.6851  
Width : 2.976  
Height : 20.078

|         |       |
|---------|-------|
| Entries | 715   |
| Mean    | 45.4  |
| Std Dev | 10.85 |

ampl



# B1L001S, U23-ch90

calib\_packv5\_042523\_0143.root, FC#2, port C2

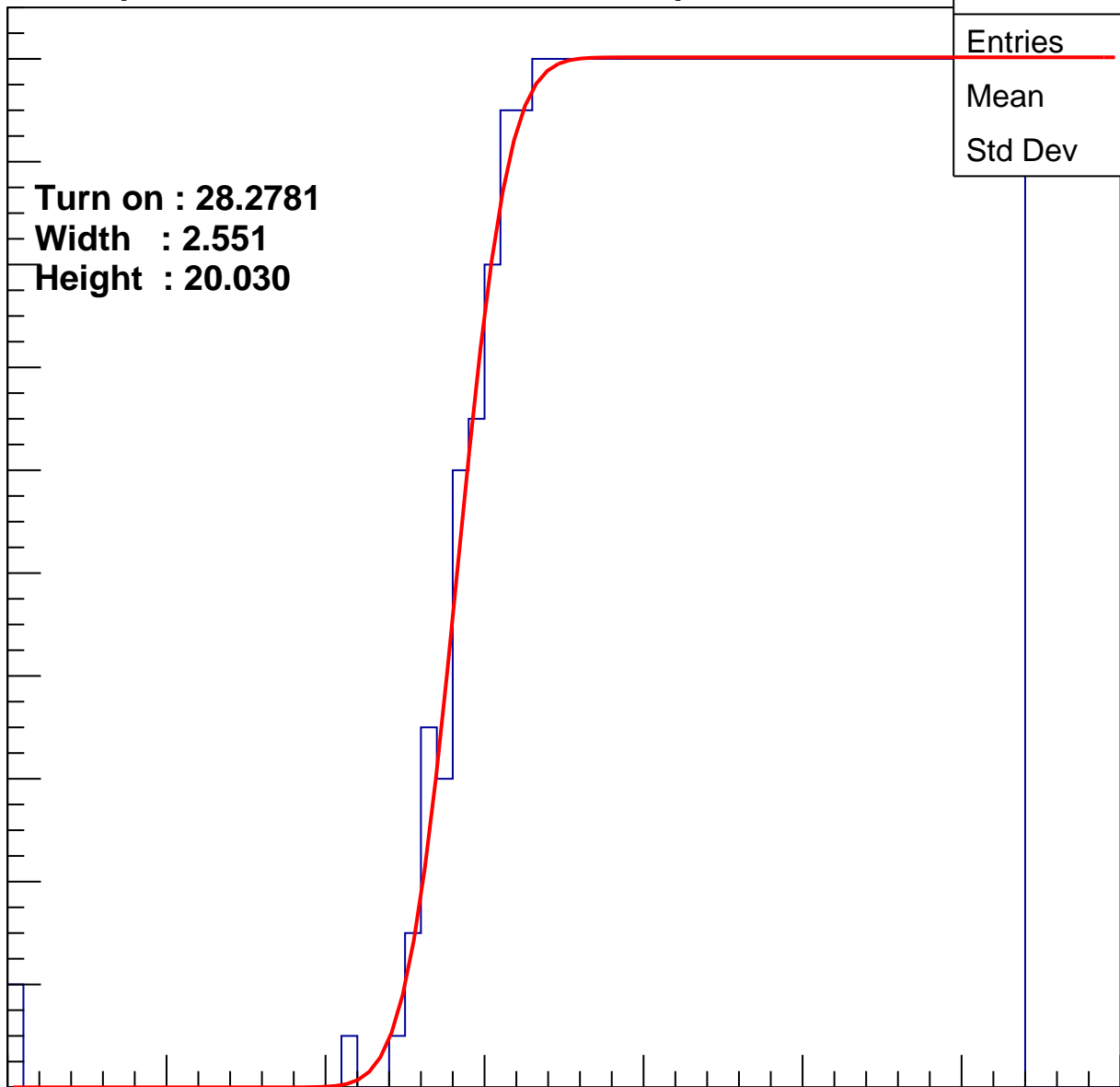
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.2781**  
**Width : 2.551**  
**Height : 20.030**

|         |       |
|---------|-------|
| Entries | 719   |
| Mean    | 45.36 |
| Std Dev | 10.75 |

ampl



# B1L001S, U23-ch91

calib\_packv5\_042523\_0143.root, FC#2, port C2

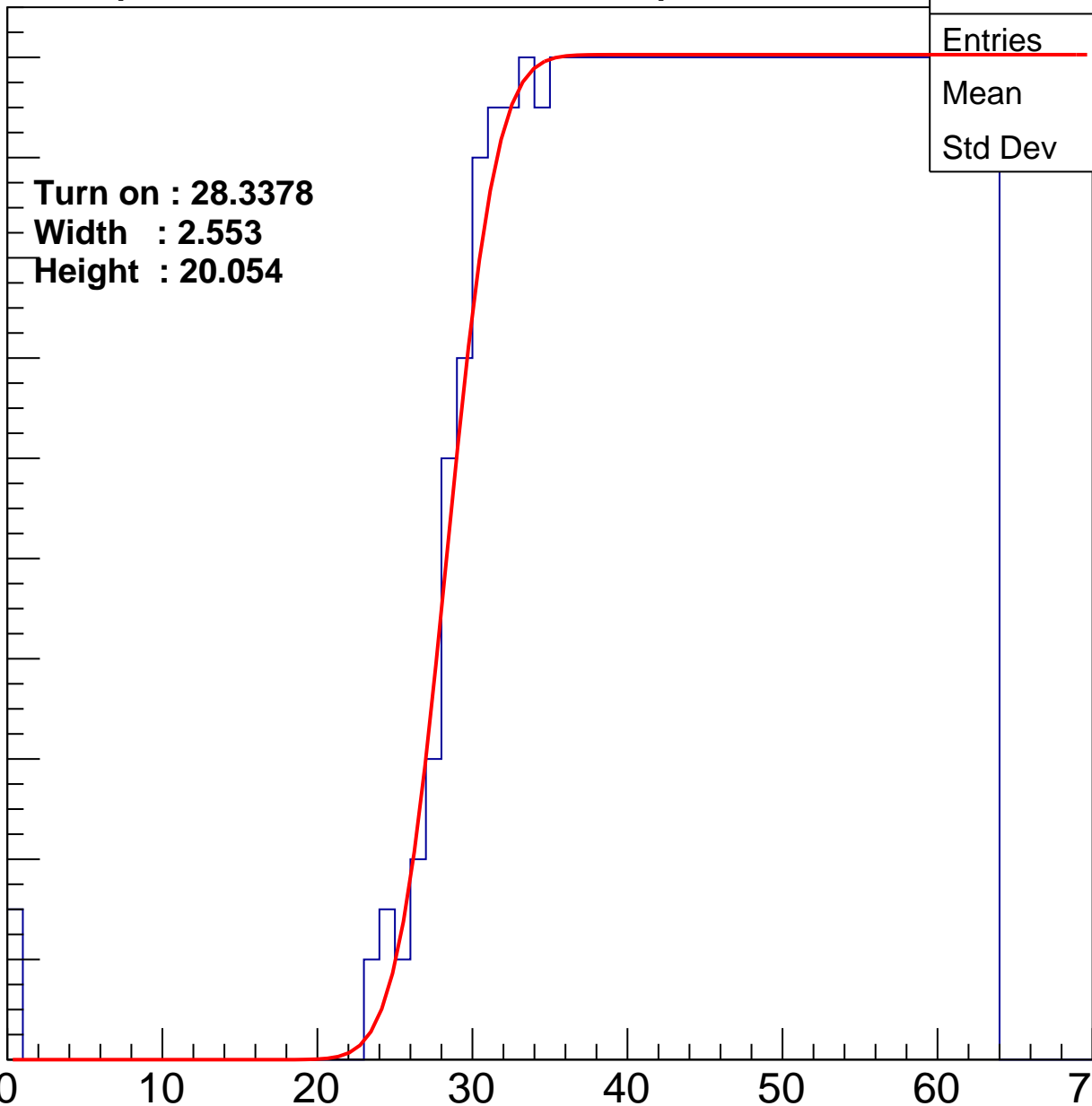
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.3378**  
**Width : 2.553**  
**Height : 20.054**

|         |       |
|---------|-------|
| Entries | 721   |
| Mean    | 45.27 |
| Std Dev | 10.9  |

ampl



# B1L001S, U23-ch92

calib\_packv5\_042523\_0143.root, FC#2, port C2

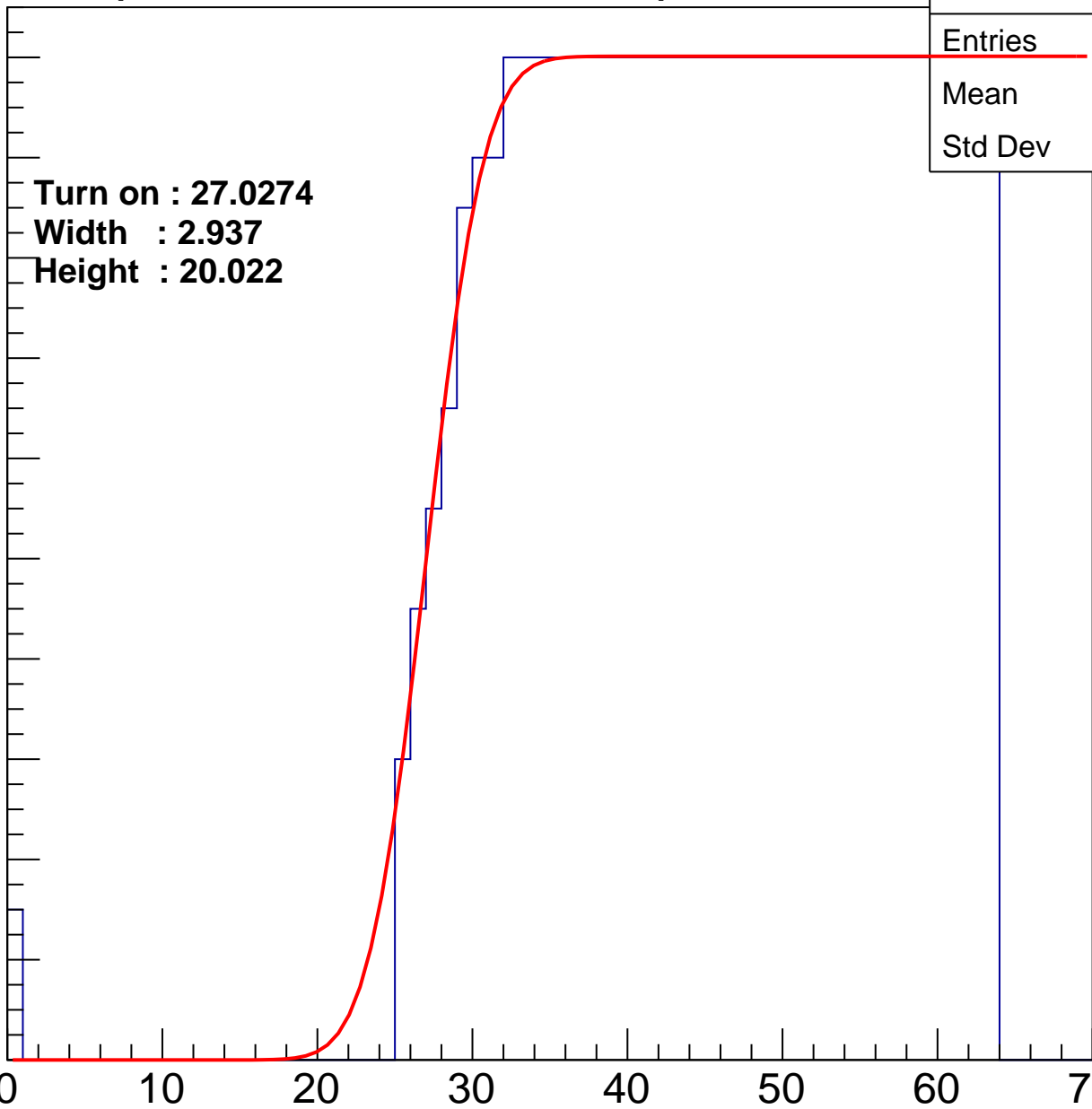
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.0274**  
**Width : 2.937**  
**Height : 20.022**

|         |       |
|---------|-------|
| Entries | 735   |
| Mean    | 44.95 |
| Std Dev | 11.04 |

ampl



# B1L001S, U23-ch93

calib\_packv5\_042523\_0143.root, FC#2, port C2

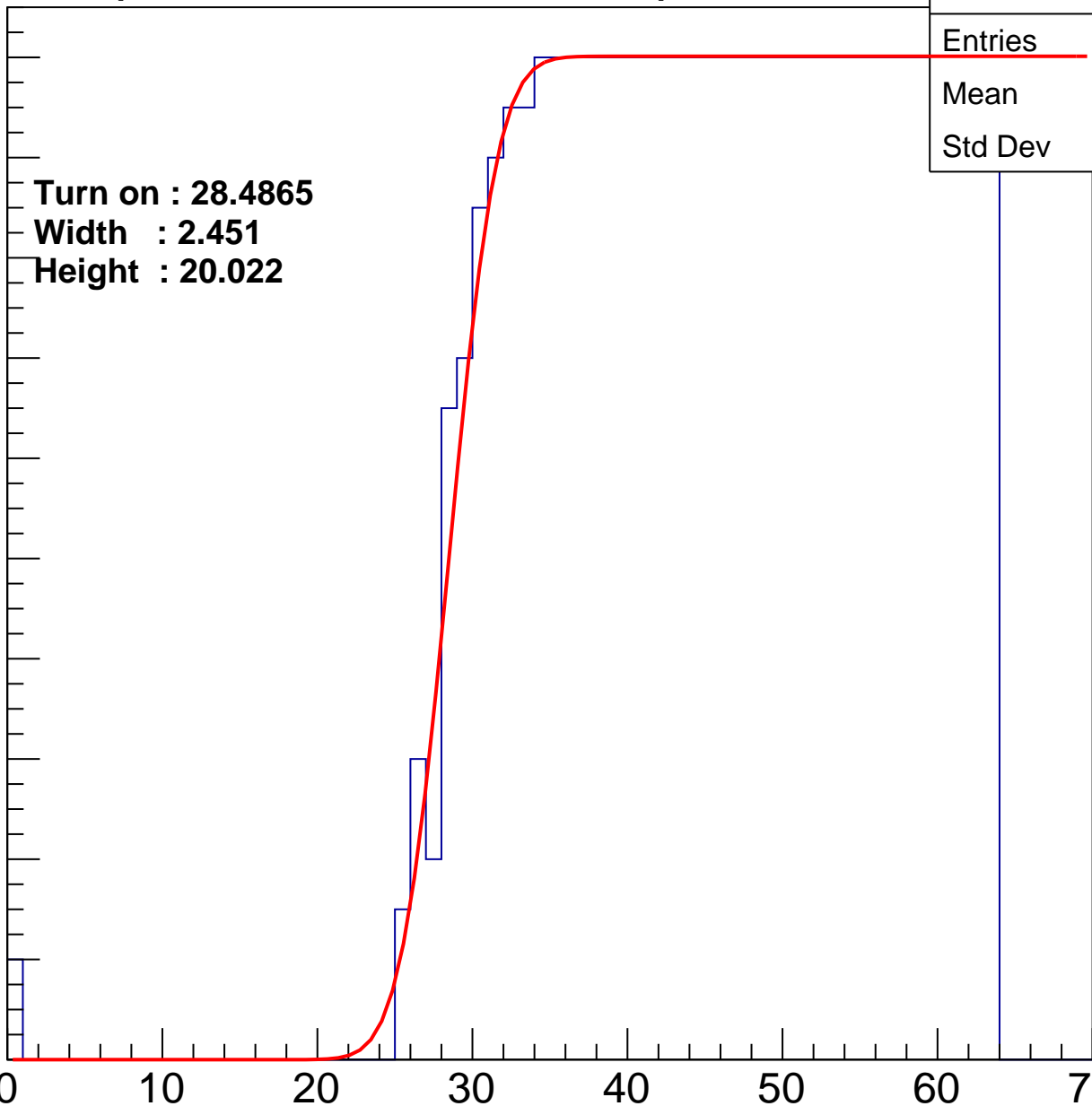
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.4865**  
**Width : 2.451**  
**Height : 20.022**

|         |       |
|---------|-------|
| Entries | 715   |
| Mean    | 45.47 |
| Std Dev | 10.68 |

ampl



# B1L001S, U23-ch94

calib\_packv5\_042523\_0143.root, FC#2, port C2

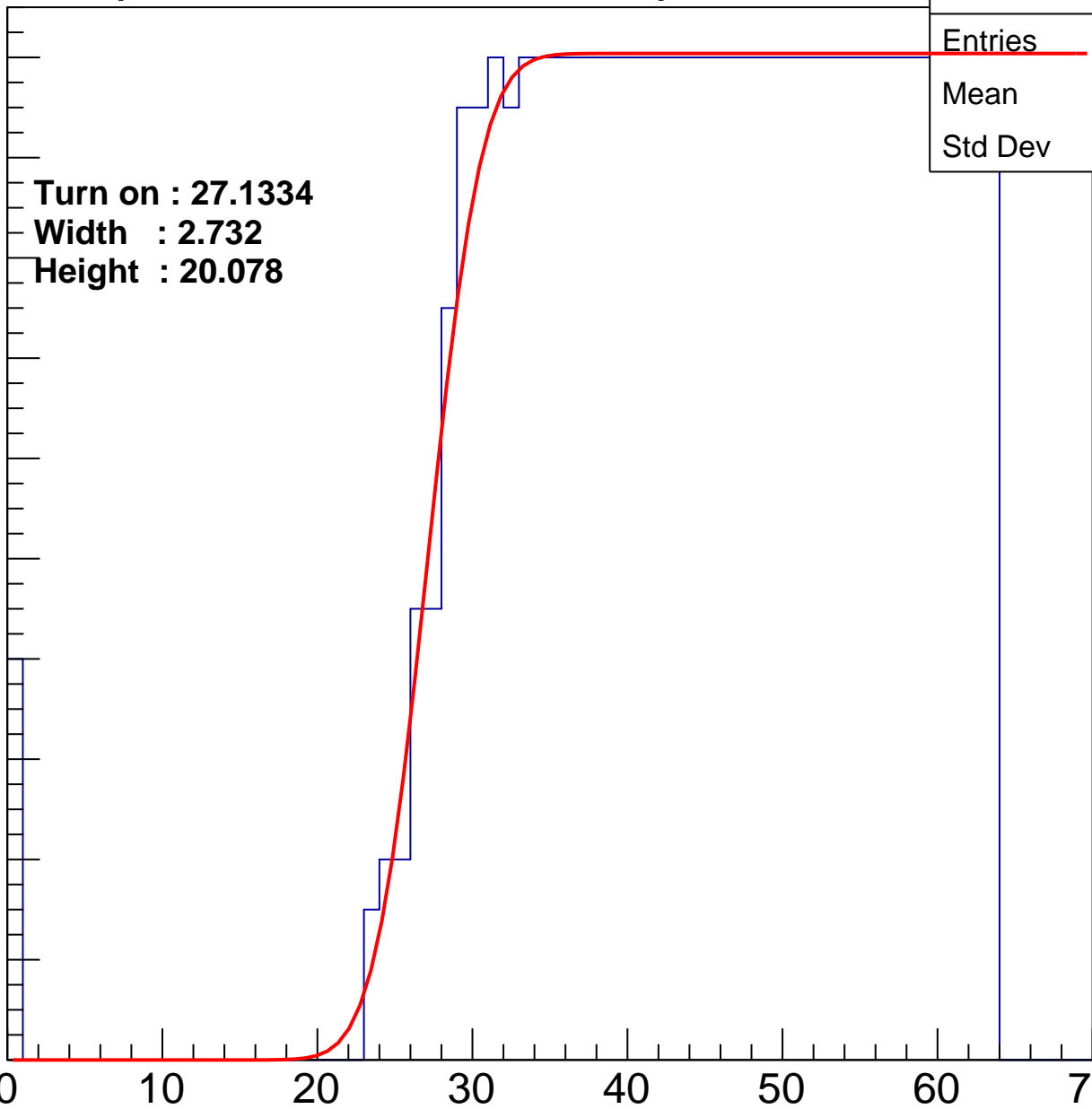
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.1334  
Width : 2.732  
Height : 20.078

|         |       |
|---------|-------|
| Entries | 749   |
| Mean    | 44.42 |
| Std Dev | 11.71 |

ampl





# B1L001S, U23-ch95

calib\_packv5\_042523\_0143.root, FC#2, port C2

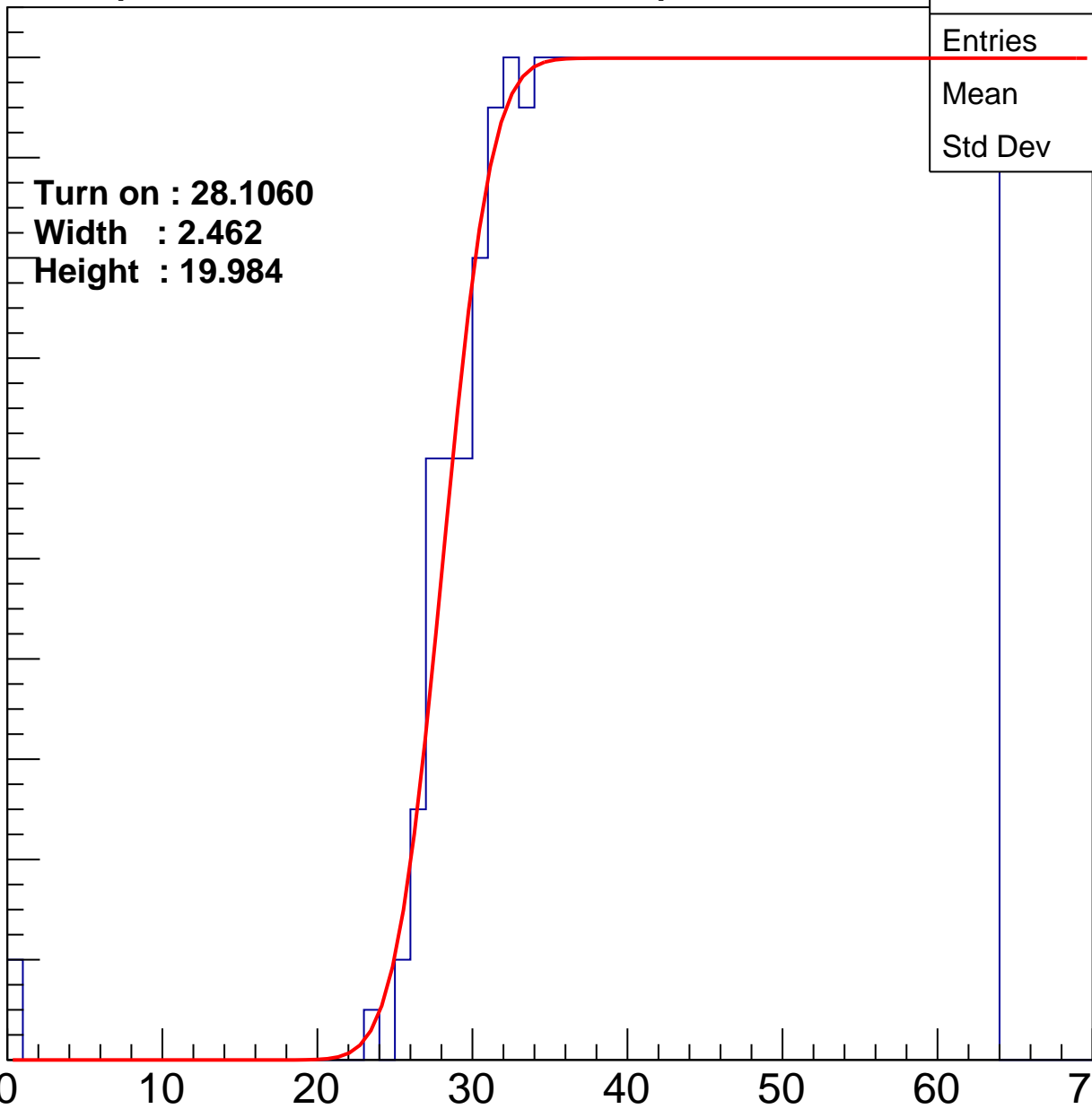
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.1060**  
**Width : 2.462**  
**Height : 19.984**

|         |       |
|---------|-------|
| Entries | 720   |
| Mean    | 45.34 |
| Std Dev | 10.75 |

ampl



# B1L001S, U23-ch96

calib\_packv5\_042523\_0143.root, FC#2, port C2

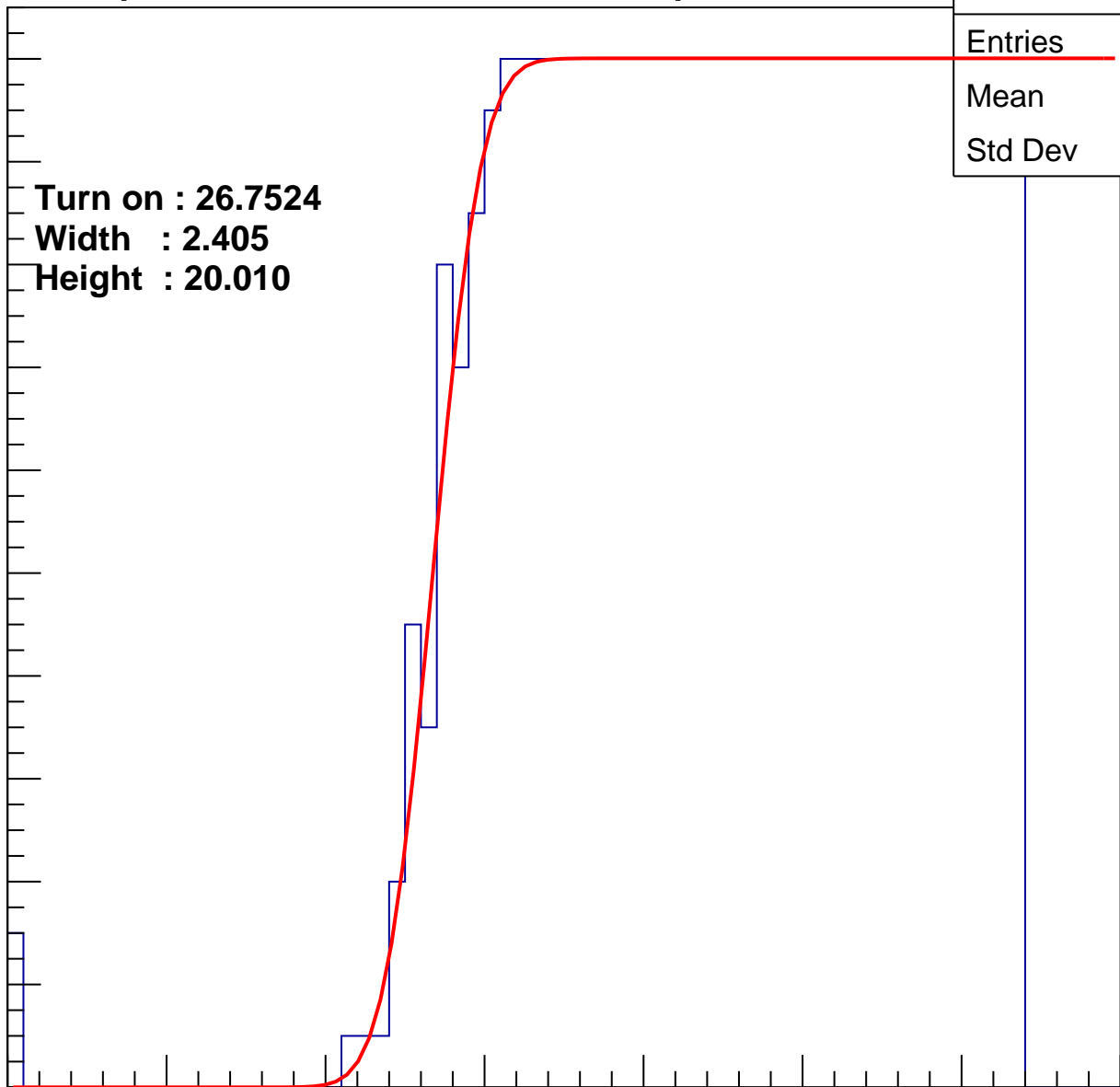
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.7524  
Width : 2.405  
Height : 20.010

|         |       |
|---------|-------|
| Entries | 752   |
| Mean    | 44.52 |
| Std Dev | 11.28 |

ampl



# B1L001S, U23-ch97

calib\_packv5\_042523\_0143.root, FC#2, port C2

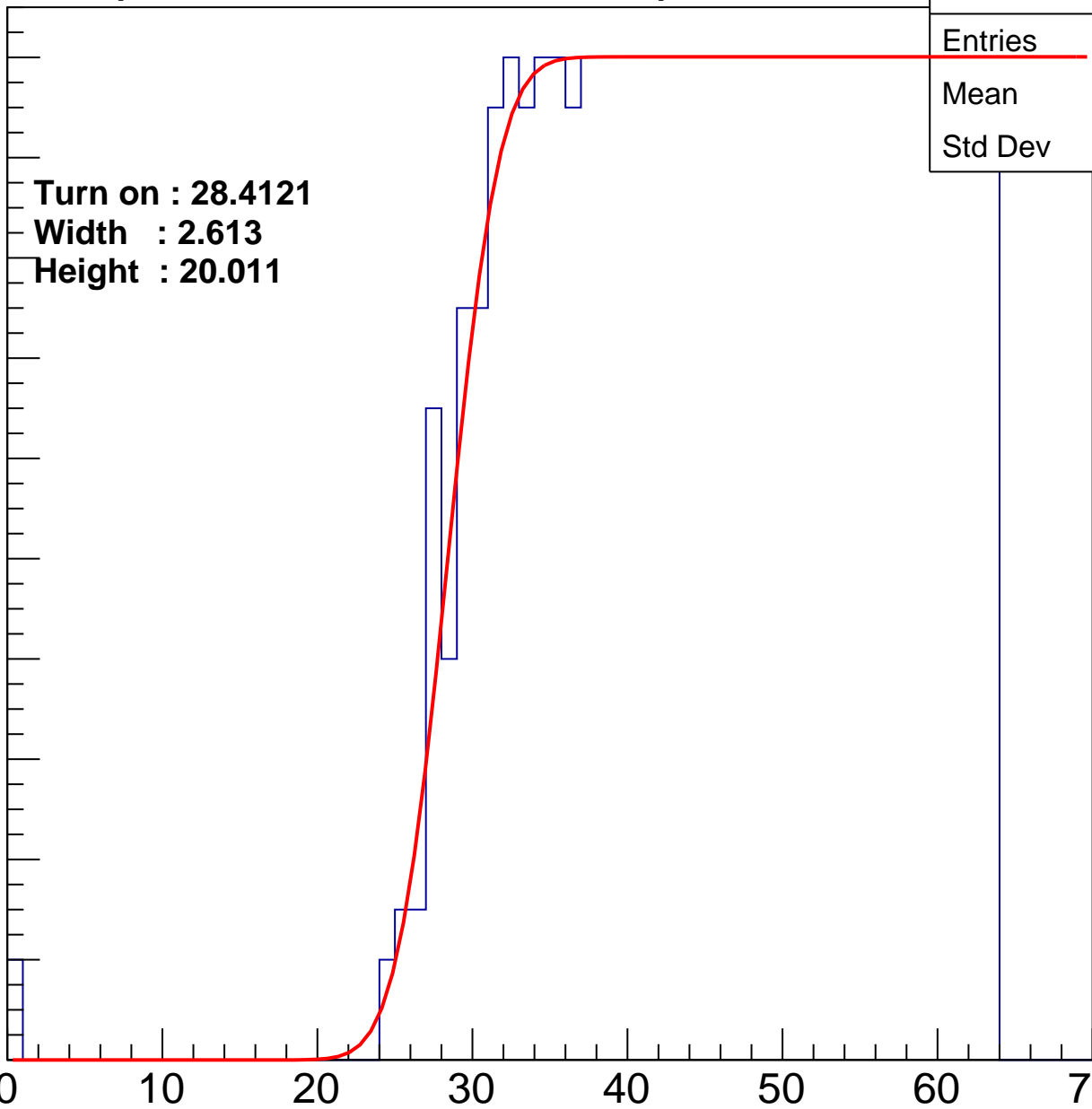
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.4121  
Width : 2.613  
Height : 20.011

|         |       |
|---------|-------|
| Entries | 718   |
| Mean    | 45.38 |
| Std Dev | 10.75 |

ampl



# B1L001S, U23-ch98

calib\_packv5\_042523\_0143.root, FC#2, port C2

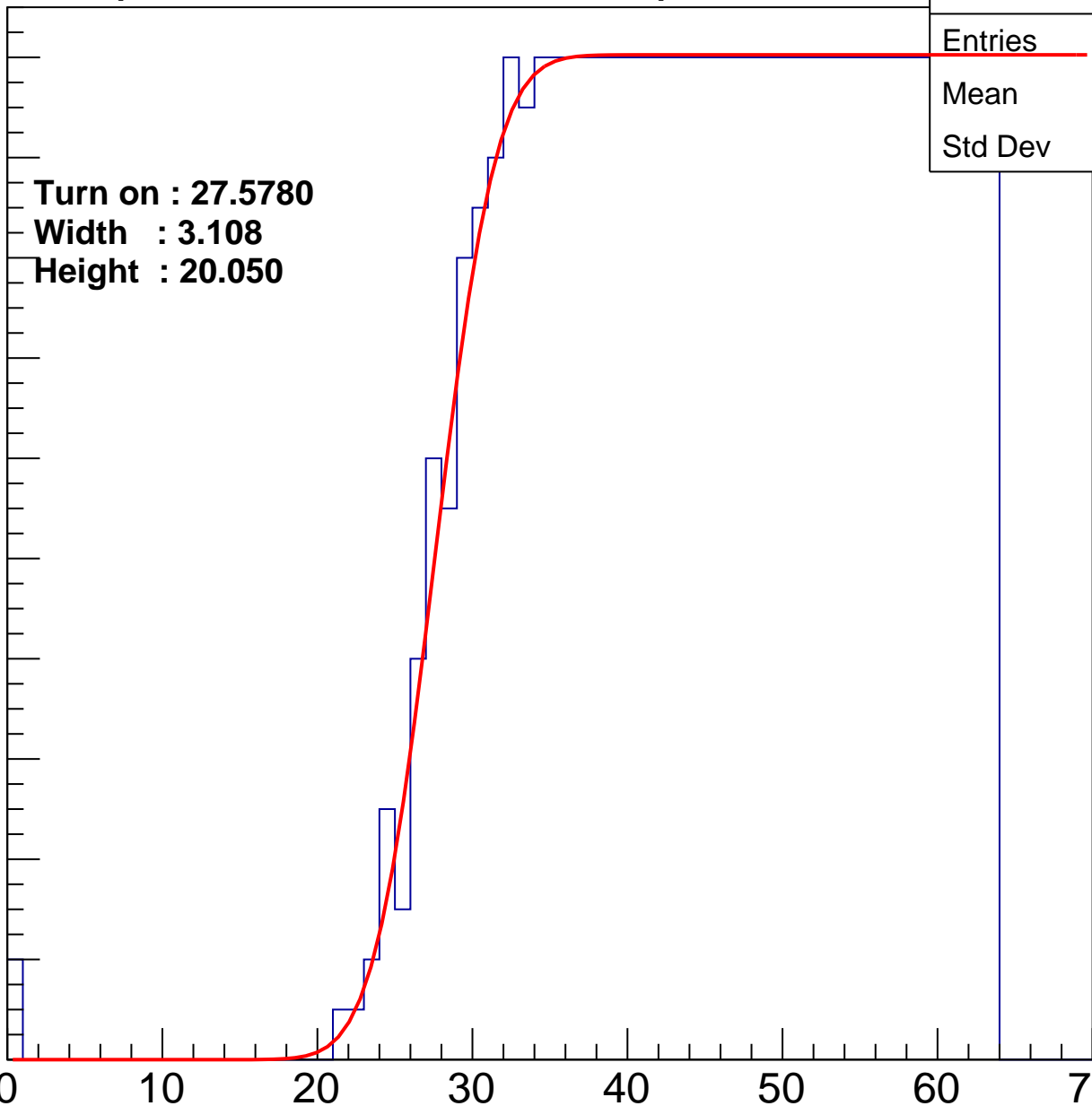
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.5780**  
**Width : 3.108**  
**Height : 20.050**

|         |       |
|---------|-------|
| Entries | 735   |
| Mean    | 44.93 |
| Std Dev | 11.03 |

ampl



# B1L001S, U23-ch99

calib\_packv5\_042523\_0143.root, FC#2, port C2

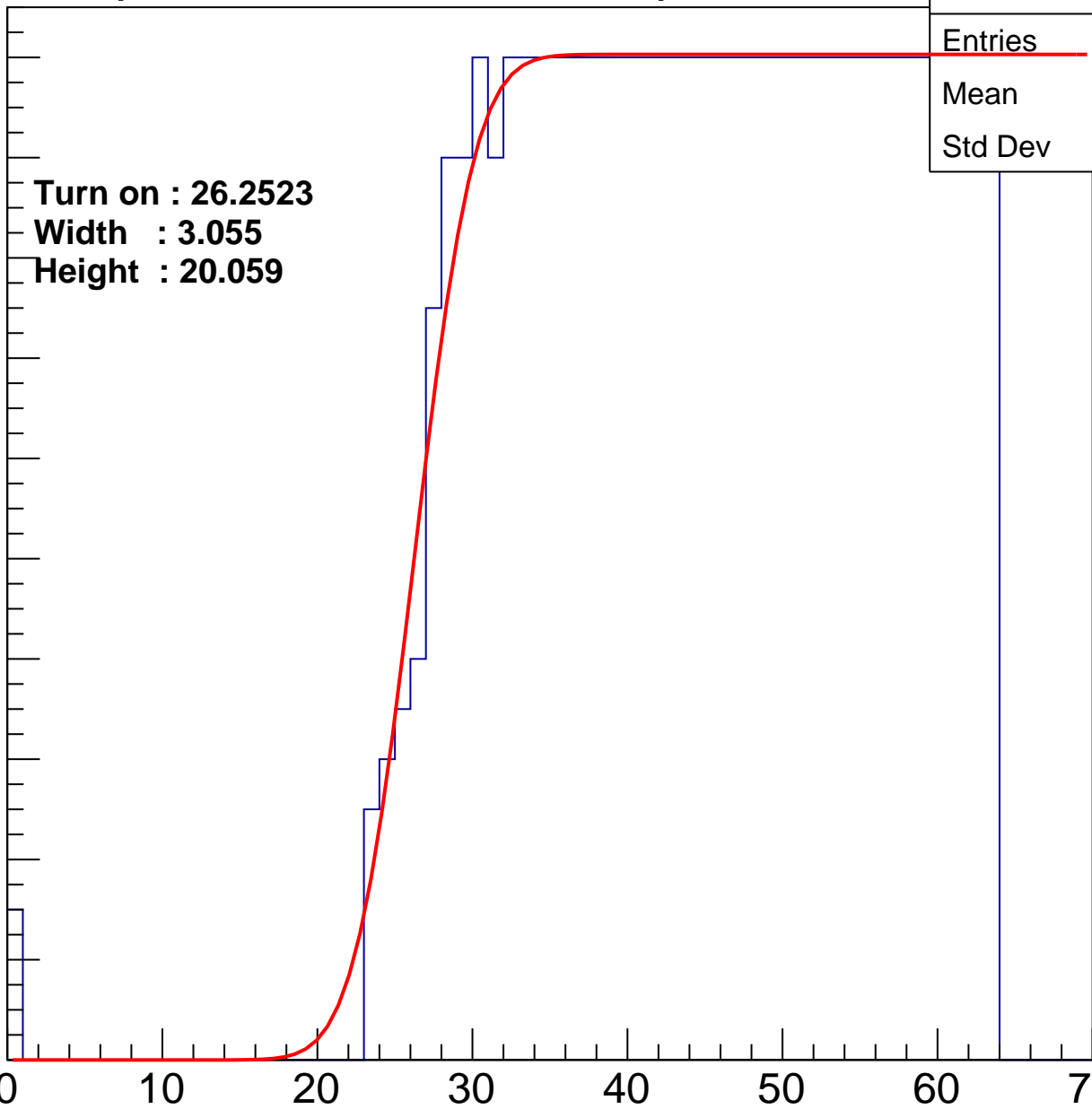
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.2523  
Width : 3.055  
Height : 20.059

|         |       |
|---------|-------|
| Entries | 758   |
| Mean    | 44.37 |
| Std Dev | 11.36 |

ampl



# B1L001S, U23-ch100

calib\_packv5\_042523\_0143.root, FC#2, port C2

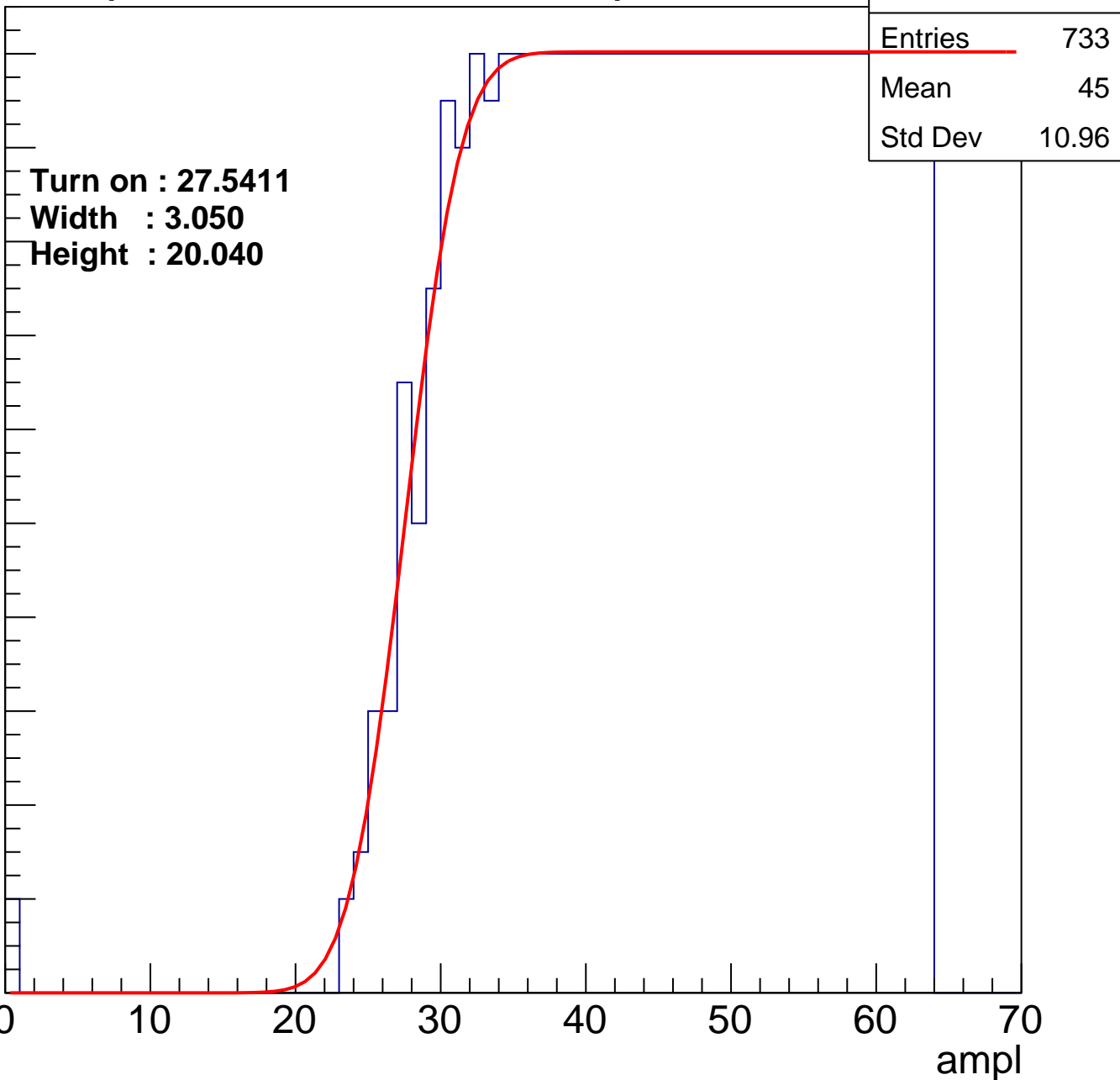
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.5411  
Width : 3.050  
Height : 20.040

|         |       |
|---------|-------|
| Entries | 733   |
| Mean    | 45    |
| Std Dev | 10.96 |

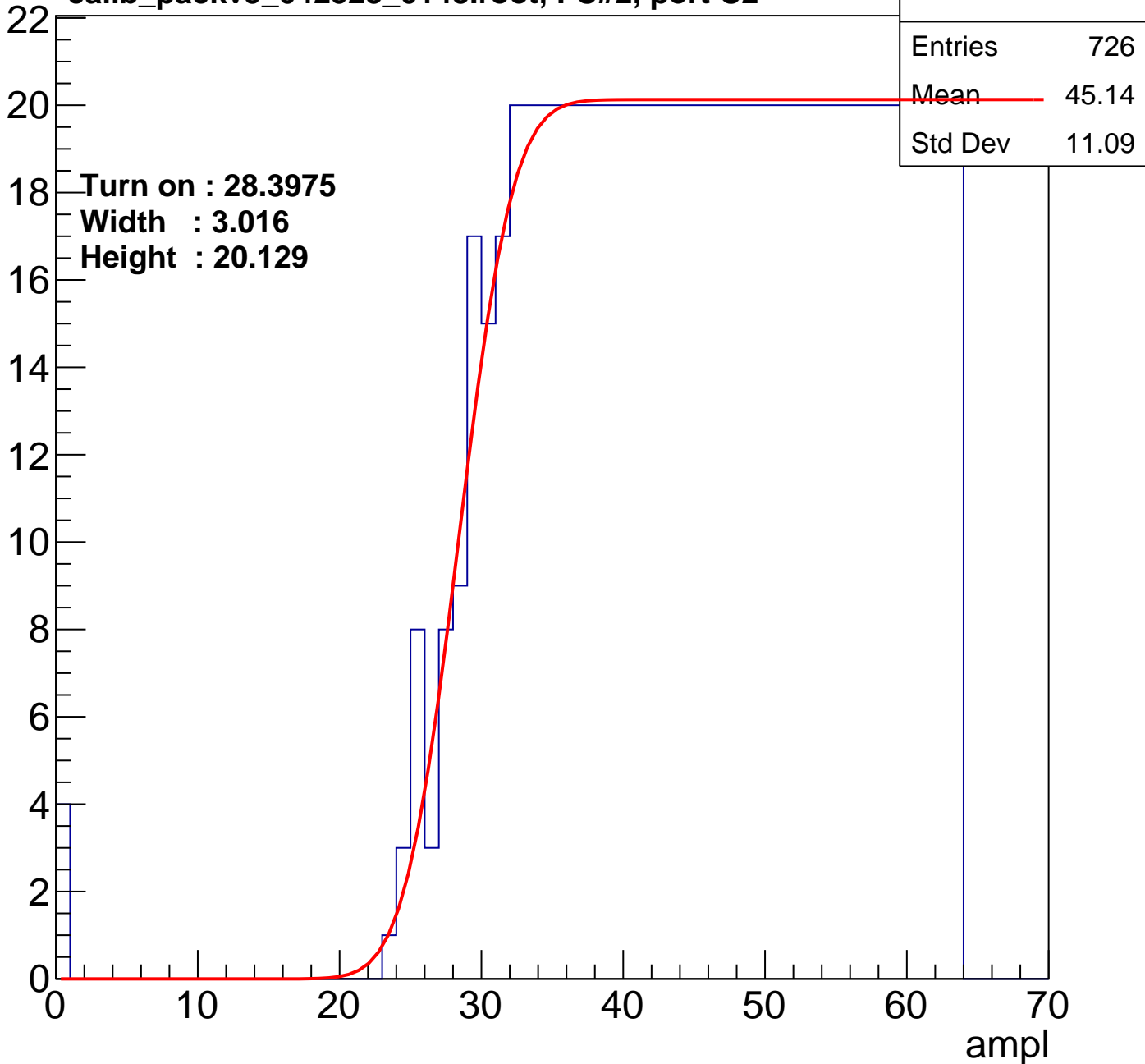
ampl



# B1L001S, U23-ch101

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry



# B1L001S, U23-ch102

calib\_packv5\_042523\_0143.root, FC#2, port C2

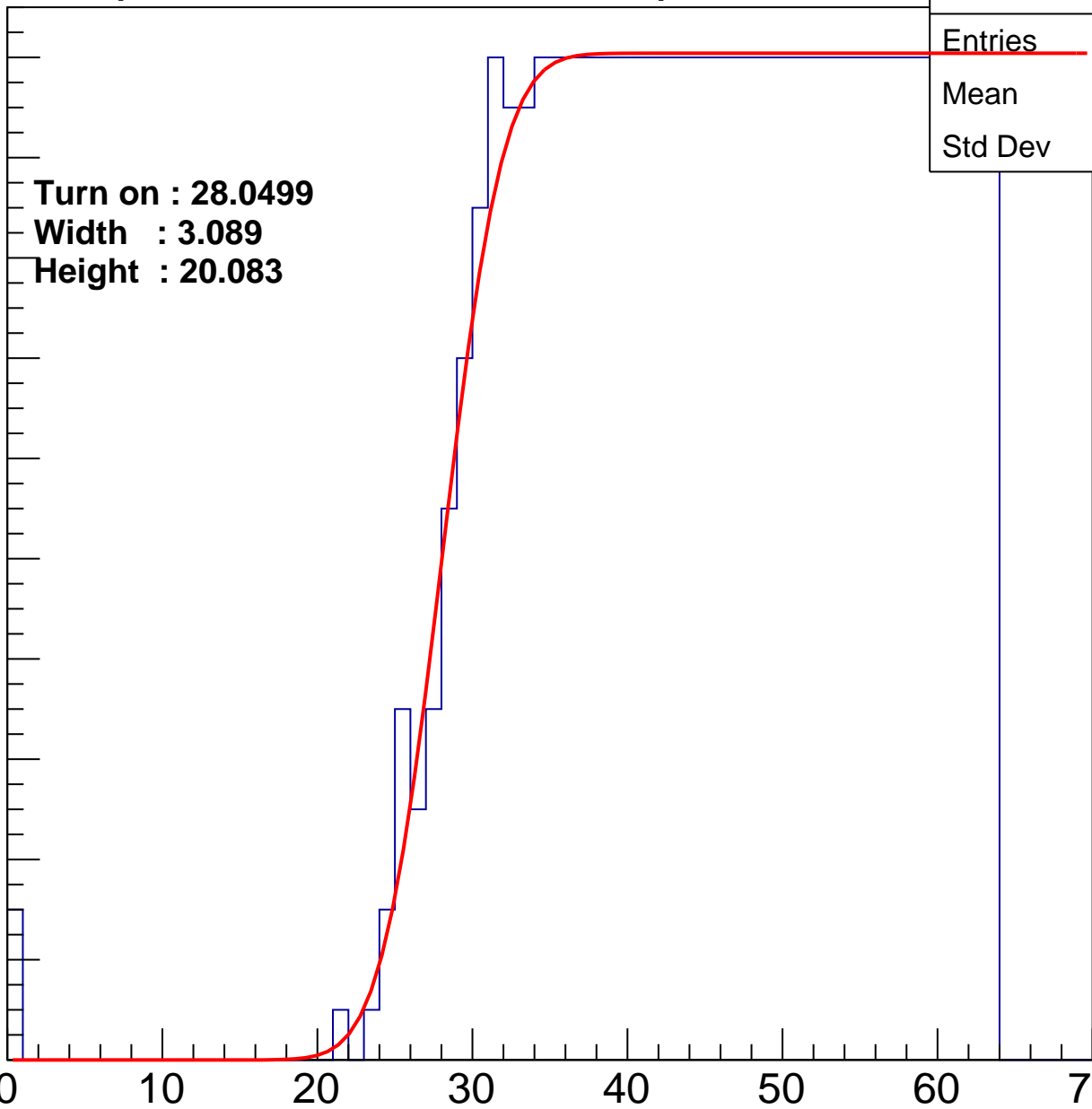
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.0499**  
**Width : 3.089**  
**Height : 20.083**

|         |       |
|---------|-------|
| Entries | 727   |
| Mean    | 45.1  |
| Std Dev | 11.01 |

ampl





# B1L001S, U23-ch103

calib\_packv5\_042523\_0143.root, FC#2, port C2

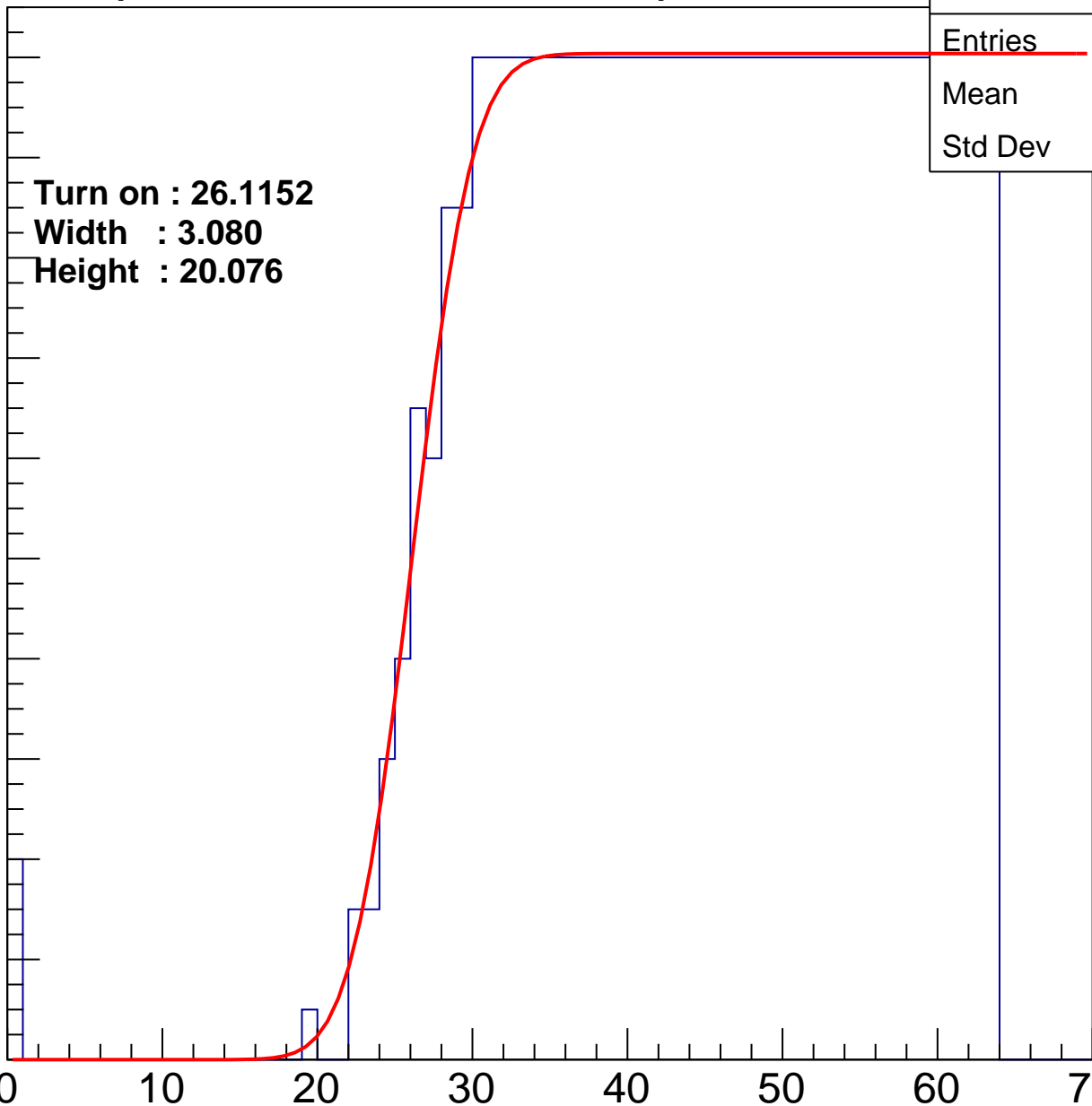
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.1152  
Width : 3.080  
Height : 20.076

|         |       |
|---------|-------|
| Entries | 764   |
| Mean    | 44.17 |
| Std Dev | 11.55 |

ampl



# B1L001S, U23-ch104

calib\_packv5\_042523\_0143.root, FC#2, port C2

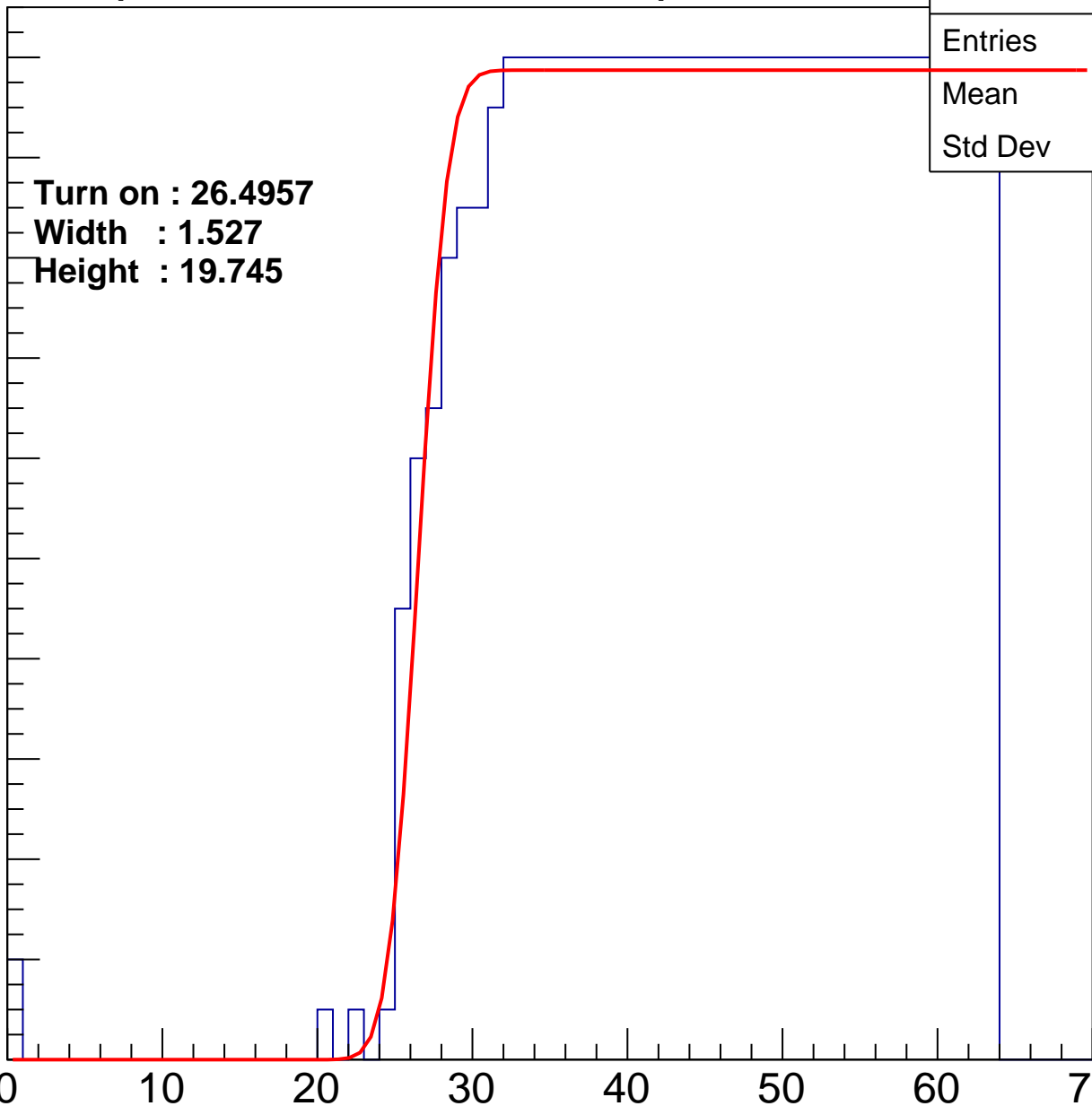
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.4957**  
**Width : 1.527**  
**Height : 19.745**

|         |       |
|---------|-------|
| Entries | 748   |
| Mean    | 44.64 |
| Std Dev | 11.14 |

ampl



# B1L001S, U23-ch105

calib\_packv5\_042523\_0143.root, FC#2, port C2

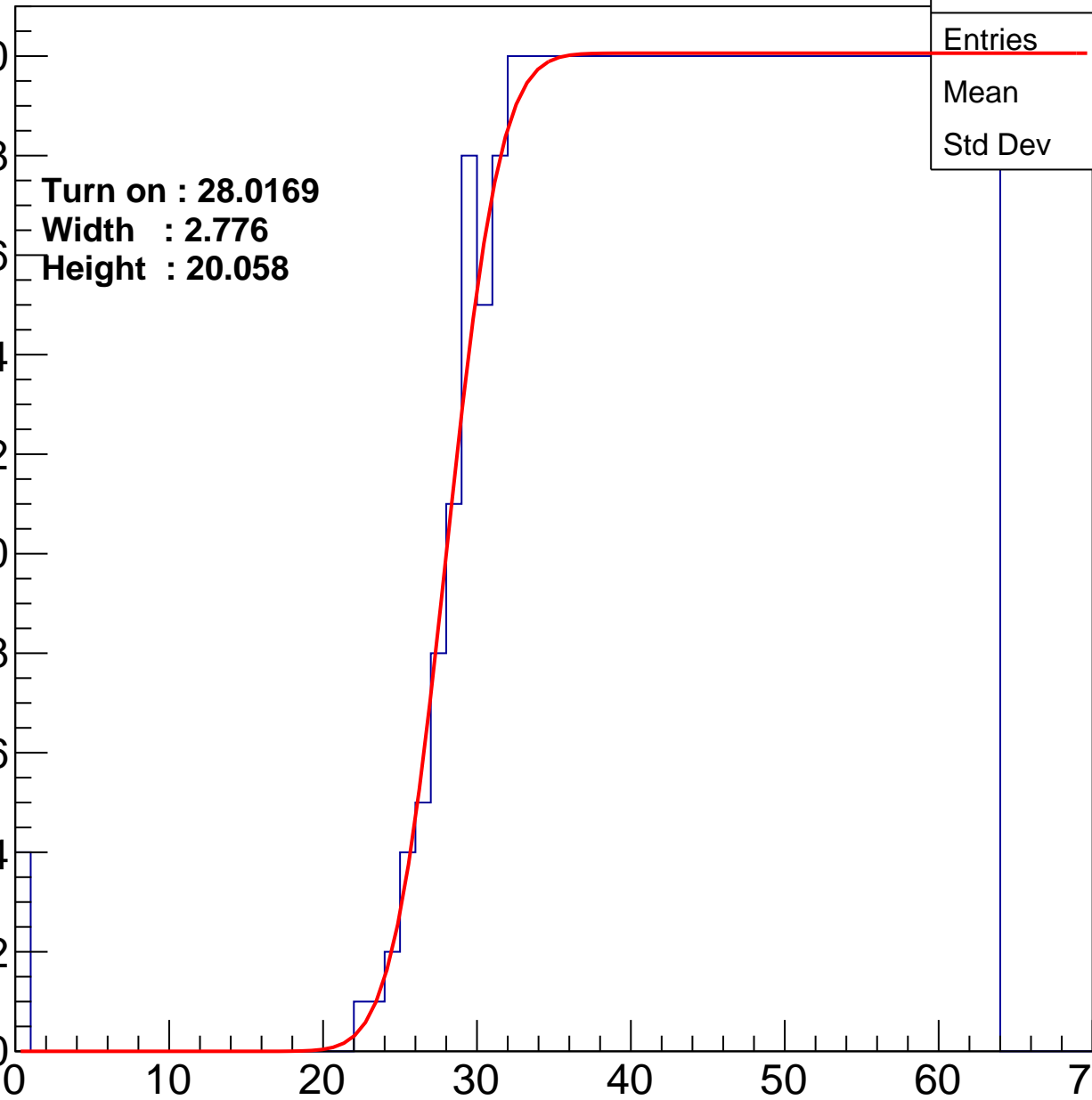
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.0169**  
**Width : 2.776**  
**Height : 20.058**

|         |       |
|---------|-------|
| Entries | 727   |
| Mean    | 45.09 |
| Std Dev | 11.08 |

ampl



# B1L001S, U23-ch106

calib\_packv5\_042523\_0143.root, FC#2, port C2

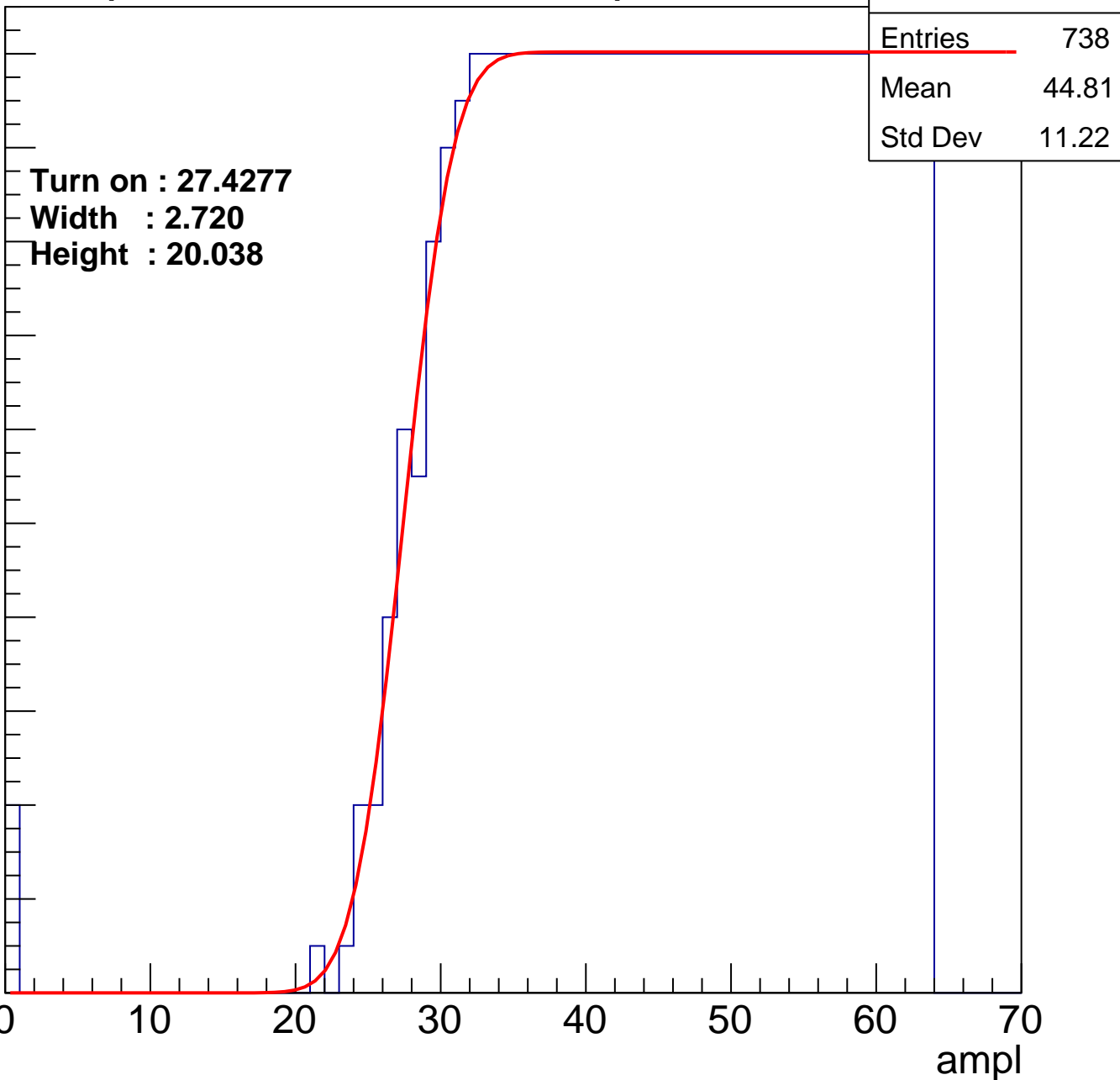
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4277  
Width : 2.720  
Height : 20.038

|         |       |
|---------|-------|
| Entries | 738   |
| Mean    | 44.81 |
| Std Dev | 11.22 |

ampl



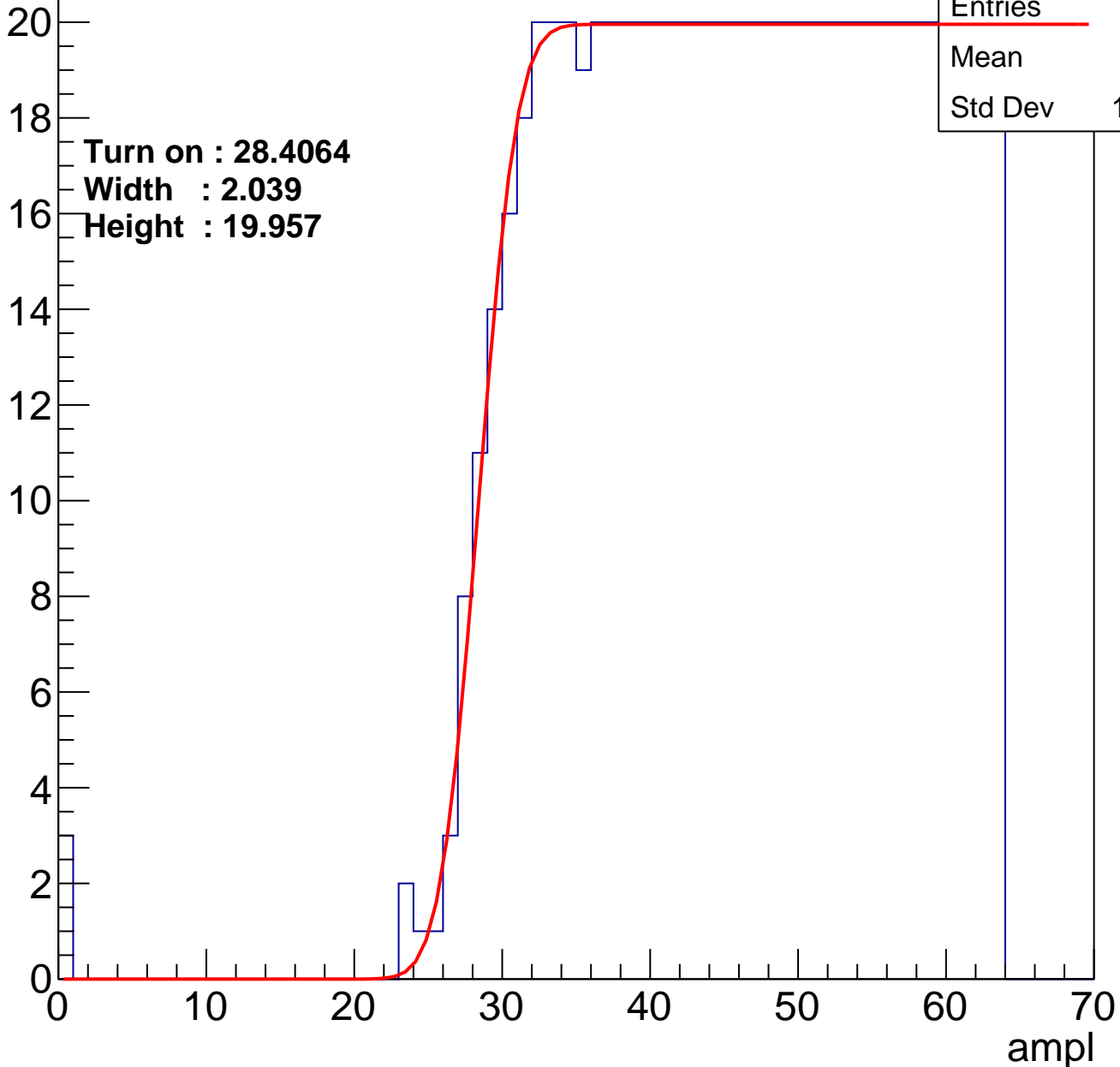
# B1L001S, U23-ch107

calib\_packv5\_042523\_0143.root, FC#2, port C2

|         |       |
|---------|-------|
| Entries | 716   |
| Mean    | 45.4  |
| Std Dev | 10.82 |

**Turn on : 28.4064**  
**Width : 2.039**  
**Height : 19.957**

Entry



# B1L001S, U23-ch108

calib\_packv5\_042523\_0143.root, FC#2, port C2

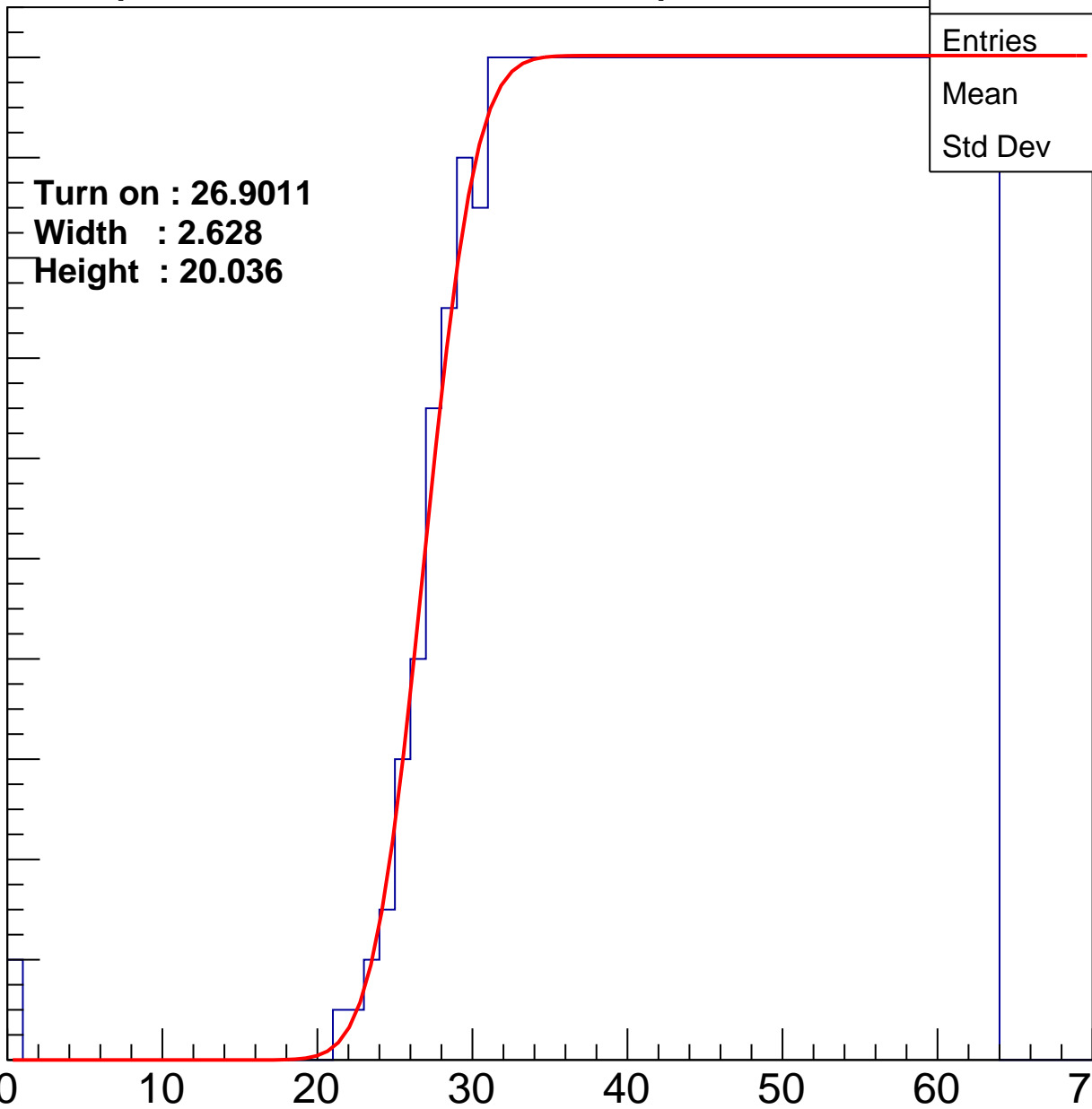
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.9011  
Width : 2.628  
Height : 20.036

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.69 |
| Std Dev | 11.11 |

ampl



# B1L001S, U23-ch109

calib\_packv5\_042523\_0143.root, FC#2, port C2

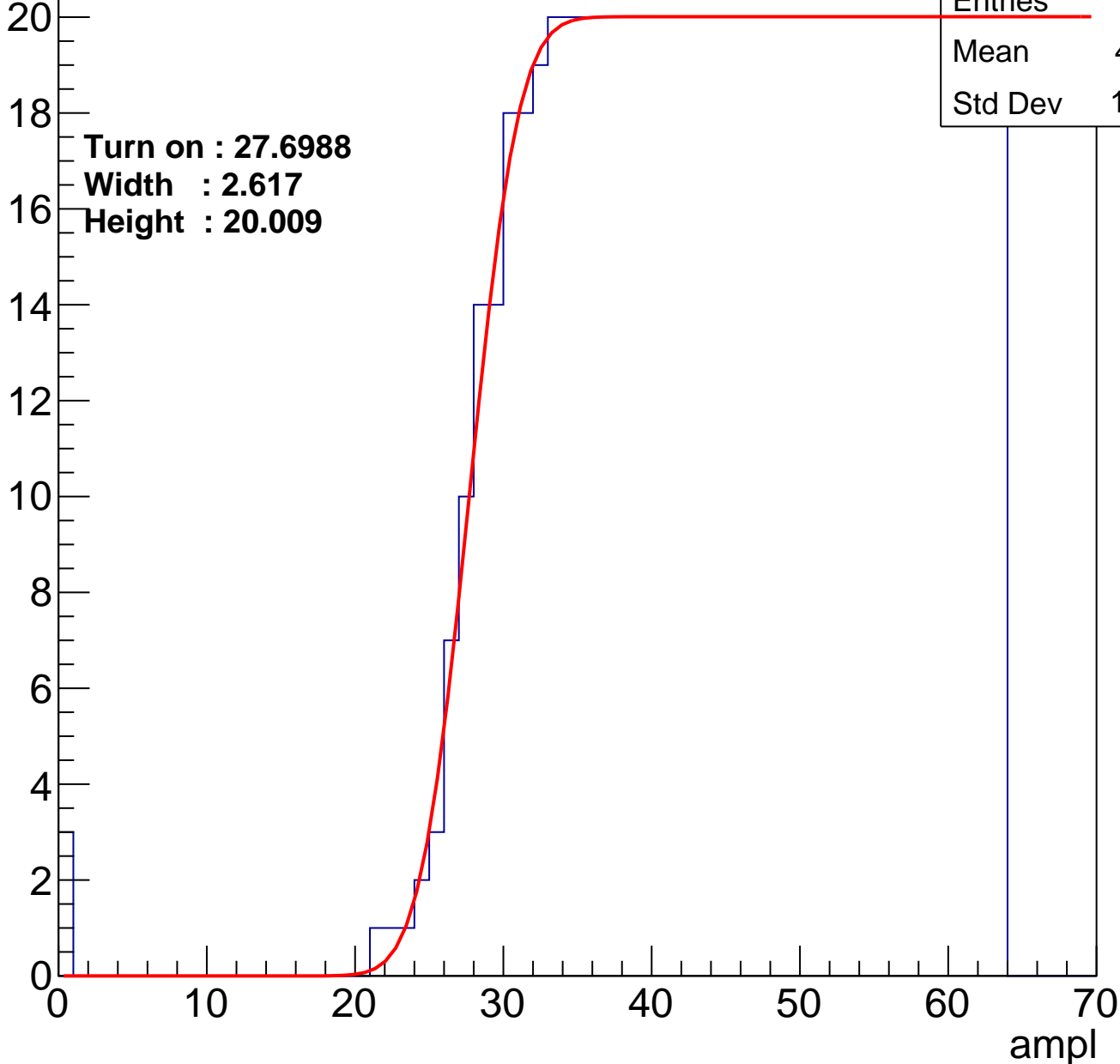
|         |       |
|---------|-------|
| Entries | 731   |
| Mean    | 45.01 |
| Std Dev | 11.04 |

Turn on : 27.6988

Width : 2.617

Height : 20.009

Entry



# B1L001S, U23-ch110

calib\_packv5\_042523\_0143.root, FC#2, port C2

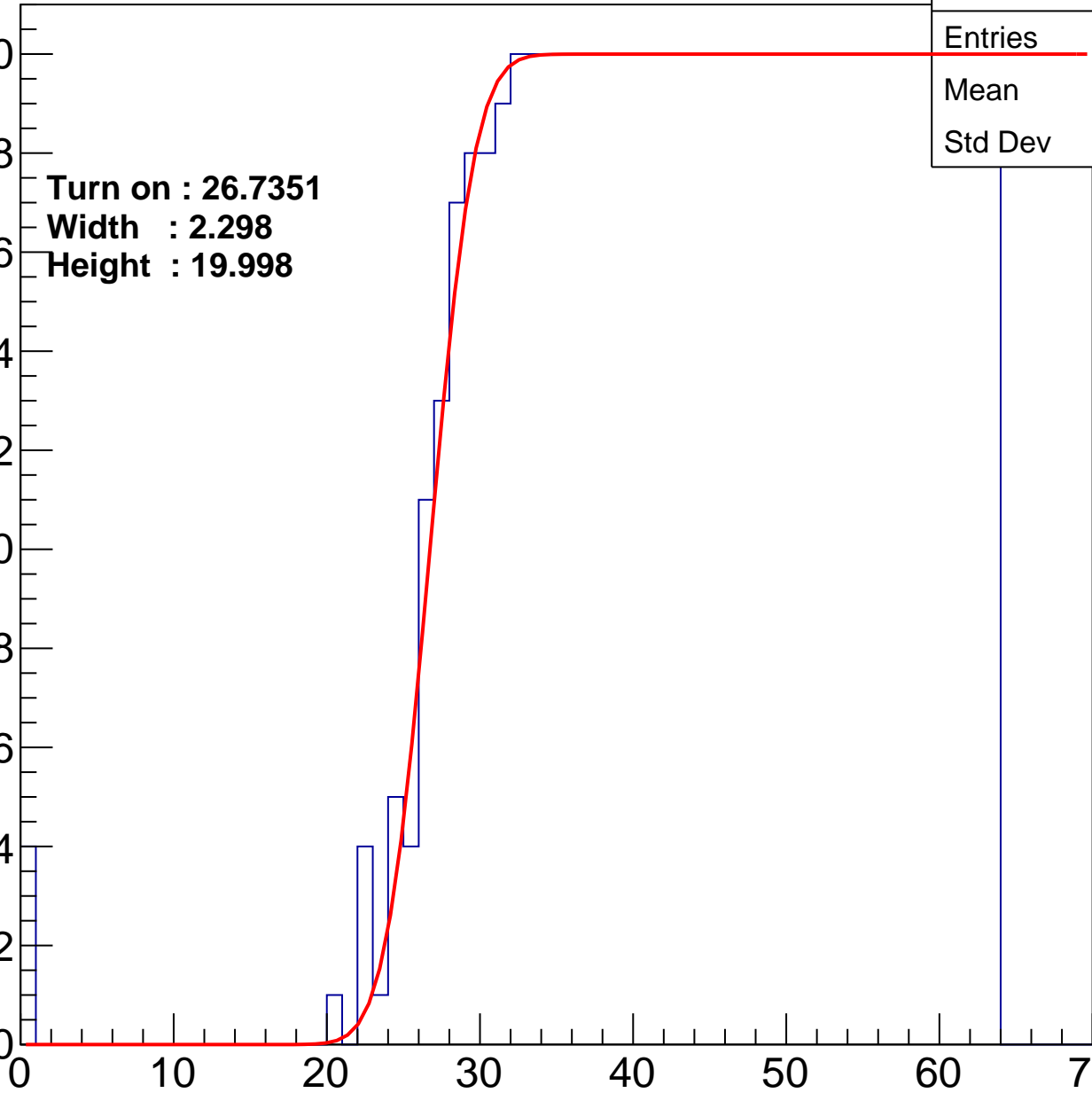
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.7351**  
**Width : 2.298**  
**Height : 19.998**

|         |       |
|---------|-------|
| Entries | 755   |
| Mean    | 44.39 |
| Std Dev | 11.44 |

ampl





# B1L001S, U23-ch111

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry

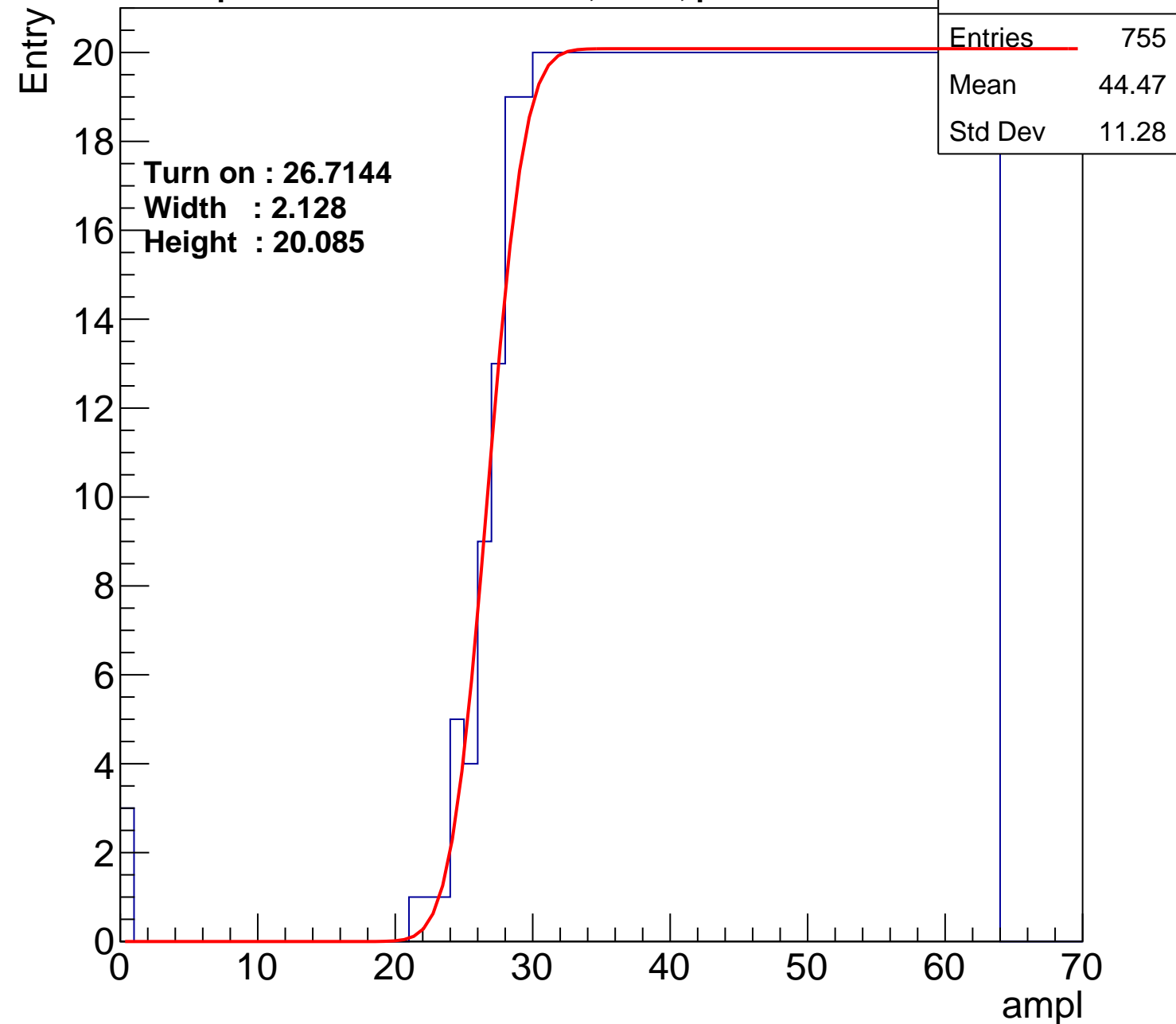
20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.7144  
Width : 2.128  
Height : 20.085

|         |       |
|---------|-------|
| Entries | 755   |
| Mean    | 44.47 |
| Std Dev | 11.28 |

ampl

0 10 20 30 40 50 60 70



# B1L001S, U23-ch112

calib\_packv5\_042523\_0143.root, FC#2, port C2

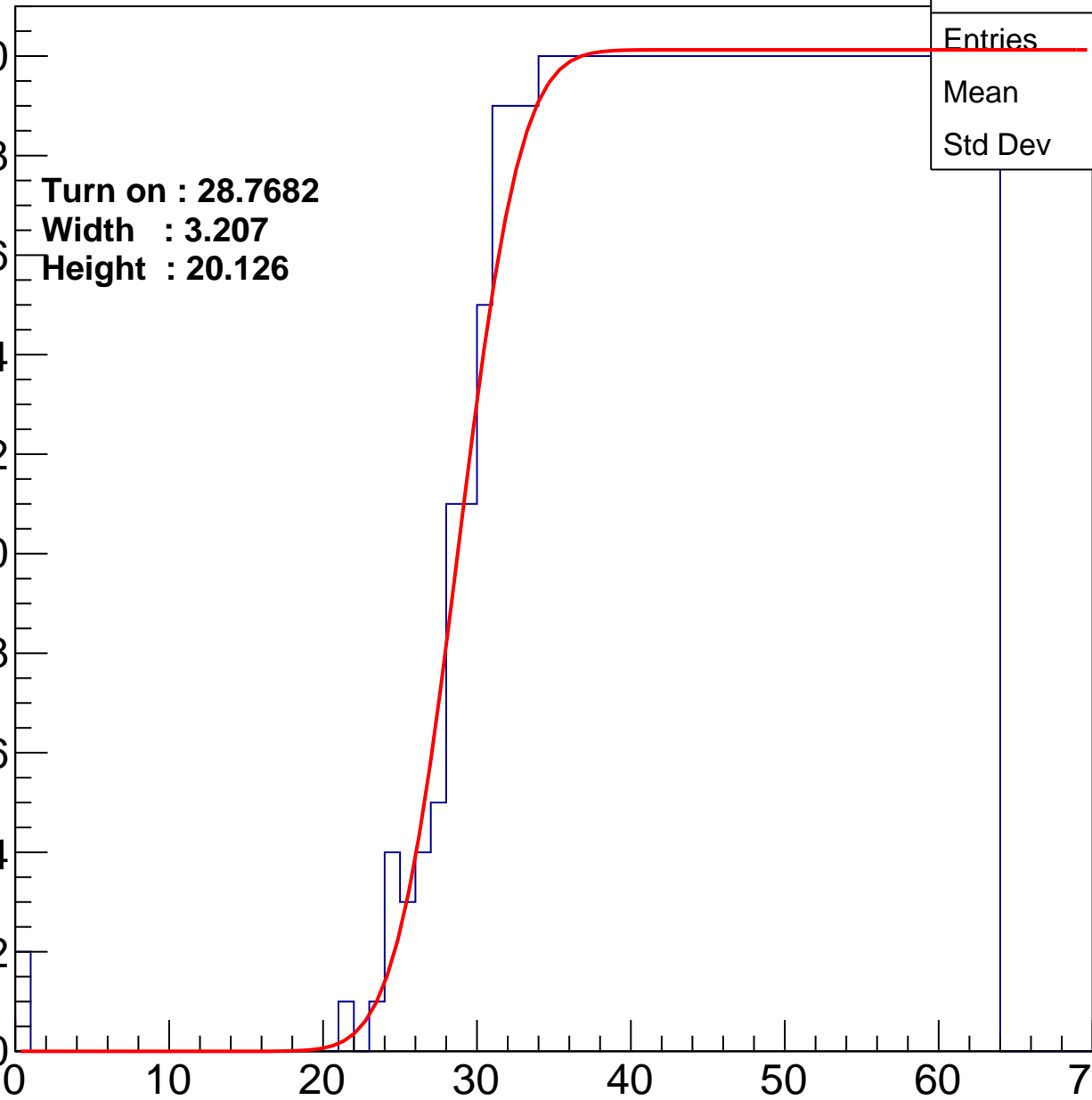
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.7682  
Width : 3.207  
Height : 20.126

|         |       |
|---------|-------|
| Entries | 714   |
| Mean    | 45.46 |
| Std Dev | 10.74 |

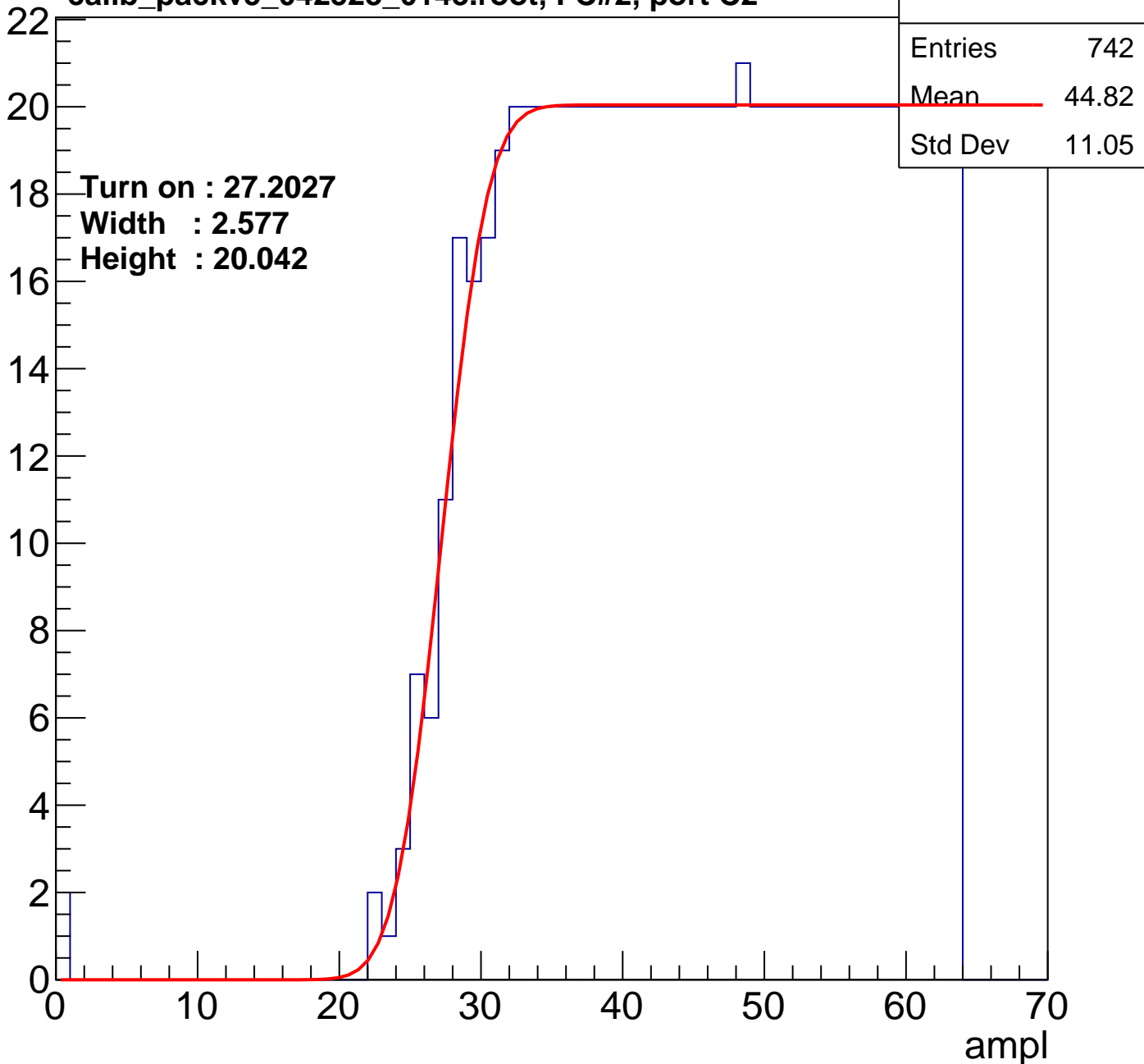
ampl



# B1L001S, U23-ch113

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry



# B1L001S, U23-ch114

calib\_packv5\_042523\_0143.root, FC#2, port C2

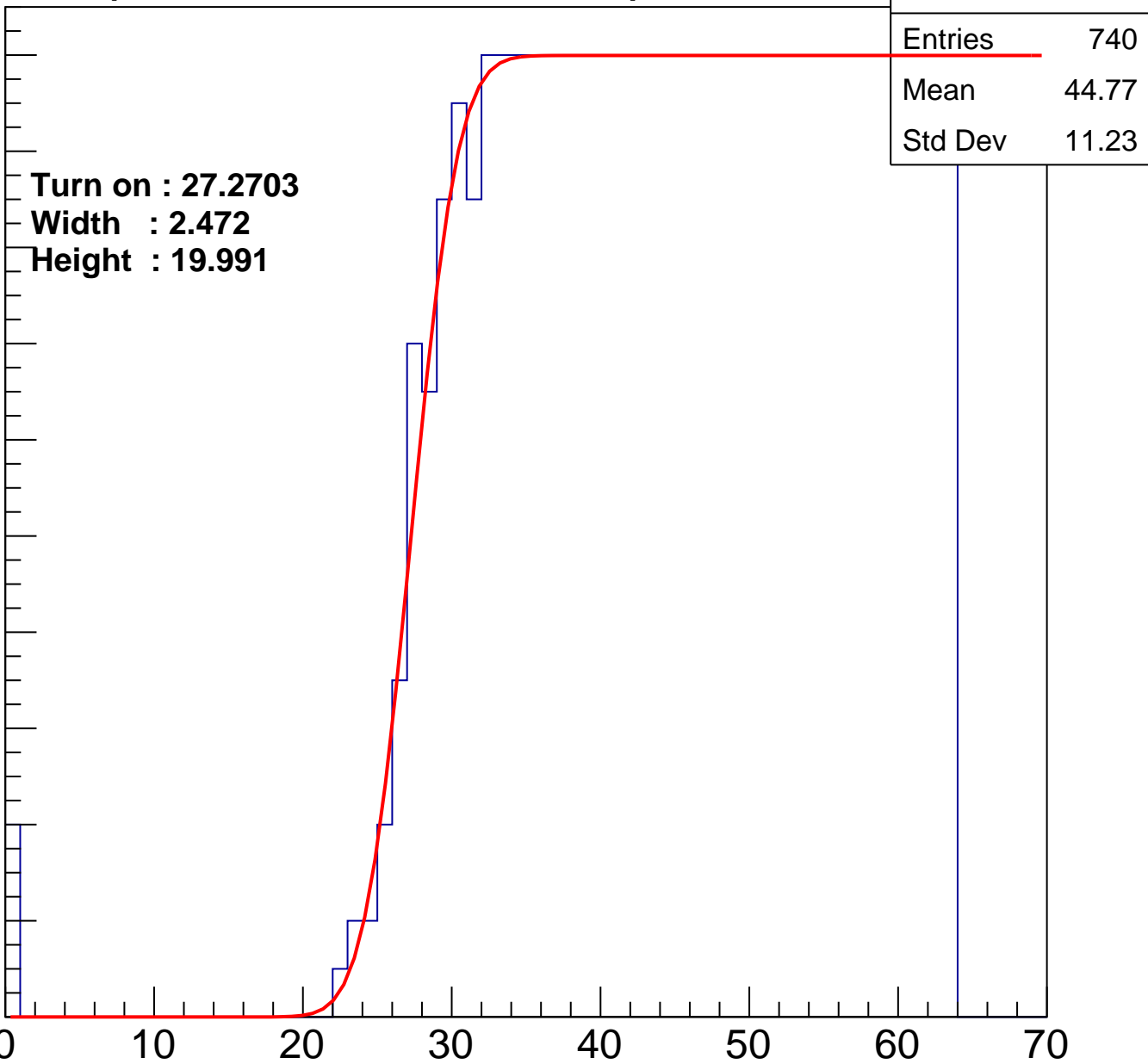
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2703**  
**Width : 2.472**  
**Height : 19.991**

|         |       |
|---------|-------|
| Entries | 740   |
| Mean    | 44.77 |
| Std Dev | 11.23 |

ampl



# B1L001S, U23-ch115

calib\_packv5\_042523\_0143.root, FC#2, port C2

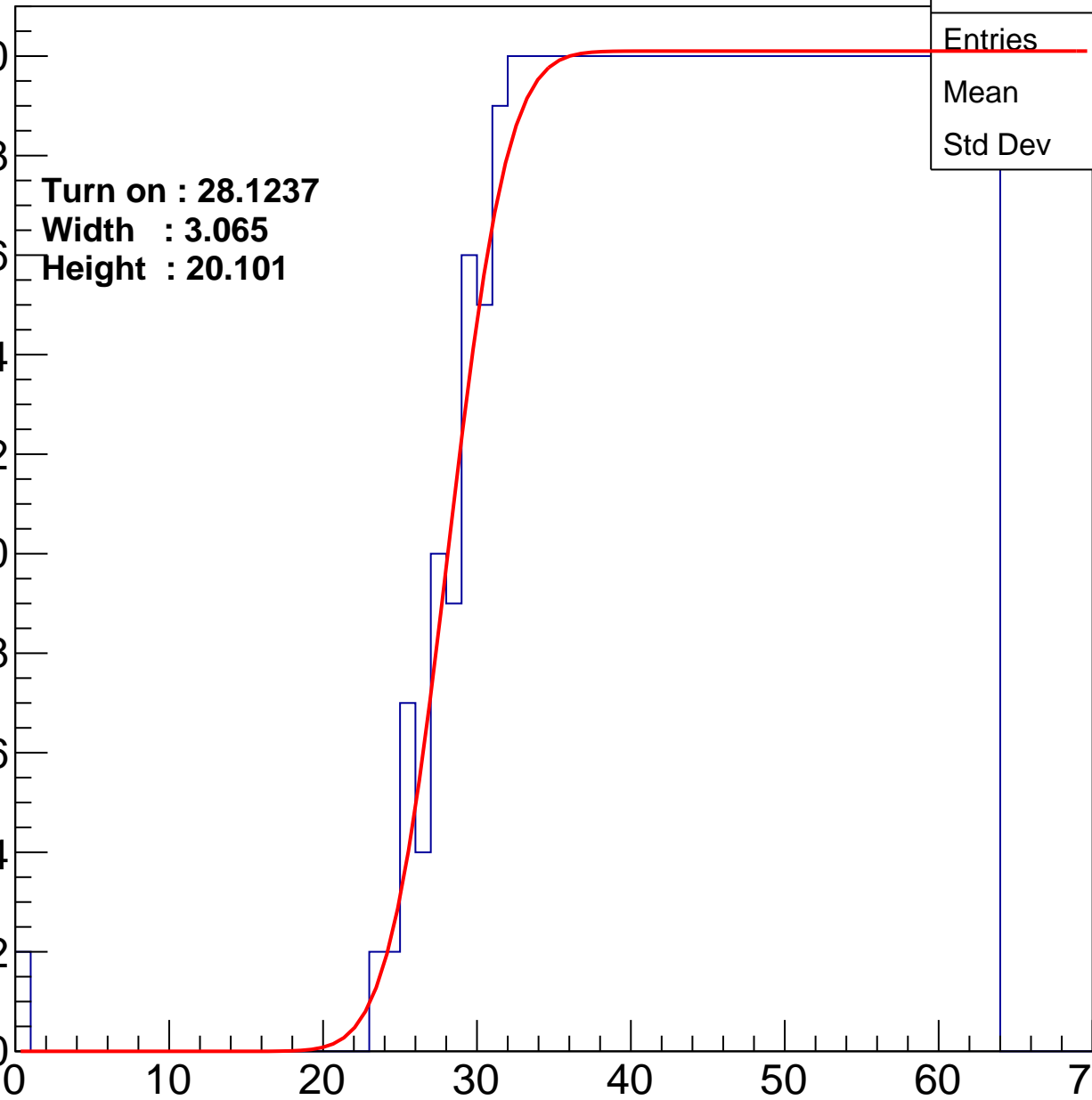
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.1237  
Width : 3.065  
Height : 20.101

|         |       |
|---------|-------|
| Entries | 726   |
| Mean    | 45.18 |
| Std Dev | 10.87 |

ampl



# B1L001S, U23-ch116

calib\_packv5\_042523\_0143.root, FC#2, port C2

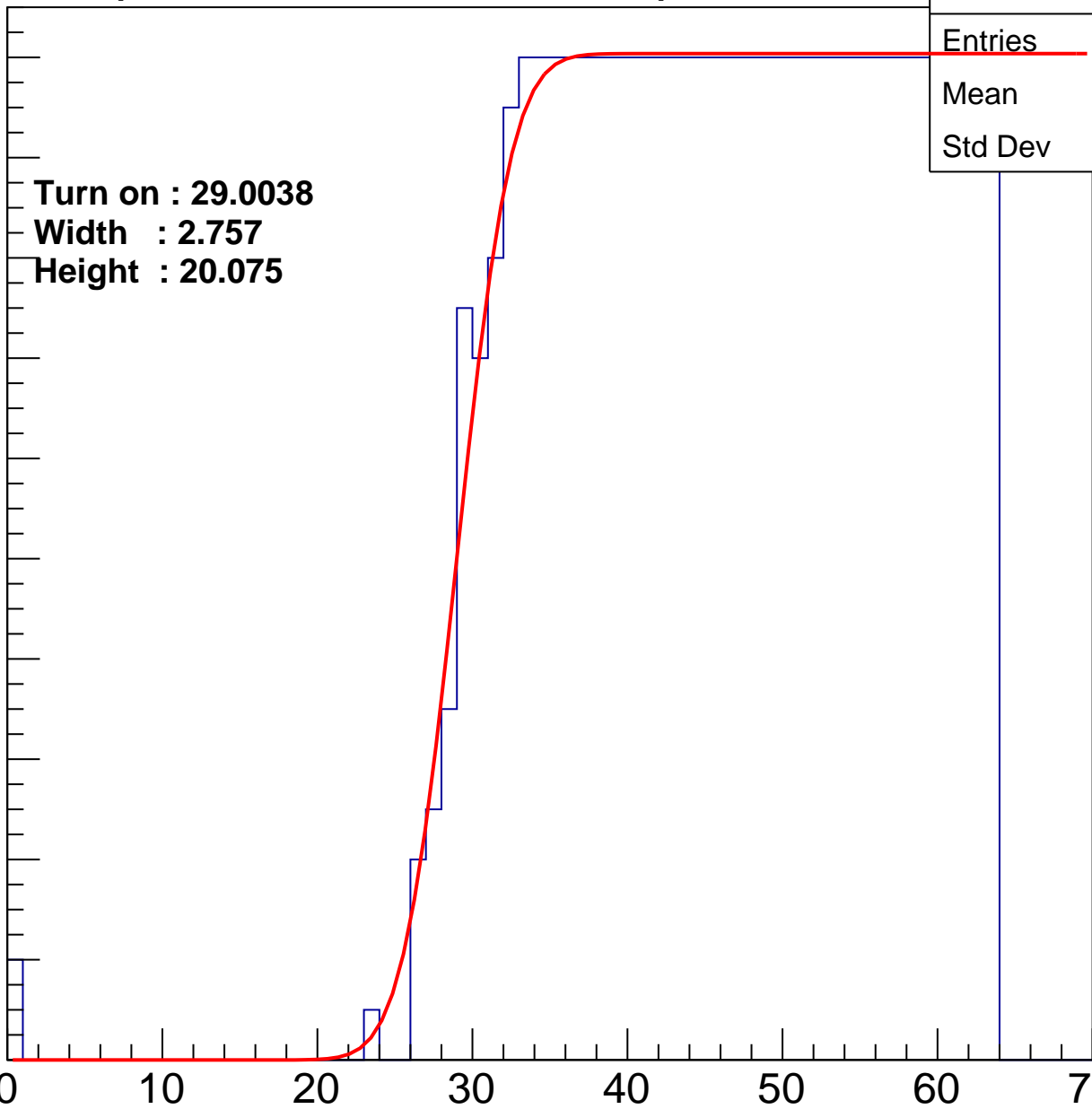
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.0038**  
**Width : 2.757**  
**Height : 20.075**

|         |       |
|---------|-------|
| Entries | 703   |
| Mean    | 45.77 |
| Std Dev | 10.51 |

ampl

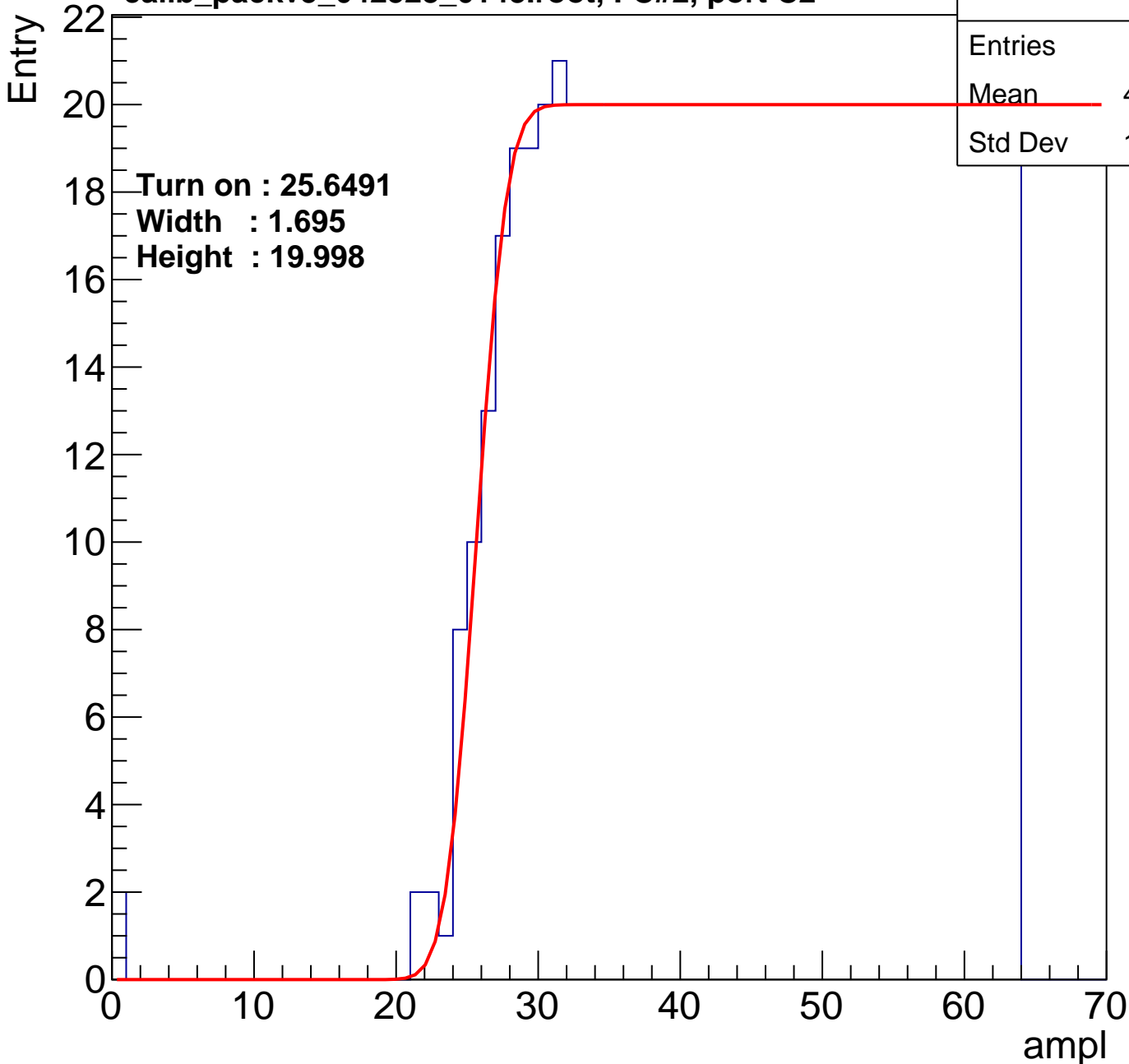


# B1L001S, U23-ch117

calib\_packv5\_042523\_0143.root, FC#2, port C2

|         |       |
|---------|-------|
| Entries | 774   |
| Mean    | 44.03 |
| Std Dev | 11.44 |

Turn on : 25.6491  
Width : 1.695  
Height : 19.998



# B1L001S, U23-ch118

calib\_packv5\_042523\_0143.root, FC#2, port C2

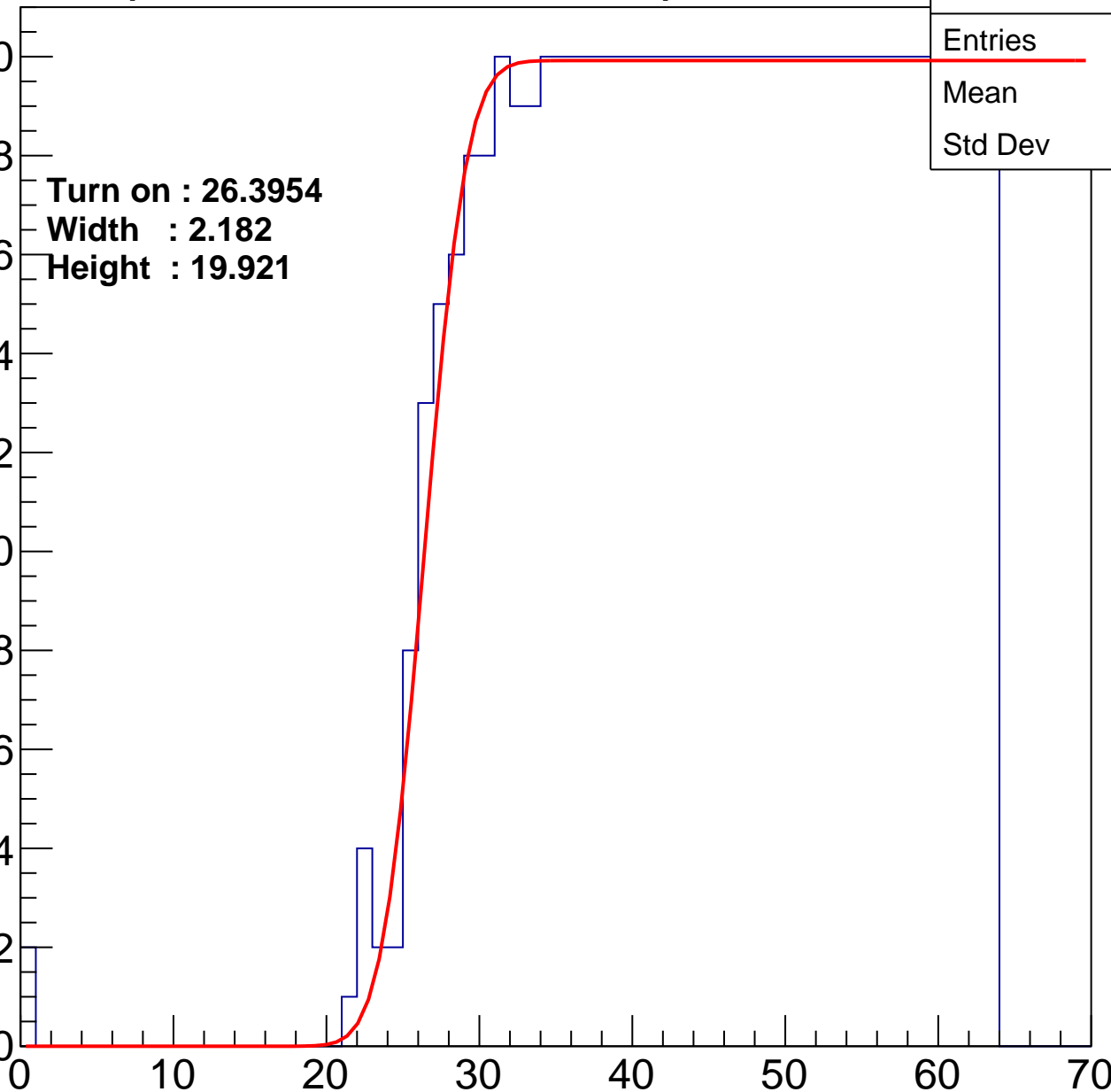
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.3954  
Width : 2.182  
Height : 19.921

|         |       |
|---------|-------|
| Entries | 757   |
| Mean    | 44.4  |
| Std Dev | 11.29 |

ampl





# B1L001S, U23-ch119

calib\_packv5\_042523\_0143.root, FC#2, port C2

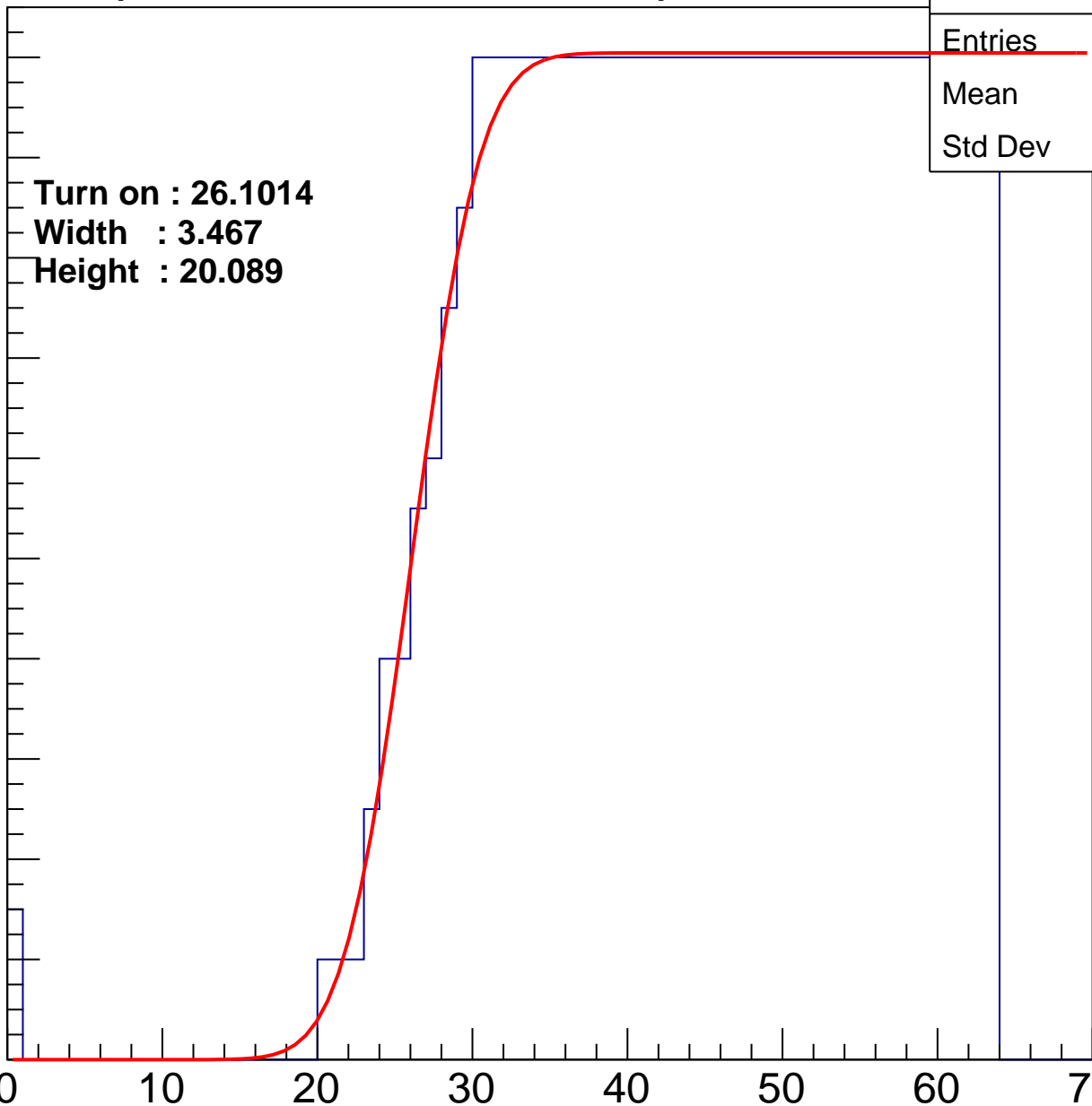
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 26.1014**  
**Width : 3.467**  
**Height : 20.089**

|         |       |
|---------|-------|
| Entries | 765   |
| Mean    | 44.15 |
| Std Dev | 11.53 |

ampl



# B1L001S, U23-ch120

calib\_packv5\_042523\_0143.root, FC#2, port C2

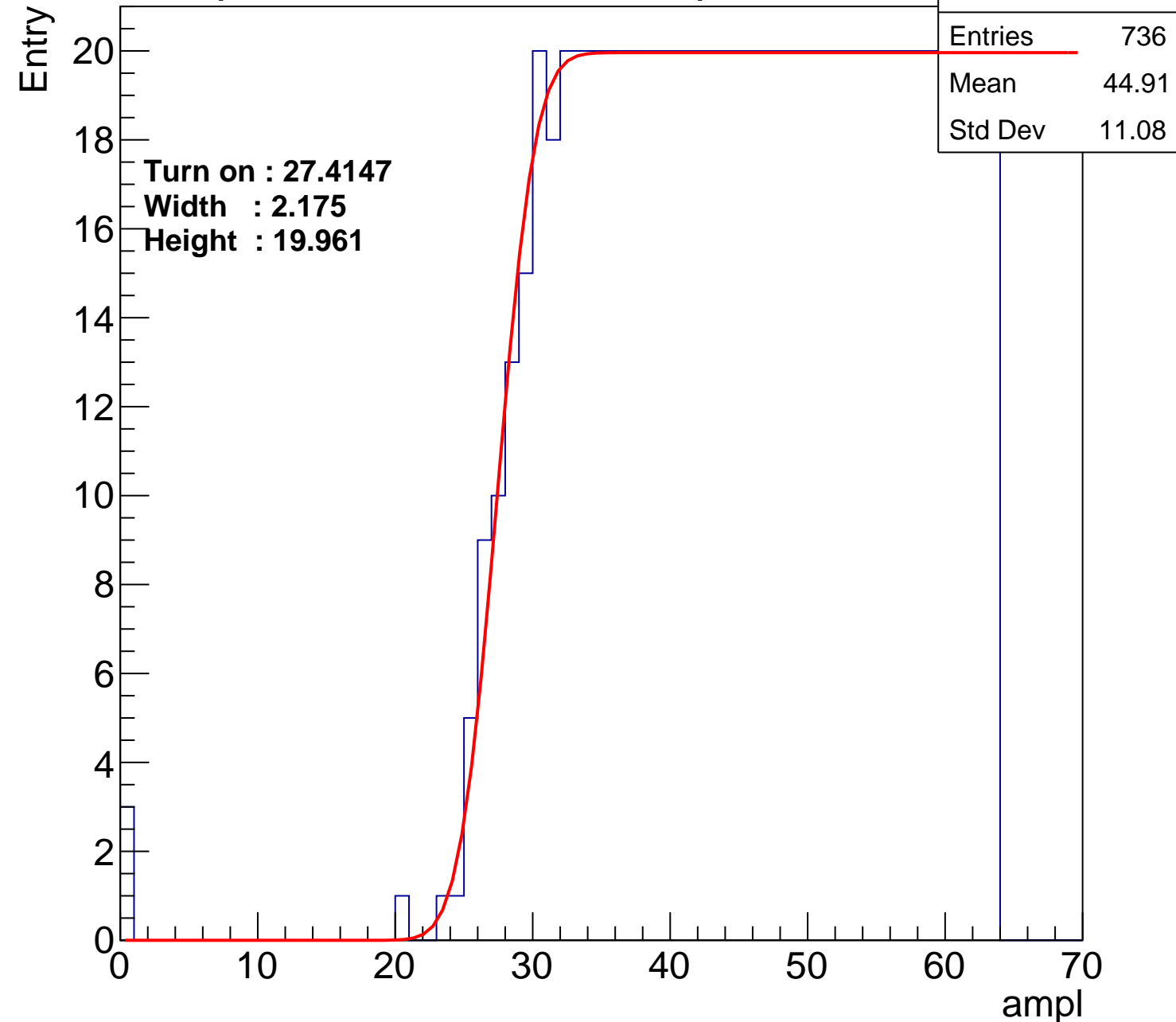
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.4147**  
**Width : 2.175**  
**Height : 19.961**

|         |       |
|---------|-------|
| Entries | 736   |
| Mean    | 44.91 |
| Std Dev | 11.08 |

ampl



# B1L001S, U23-ch121

calib\_packv5\_042523\_0143.root, FC#2, port C2

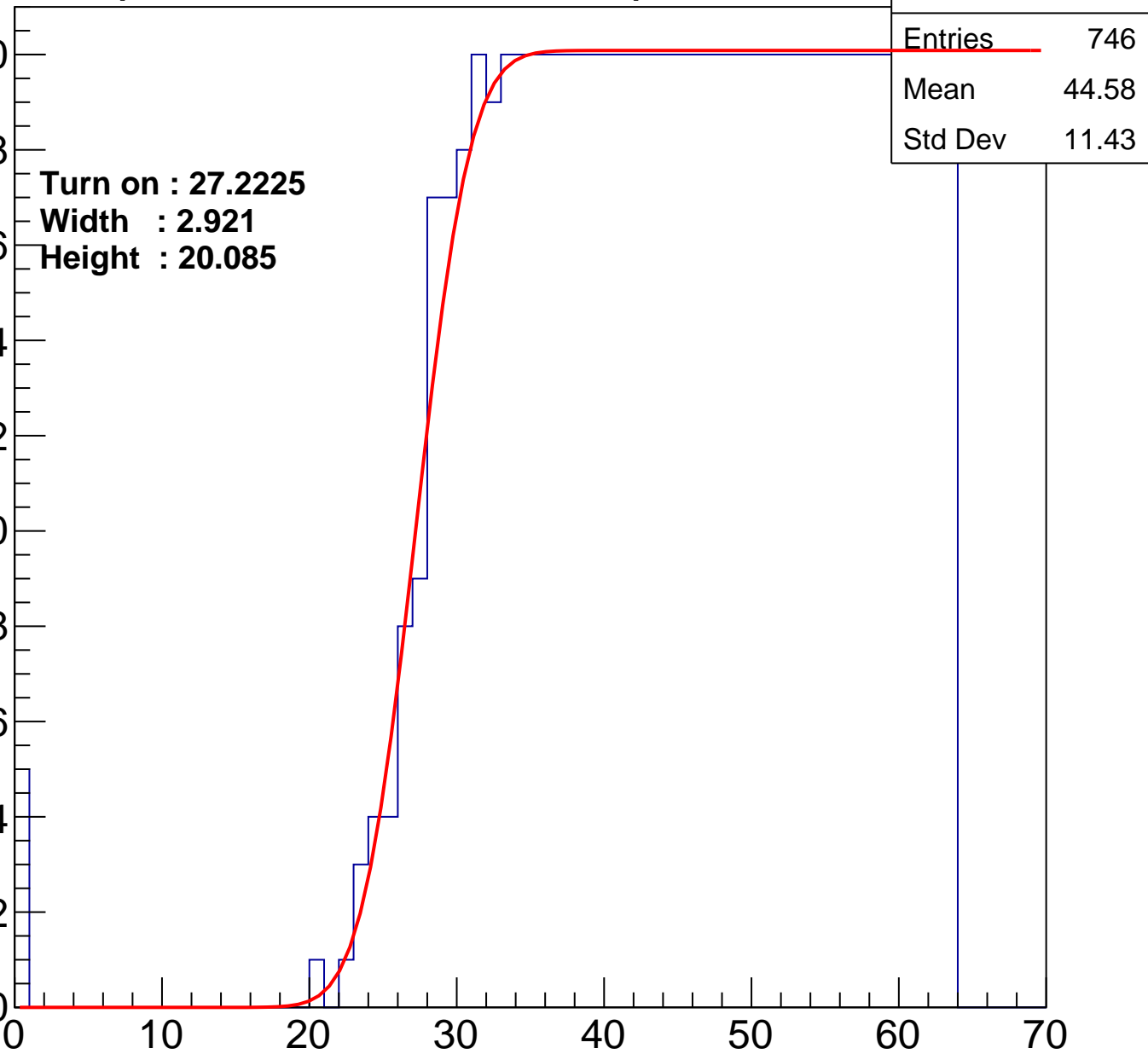
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 27.2225**  
**Width : 2.921**  
**Height : 20.085**

|         |       |
|---------|-------|
| Entries | 746   |
| Mean    | 44.58 |
| Std Dev | 11.43 |

ampl



# B1L001S, U23-ch122

calib\_packv5\_042523\_0143.root, FC#2, port C2

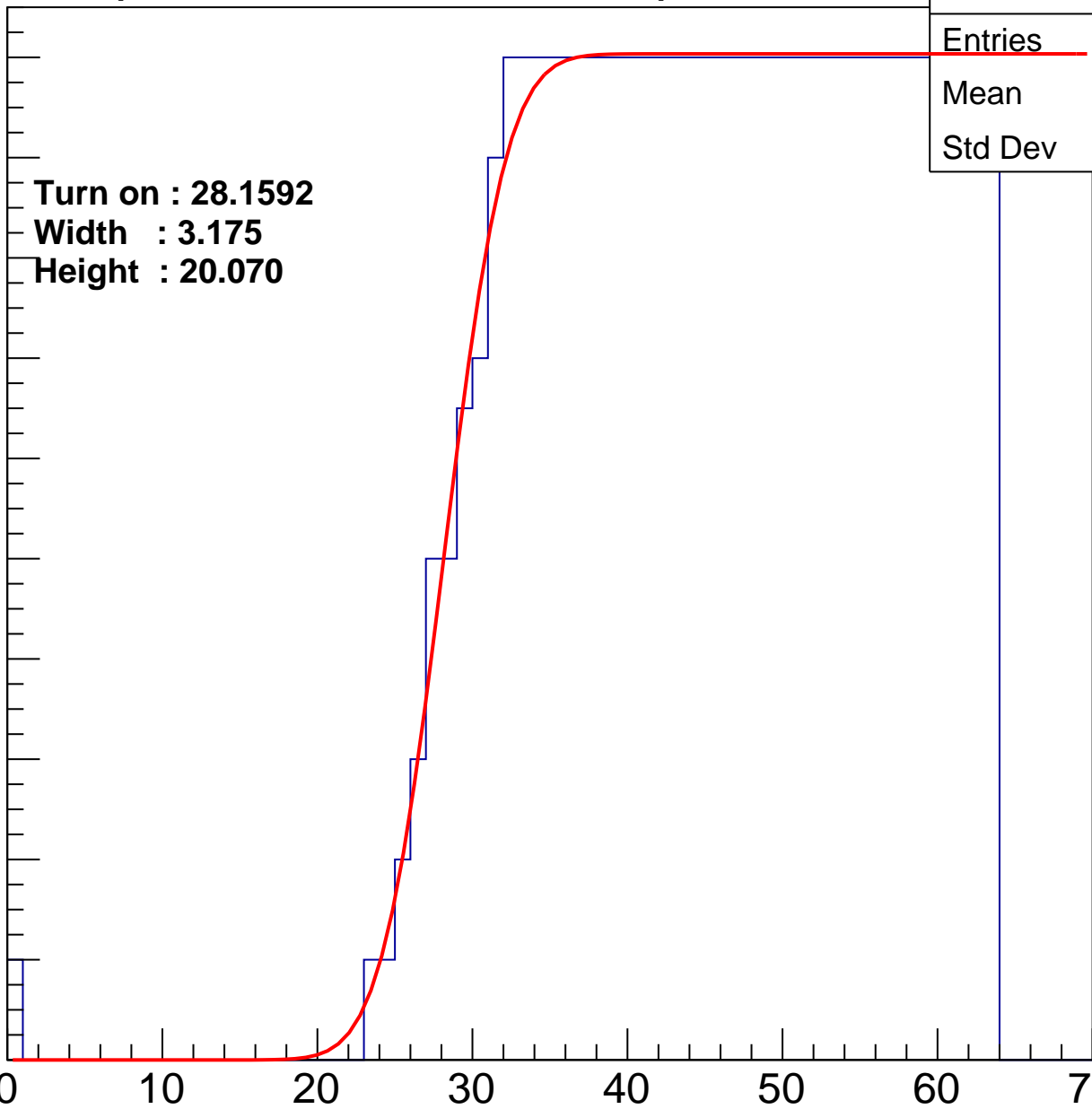
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 28.1592**  
**Width : 3.175**  
**Height : 20.070**

|         |       |
|---------|-------|
| Entries | 721   |
| Mean    | 45.29 |
| Std Dev | 10.81 |

ampl



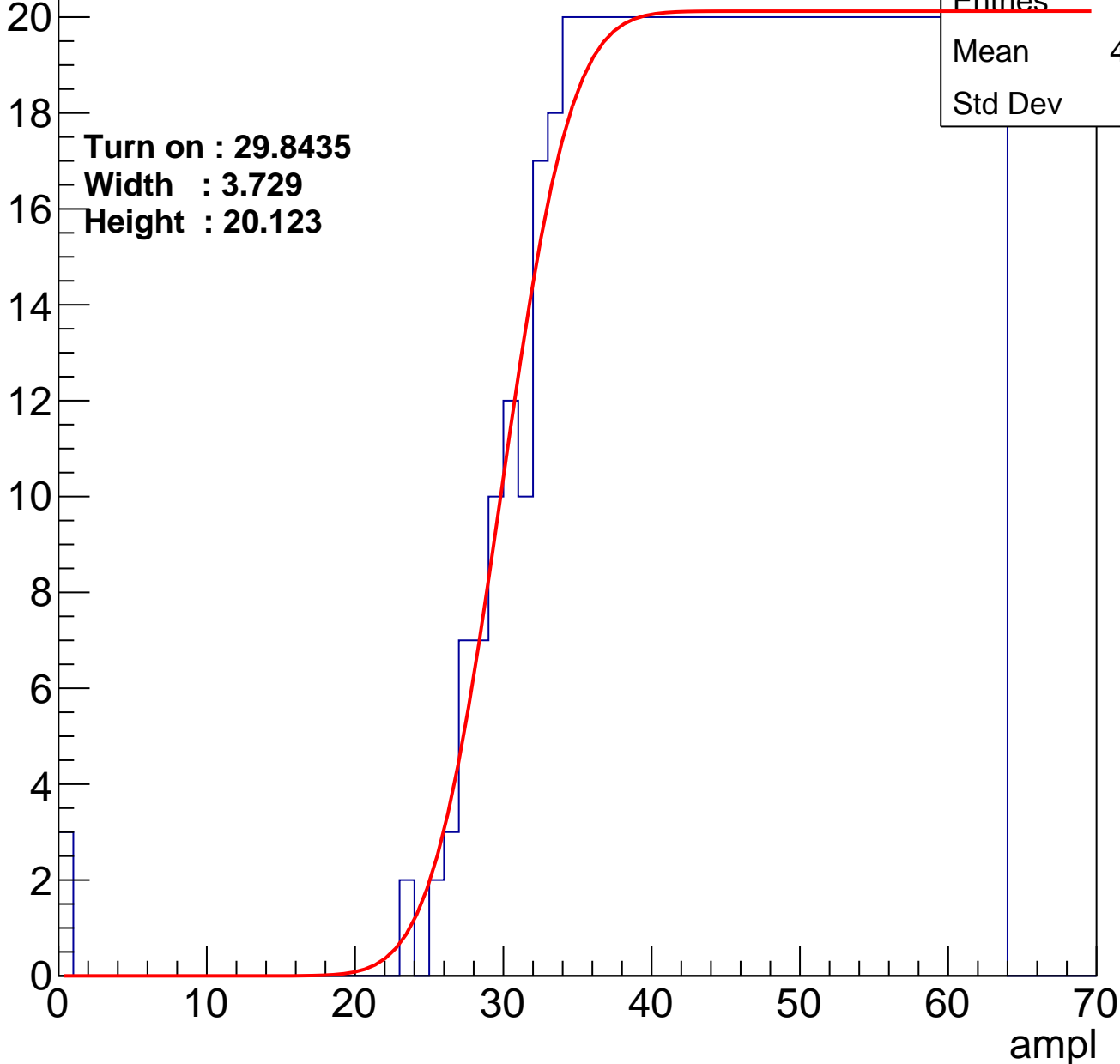
# B1L001S, U23-ch123

calib\_packv5\_042523\_0143.root, FC#2, port C2

|         |       |
|---------|-------|
| Entries | 691   |
| Mean    | 45.96 |
| Std Dev | 10.6  |

**Turn on : 29.8435**  
**Width : 3.729**  
**Height : 20.123**

Entry



# B1L001S, U23-ch124

calib\_packv5\_042523\_0143.root, FC#2, port C2

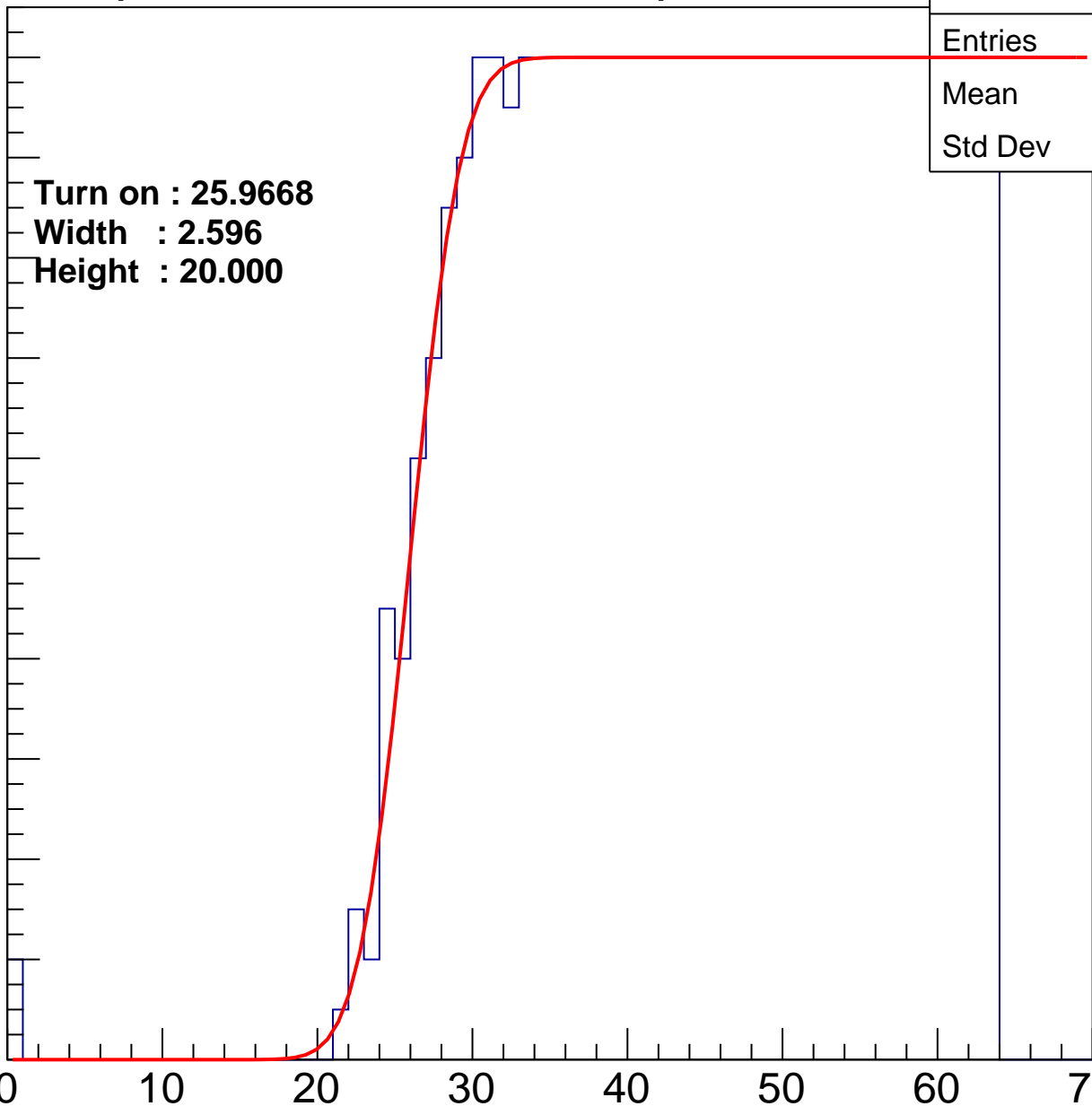
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 25.9668**  
**Width : 2.596**  
**Height : 20.000**

|         |       |
|---------|-------|
| Entries | 765   |
| Mean    | 44.22 |
| Std Dev | 11.38 |

ampl



# B1L001S, U23-ch125

calib\_packv5\_042523\_0143.root, FC#2, port C2

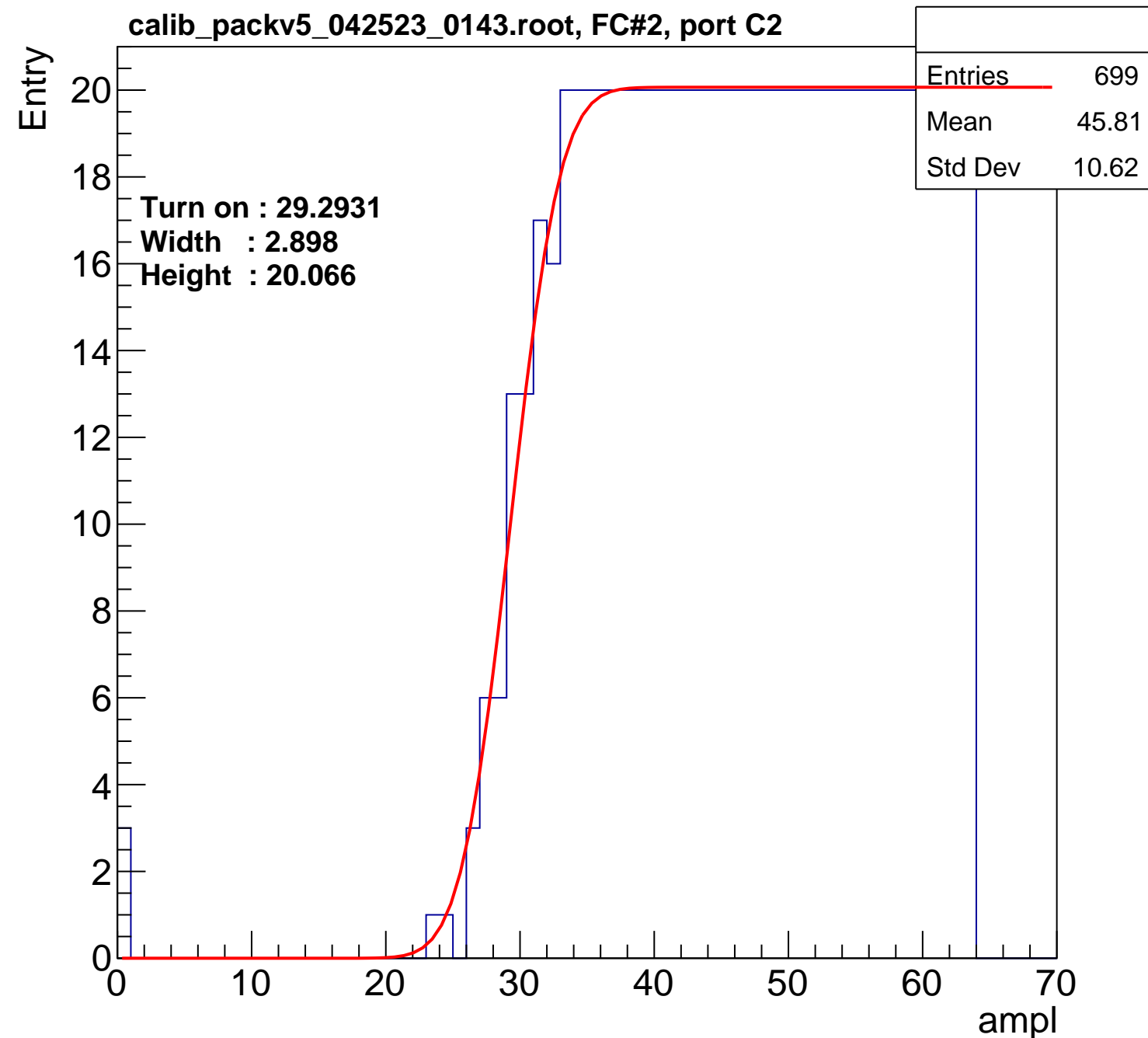
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.2931**  
**Width : 2.898**  
**Height : 20.066**

|         |       |
|---------|-------|
| Entries | 699   |
| Mean    | 45.81 |
| Std Dev | 10.62 |

ampl



# B1L001S, U23-ch126

calib\_packv5\_042523\_0143.root, FC#2, port C2

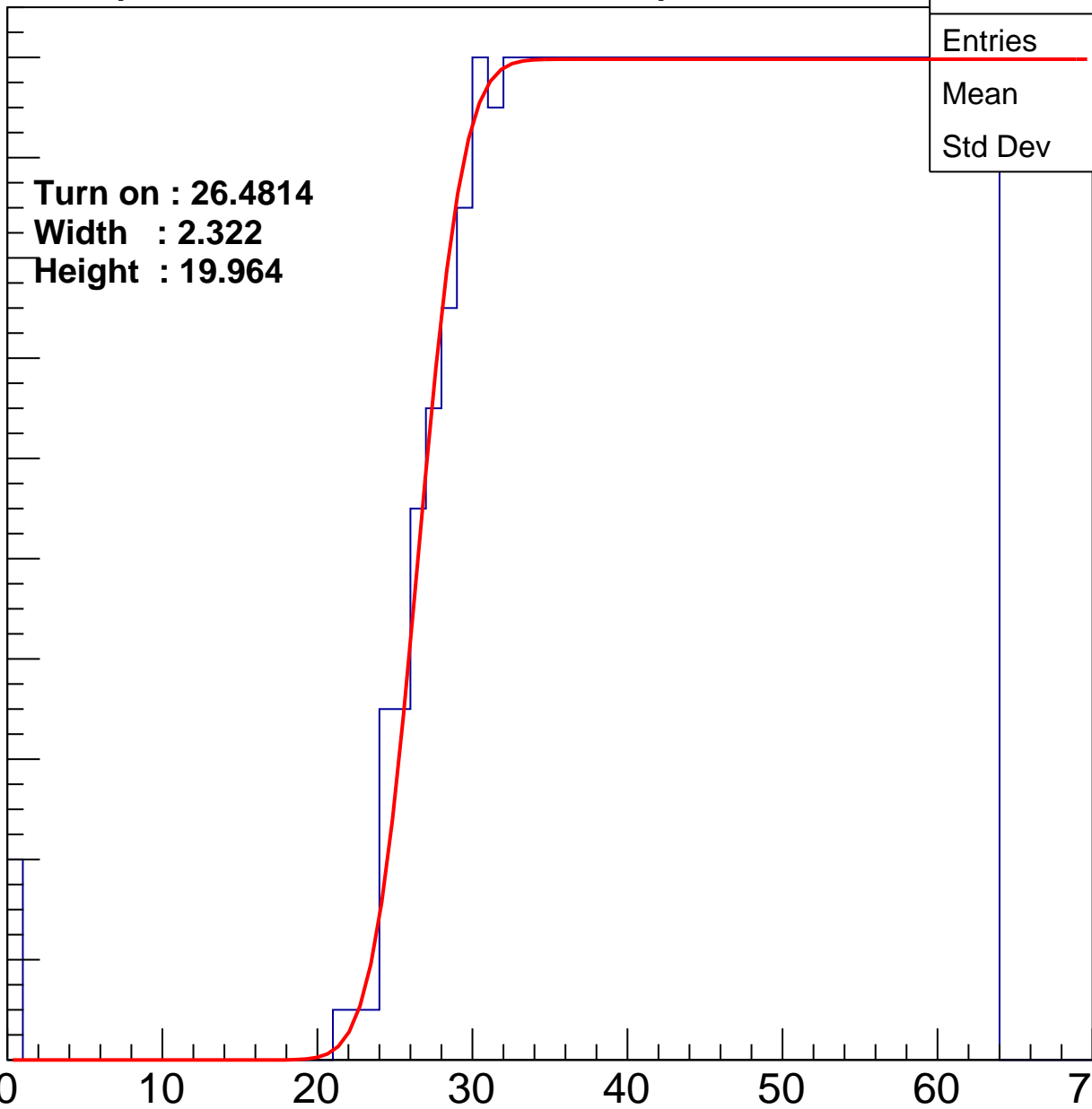
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

Turn on : 26.4814  
Width : 2.322  
Height : 19.964

|         |       |
|---------|-------|
| Entries | 756   |
| Mean    | 44.38 |
| Std Dev | 11.44 |

ampl





# B1L001S, U23-ch127

calib\_packv5\_042523\_0143.root, FC#2, port C2

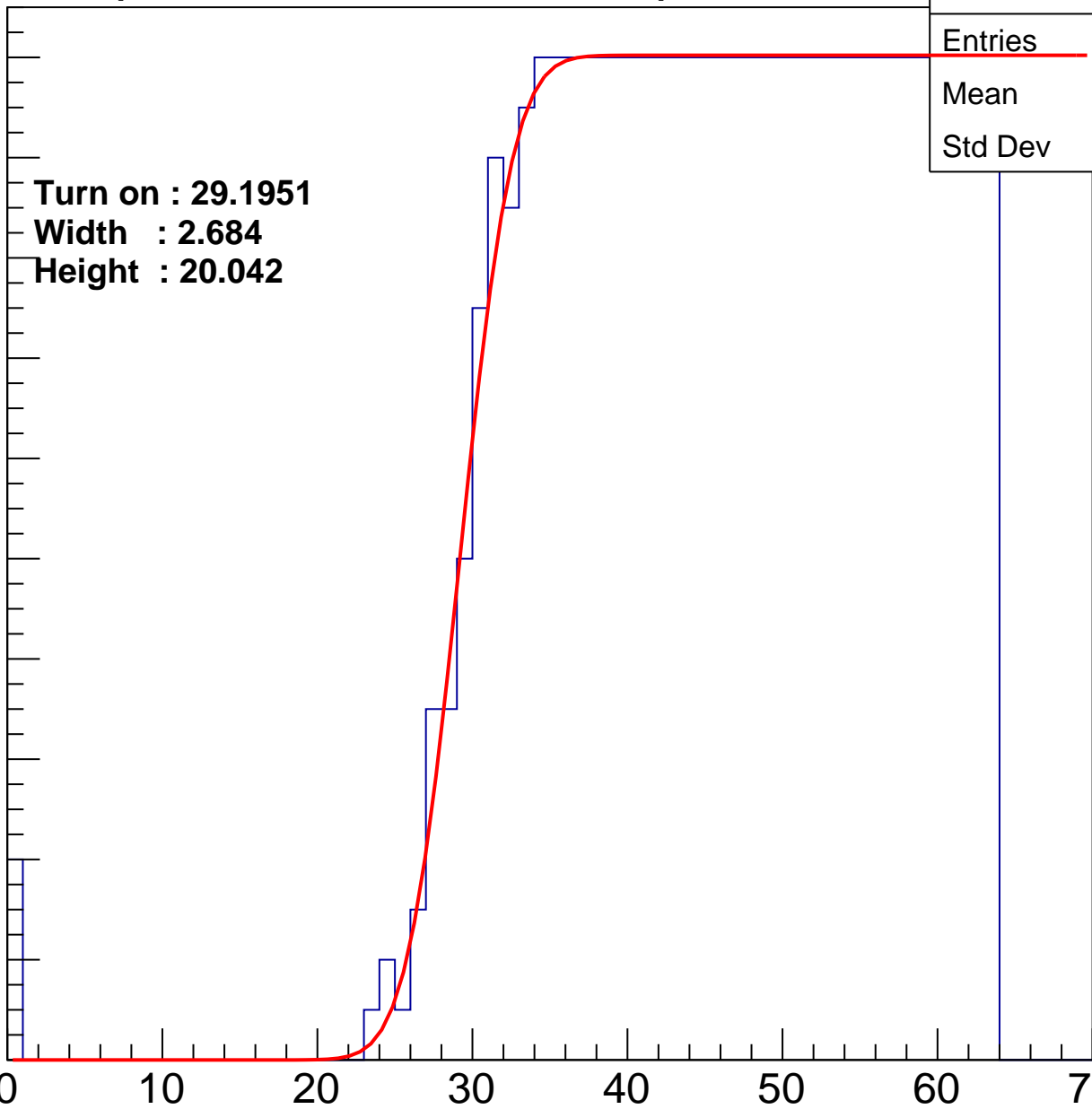
Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.1951**  
**Width : 2.684**  
**Height : 20.042**

|         |       |
|---------|-------|
| Entries | 704   |
| Mean    | 45.64 |
| Std Dev | 10.81 |

ampl



# B1L001S, U23-ch127

calib\_packv5\_042523\_0143.root, FC#2, port C2

Entry

20  
18  
16  
14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.1951**  
**Width : 2.684**  
**Height : 20.042**

|         |       |
|---------|-------|
| Entries | 704   |
| Mean    | 45.64 |
| Std Dev | 10.81 |

ampl

