

B1L002S, U13-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.73
Std Dev	11.79

Turn on : 25.7067

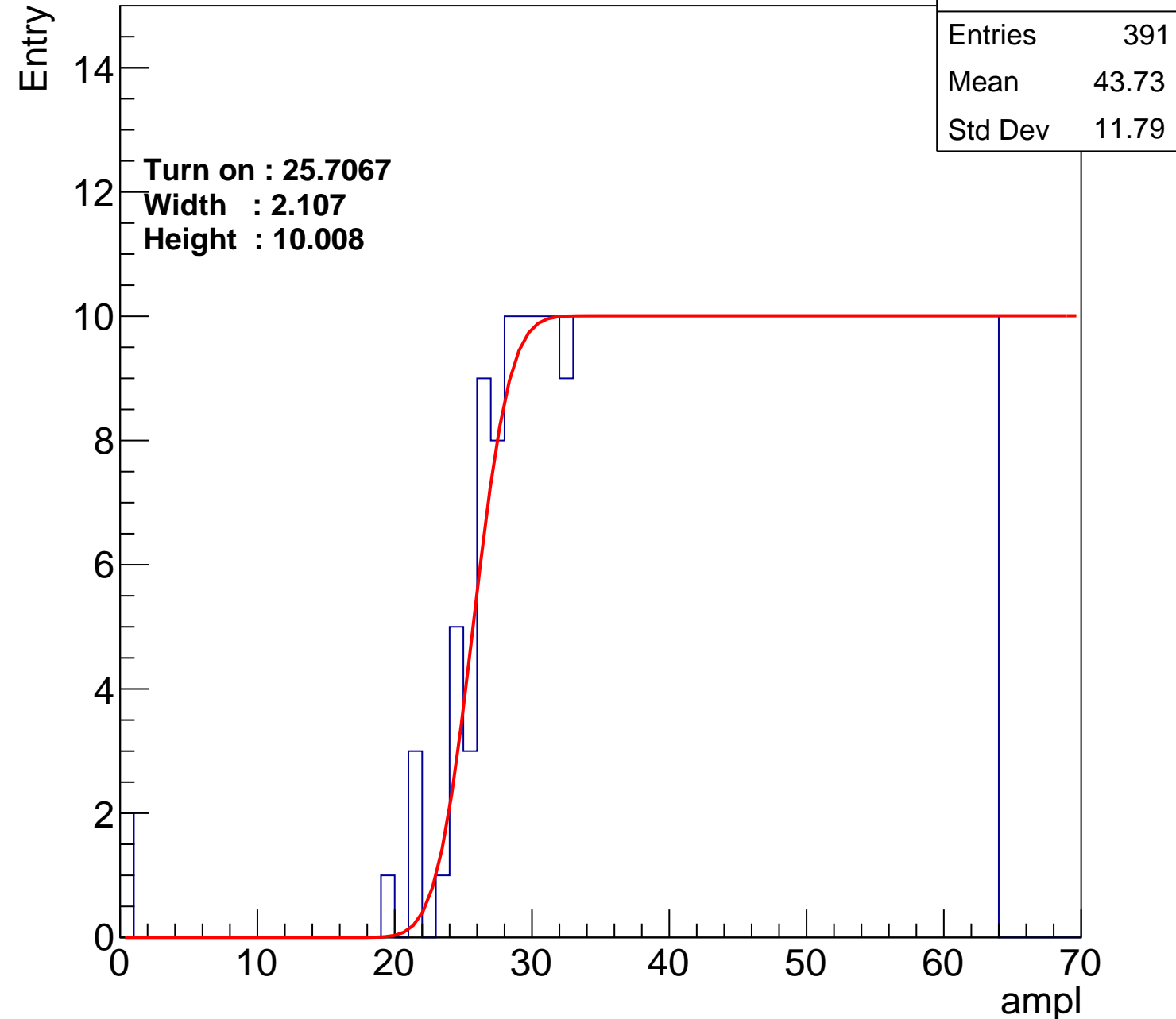
Width : 2.107

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.62
Std Dev	11.73

Turn on : 24.6494

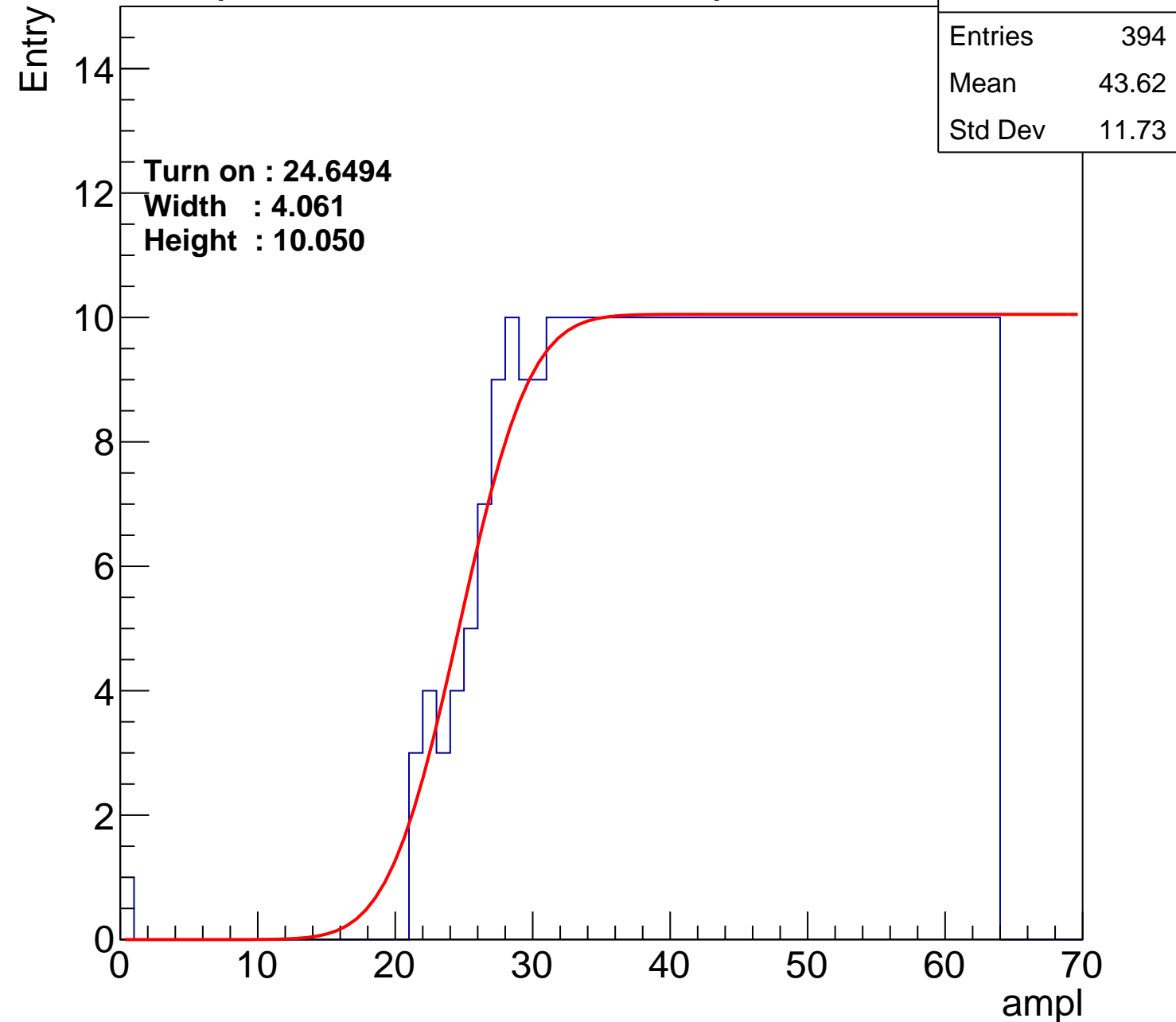
Width : 4.061

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.89
Std Dev	11.82

Turn on : 25.9542

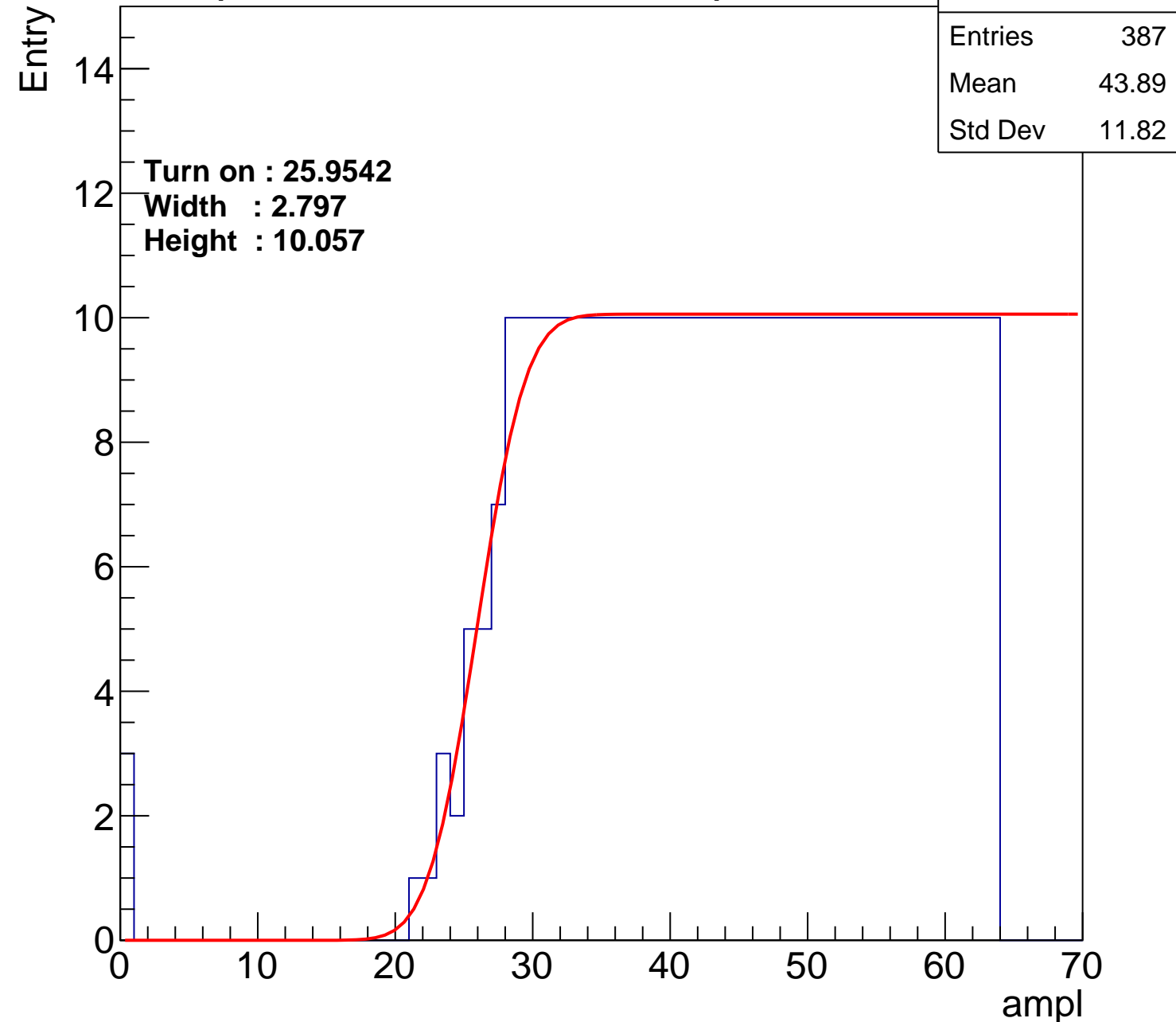
Width : 2.797

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.57
Std Dev	11.89

Turn on : 25.0349

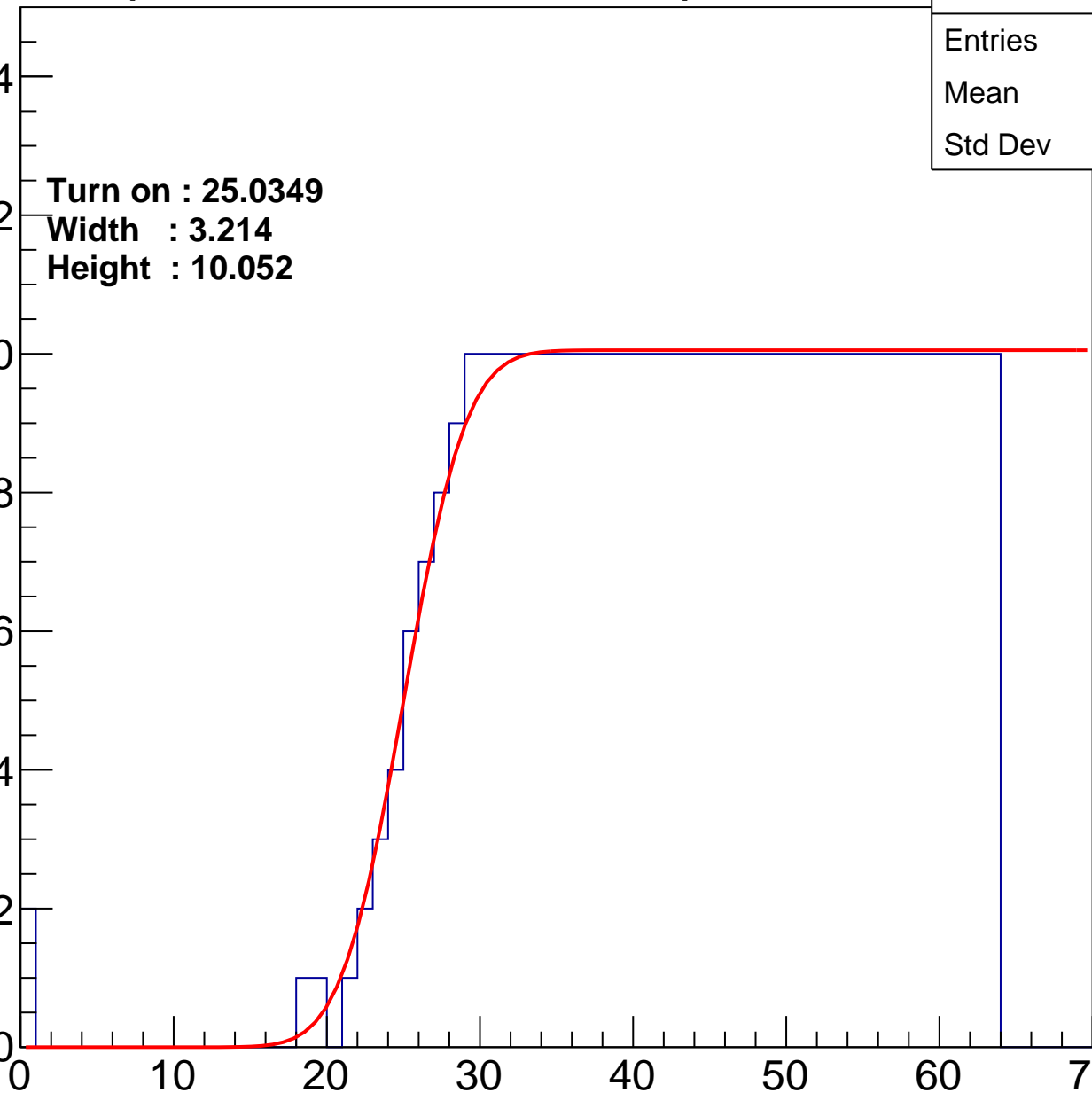
Width : 3.214

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch4

calib_packv5_042523_0143.root, FC#10, port B3

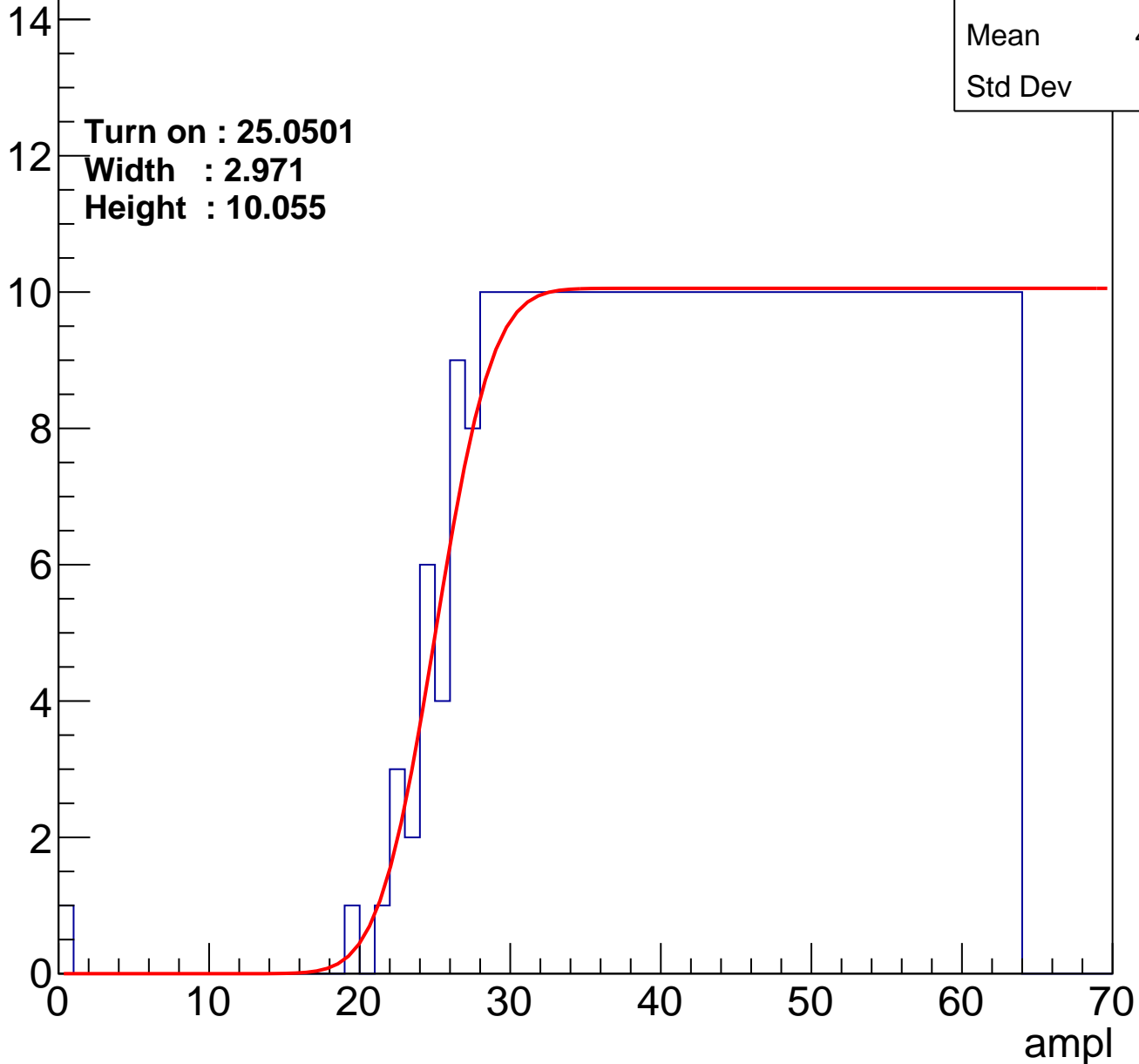
Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 25.0501

Width : 2.971

Height : 10.055

Entry



B1L002S, U13-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.41
Std Dev	11.82

Turn on : 24.9528

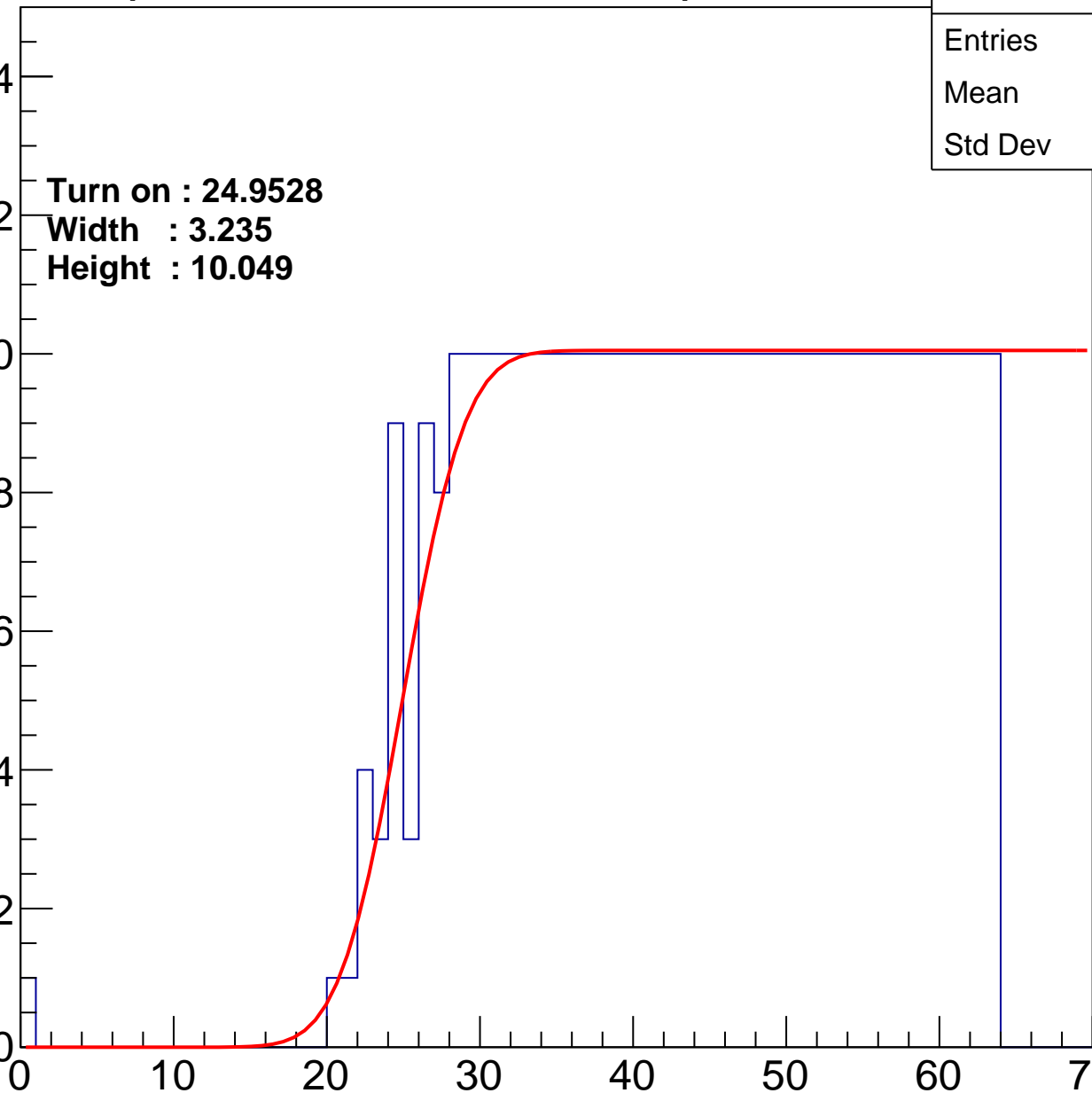
Width : 3.235

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.28
Std Dev	11.36

Turn on : 26.5066

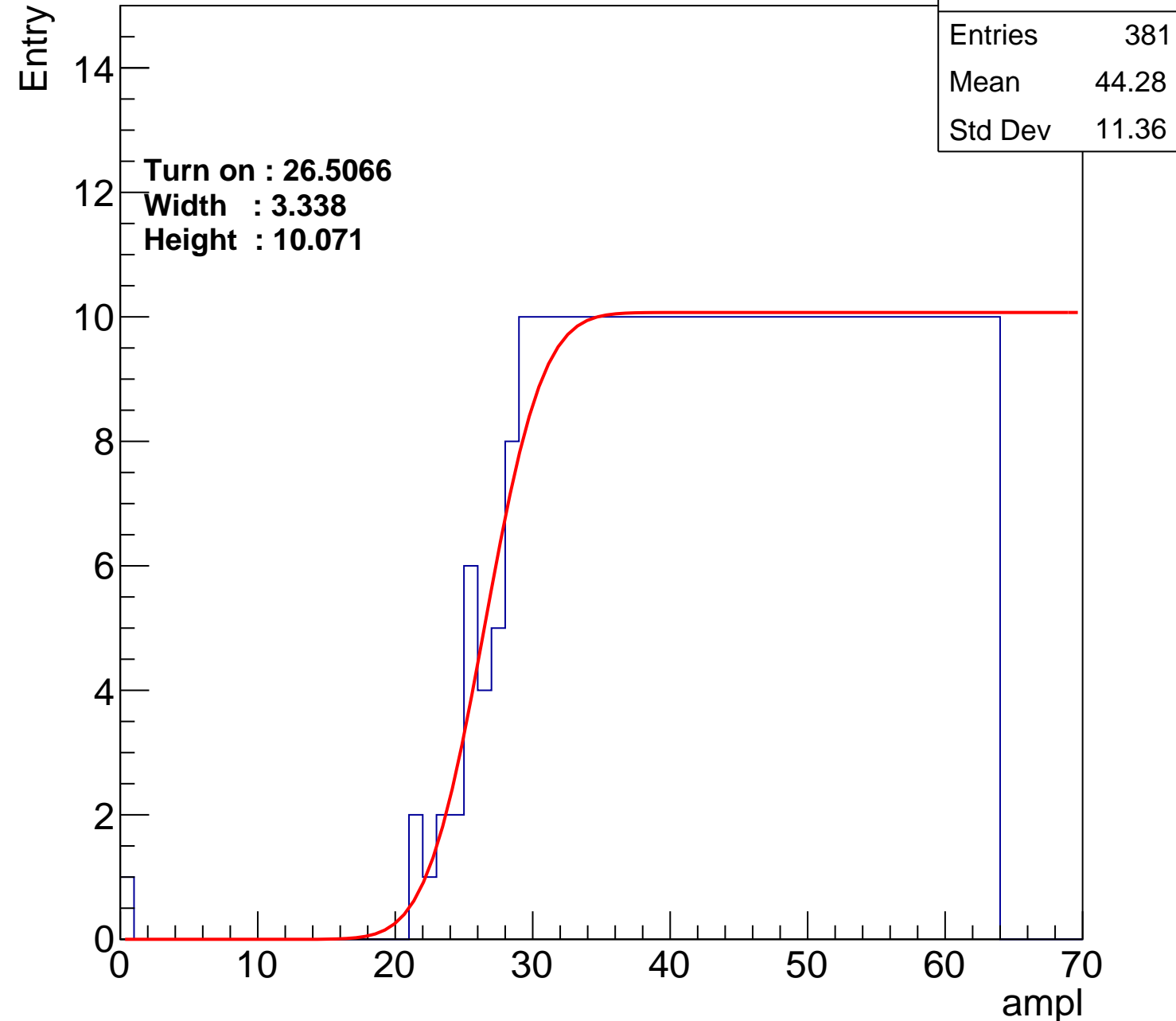
Width : 3.338

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.61
Std Dev	11.82

Turn on : 25.0988

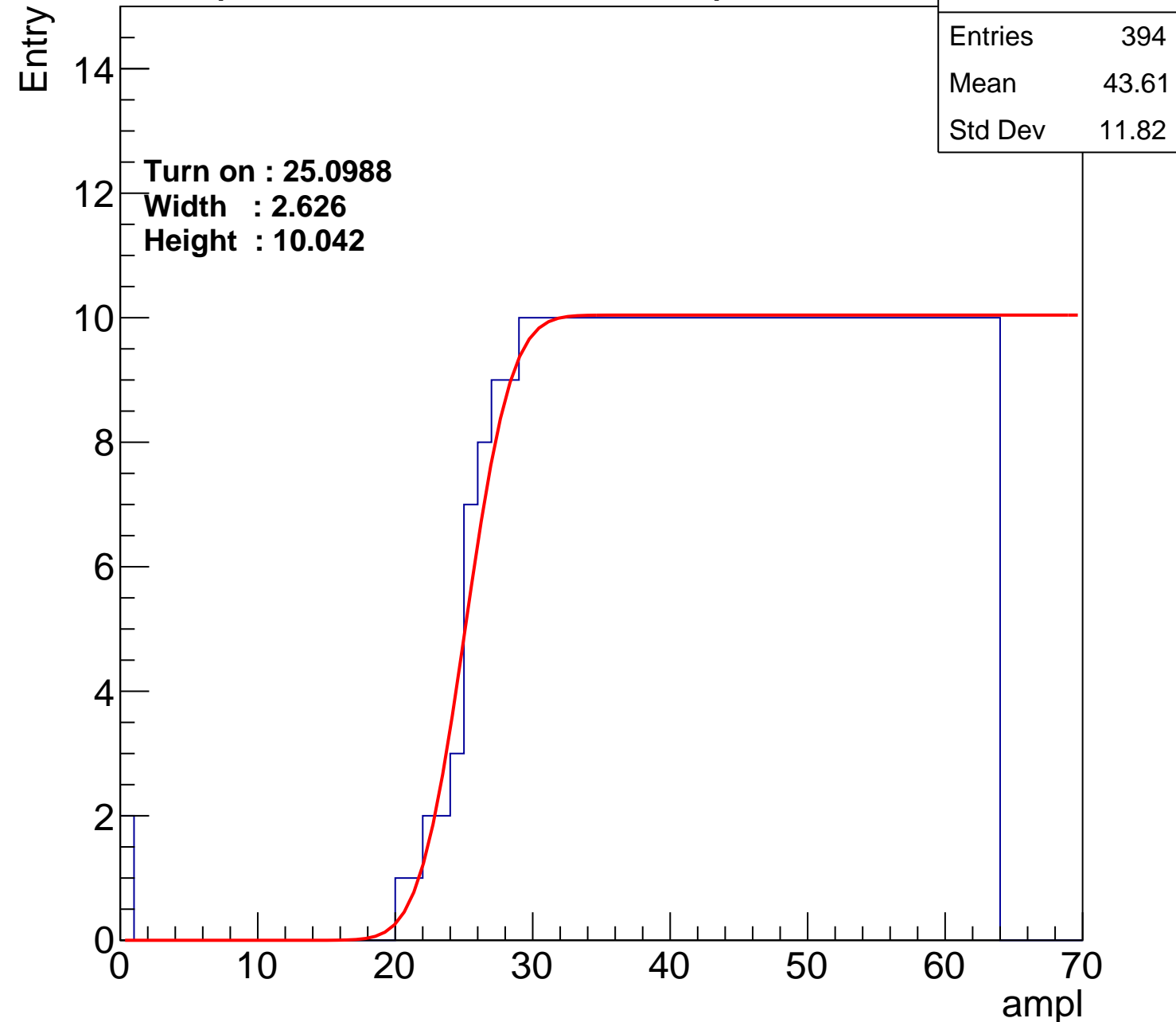
Width : 2.626

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.51
Std Dev	12.05

Turn on : 24.3297

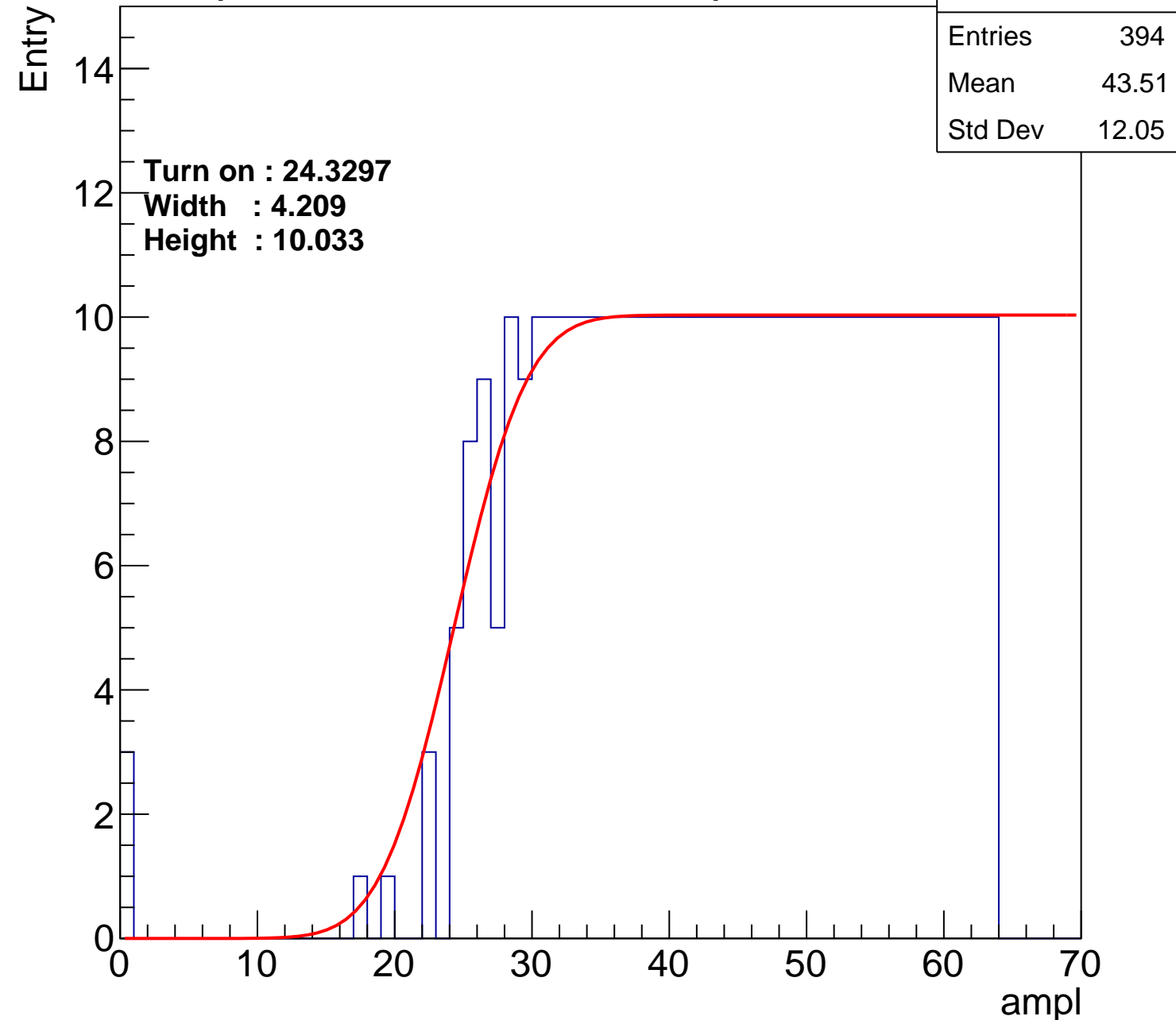
Width : 4.209

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.81
Std Dev	12.06

Turn on : 26.0790

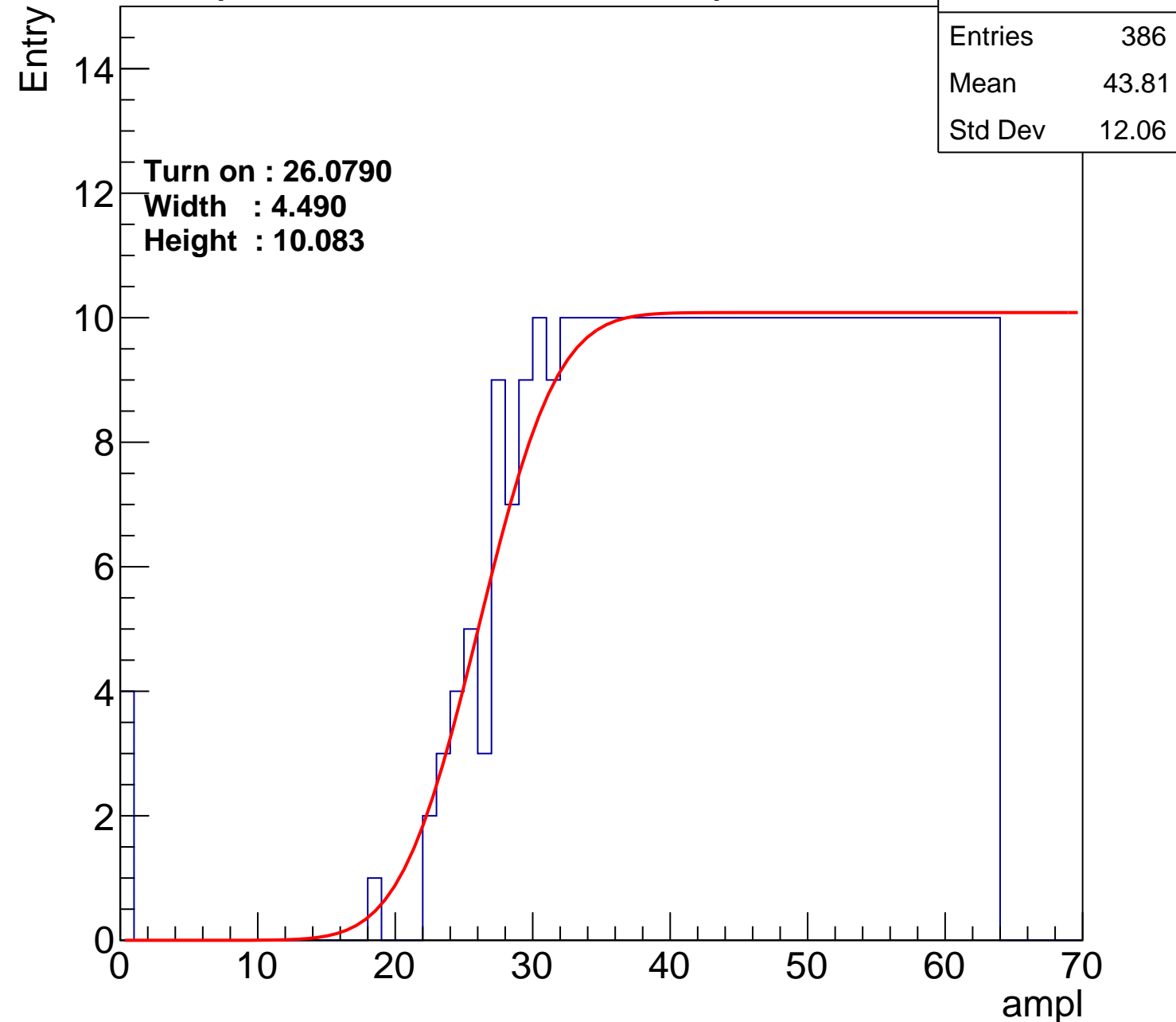
Width : 4.490

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.27
Std Dev	11.63

Turn on : 26.5894

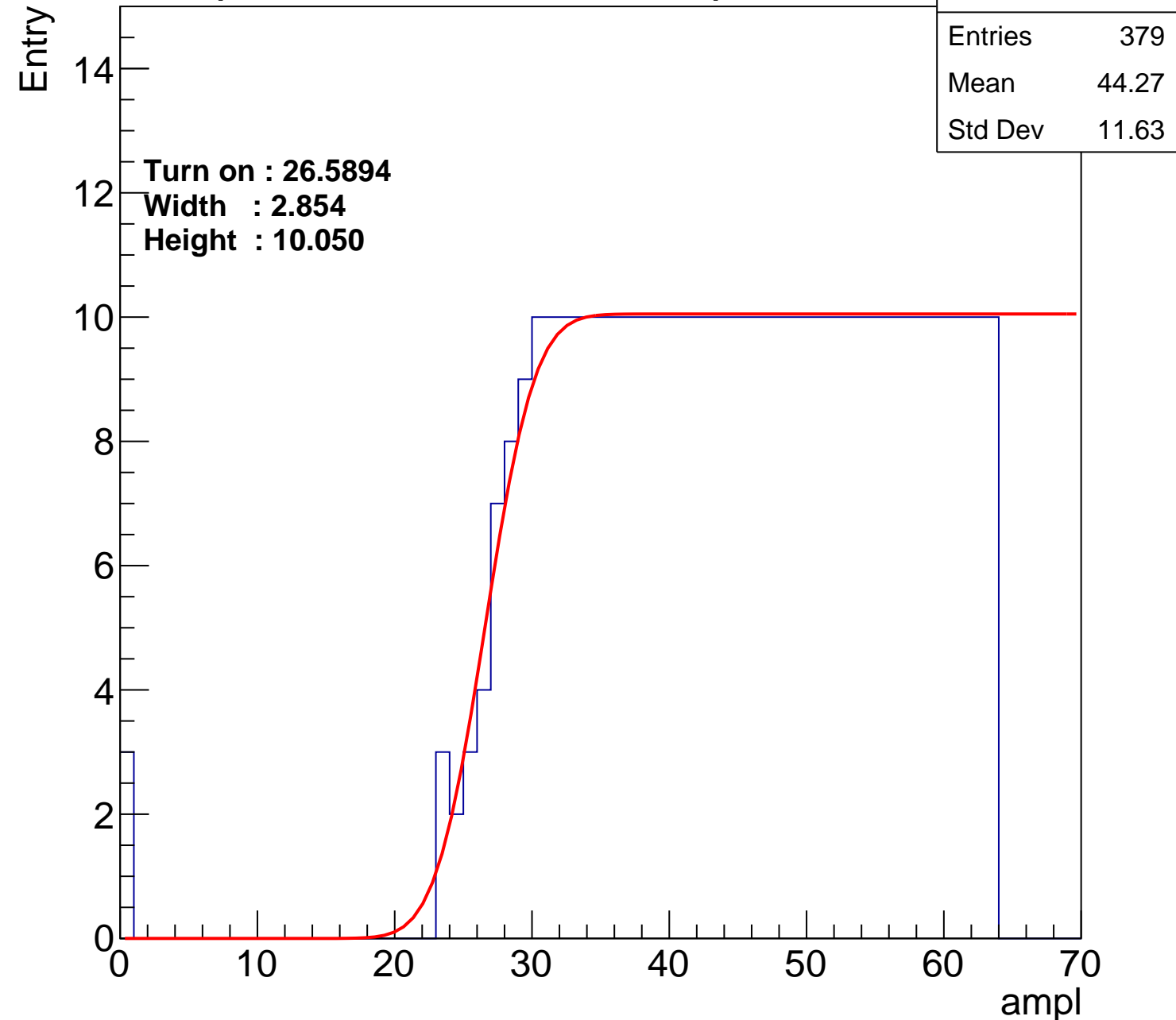
Width : 2.854

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 27.2683

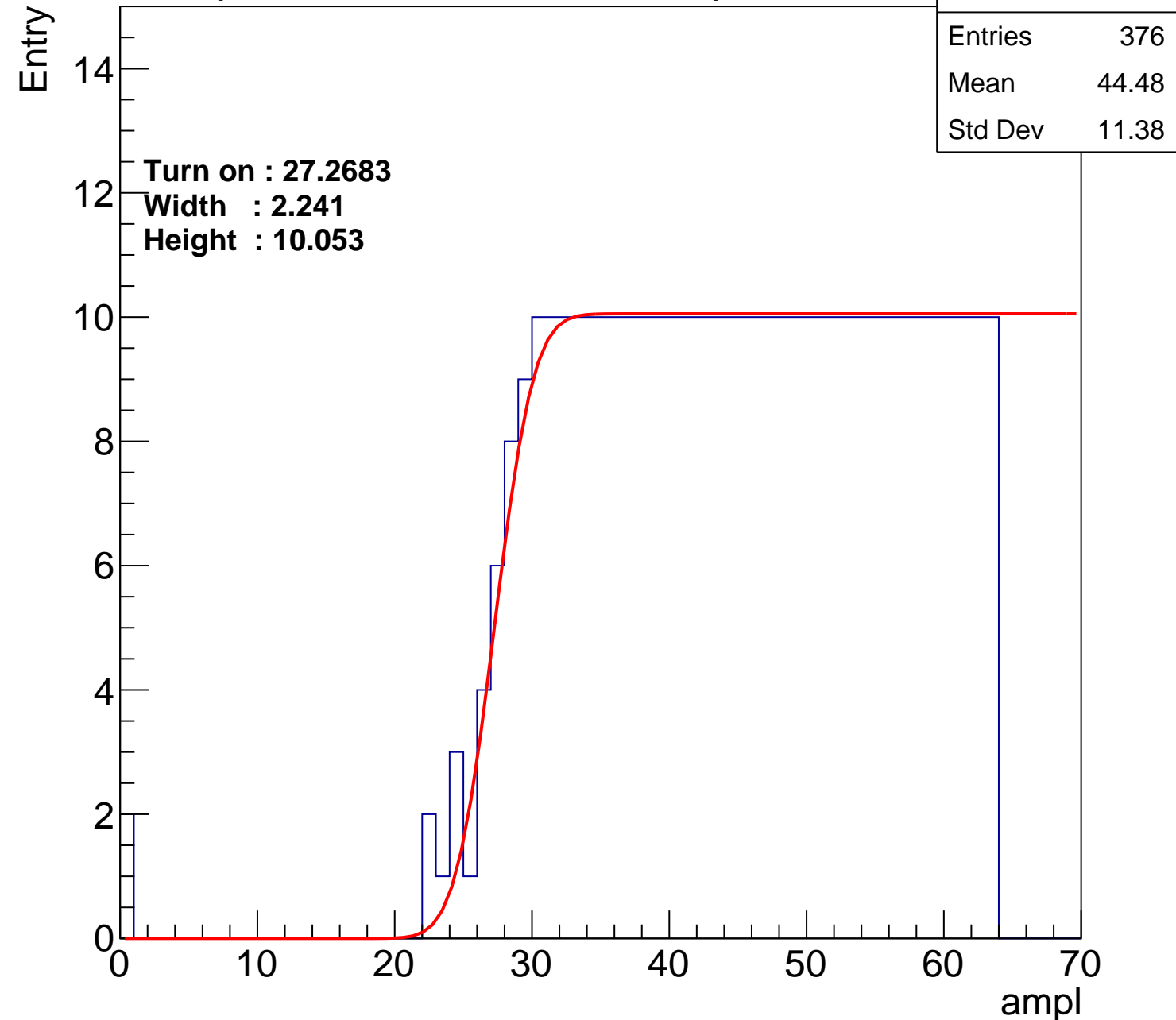
Width : 2.241

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.07
Std Dev	11.5

Turn on : 25.8861

Width : 2.627

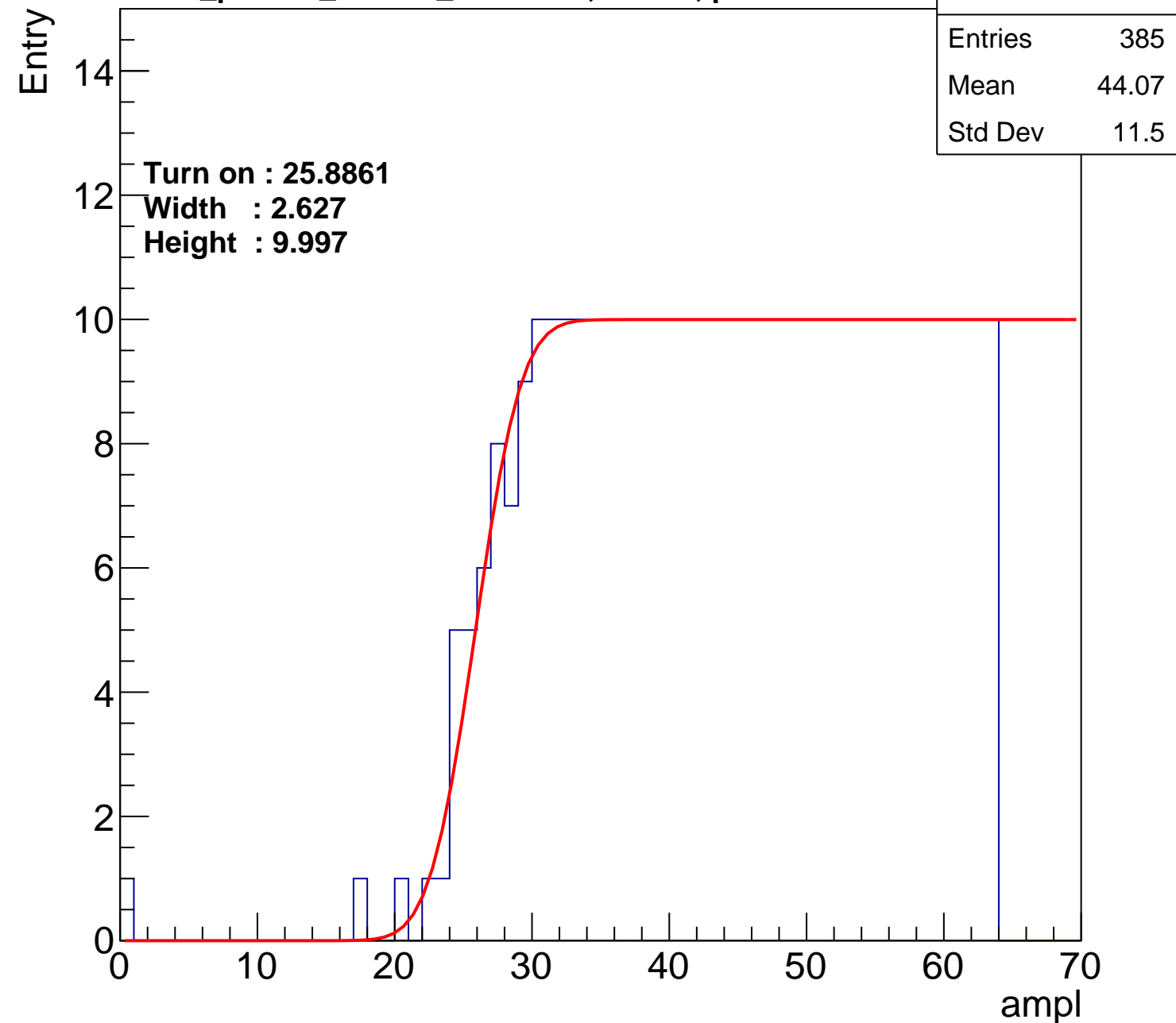
Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U13-ch13

calib_packv5_042523_0143.root, FC#10, port B3

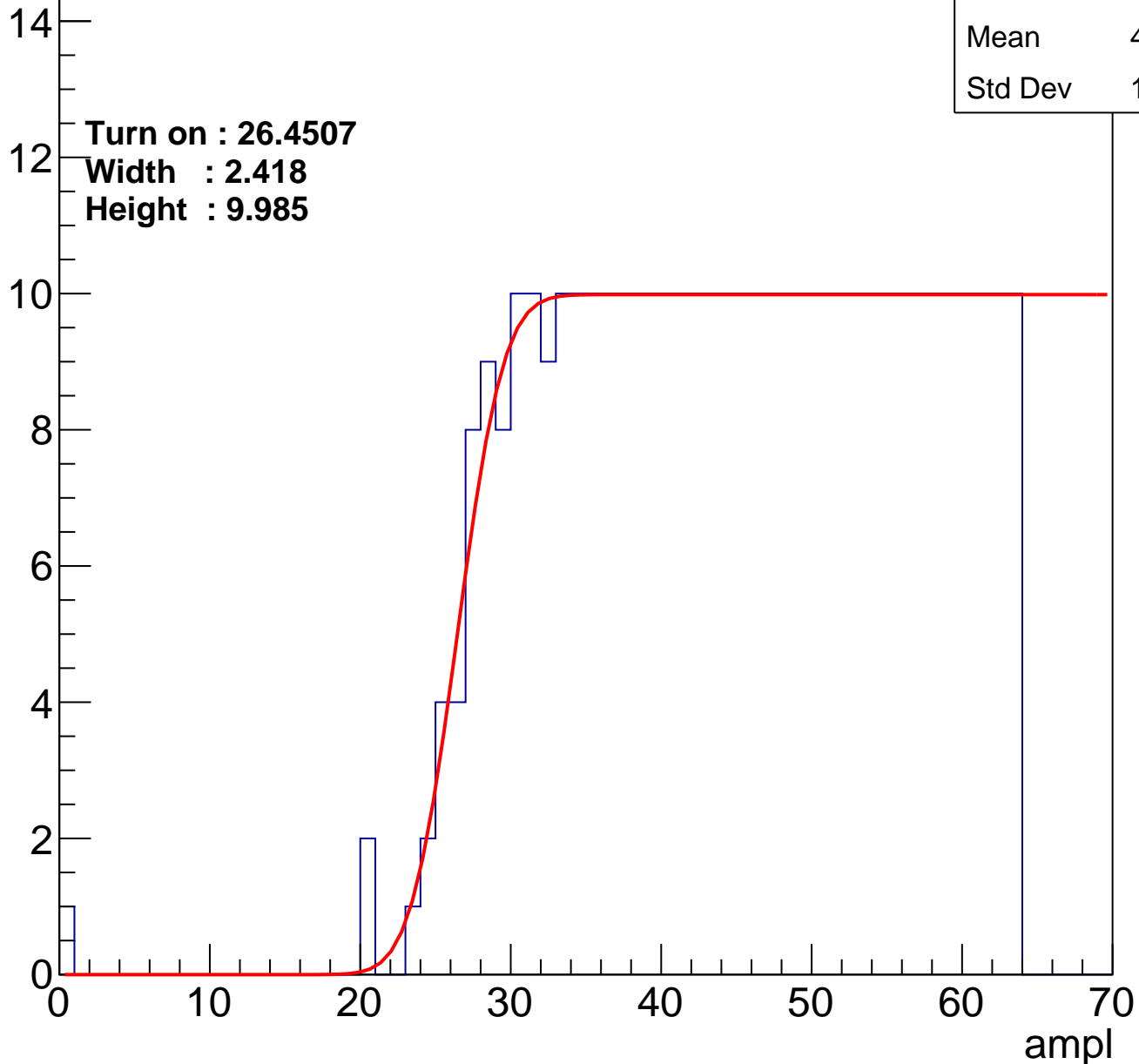
Entries	378
Mean	44.43
Std Dev	11.28

Turn on : 26.4507

Width : 2.418

Height : 9.985

Entry



B1L002S, U13-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.32
Std Dev	11.34

Turn on : 26.1174

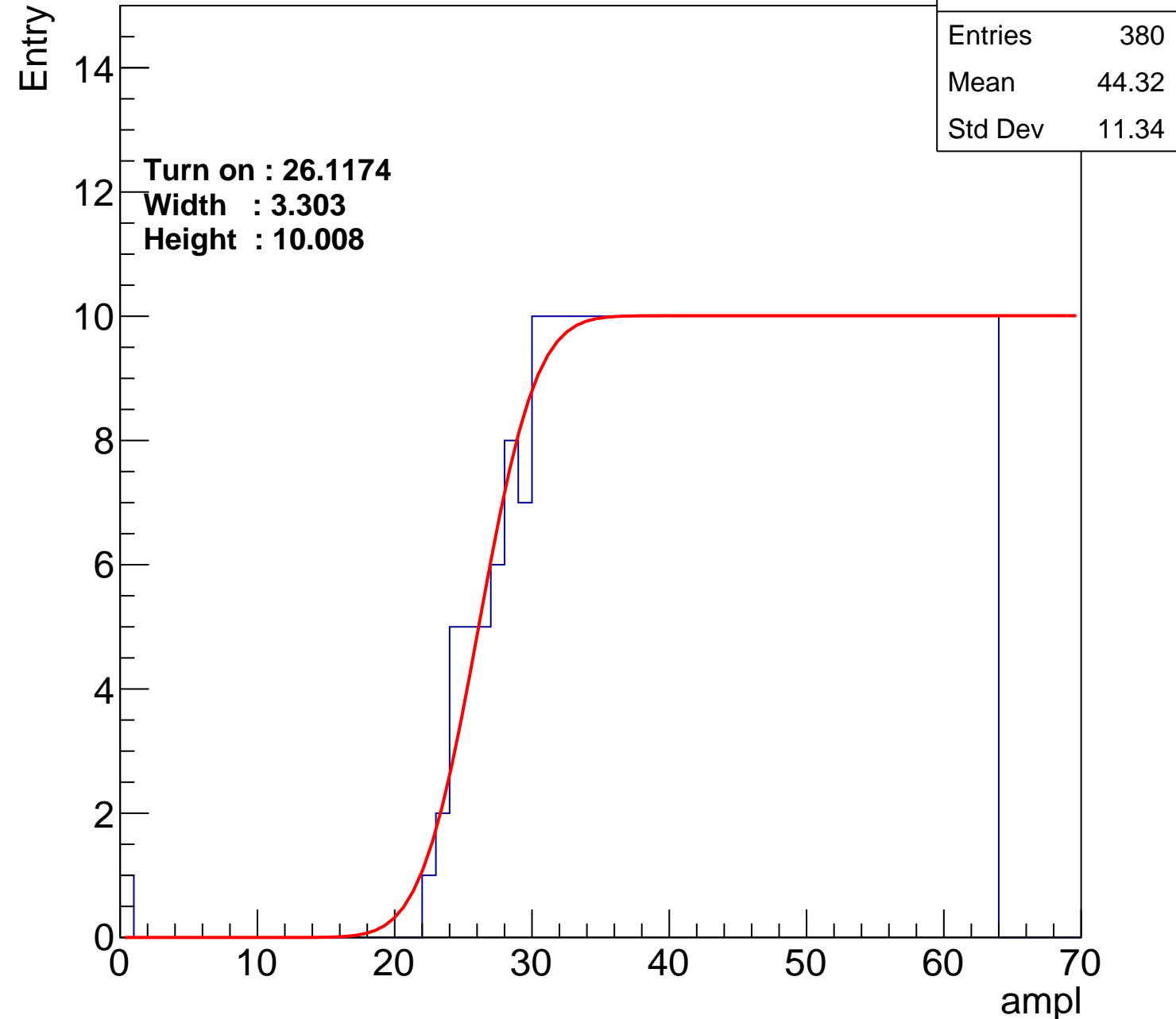
Width : 3.303

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch15

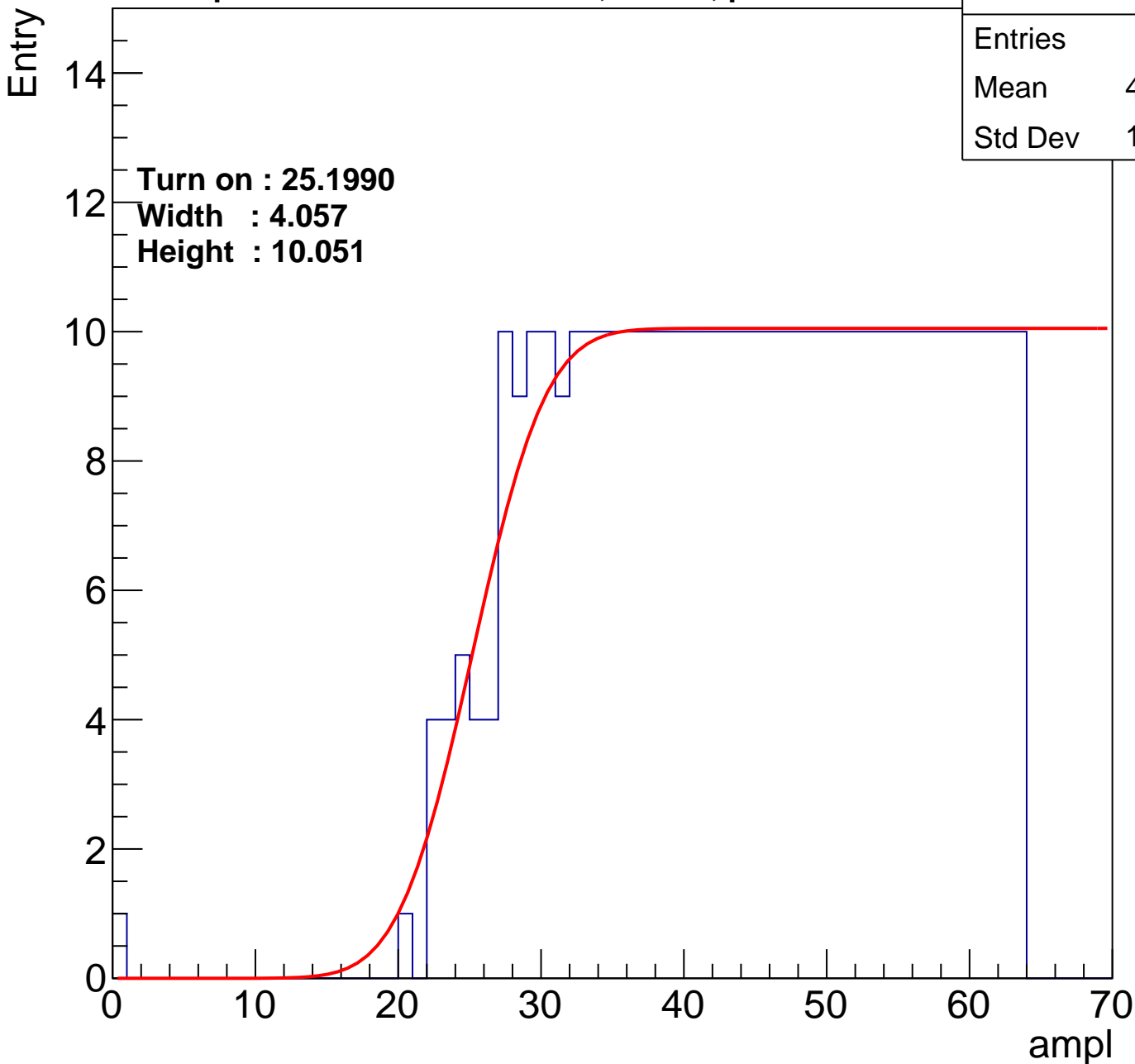
calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.77
Std Dev	11.65

Turn on : 25.1990

Width : 4.057

Height : 10.051



B1L002S, U13-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.9646

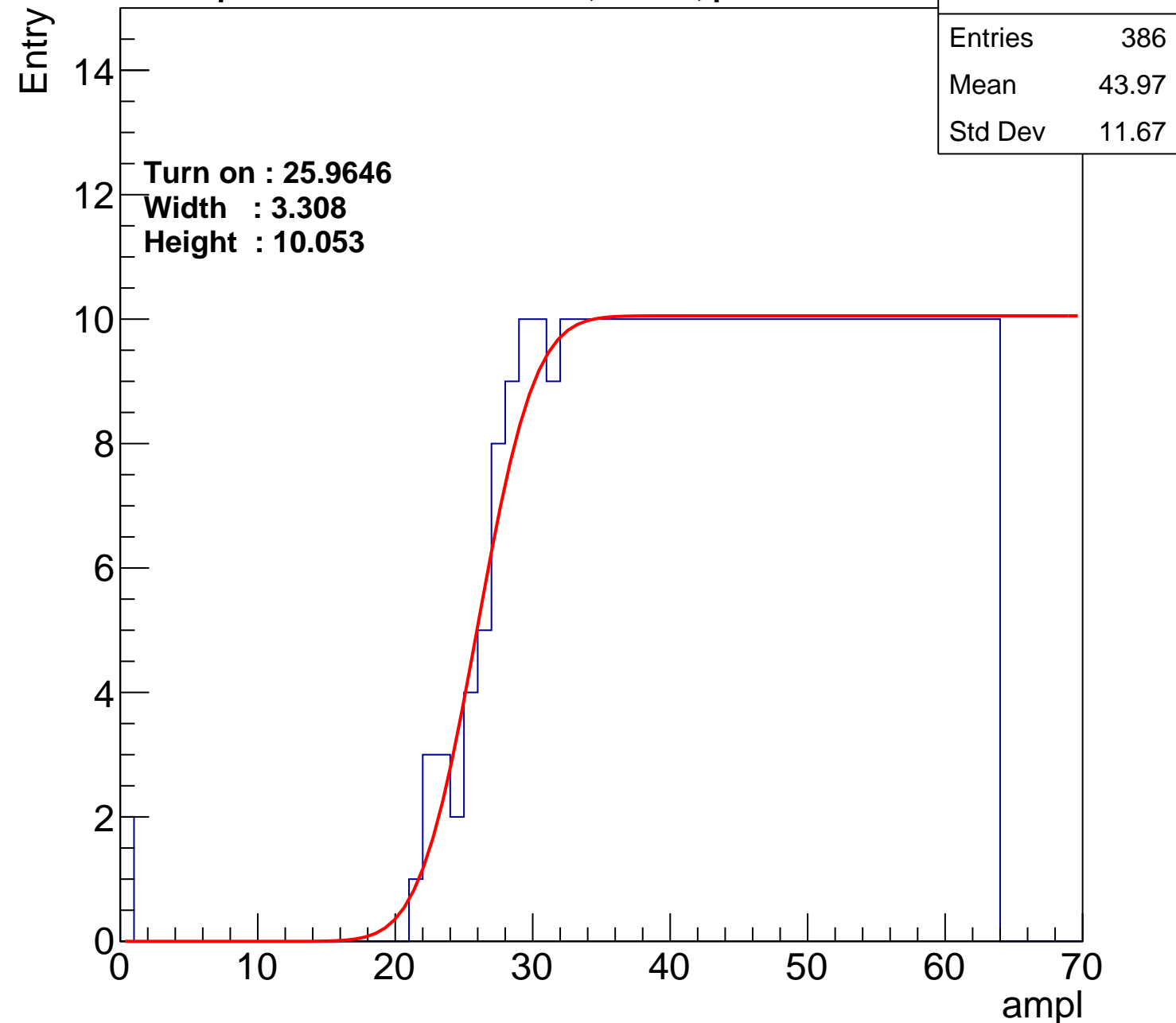
Width : 3.308

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.65
Std Dev	11.81

Turn on : 24.9334

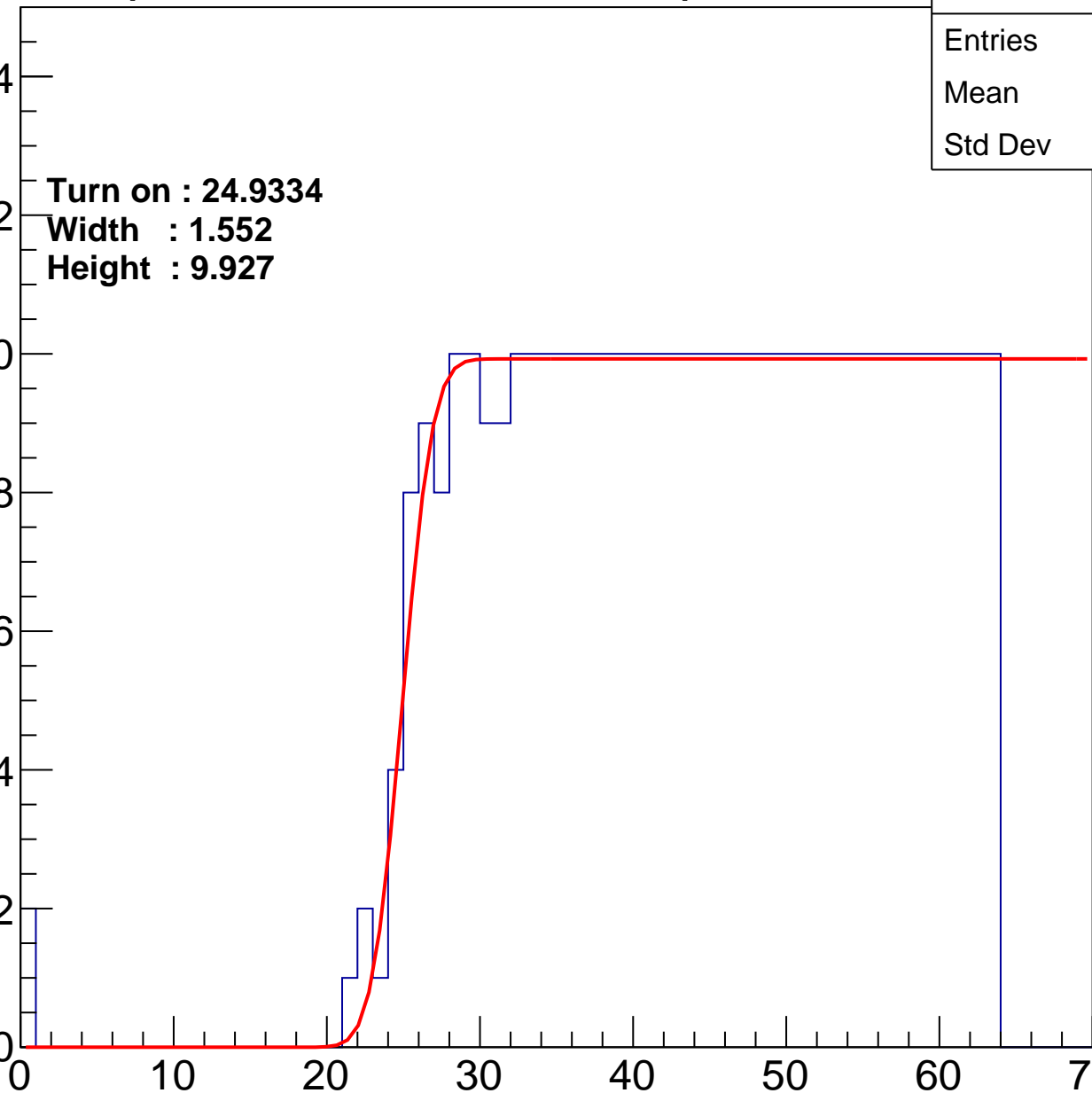
Width : 1.552

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.89
Std Dev	11.51

Turn on : 24.8921

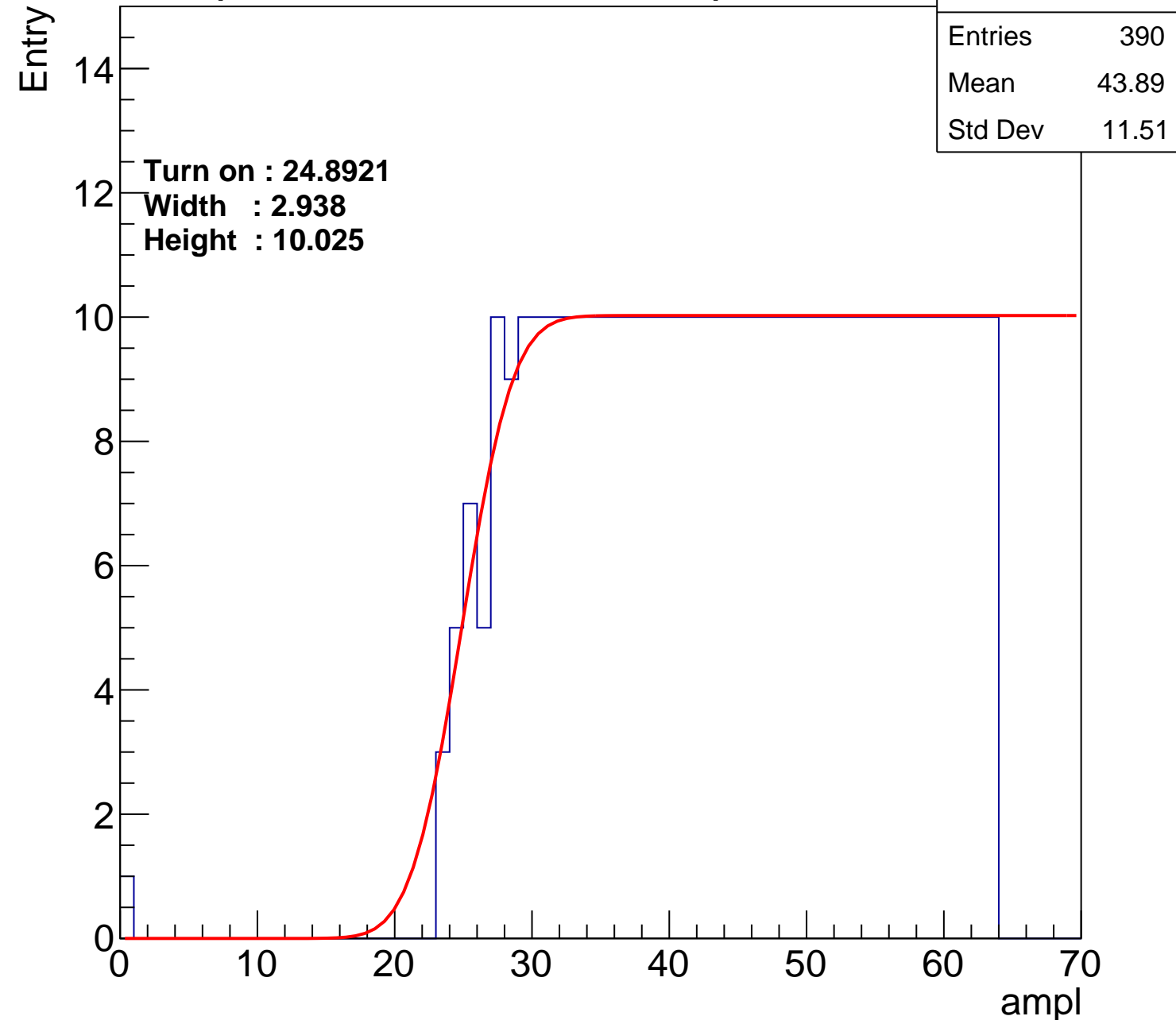
Width : 2.938

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch19

calib_packv5_042523_0143.root, FC#10, port B3

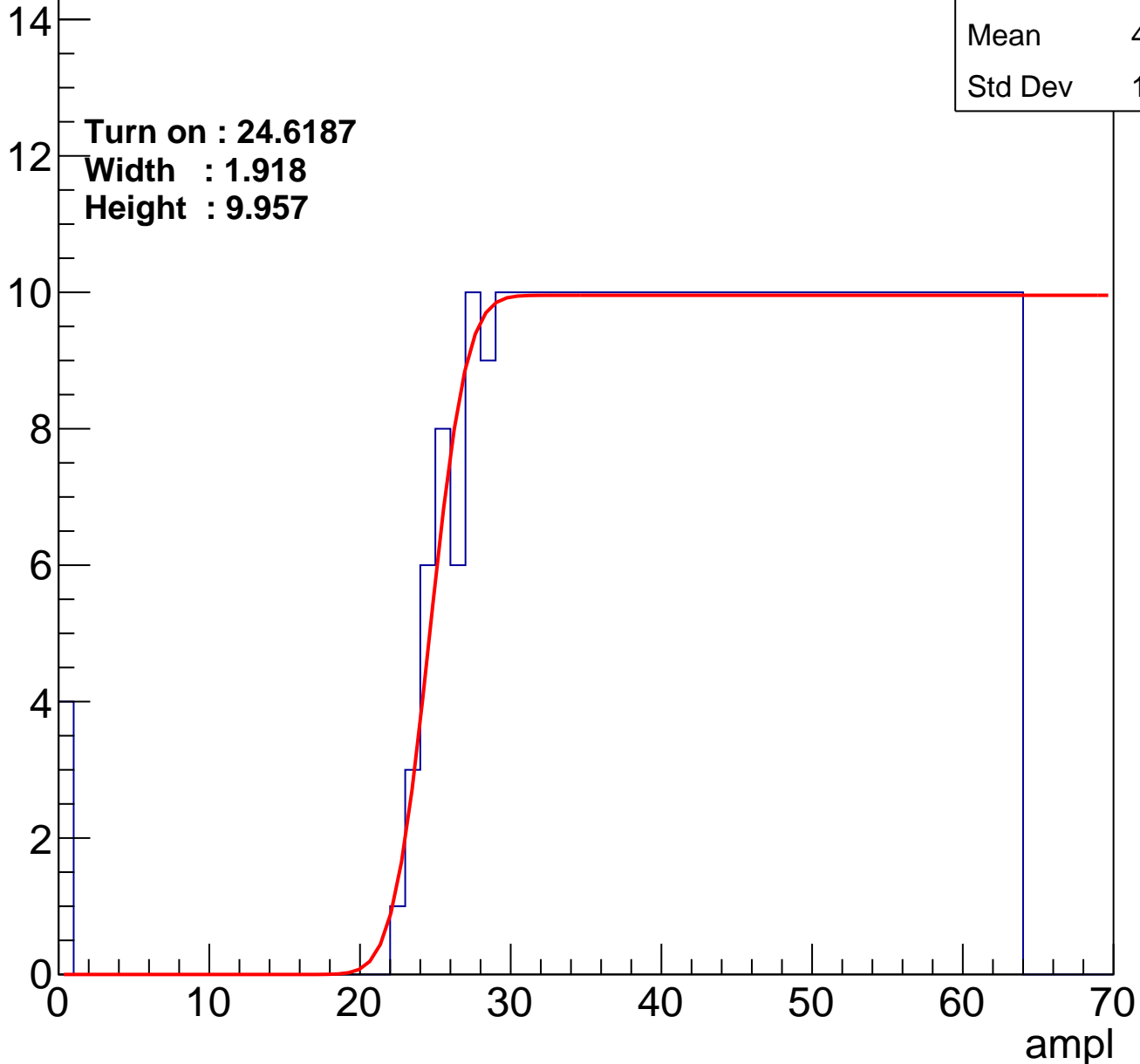
Entries	397
Mean	43.36
Std Dev	12.18

Turn on : 24.6187

Width : 1.918

Height : 9.957

Entry



B1L002S, U13-ch20

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.29
Std Dev	12.23

Turn on : 24.3985

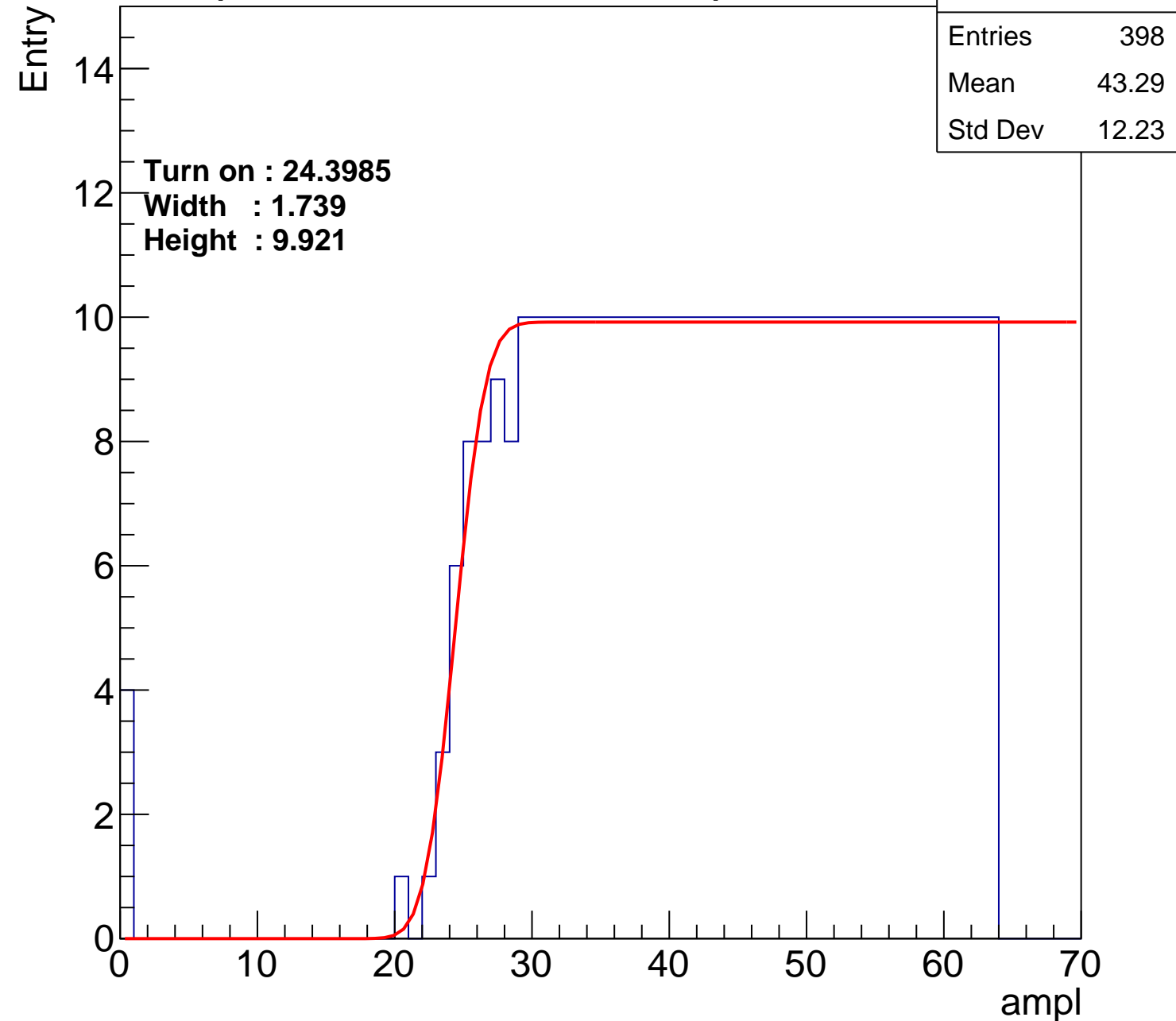
Width : 1.739

Height : 9.921

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch21

calib_packv5_042523_0143.root, FC#10, port B3

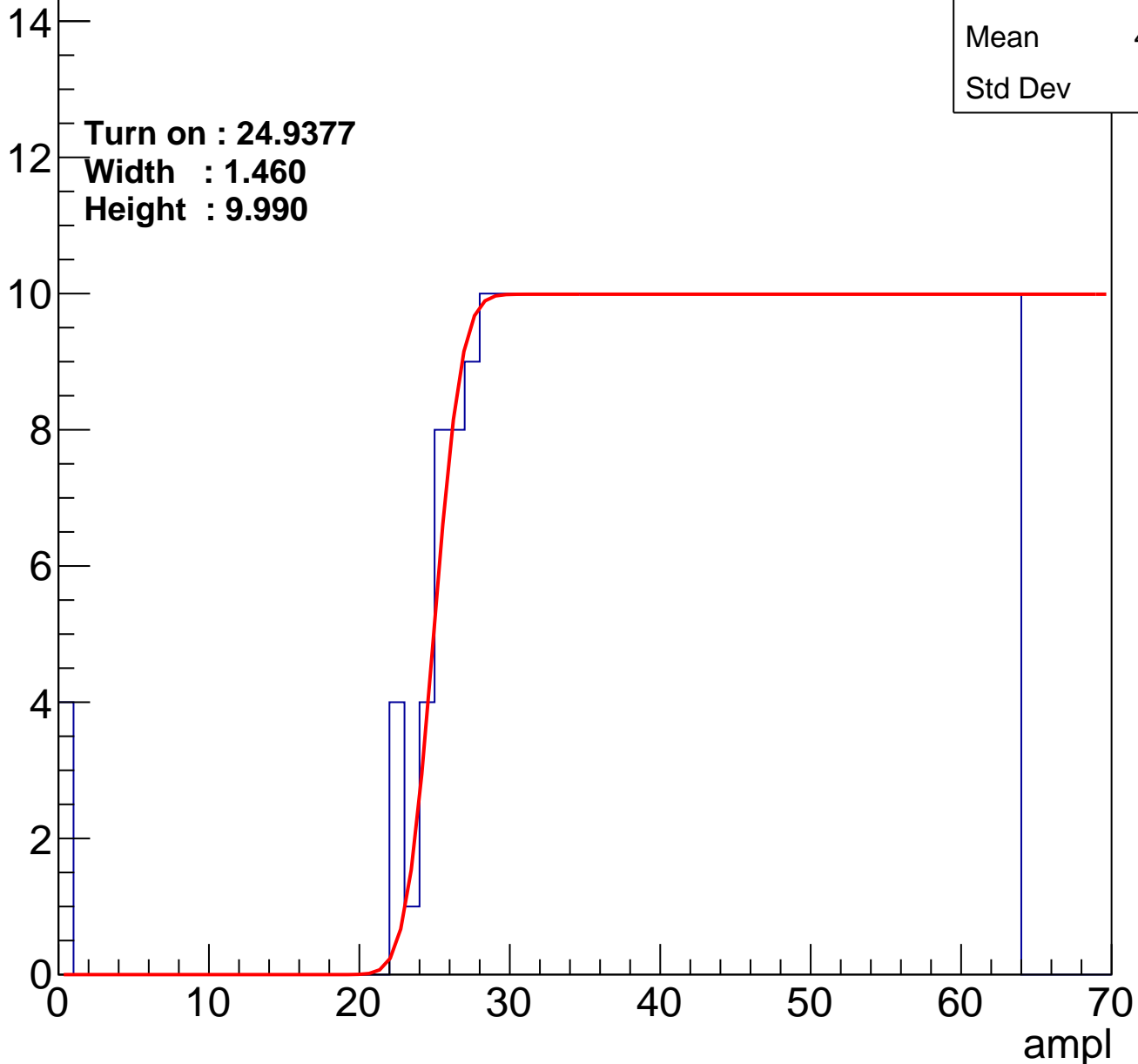
Entries	398
Mean	43.31
Std Dev	12.2

Turn on : 24.9377

Width : 1.460

Height : 9.990

Entry



B1L002S, U13-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.91
Std Dev	11.58

Turn on : 25.5853

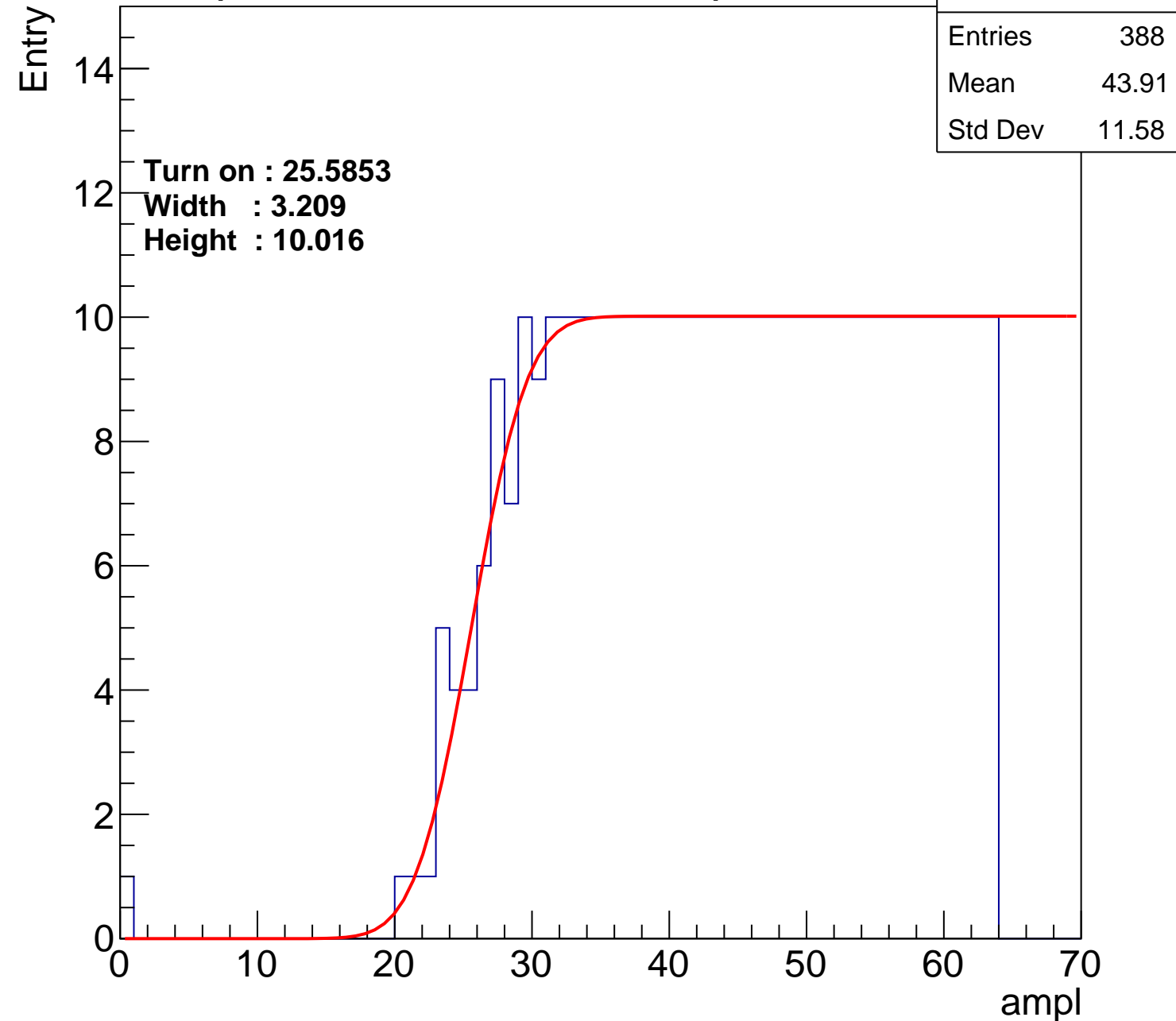
Width : 3.209

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.97
Std Dev	11.61

Turn on : 25.5808

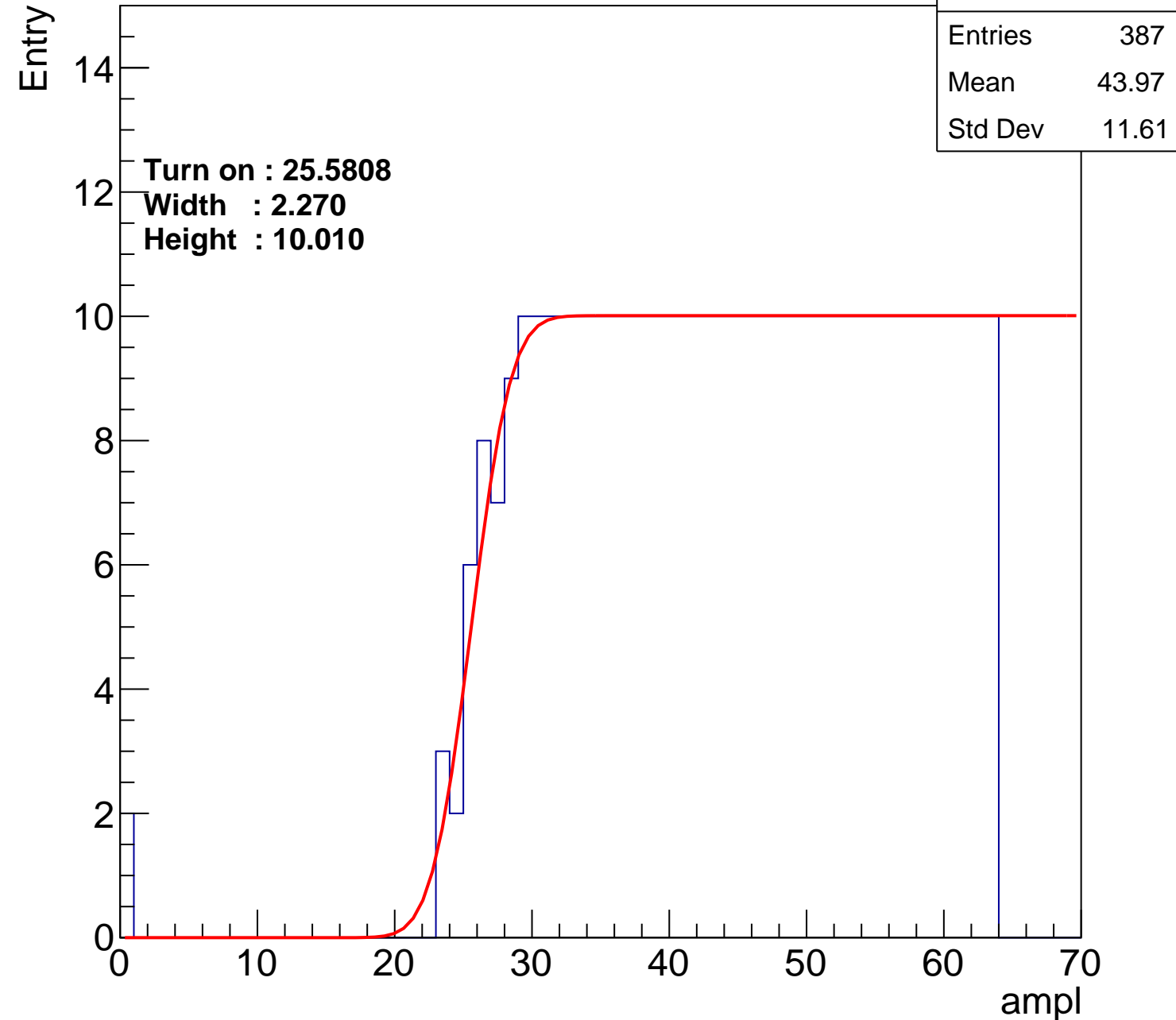
Width : 2.270

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.52
Std Dev	11.5

Turn on : 26.9386

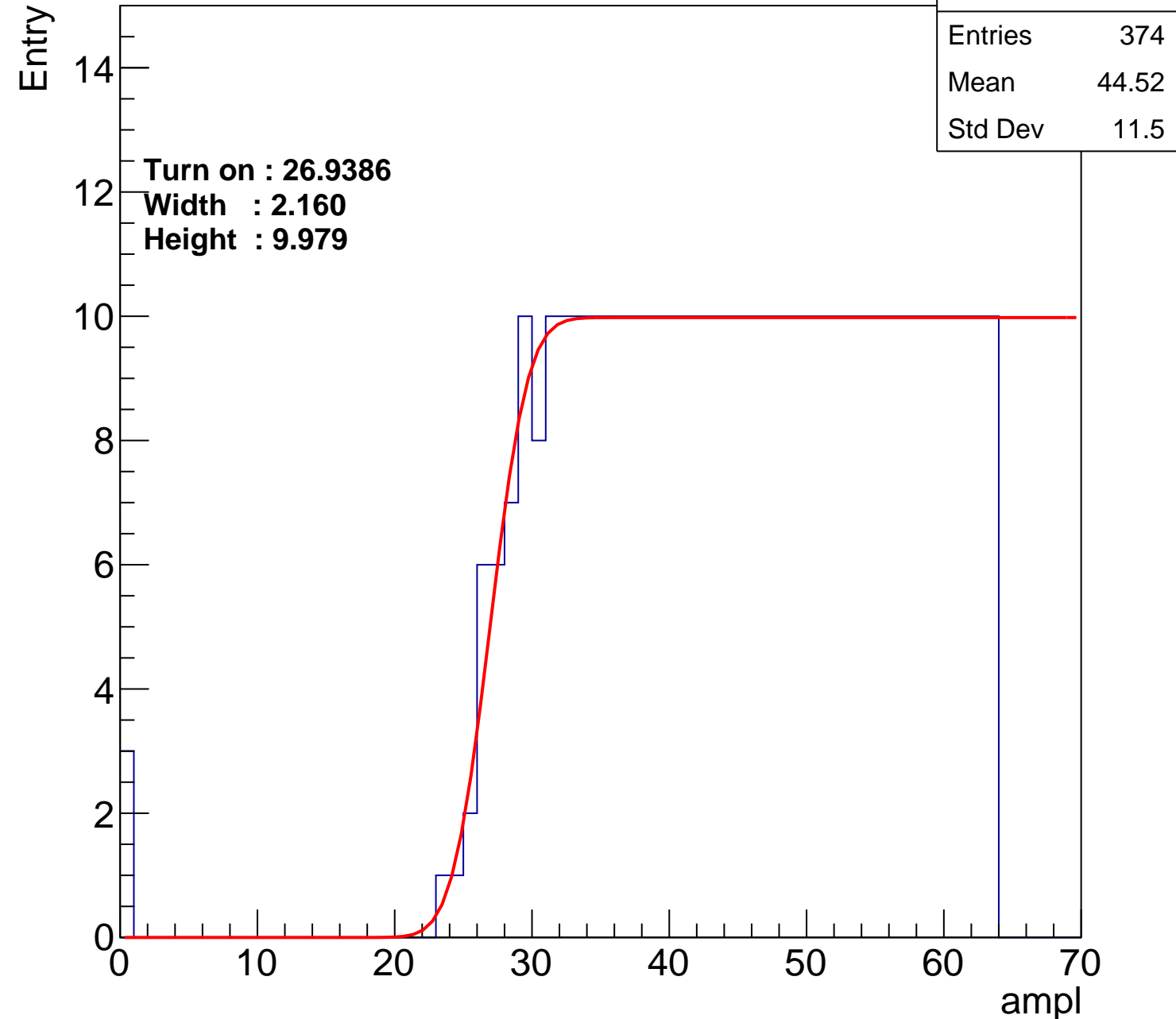
Width : 2.160

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl

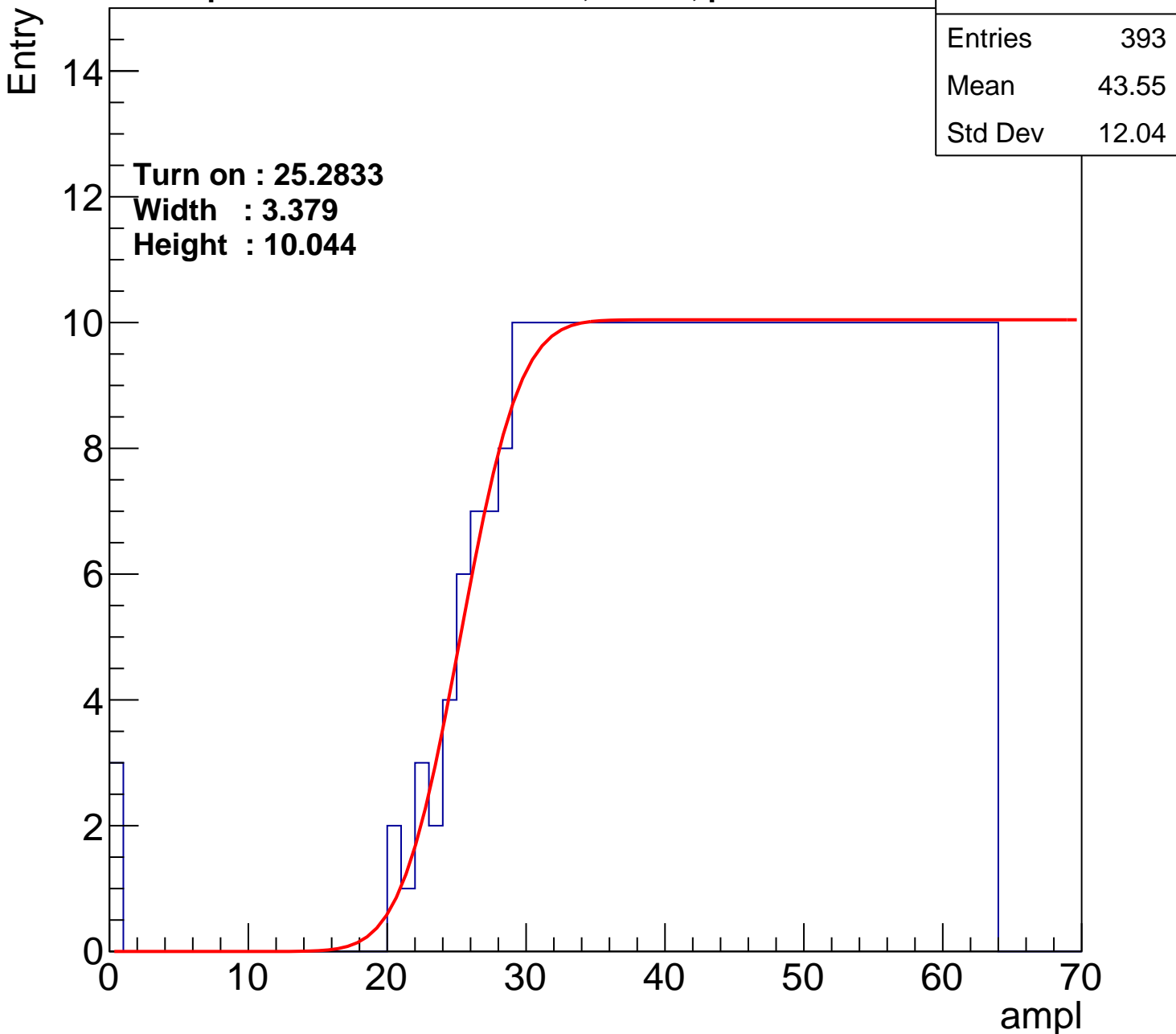


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.55
Std Dev	12.04

Height : 10.044



B1L002S, U13-ch26

calib_packv5_042523_0143.root, FC#10, port B3

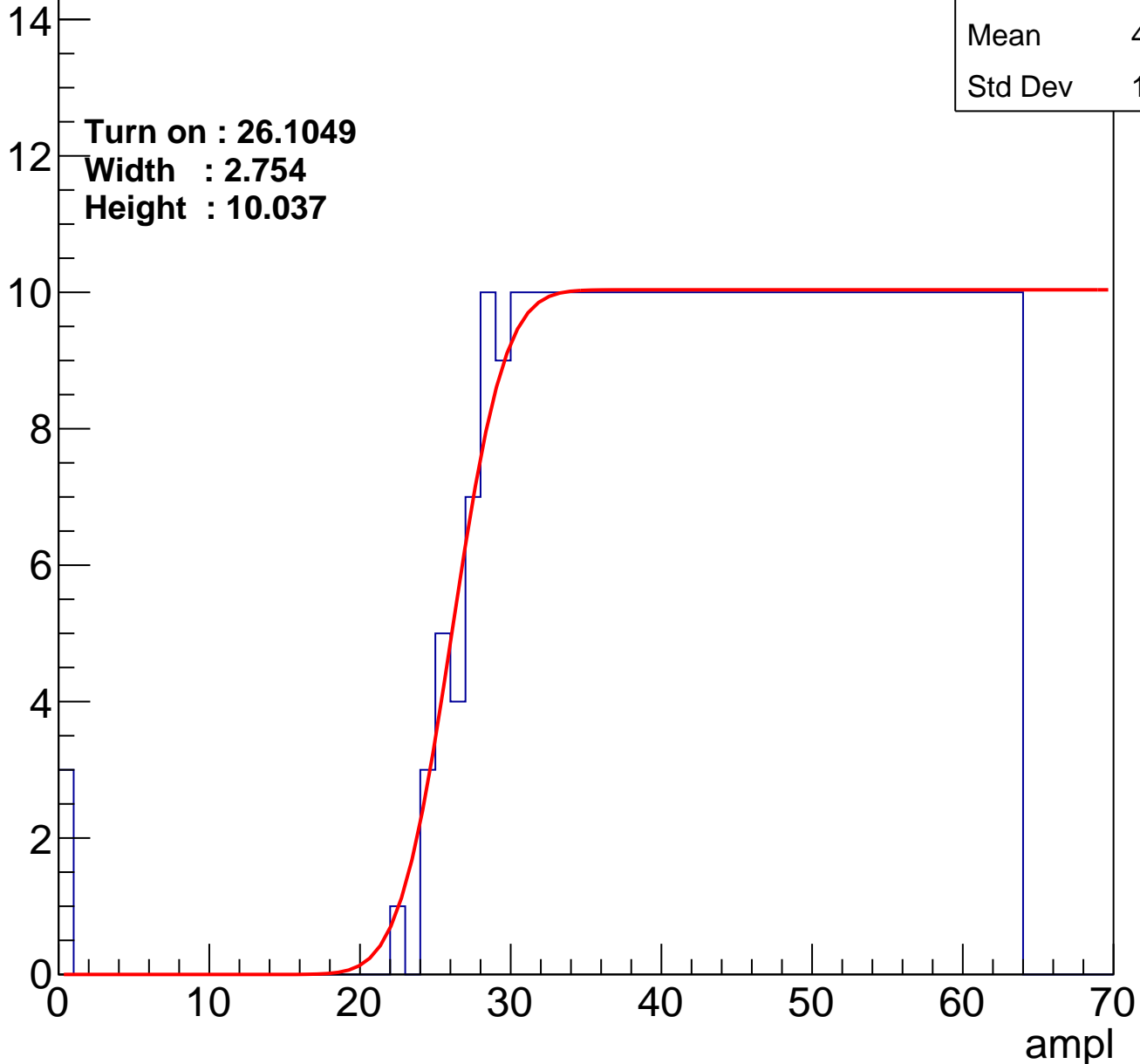
Entries	382
Mean	44.14
Std Dev	11.67

Turn on : 26.1049

Width : 2.754

Height : 10.037

Entry



B1L002S, U13-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.17
Std Dev	12.11

Turn on : 26.9460

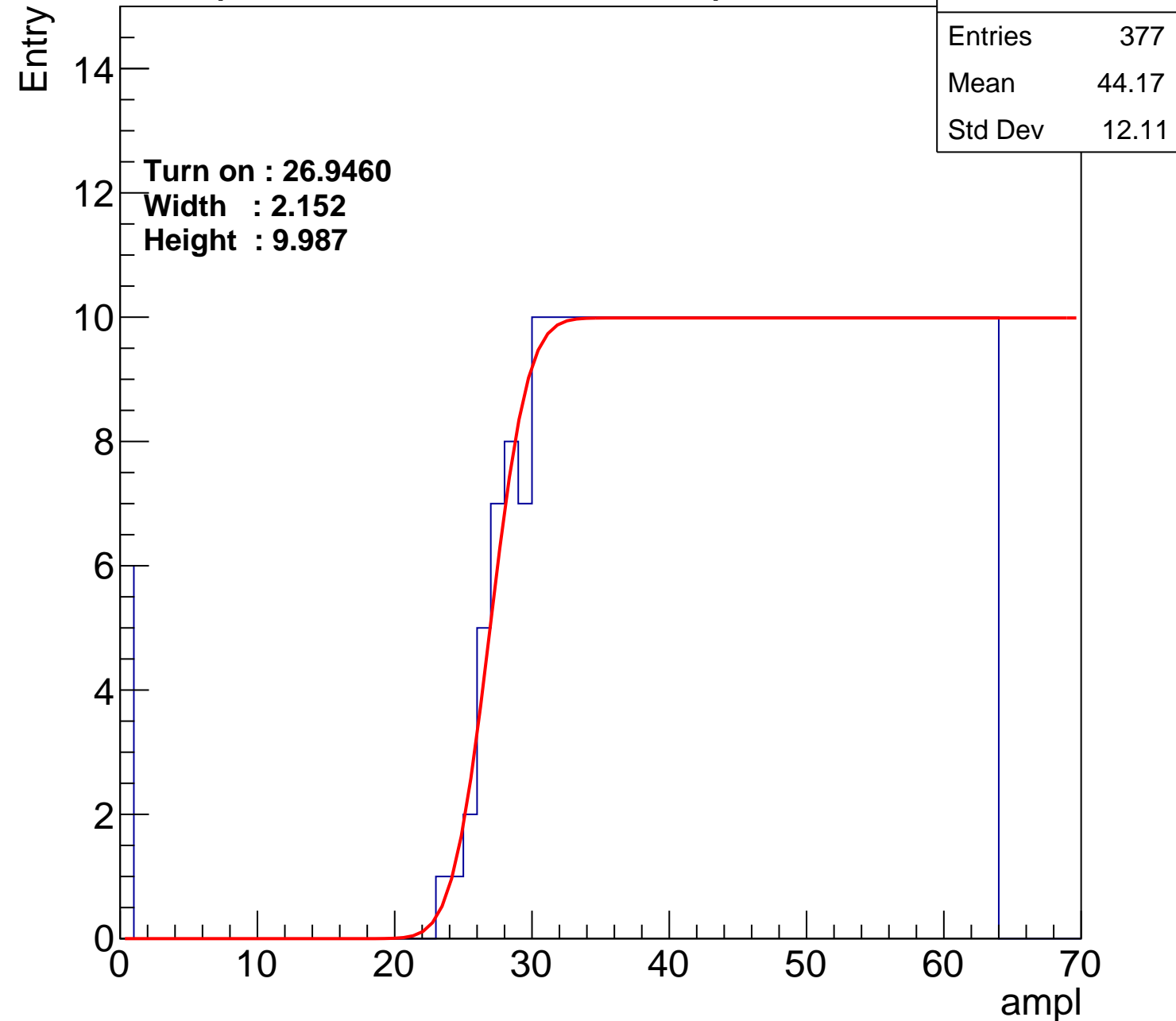
Width : 2.152

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch28

calib_packv5_042523_0143.root, FC#10, port B3

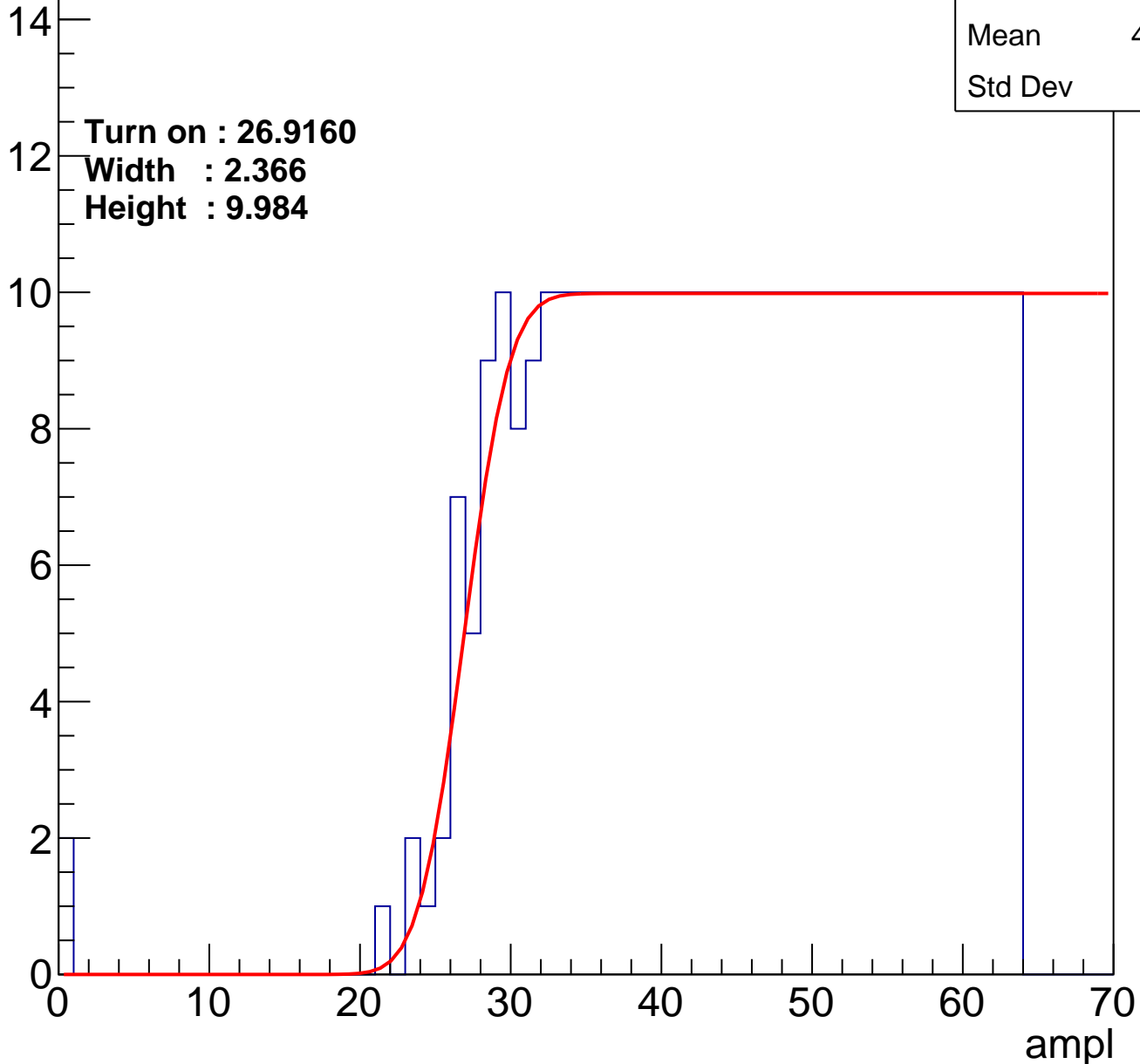
Entries	376
Mean	44.47
Std Dev	11.4

Turn on : 26.9160

Width : 2.366

Height : 9.984

Entry



B1L002S, U13-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.73
Std Dev	11.91

Turn on : 25.6532

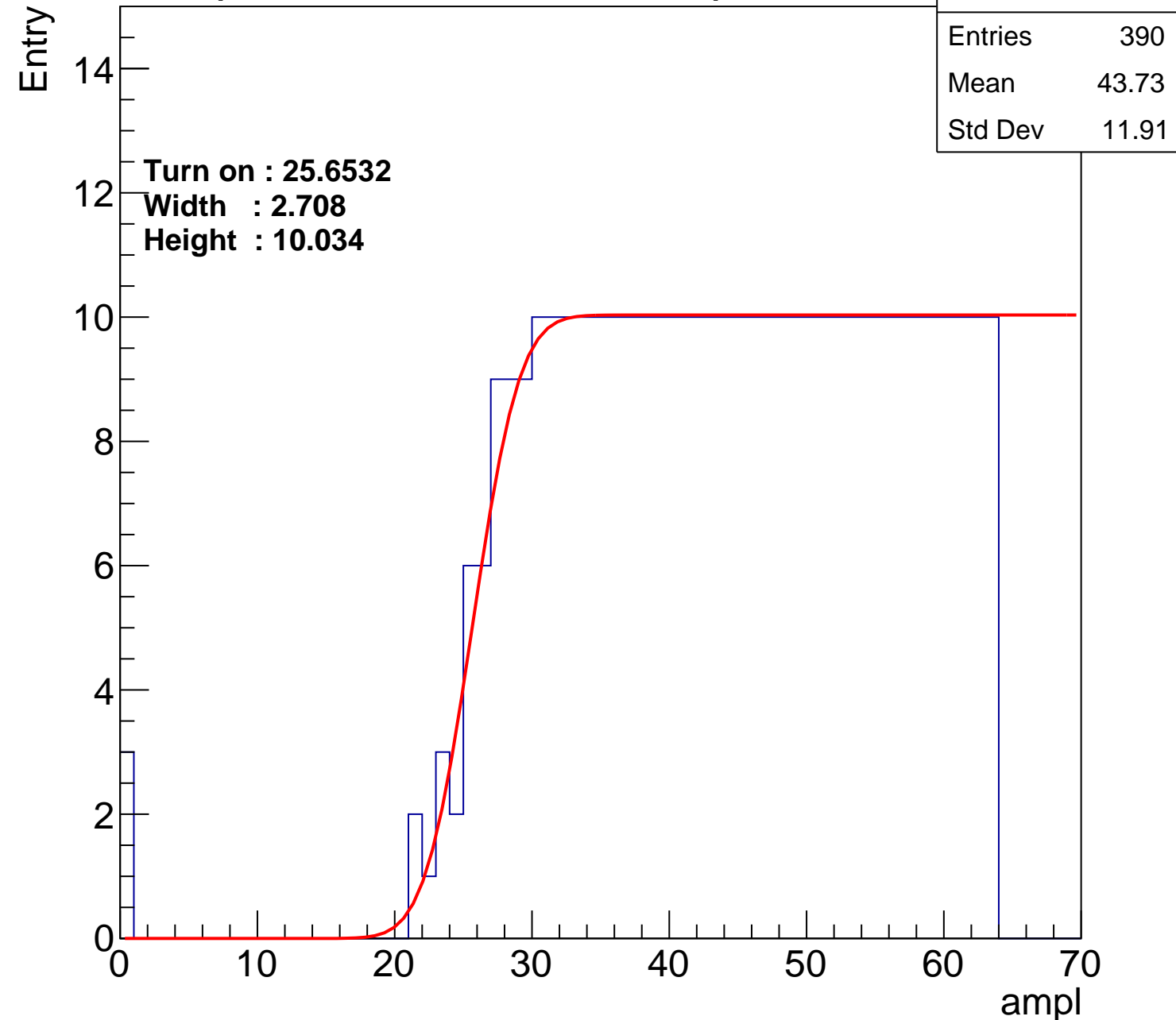
Width : 2.708

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.52
Std Dev	11.7

Turn on : 27.4227

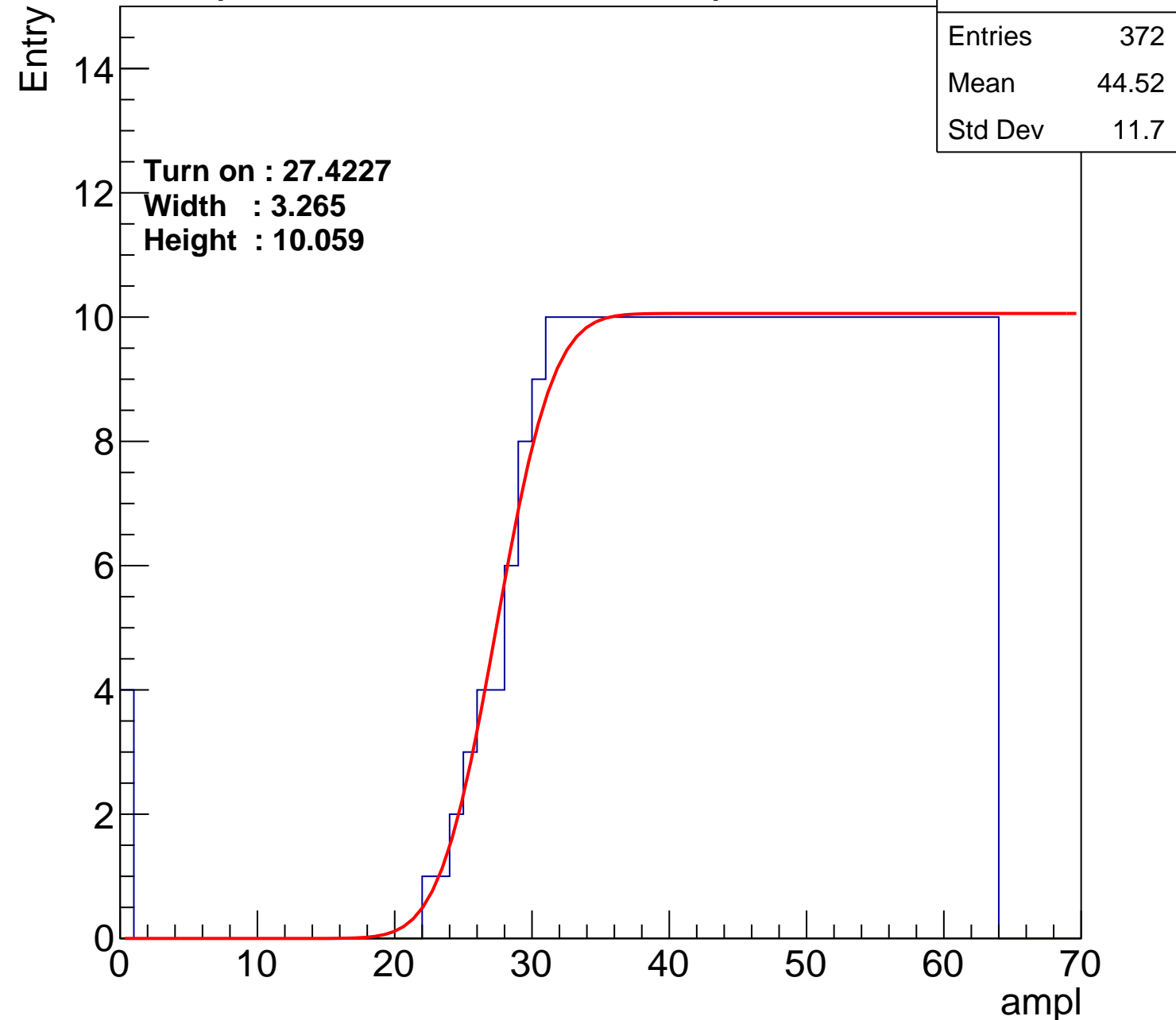
Width : 3.265

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch31

calib_packv5_042523_0143.root, FC#10, port B3

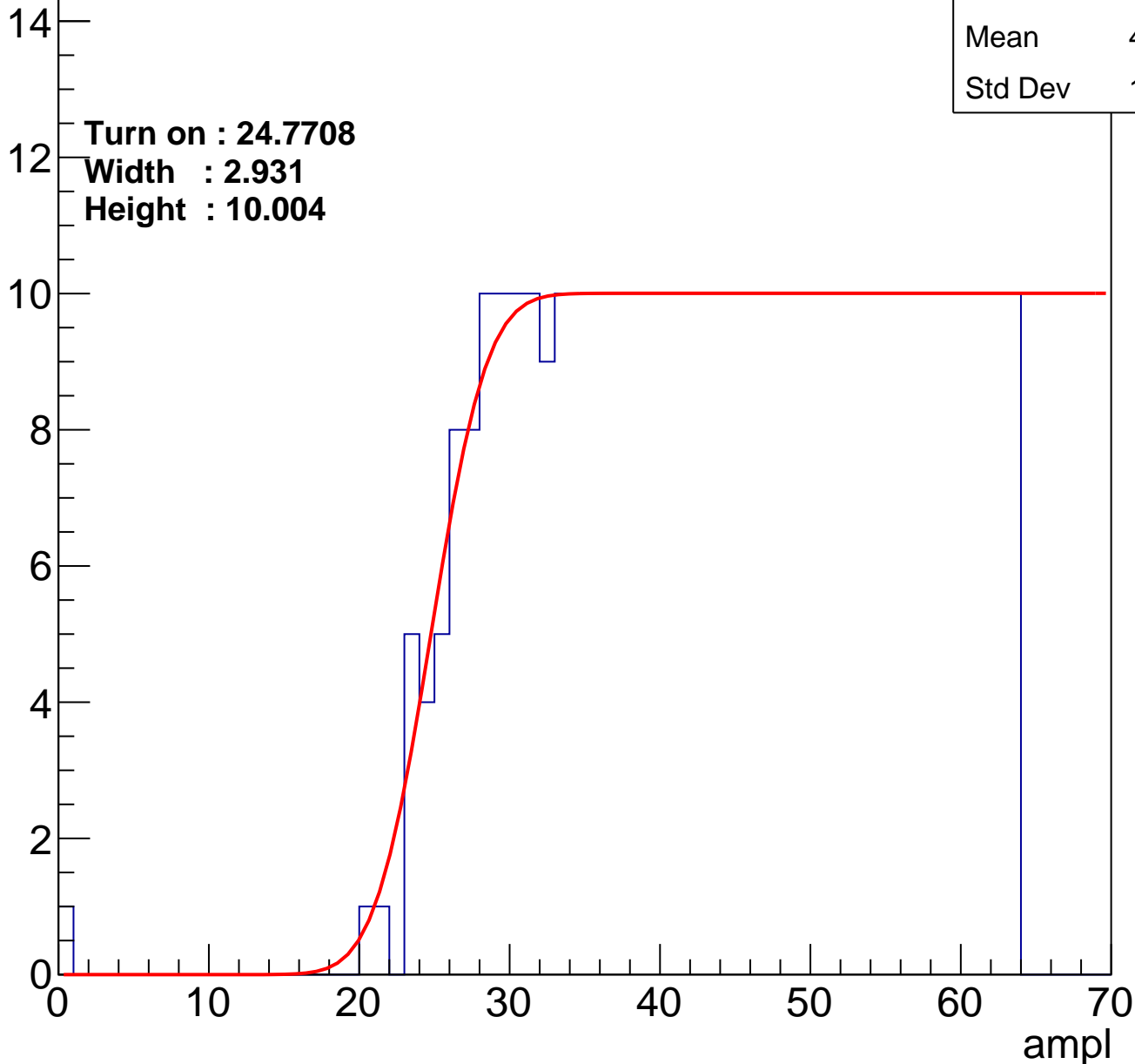
Entries	392
Mean	43.75
Std Dev	11.63

Turn on : 24.7708

Width : 2.931

Height : 10.004

Entry



B1L002S, U13-ch32

calib_packv5_042523_0143.root, FC#10, port B3

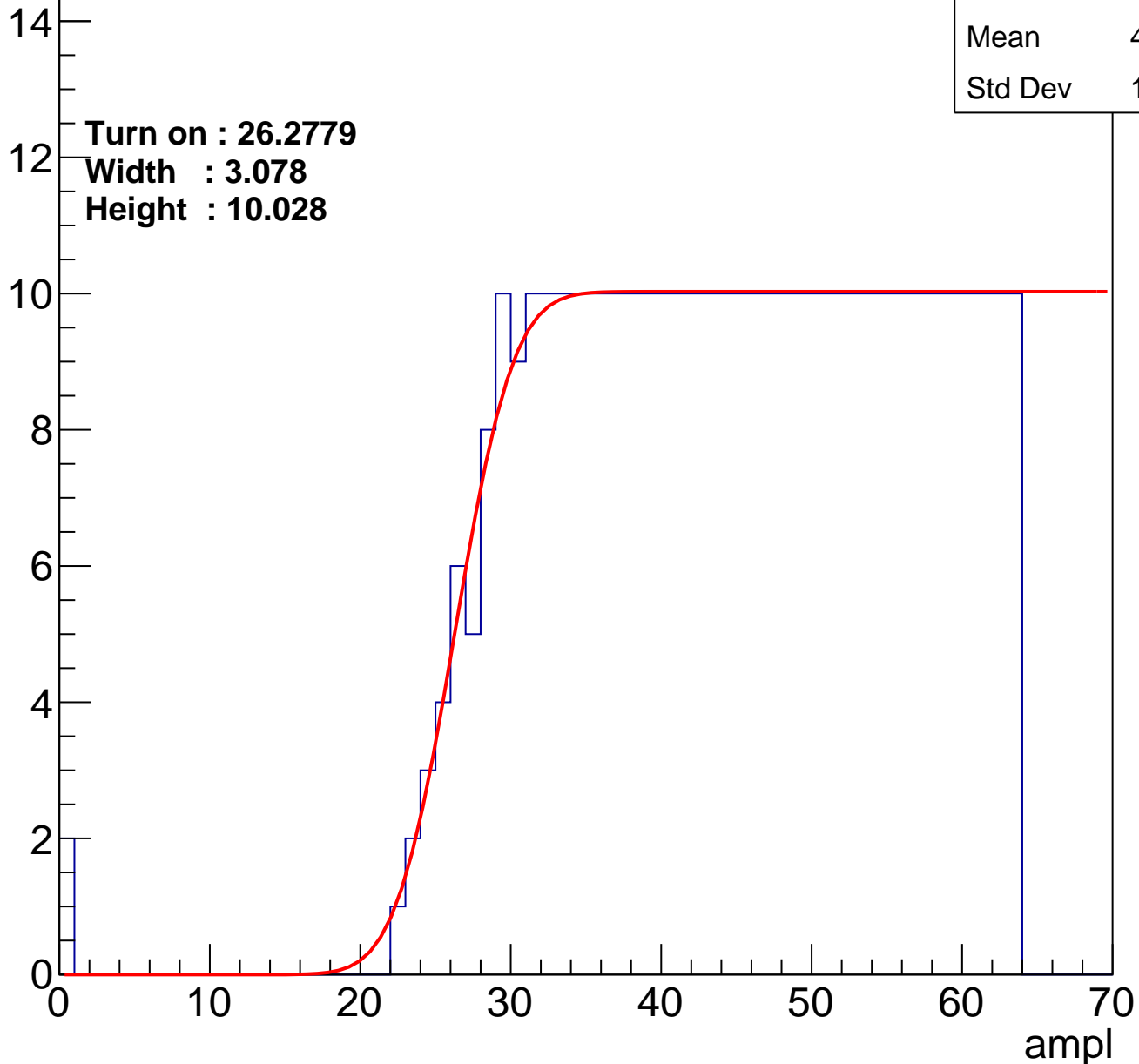
Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.2779

Width : 3.078

Height : 10.028

Entry



B1L002S, U13-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.17
Std Dev	11.42

Turn on : 25.5709

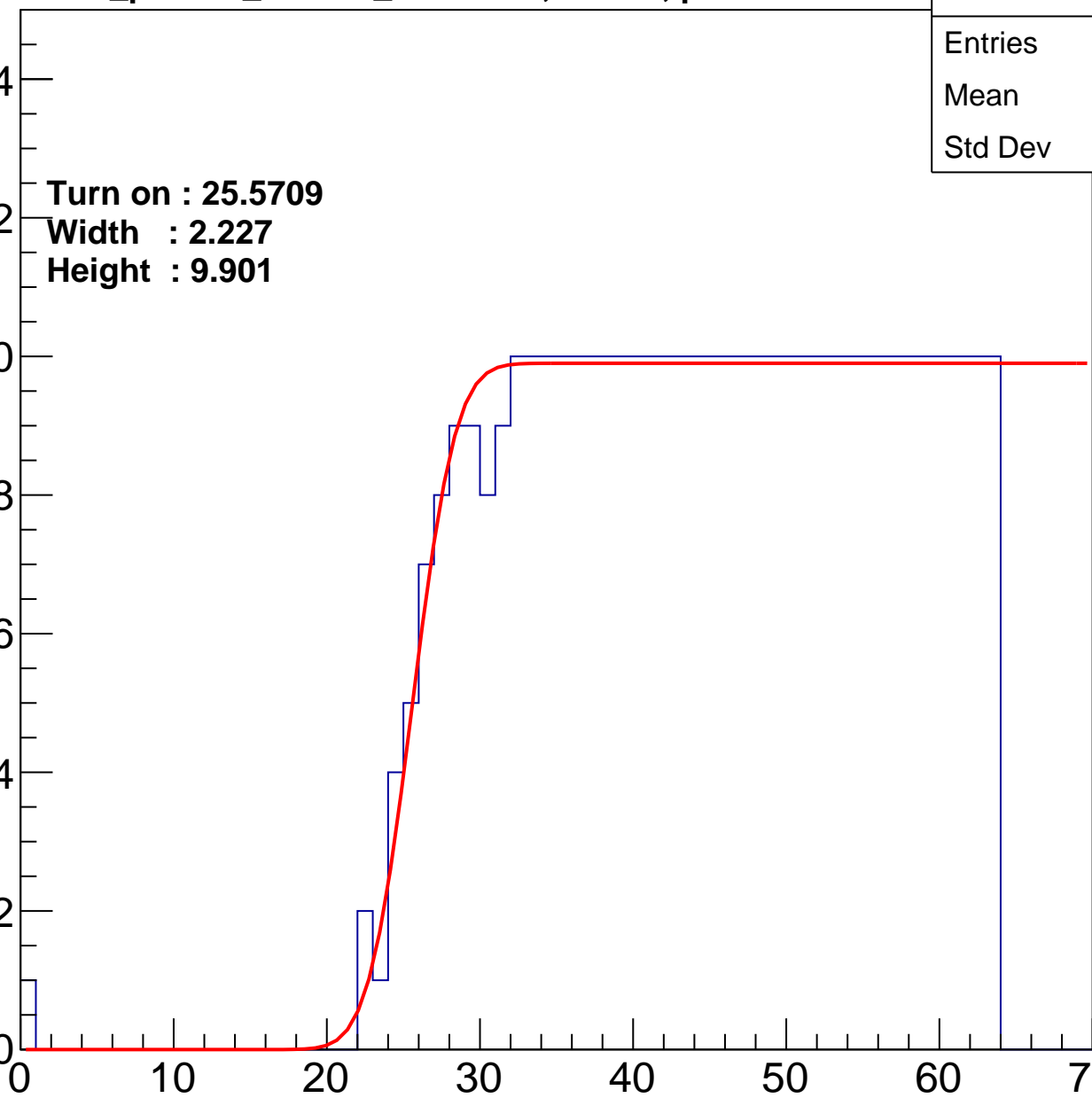
Width : 2.227

Height : 9.901

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch34

calib_packv5_042523_0143.root, FC#10, port B3

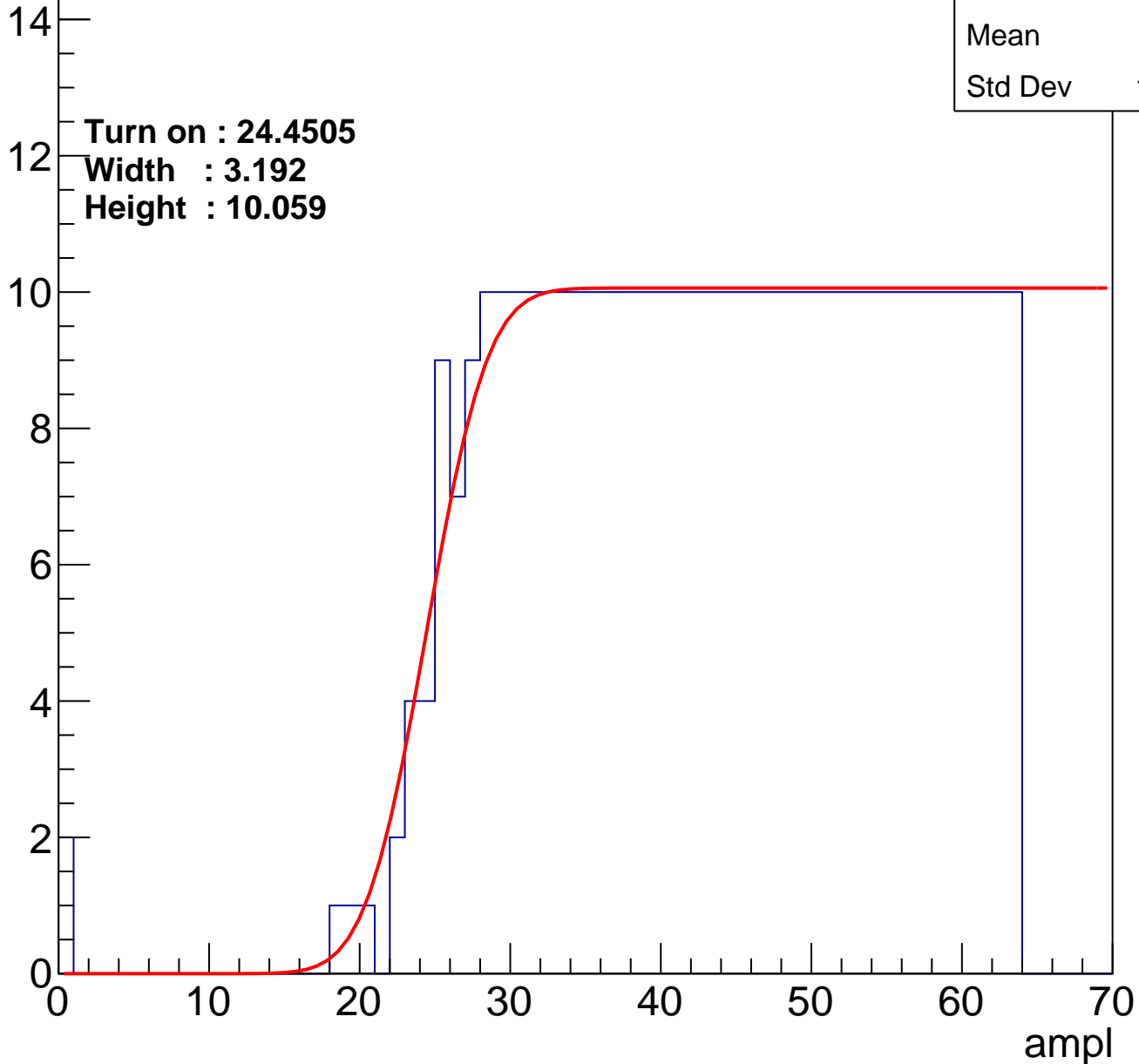
Entries	400
Mean	43.3
Std Dev	12.01

Turn on : 24.4505

Width : 3.192

Height : 10.059

Entry



B1L002S, U13-ch35

calib_packv5_042523_0143.root, FC#10, port B3

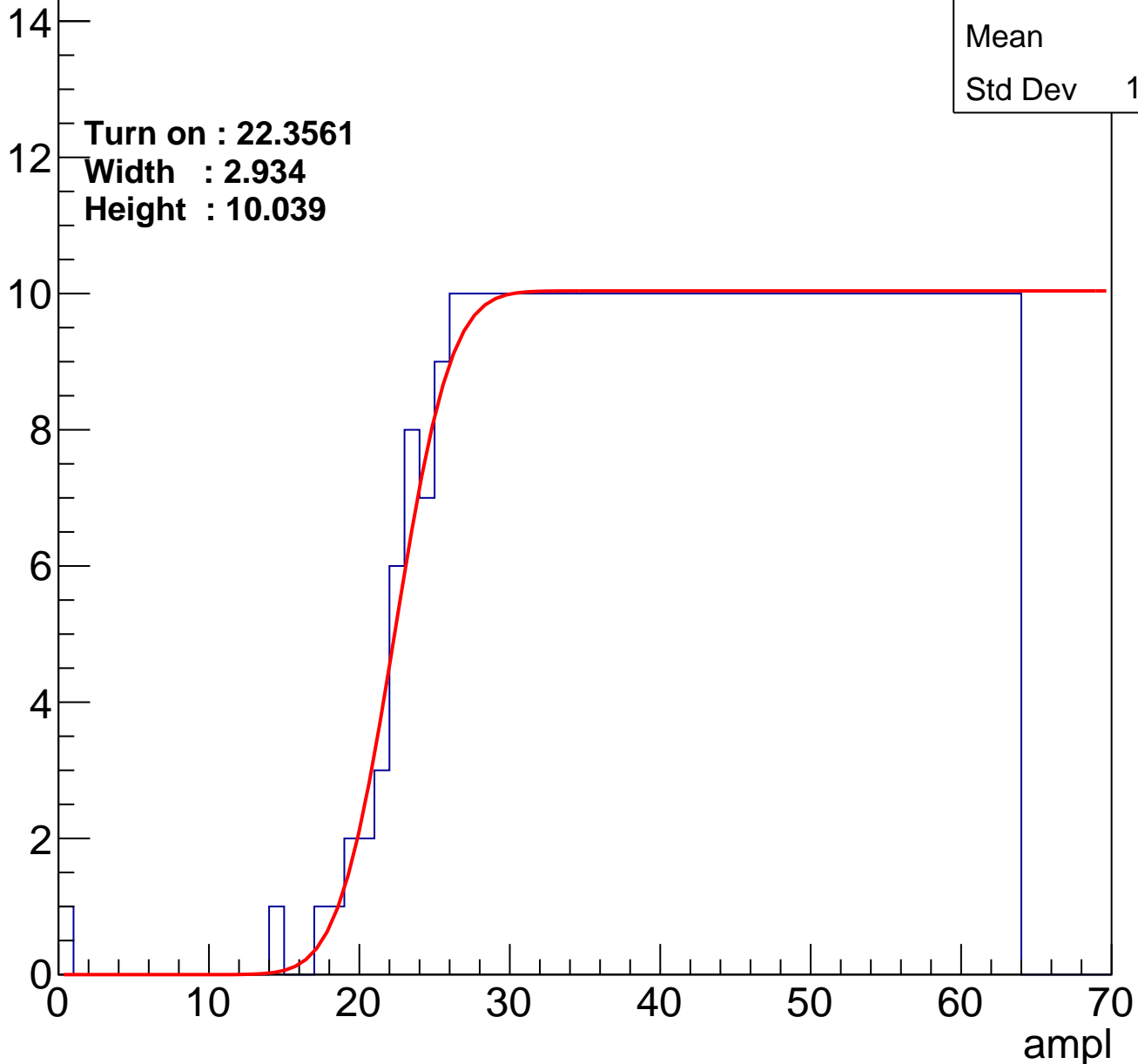
Entries	421
Mean	42.3
Std Dev	12.45

Turn on : 22.3561

Width : 2.934

Height : 10.039

Entry

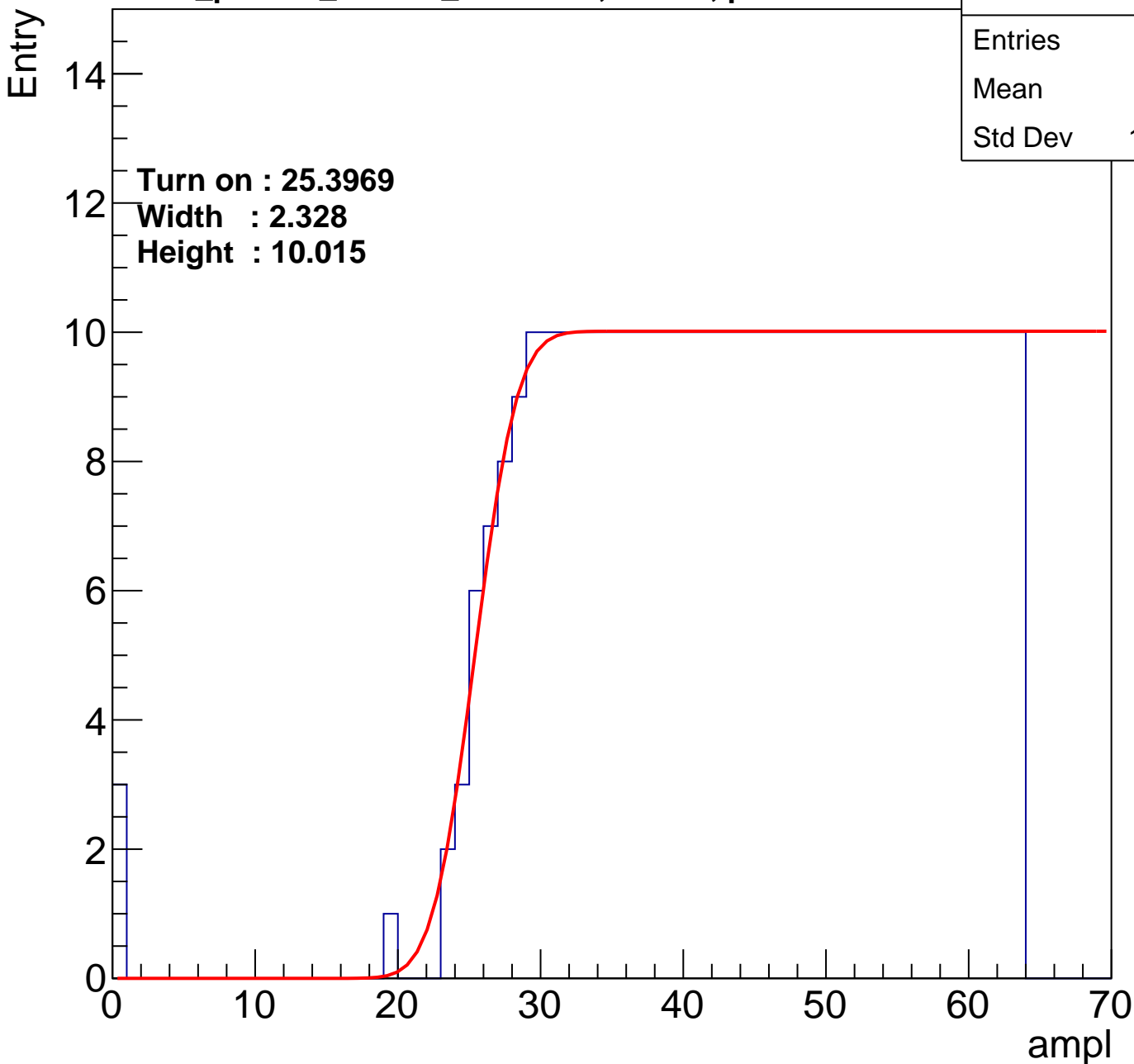


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.8
Std Dev	11.85

Height : 10.015



B1L002S, U13-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.81
Std Dev	12.05

Turn on : 25.4987

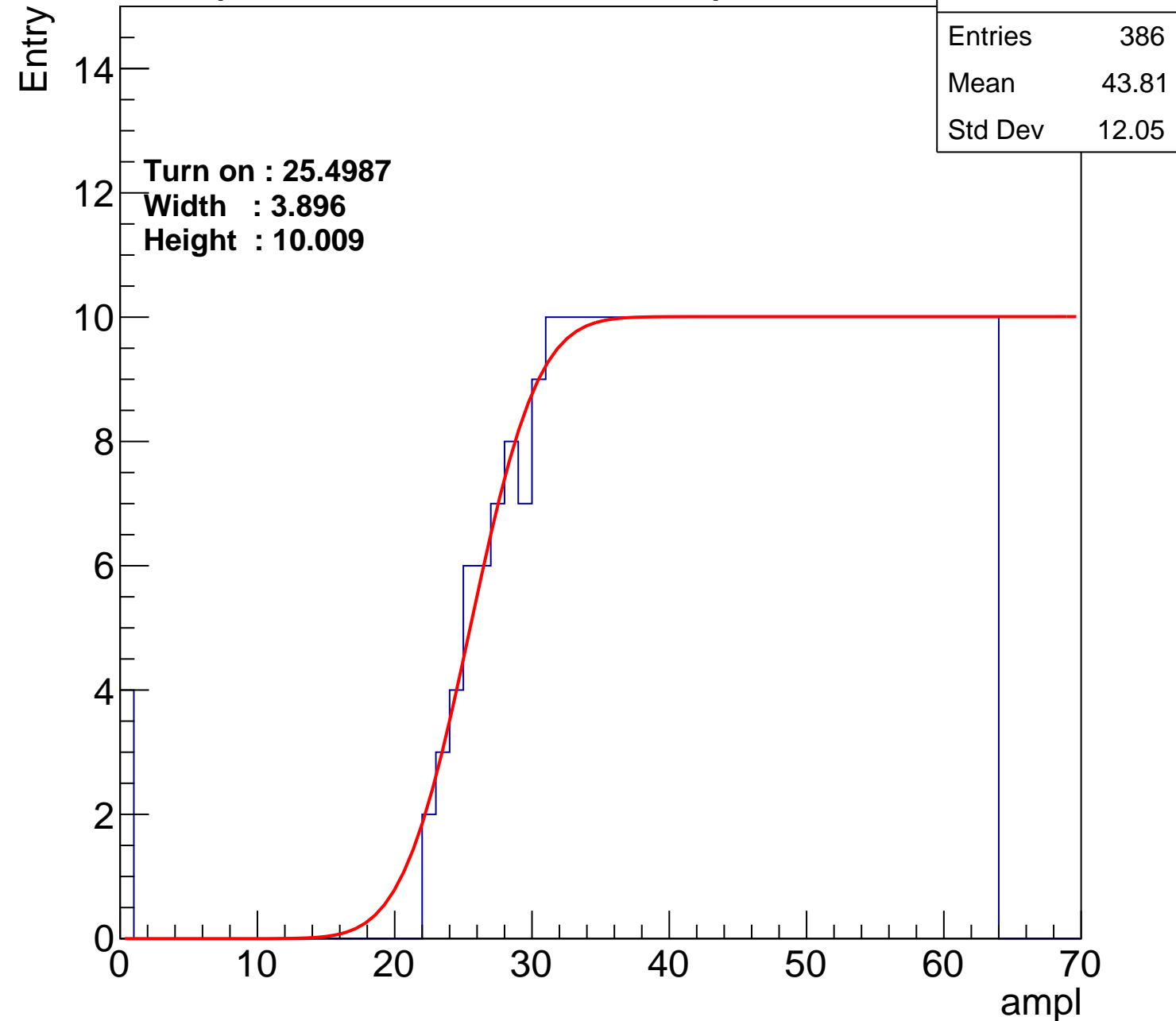
Width : 3.896

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.17
Std Dev	12.36

Turn on : 24.6989

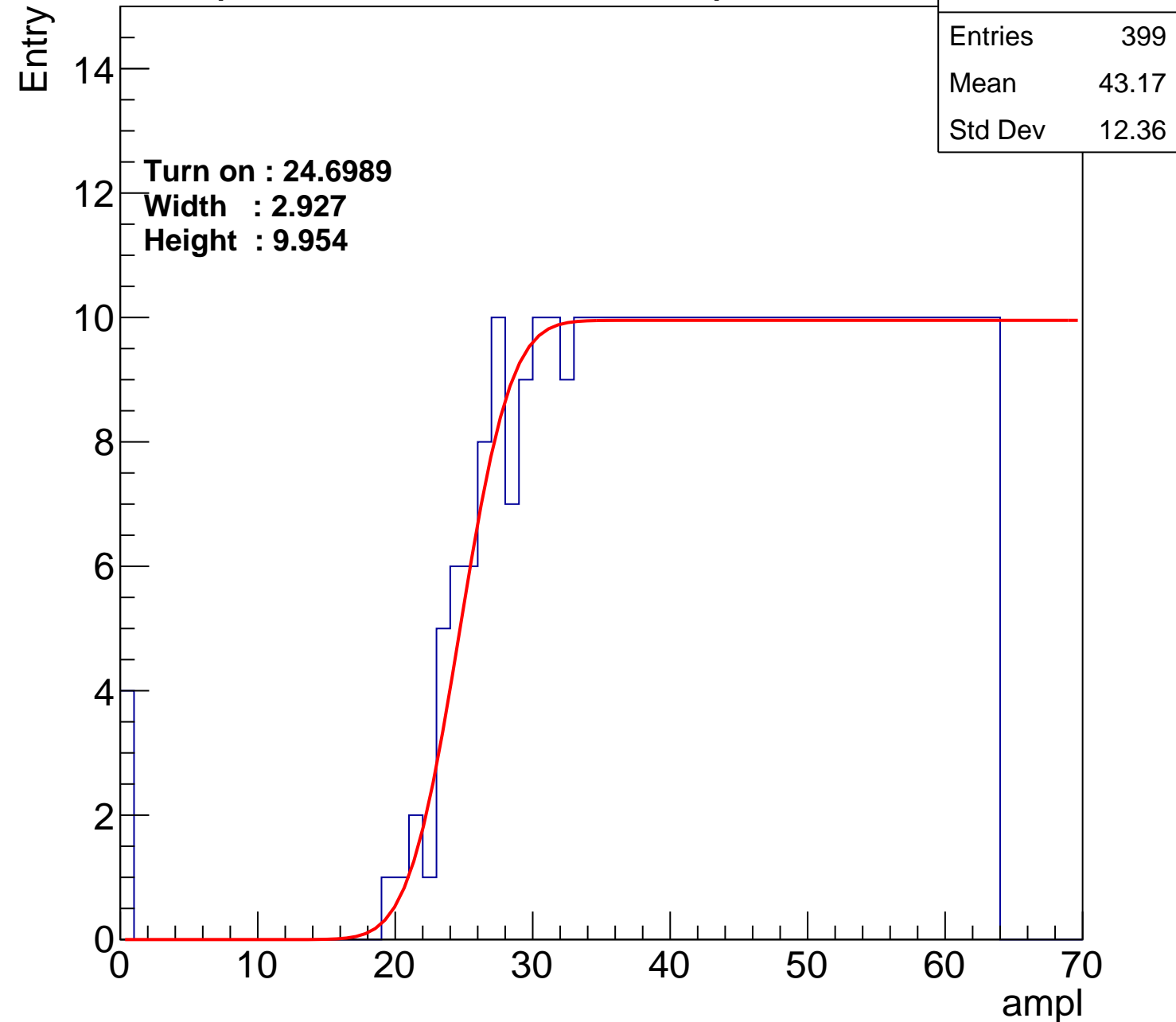
Width : 2.927

Height : 9.954

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.78

Turn on : 24.2418

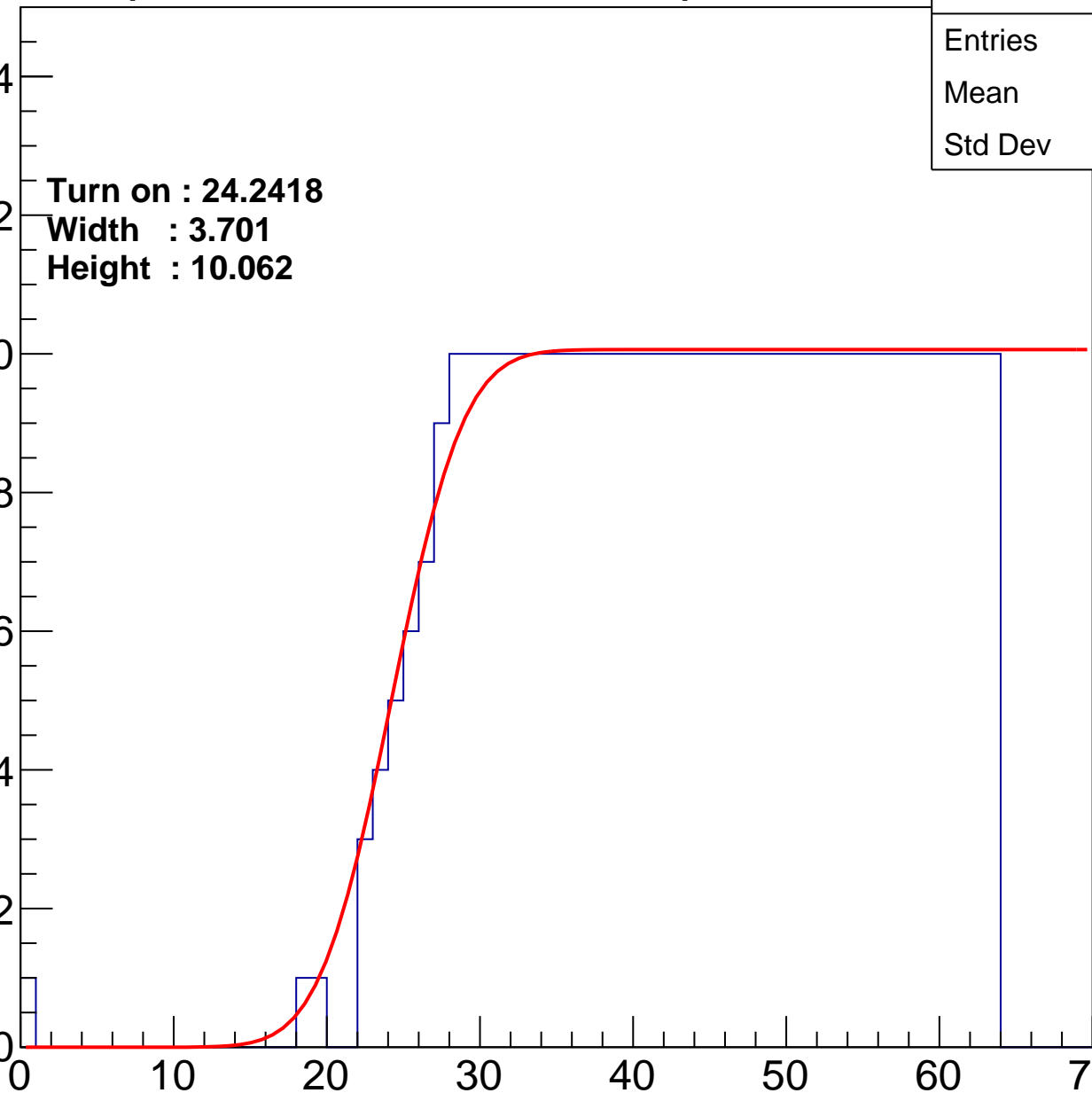
Width : 3.701

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.32
Std Dev	12.02

Turn on : 24.3324

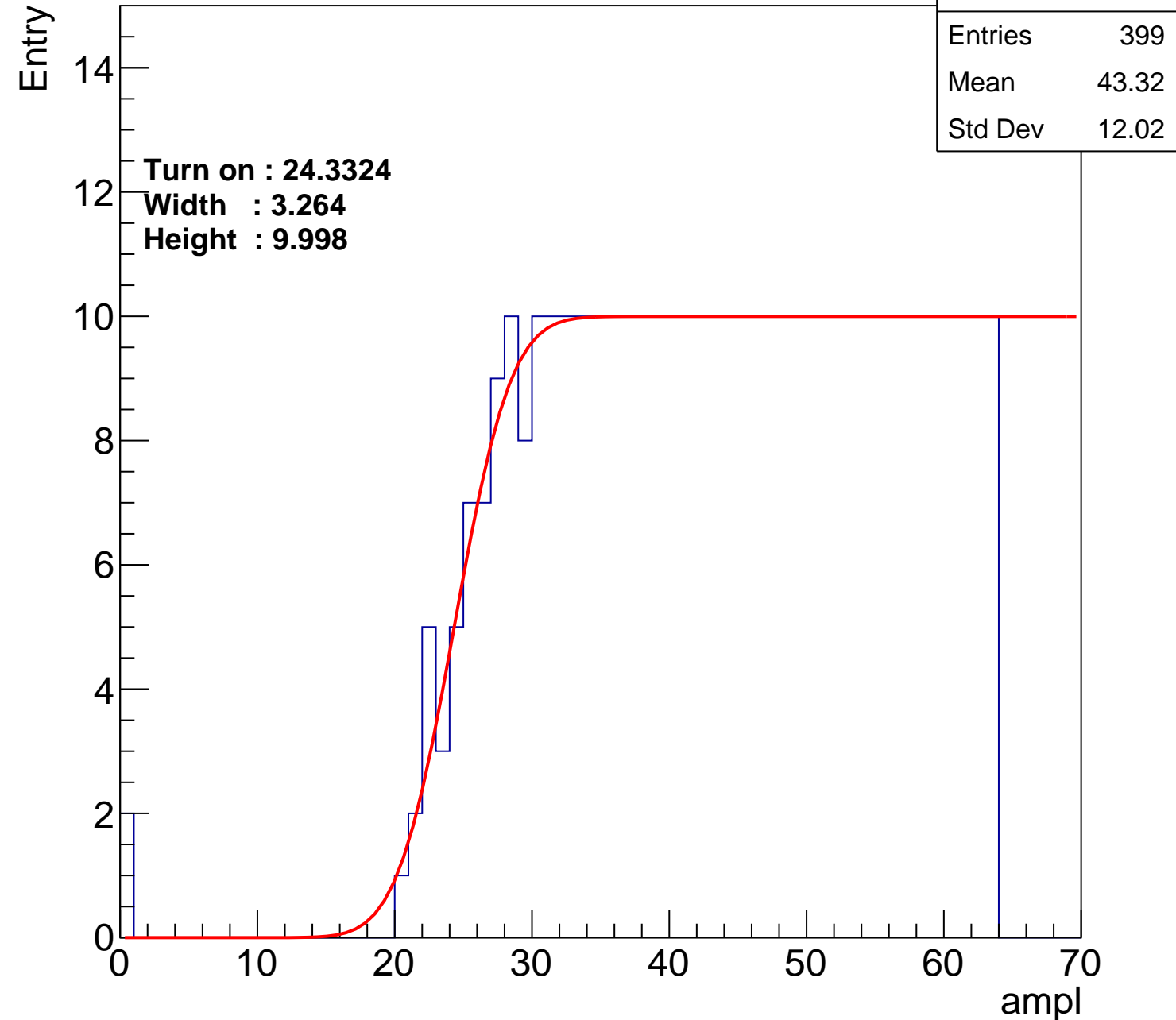
Width : 3.264

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.82
Std Dev	11.25

Turn on : 27.3658

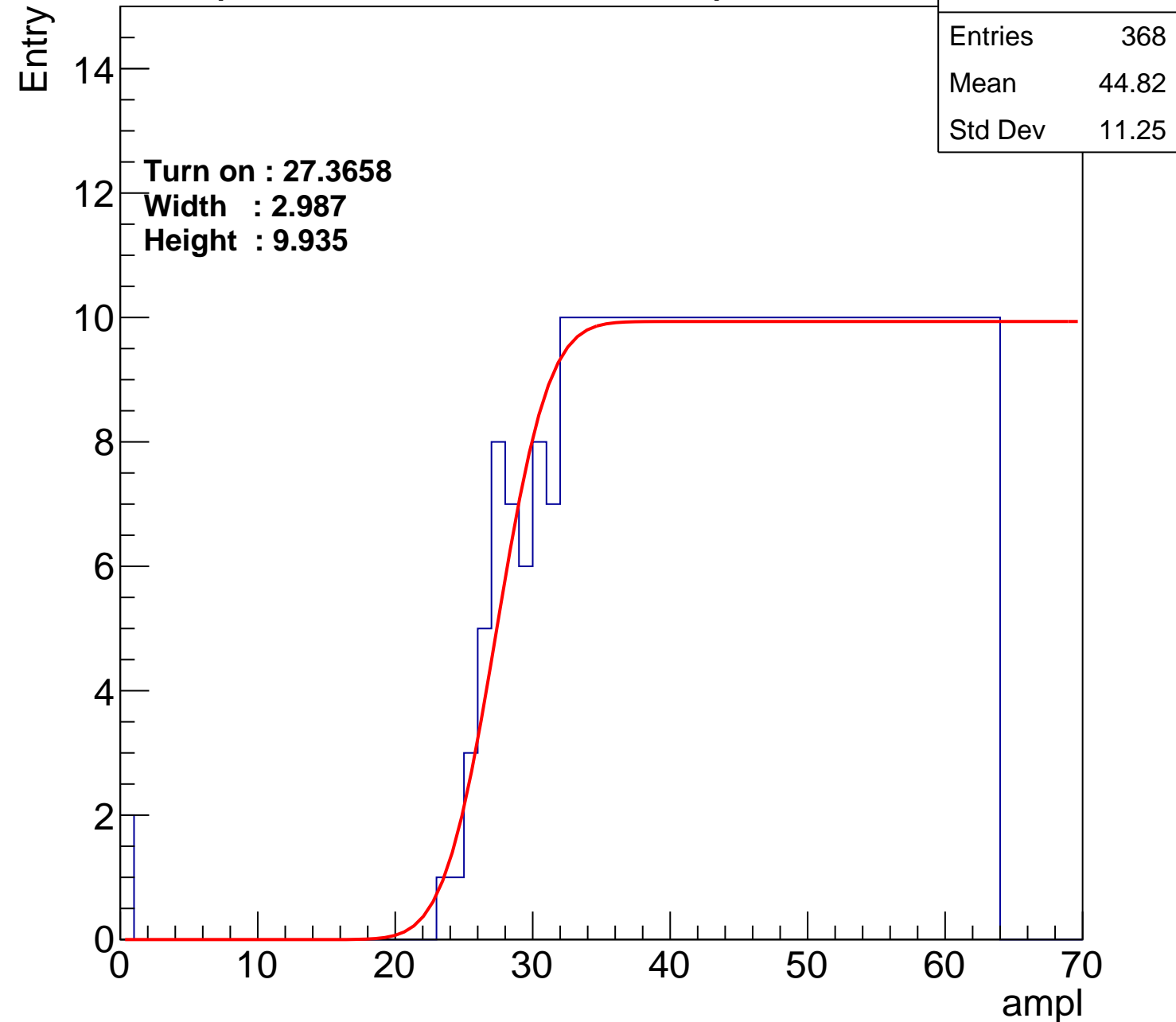
Width : 2.987

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.33
Std Dev	12.11

Turn on : 24.8119

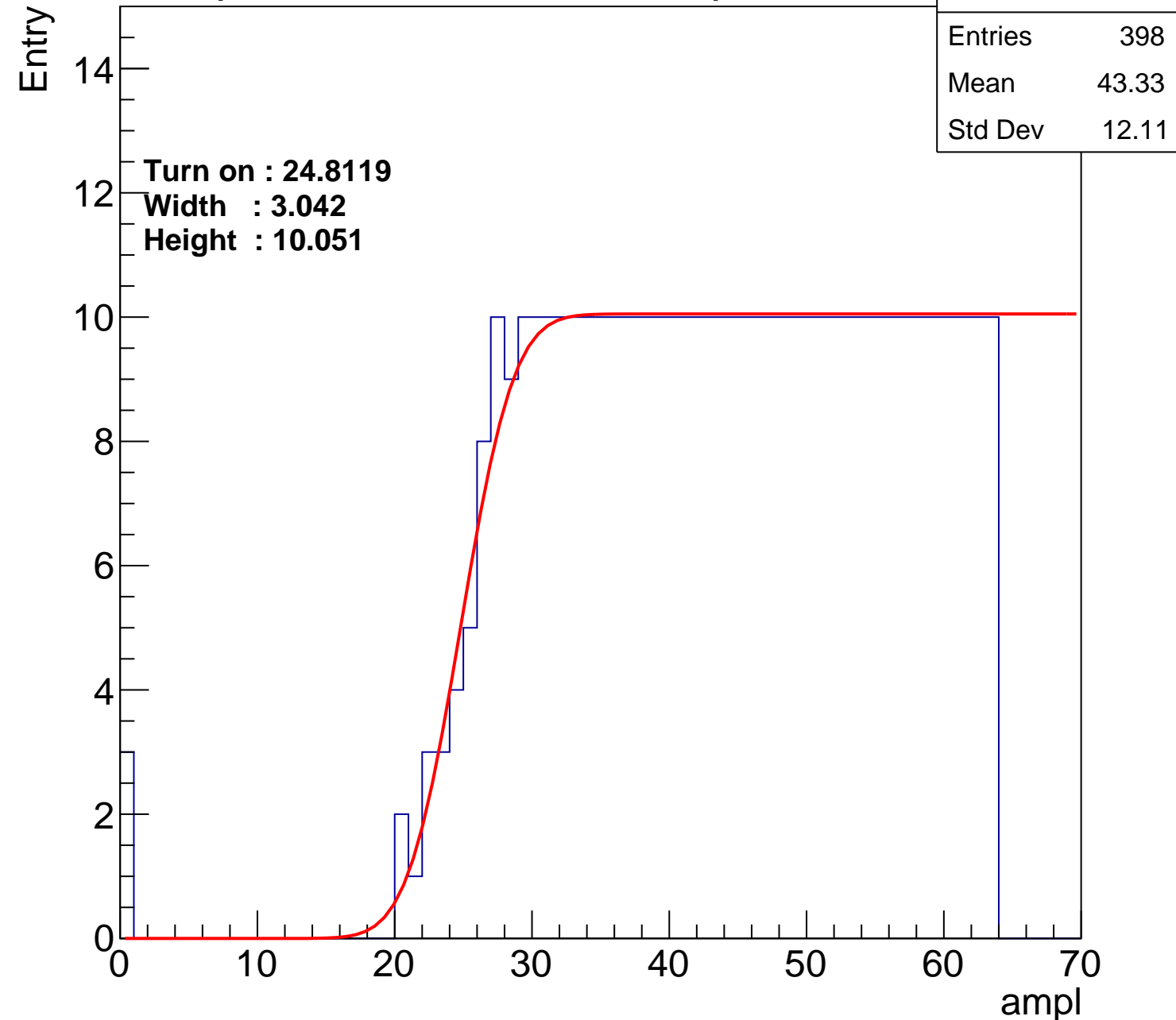
Width : 3.042

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

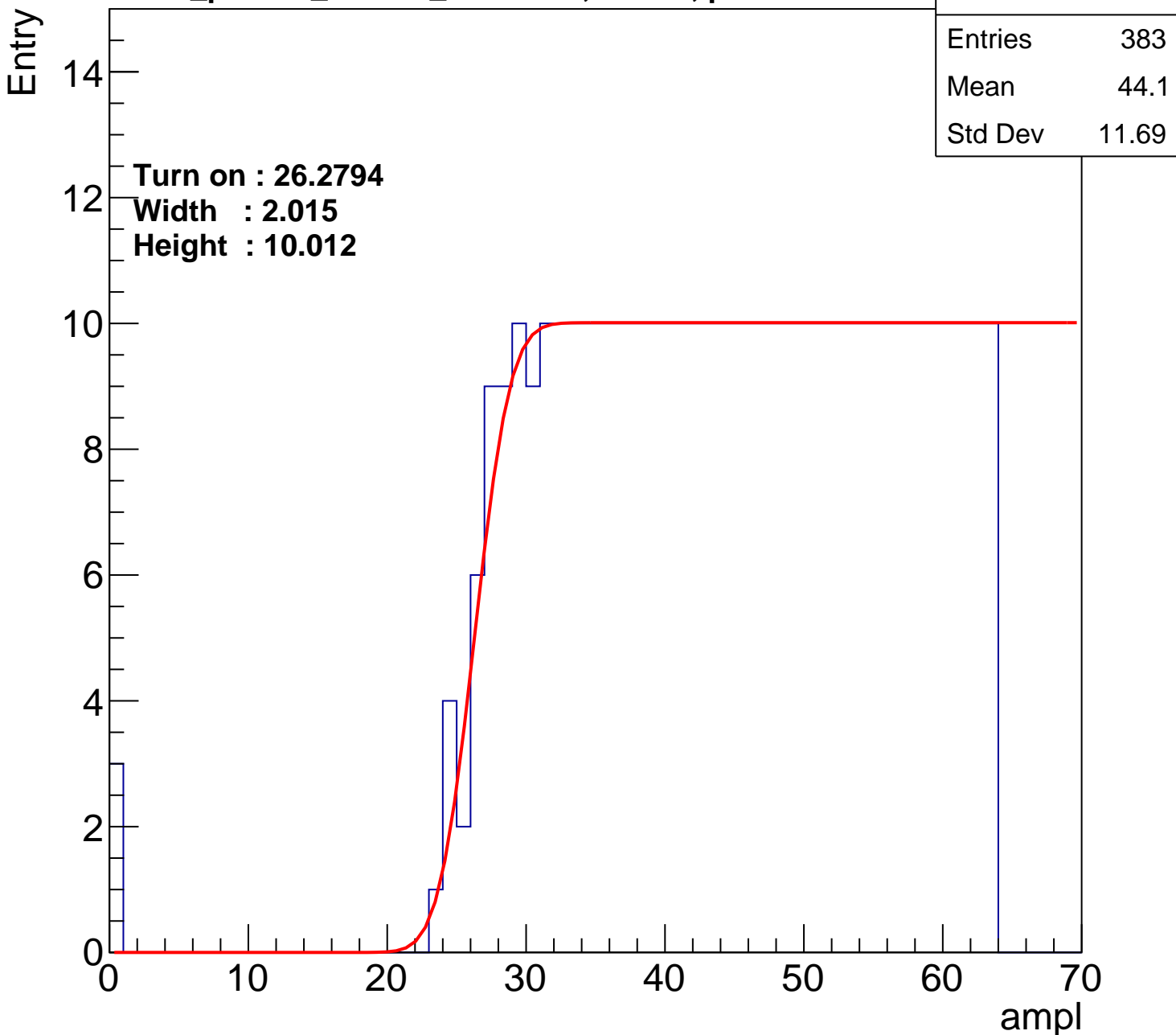


calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.1
Std Dev	11.69

Height : 10.012



B1L002S, U13-ch44

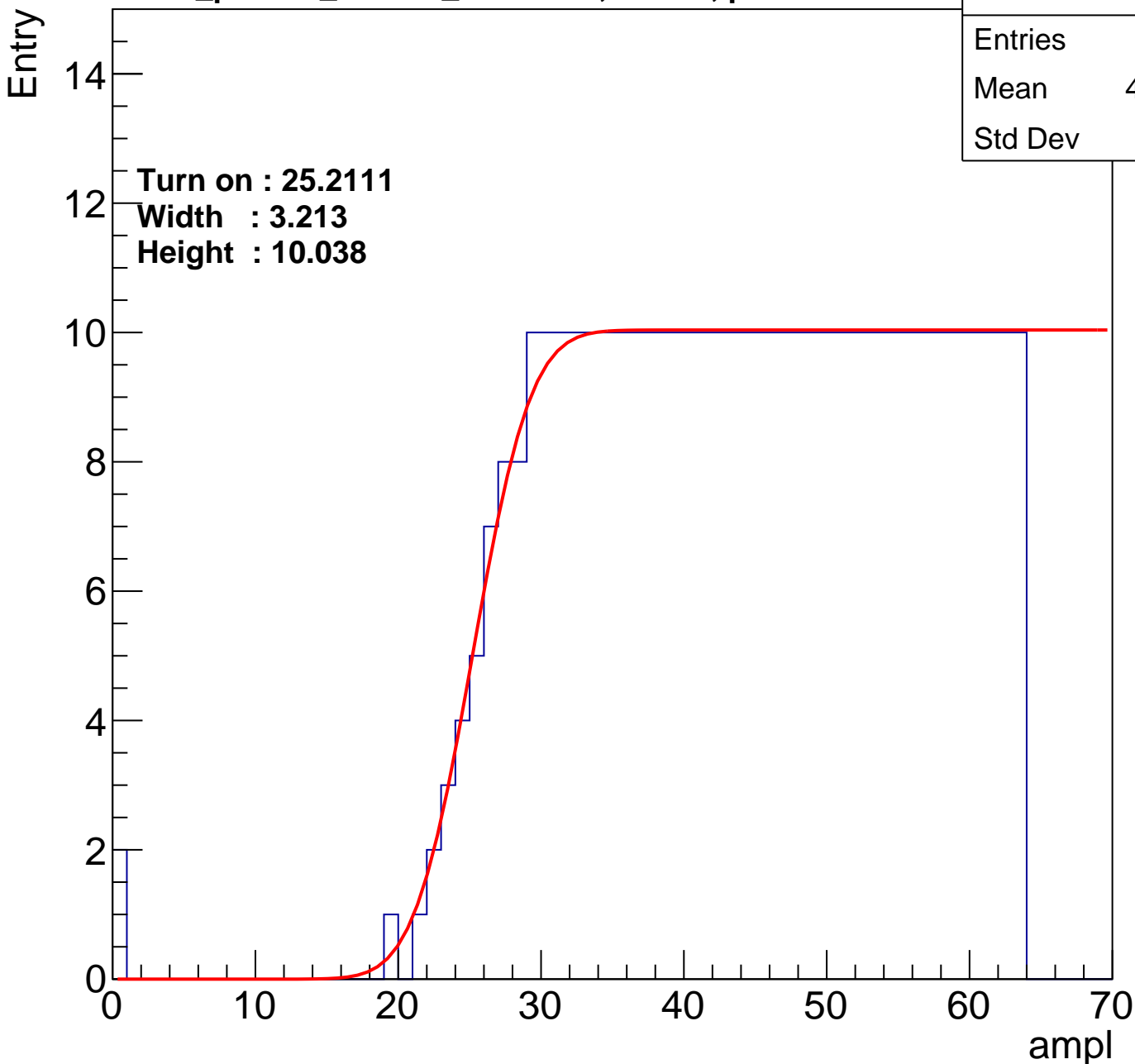
calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.2111

Width : 3.213

Height : 10.038



B1L002S, U13-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 25.4301

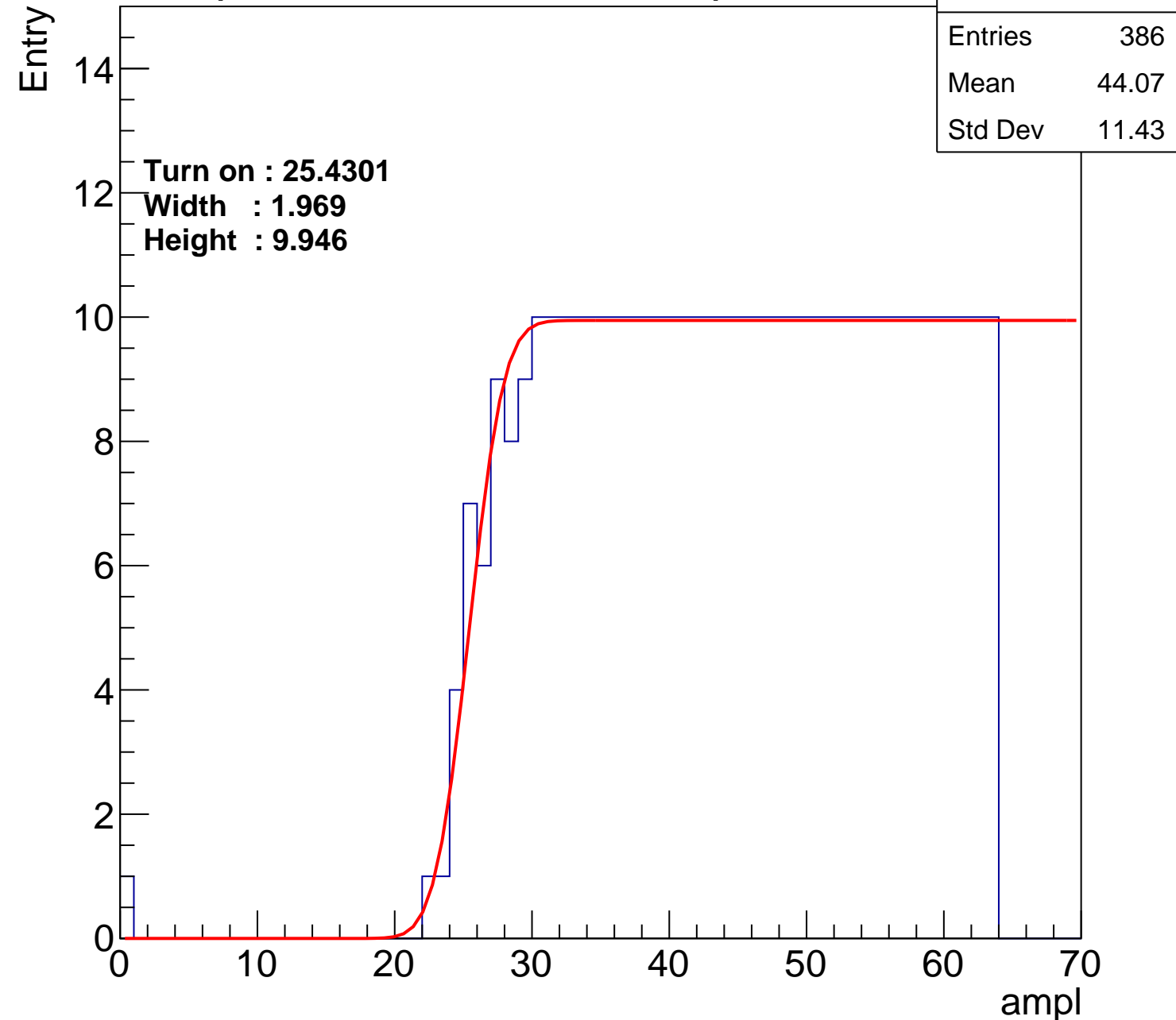
Width : 1.969

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.78
Std Dev	11.75

Turn on : 25.2428

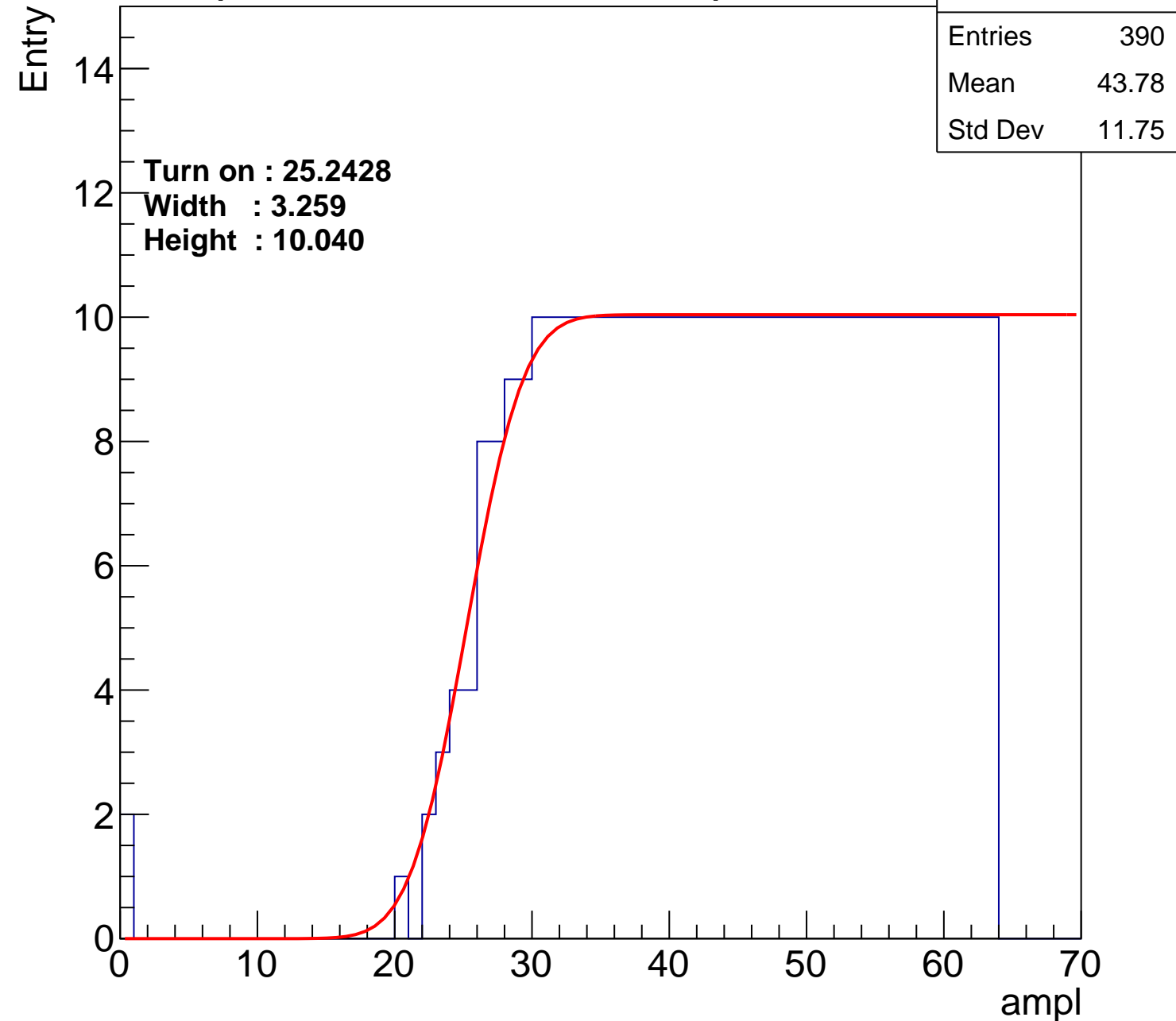
Width : 3.259

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch47

calib_packv5_042523_0143.root, FC#10, port B3

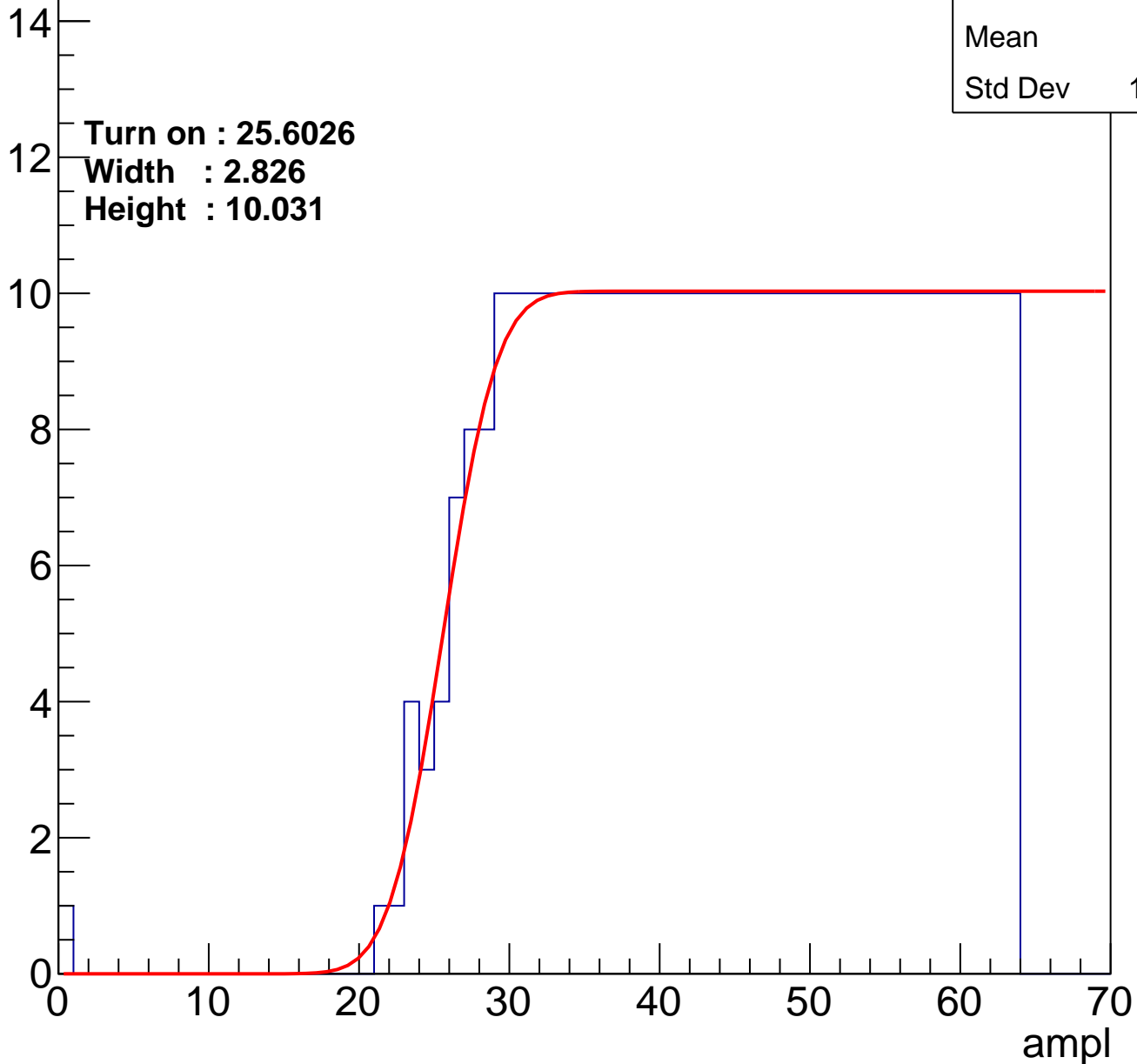
Entries	387
Mean	44
Std Dev	11.49

Turn on : 25.6026

Width : 2.826

Height : 10.031

Entry



B1L002S, U13-ch48

calib_packv5_042523_0143.root, FC#10, port B3

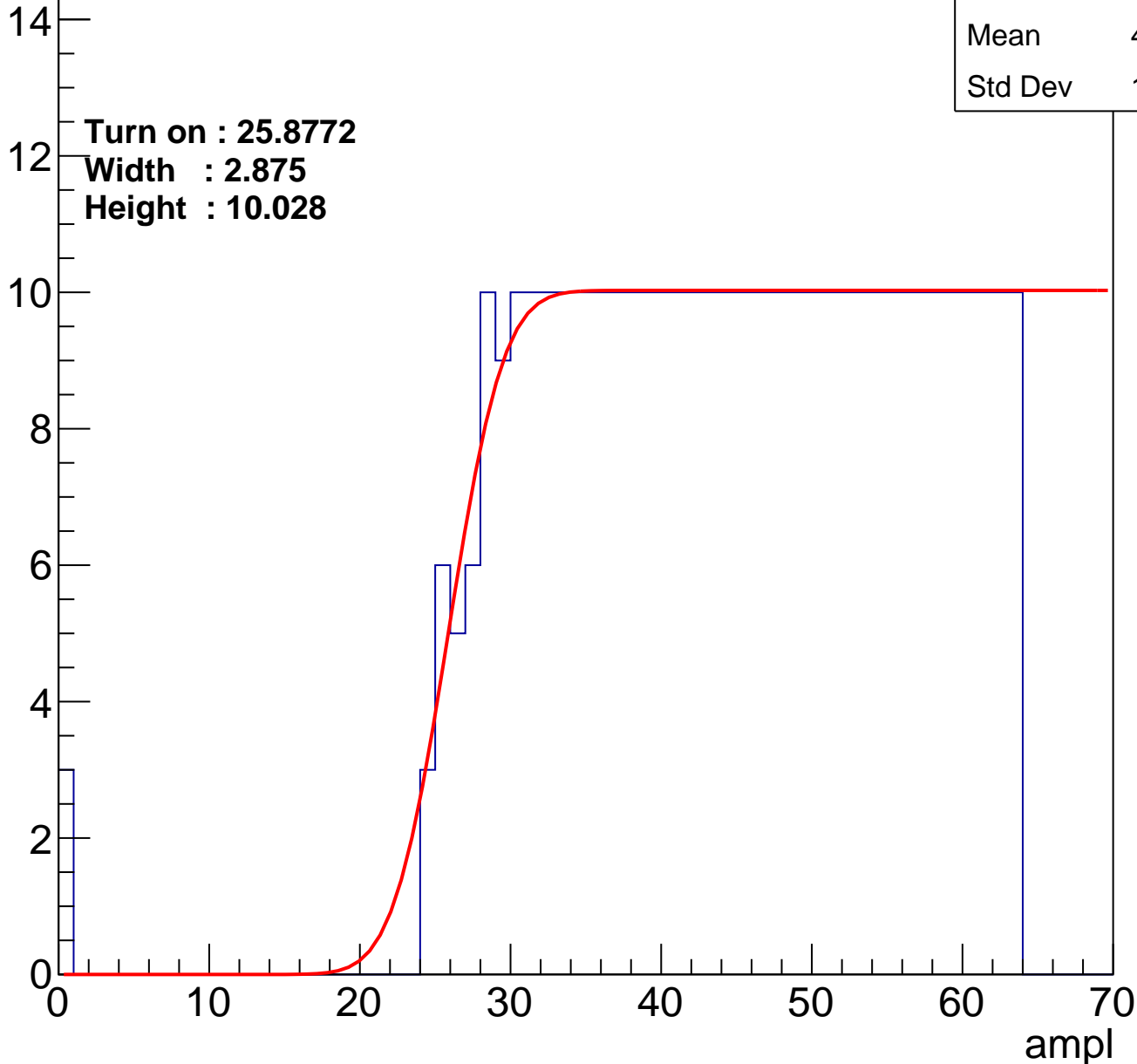
Entries	382
Mean	44.15
Std Dev	11.66

Turn on : 25.8772

Width : 2.875

Height : 10.028

Entry



B1L002S, U13-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.46
Std Dev	11.22

Turn on : 25.8224

Width : 2.231

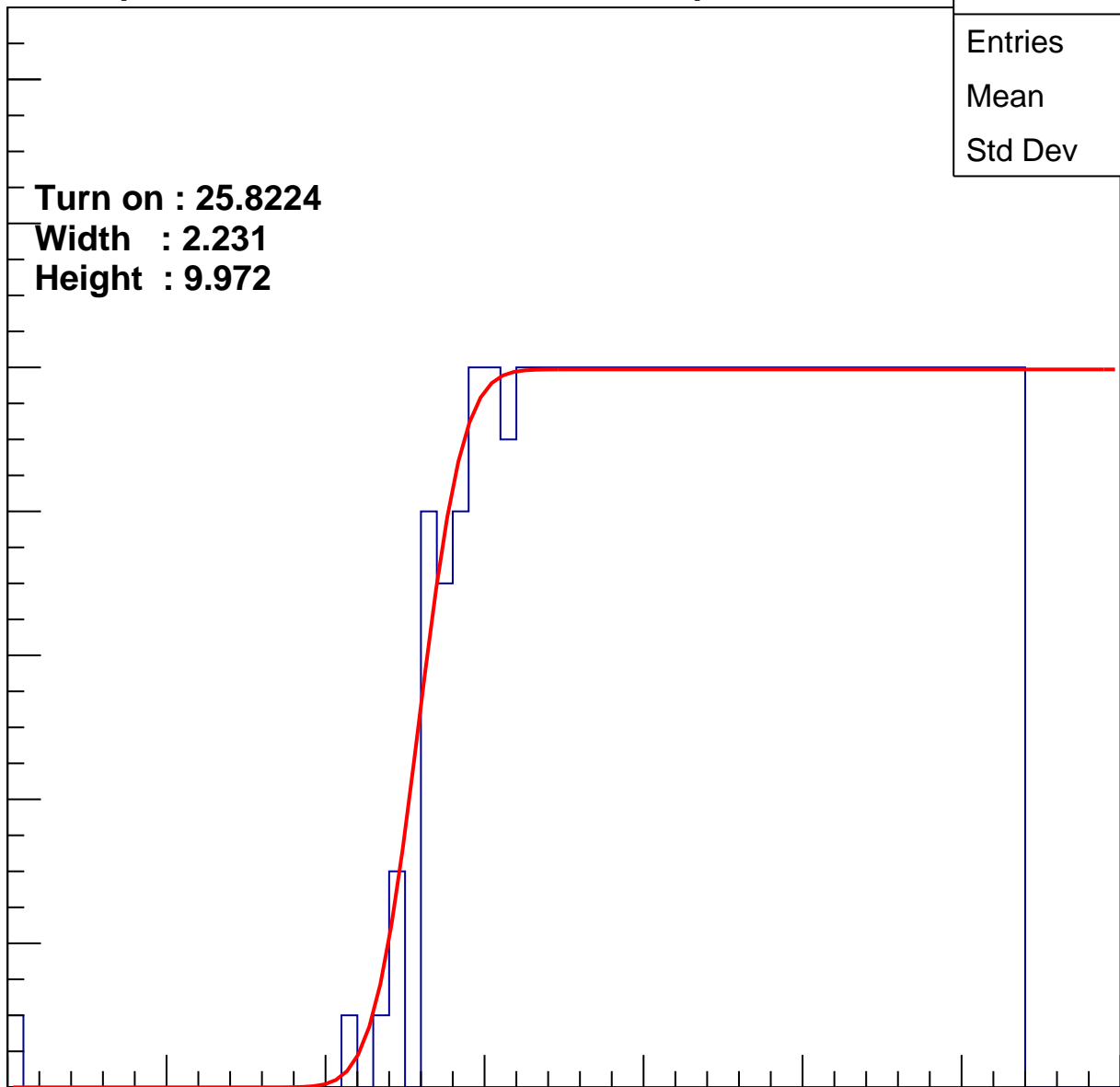
Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U13-ch50

calib_packv5_042523_0143.root, FC#10, port B3

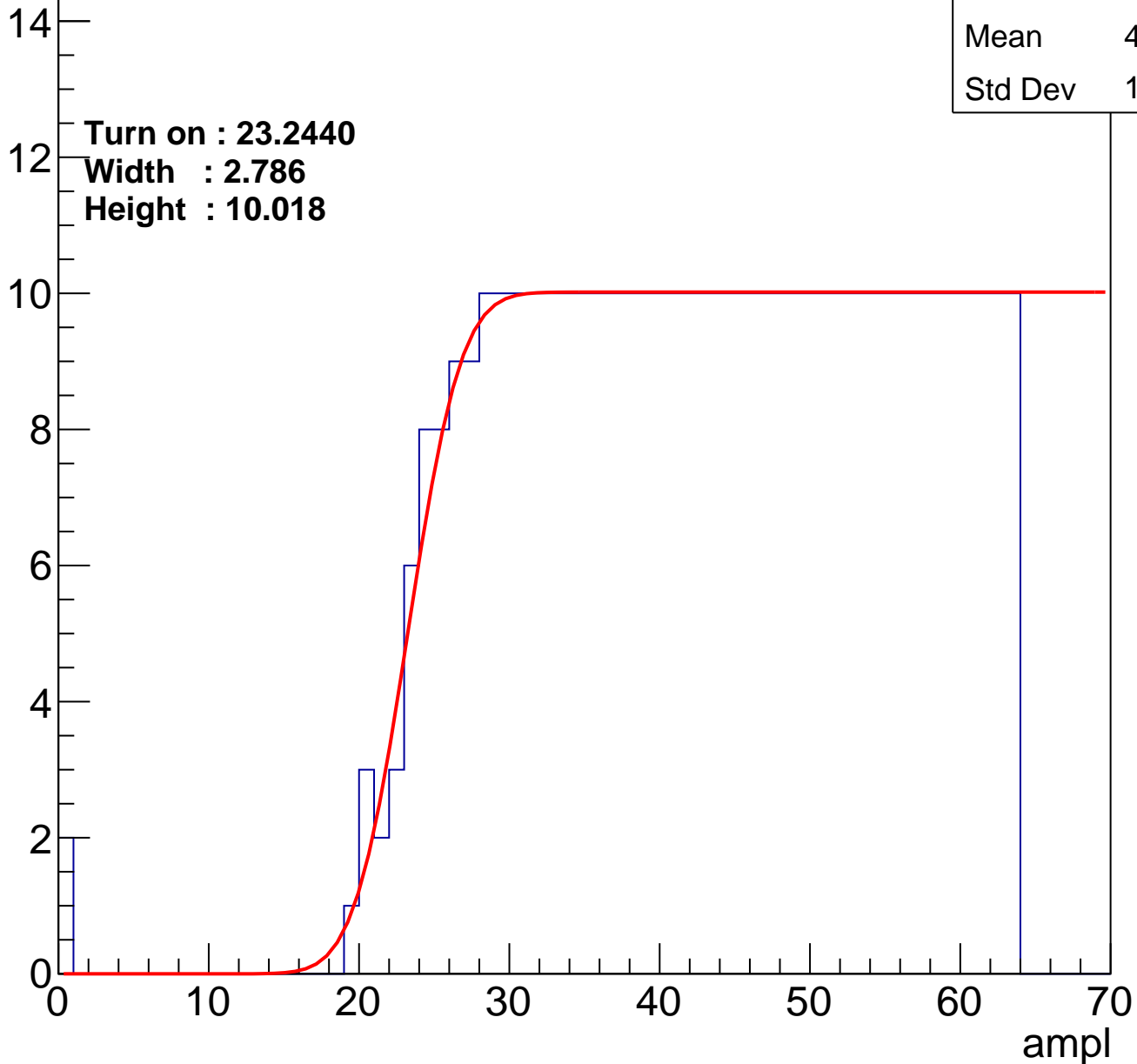
Entries	411
Mean	42.76
Std Dev	12.28

Turn on : 23.2440

Width : 2.786

Height : 10.018

Entry



B1L002S, U13-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.64
Std Dev	11.82

Turn on : 25.1872

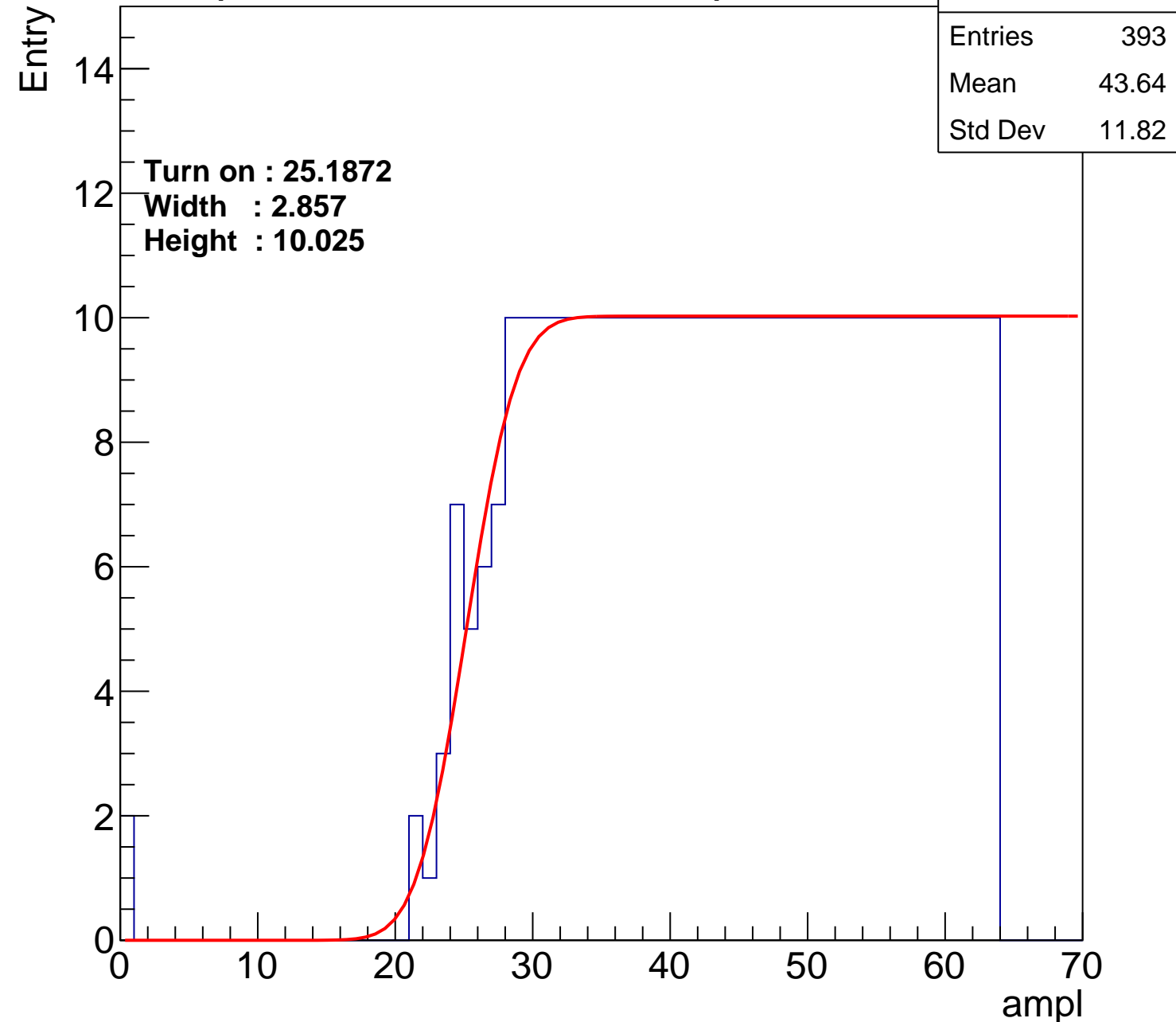
Width : 2.857

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.01
Std Dev	11.9

Turn on : 26.1750

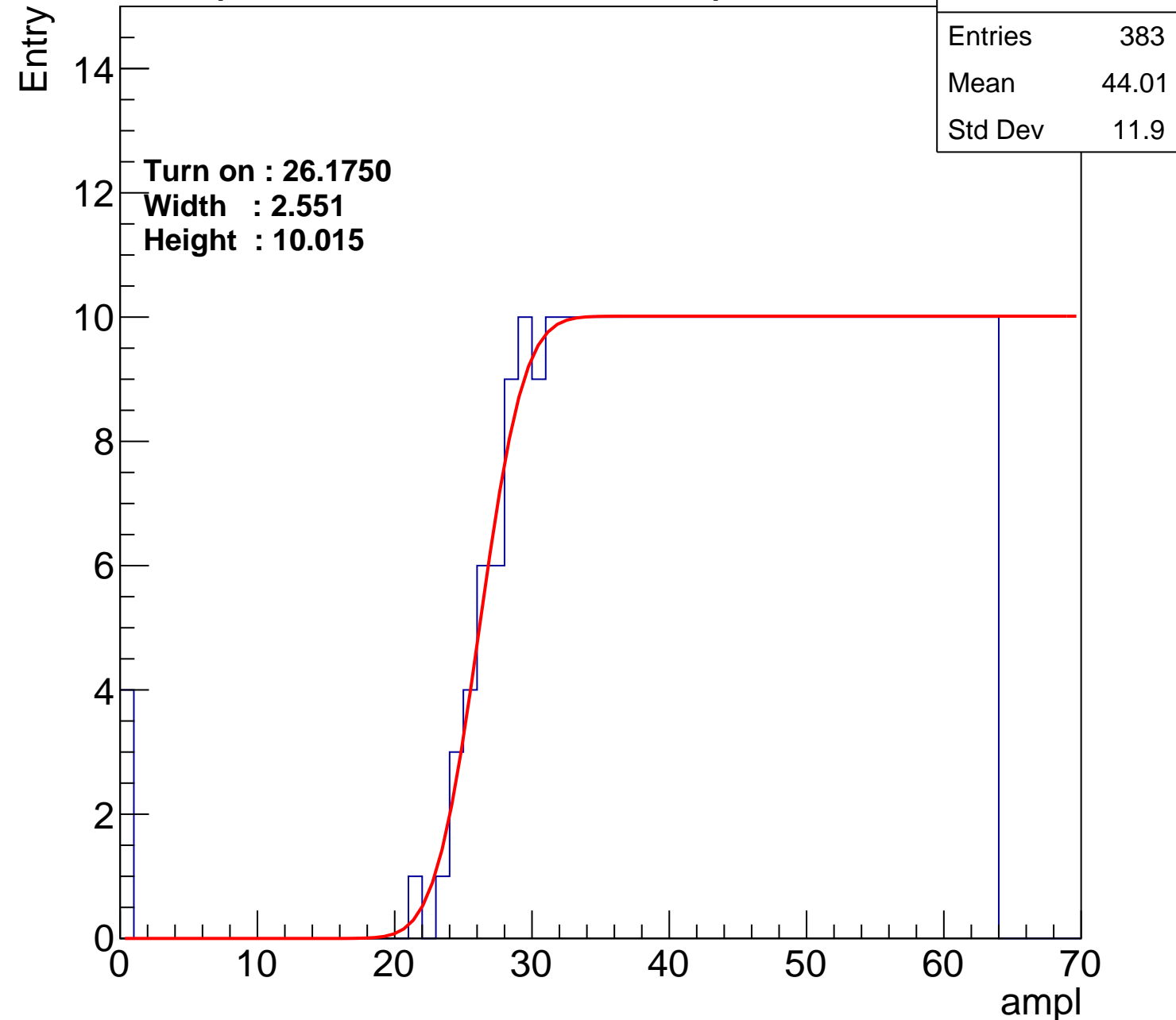
Width : 2.551

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.56
Std Dev	11.17

Turn on : 26.7561

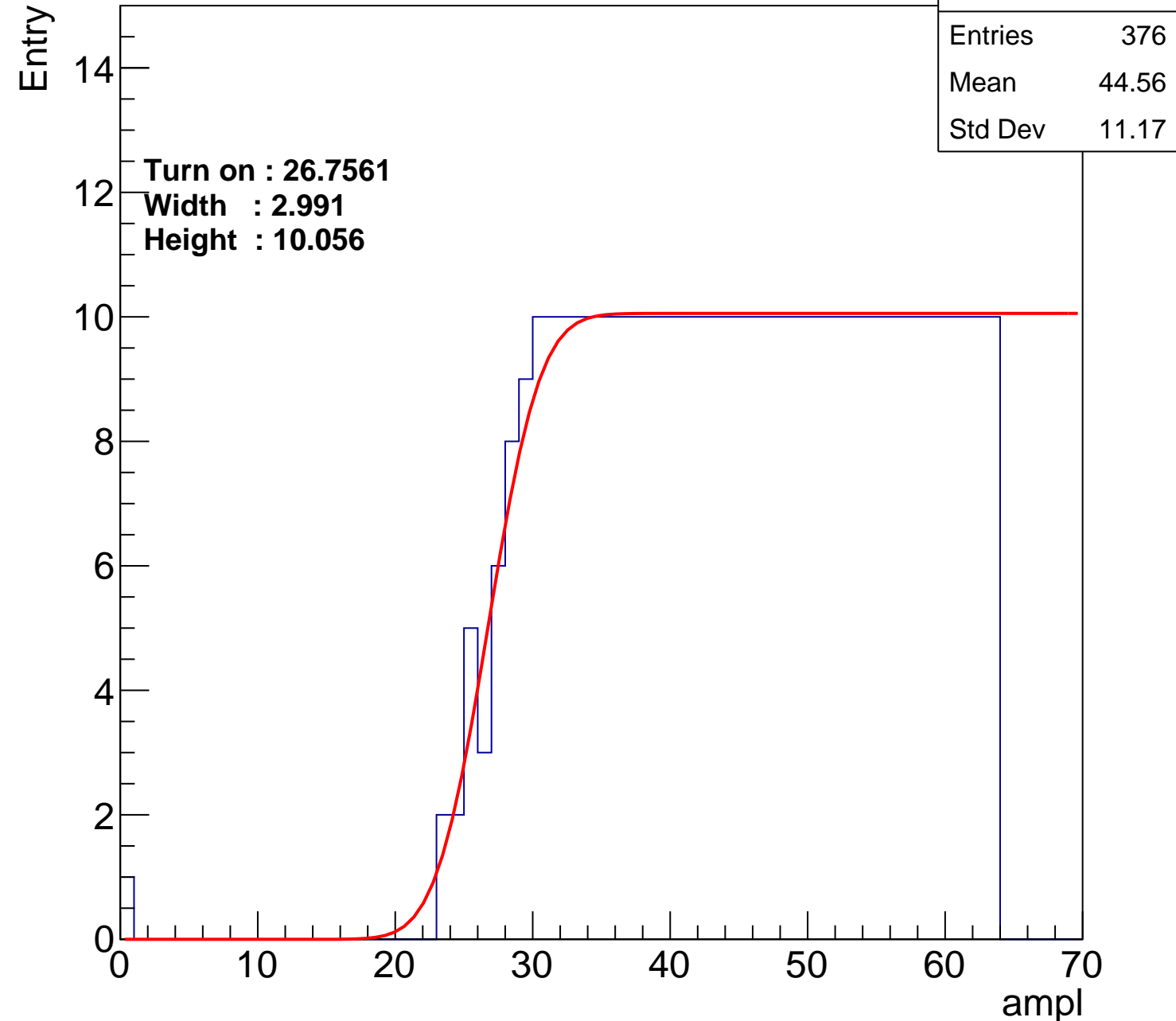
Width : 2.991

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch54

calib_packv5_042523_0143.root, FC#10, port B3

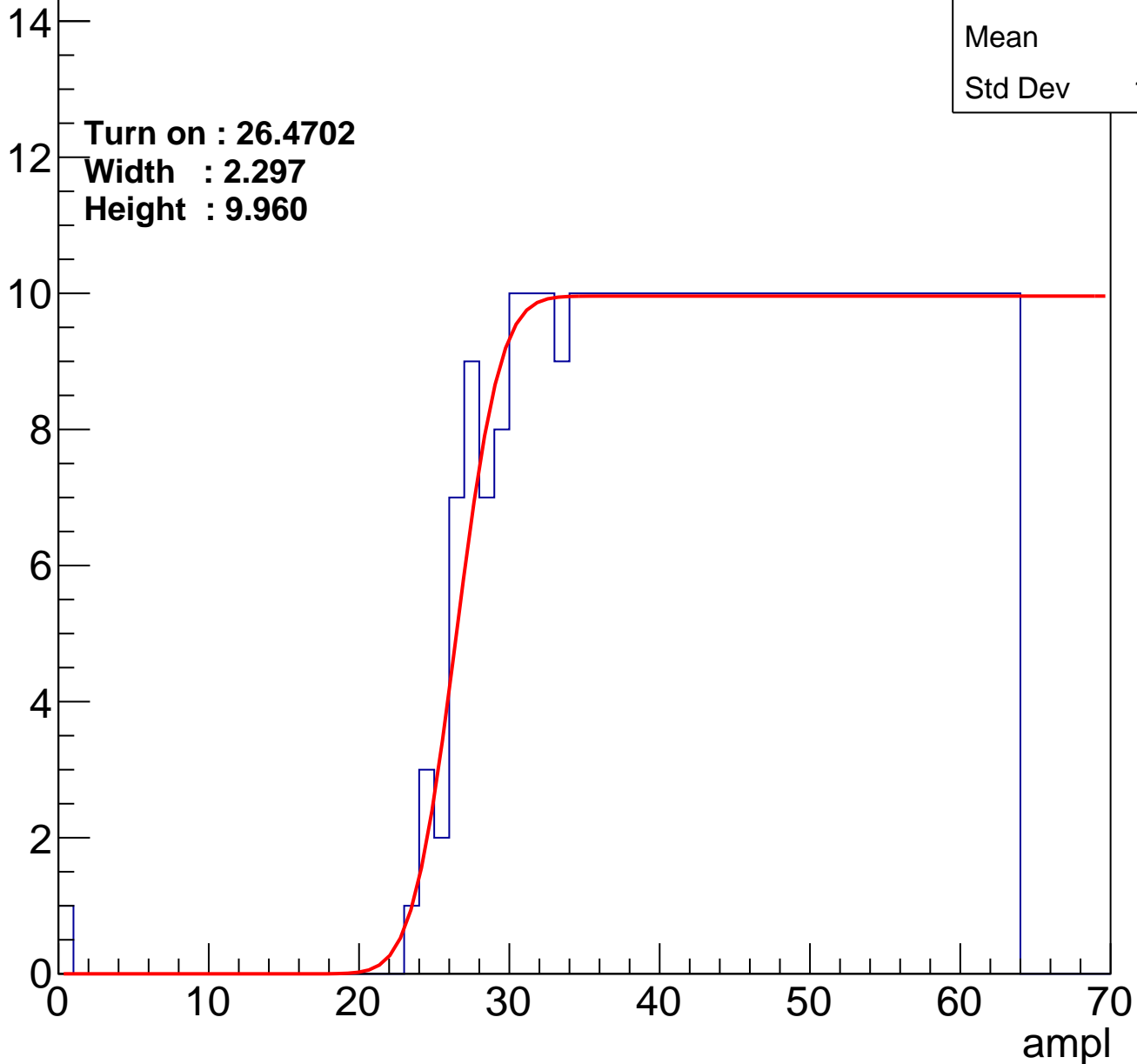
Entries	377
Mean	44.5
Std Dev	11.21

Turn on : 26.4702

Width : 2.297

Height : 9.960

Entry



B1L002S, U13-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.28
Std Dev	11.62

Turn on : 27.0556

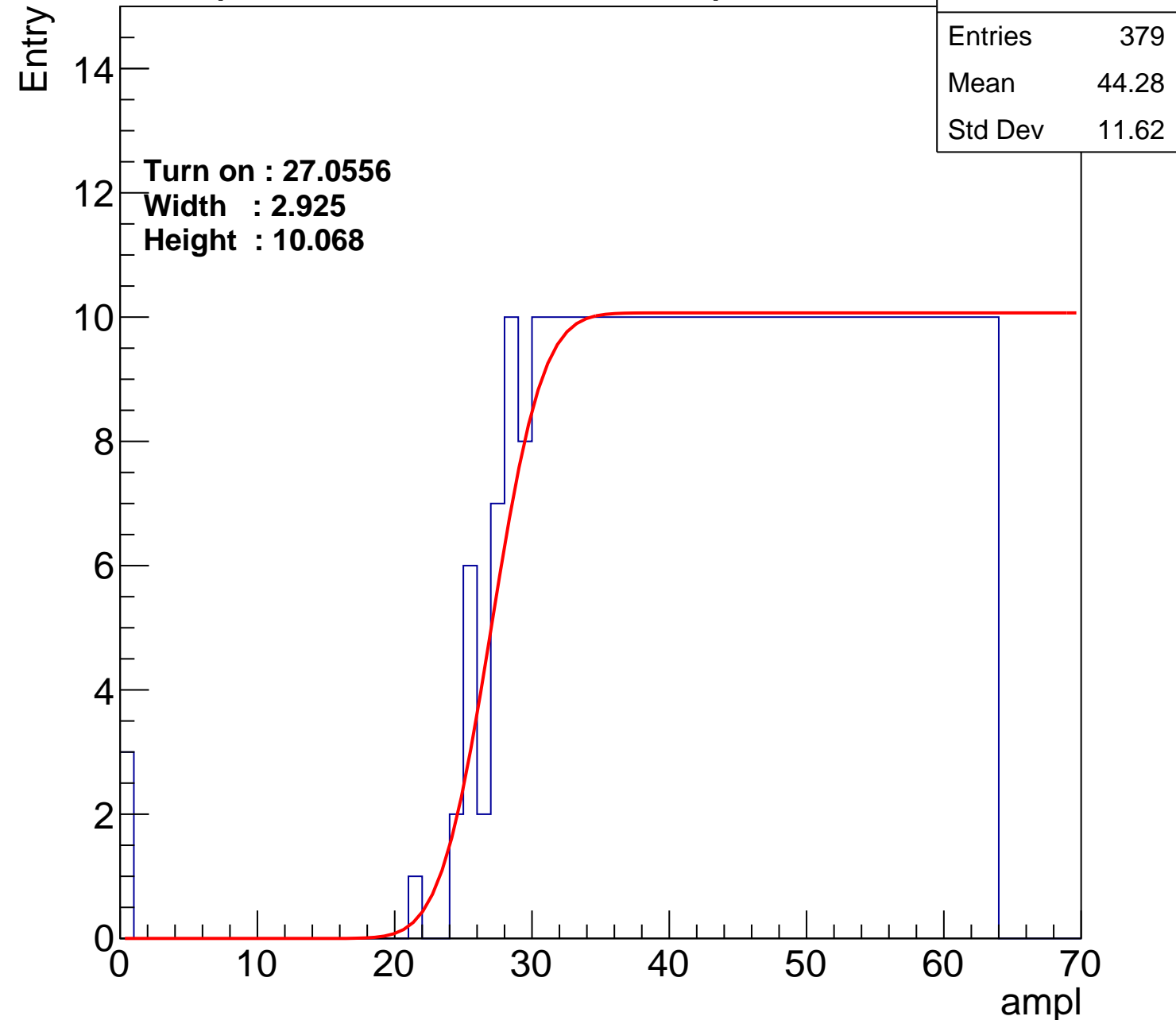
Width : 2.925

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.6
Std Dev	12.12

Turn on : 25.4304

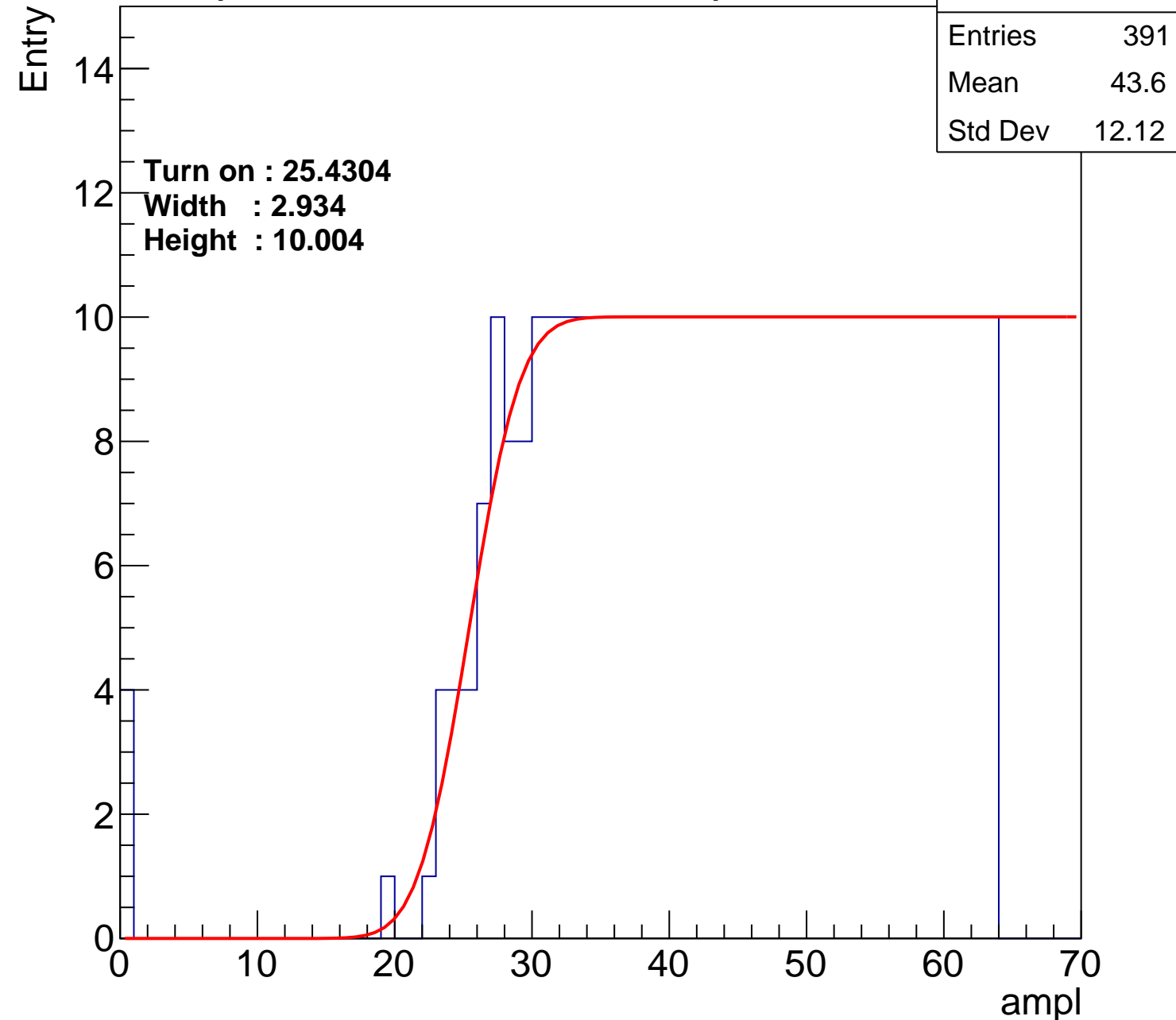
Width : 2.934

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch57

calib_packv5_042523_0143.root, FC#10, port B3

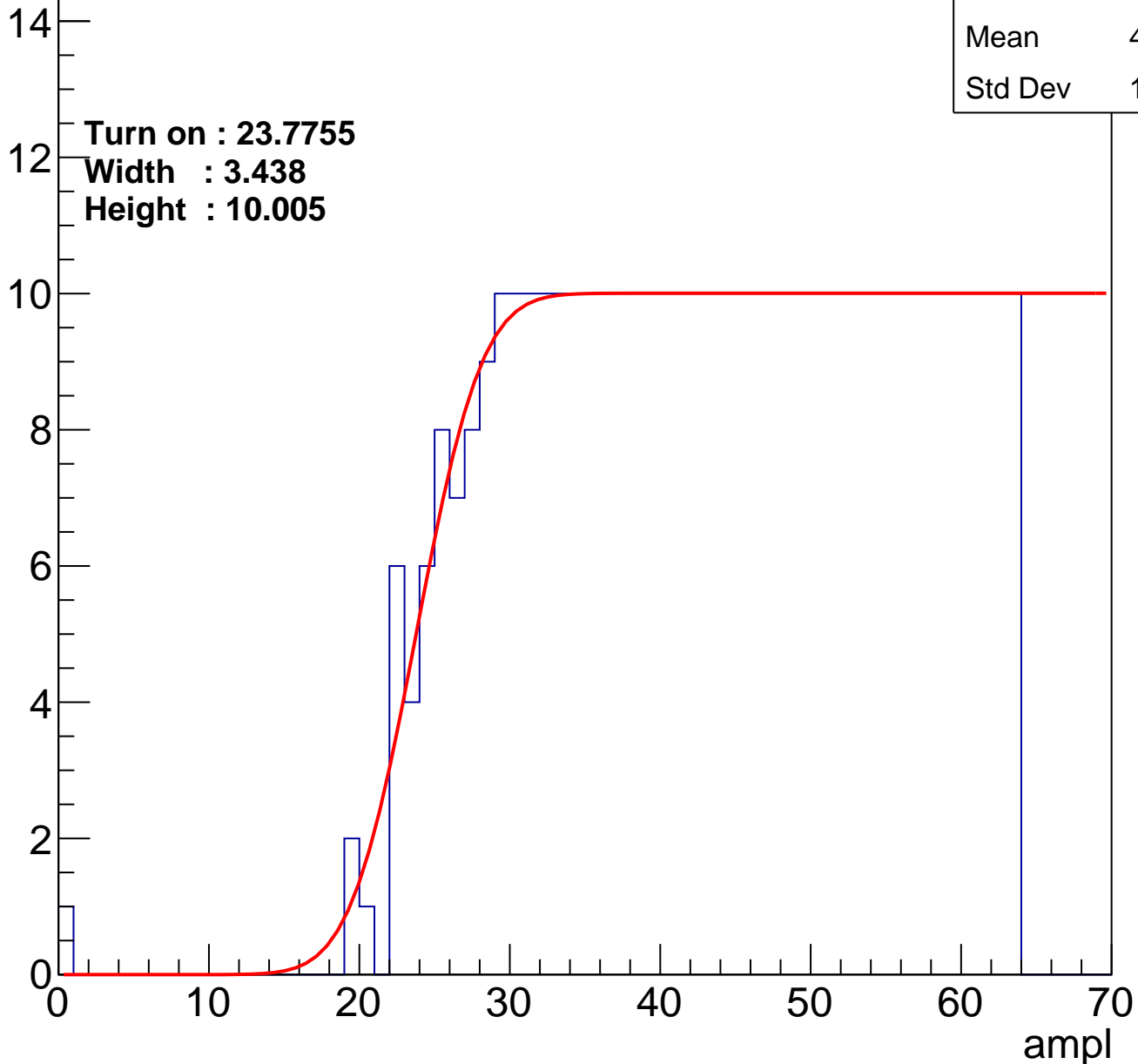
Entries	402
Mean	43.22
Std Dev	11.96

Turn on : 23.7755

Width : 3.438

Height : 10.005

Entry



B1L002S, U13-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.96
Std Dev	12.28

Turn on : 23.6502

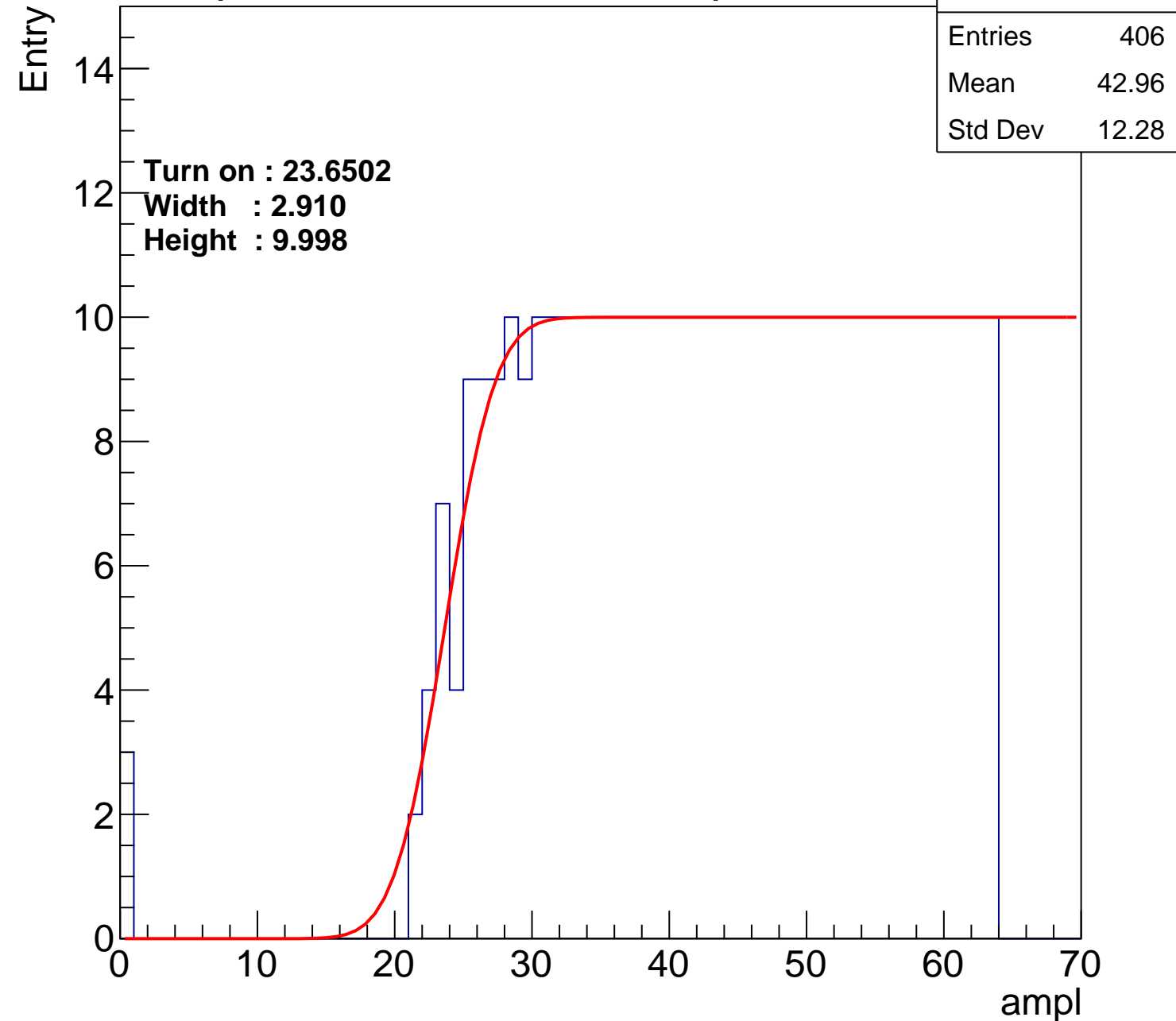
Width : 2.910

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.8406

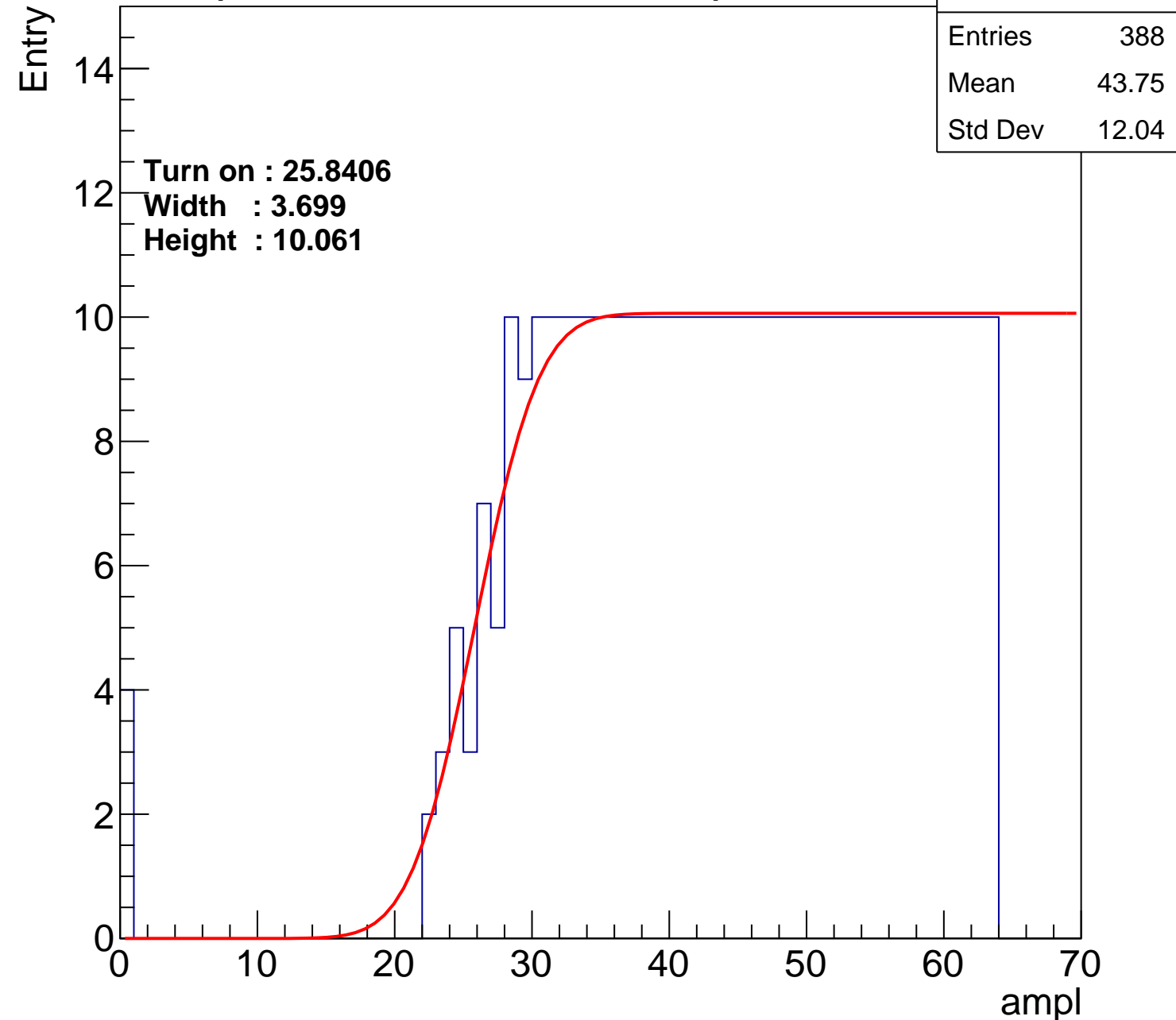
Width : 3.699

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.36
Std Dev	11.95

Turn on : 24.2549

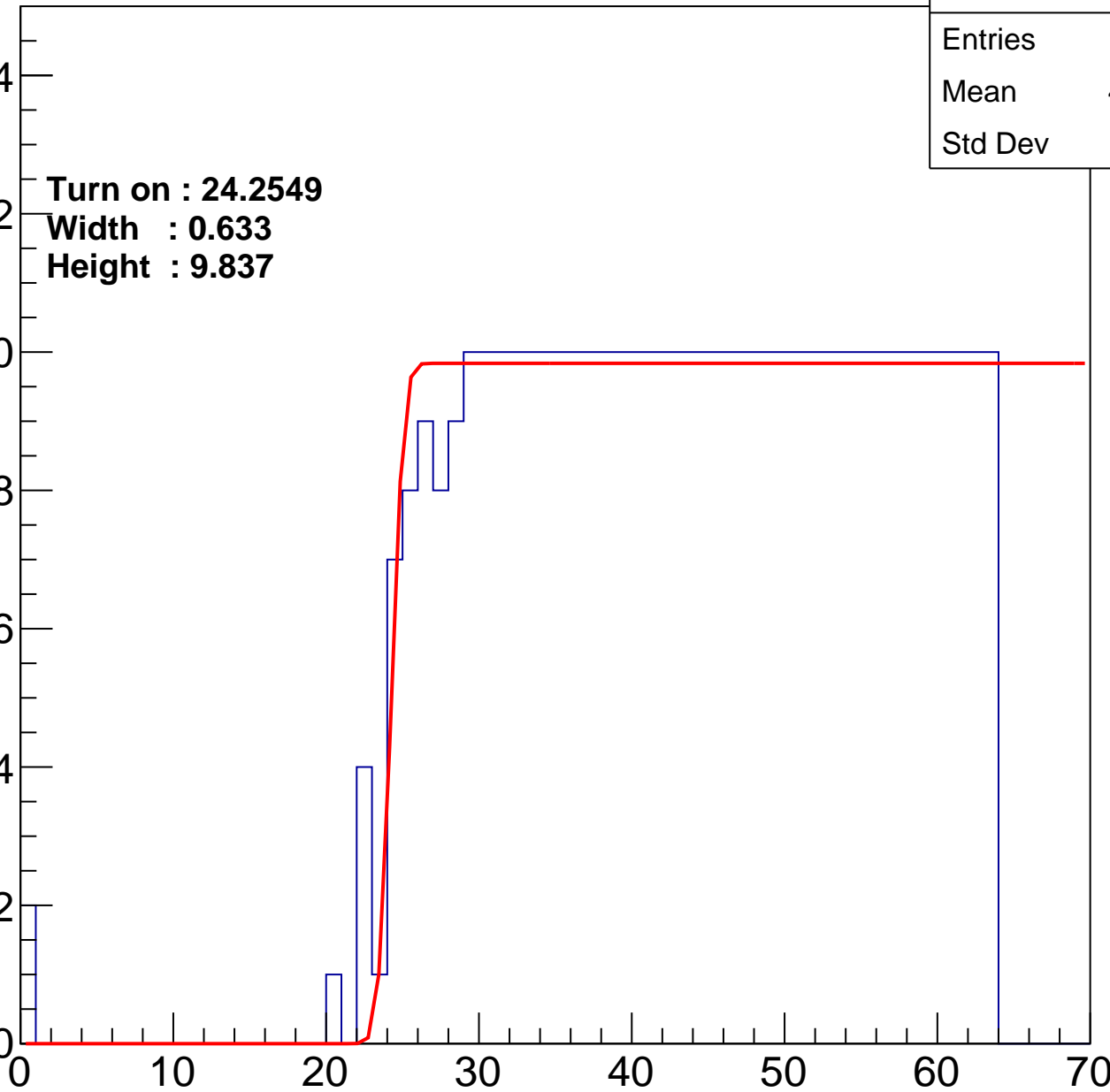
Width : 0.633

Height : 9.837

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch61

calib_packv5_042523_0143.root, FC#10, port B3

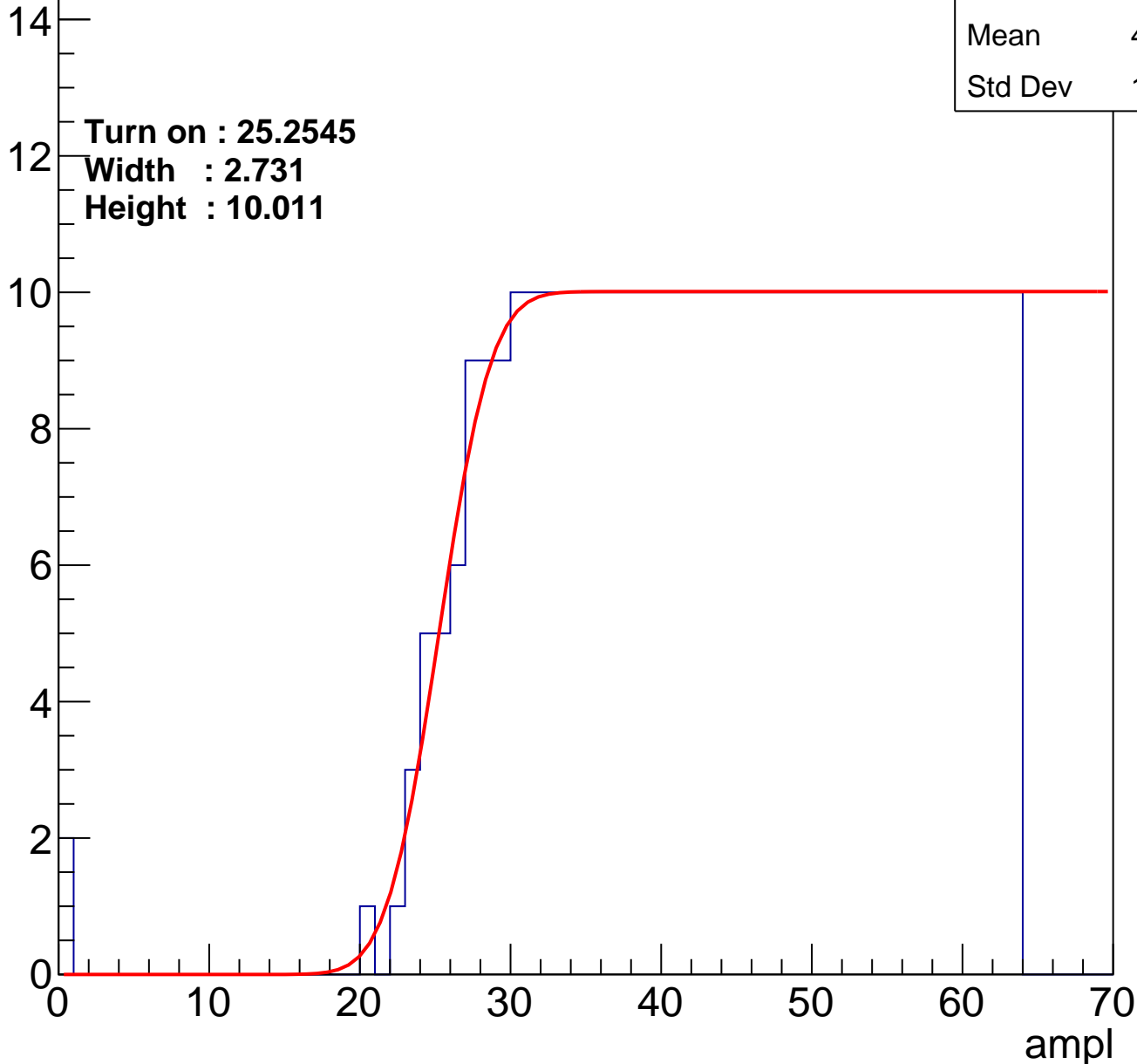
Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.2545

Width : 2.731

Height : 10.011

Entry



B1L002S, U13-ch62

calib_packv5_042523_0143.root, FC#10, port B3

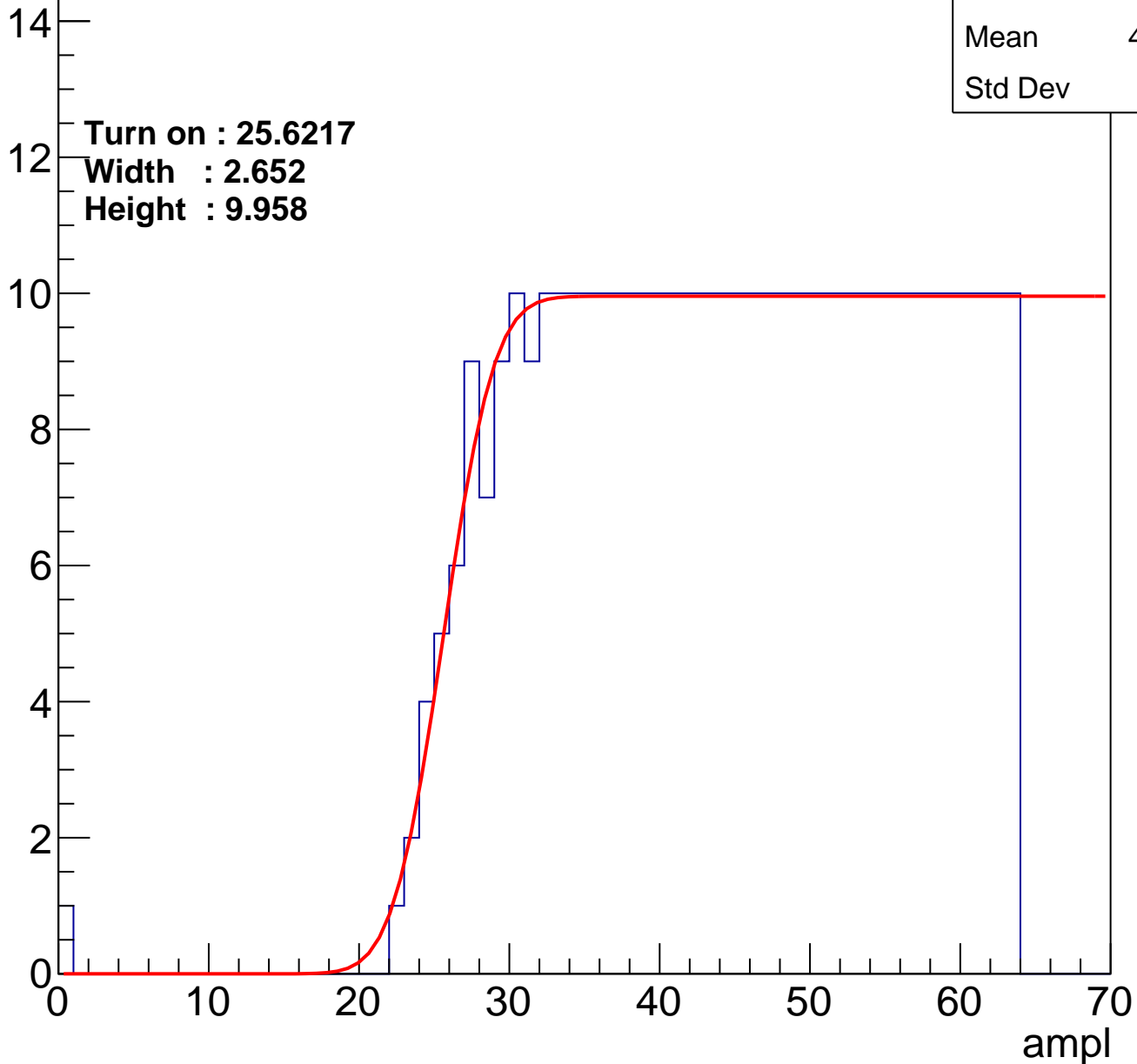
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 25.6217

Width : 2.652

Height : 9.958

Entry



B1L002S, U13-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.3929

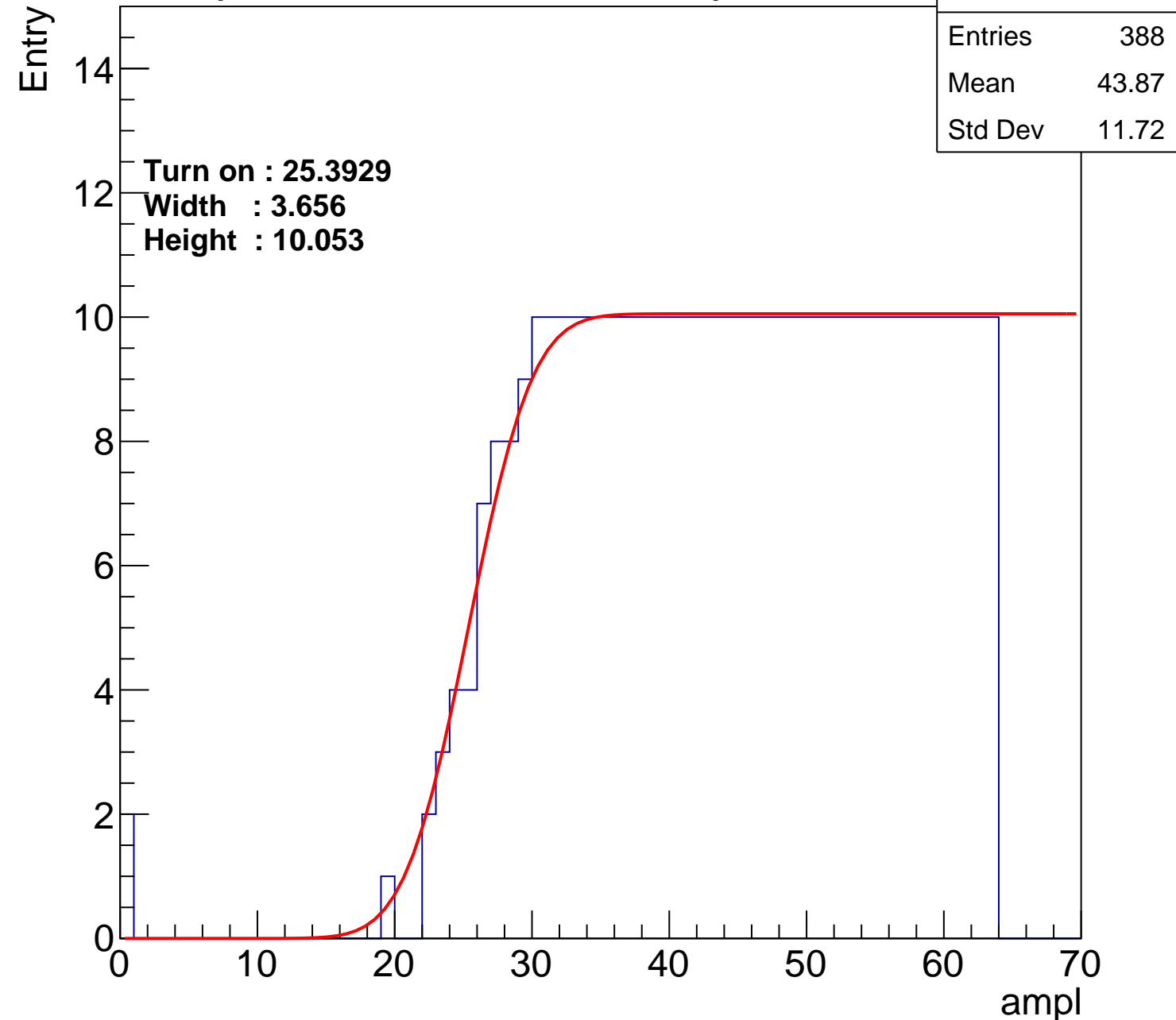
Width : 3.656

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch64

calib_packv5_042523_0143.root, FC#10, port B3

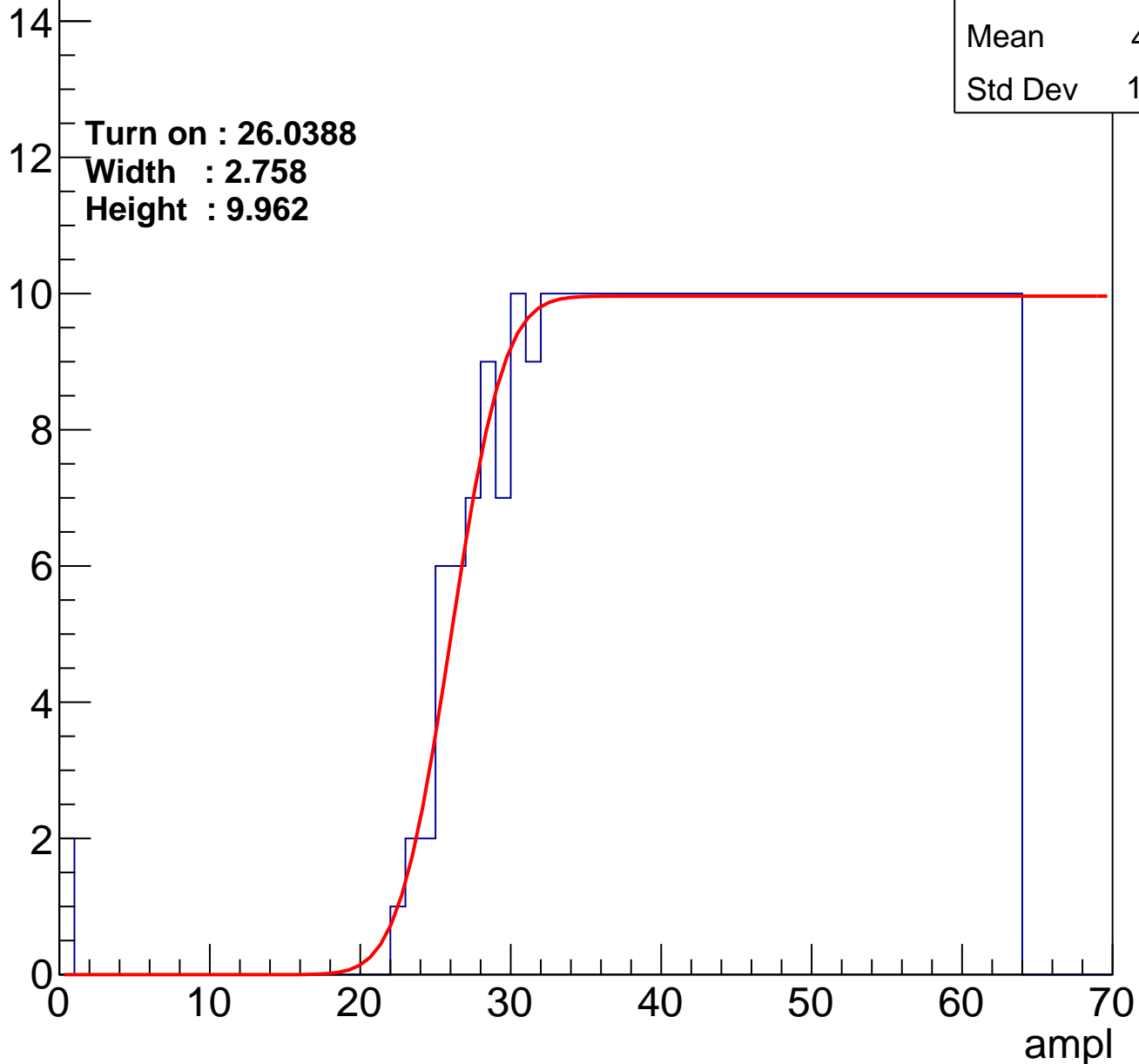
Entries	381
Mean	44.21
Std Dev	11.54

Turn on : 26.0388

Width : 2.758

Height : 9.962

Entry



B1L002S, U13-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 27.2494

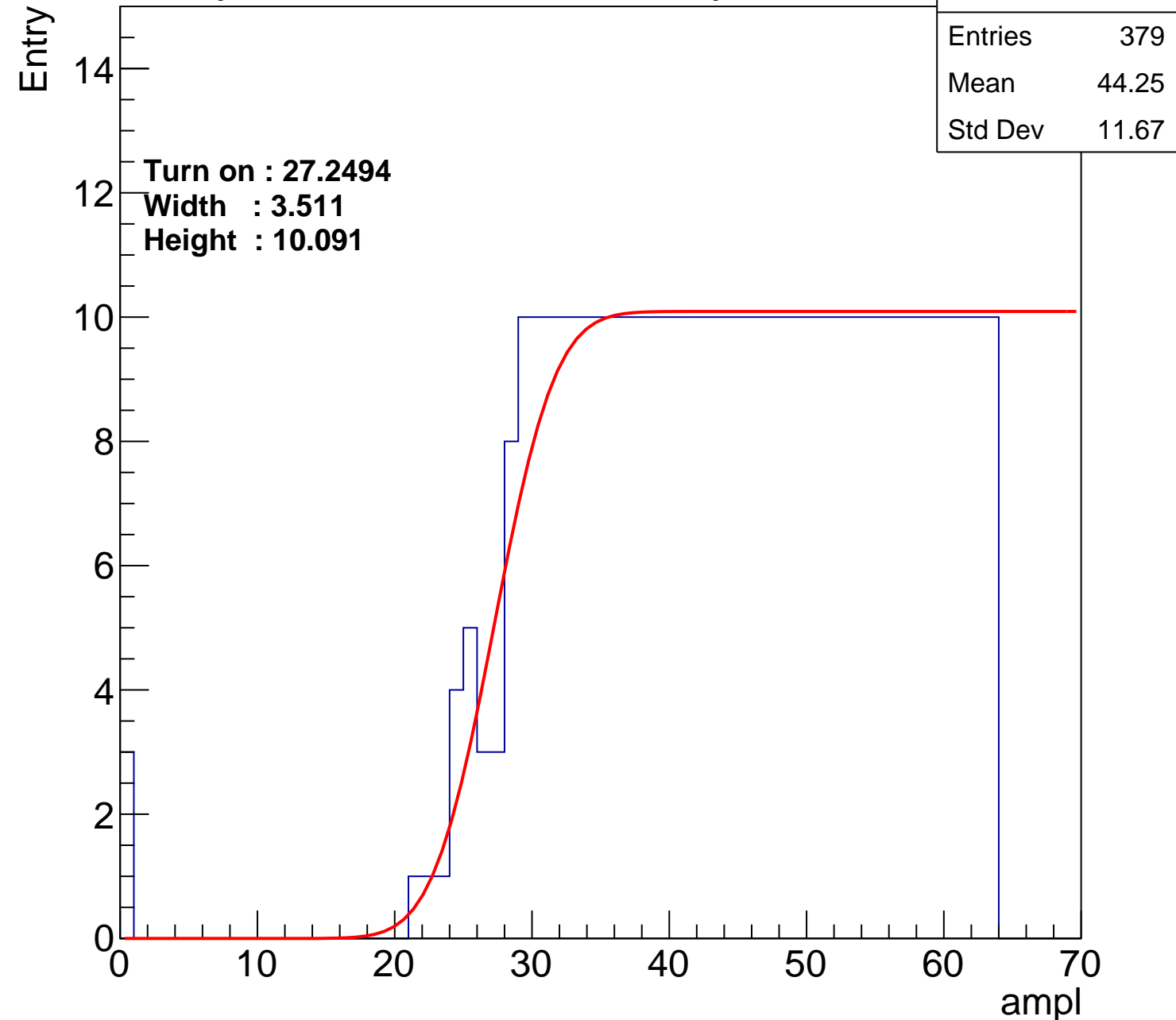
Width : 3.511

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch66

calib_packv5_042523_0143.root, FC#10, port B3

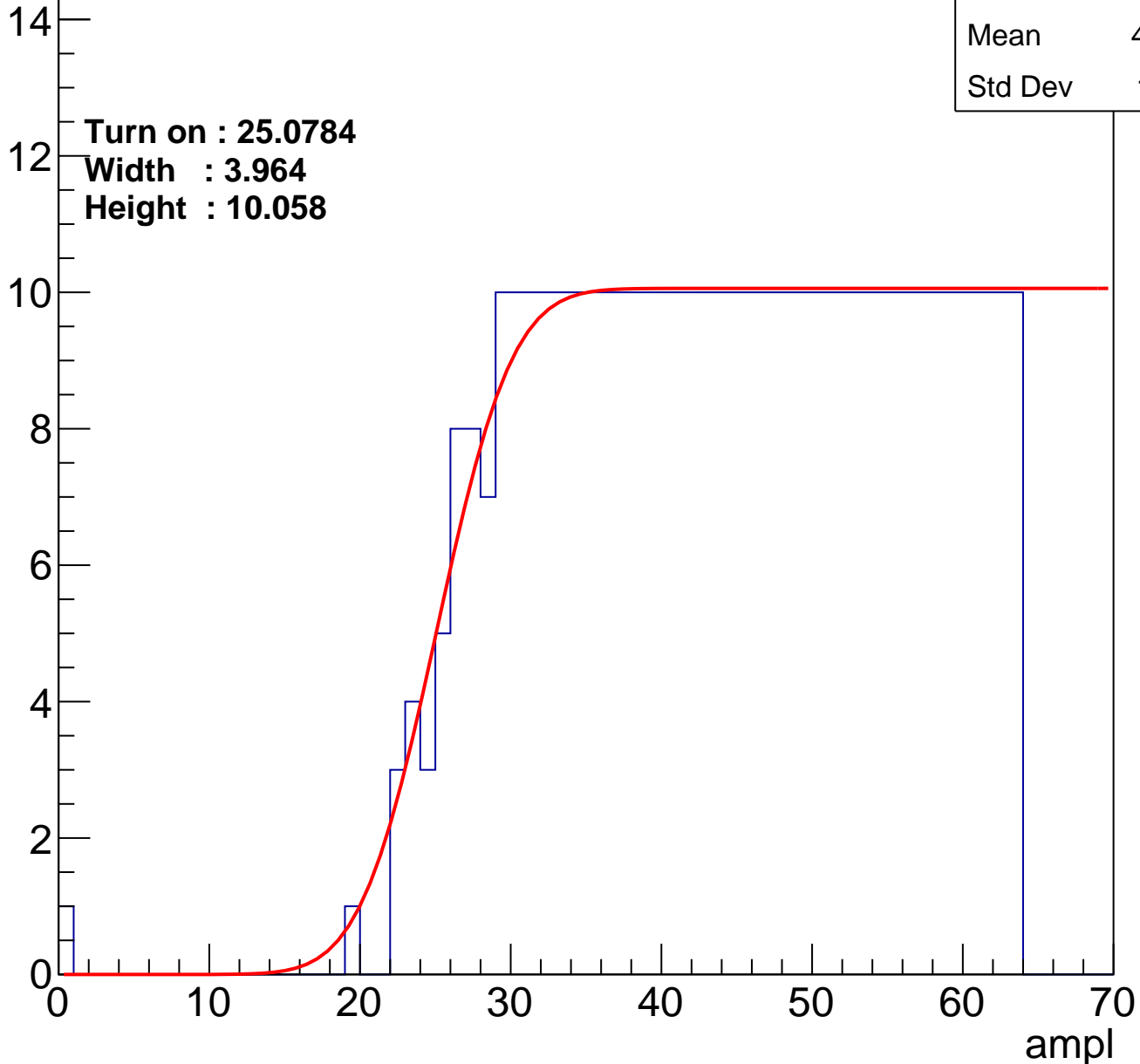
Entries	390
Mean	43.83
Std Dev	11.61

Turn on : 25.0784

Width : 3.964

Height : 10.058

Entry



B1L002S, U13-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.4

Turn on : 25.9515

Width : 1.758

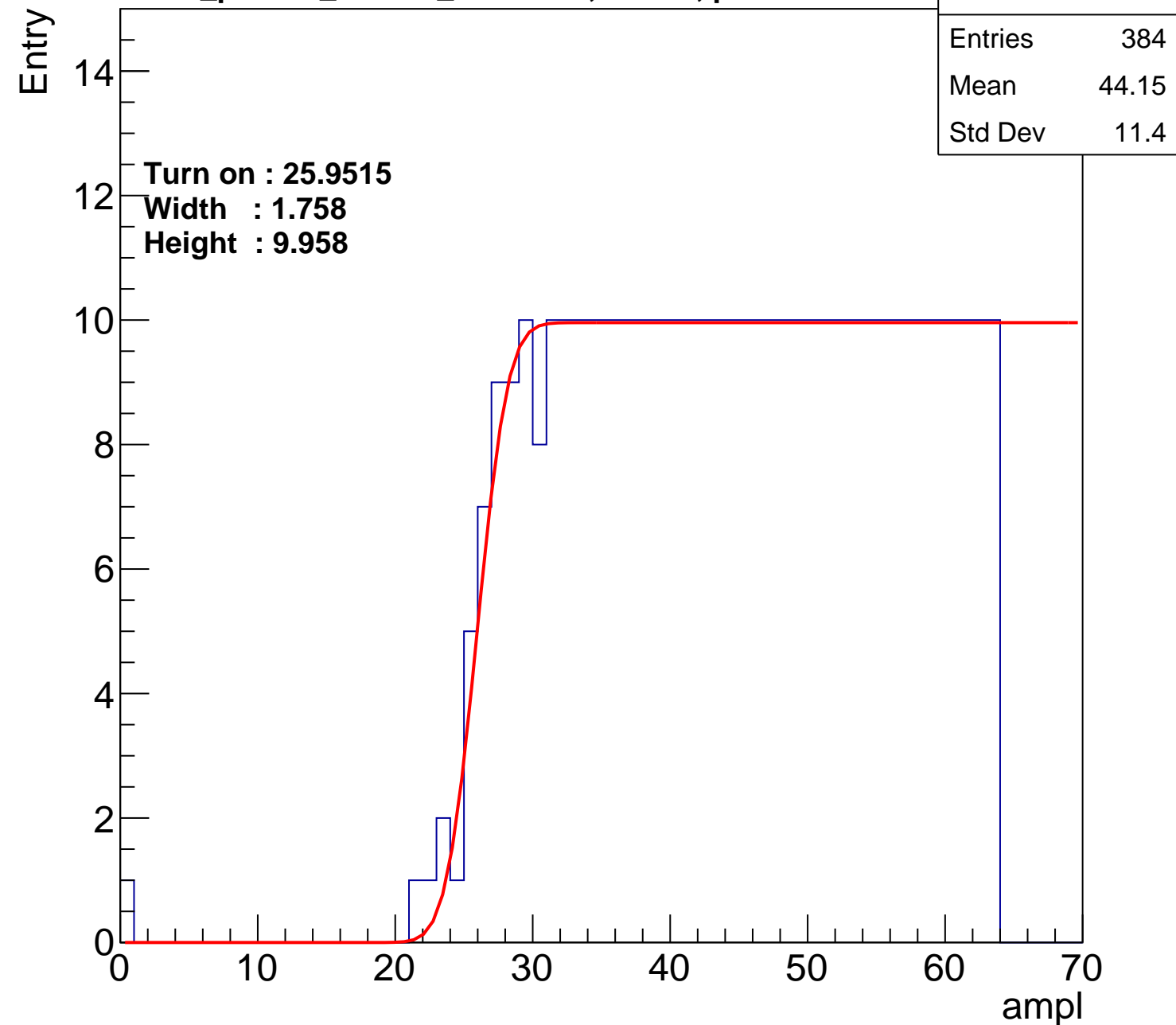
Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U13-ch68

calib_packv5_042523_0143.root, FC#10, port B3

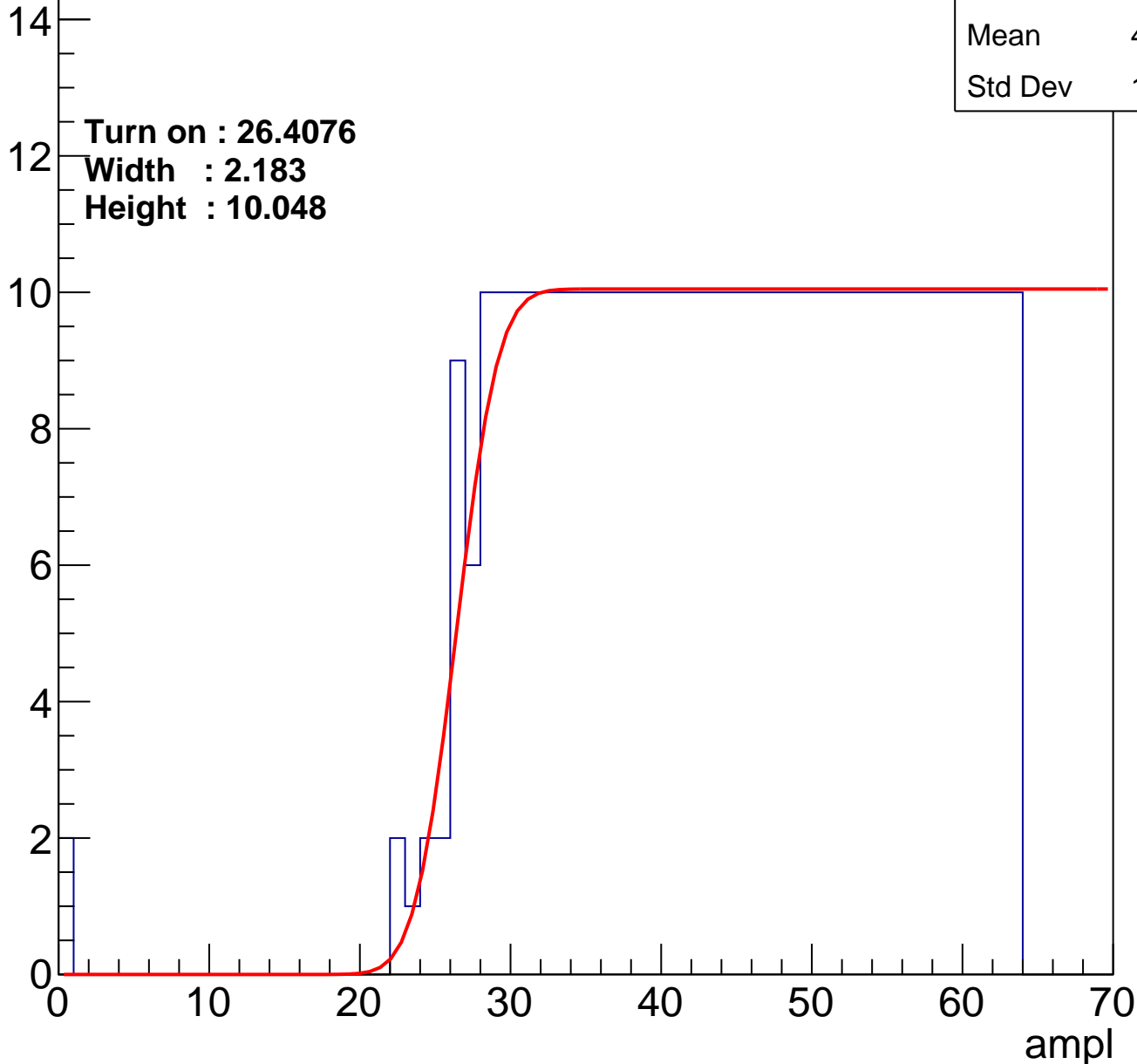
Entries	384
Mean	44.12
Std Dev	11.54

Turn on : 26.4076

Width : 2.183

Height : 10.048

Entry



B1L002S, U13-ch69

calib_packv5_042523_0143.root, FC#10, port B3

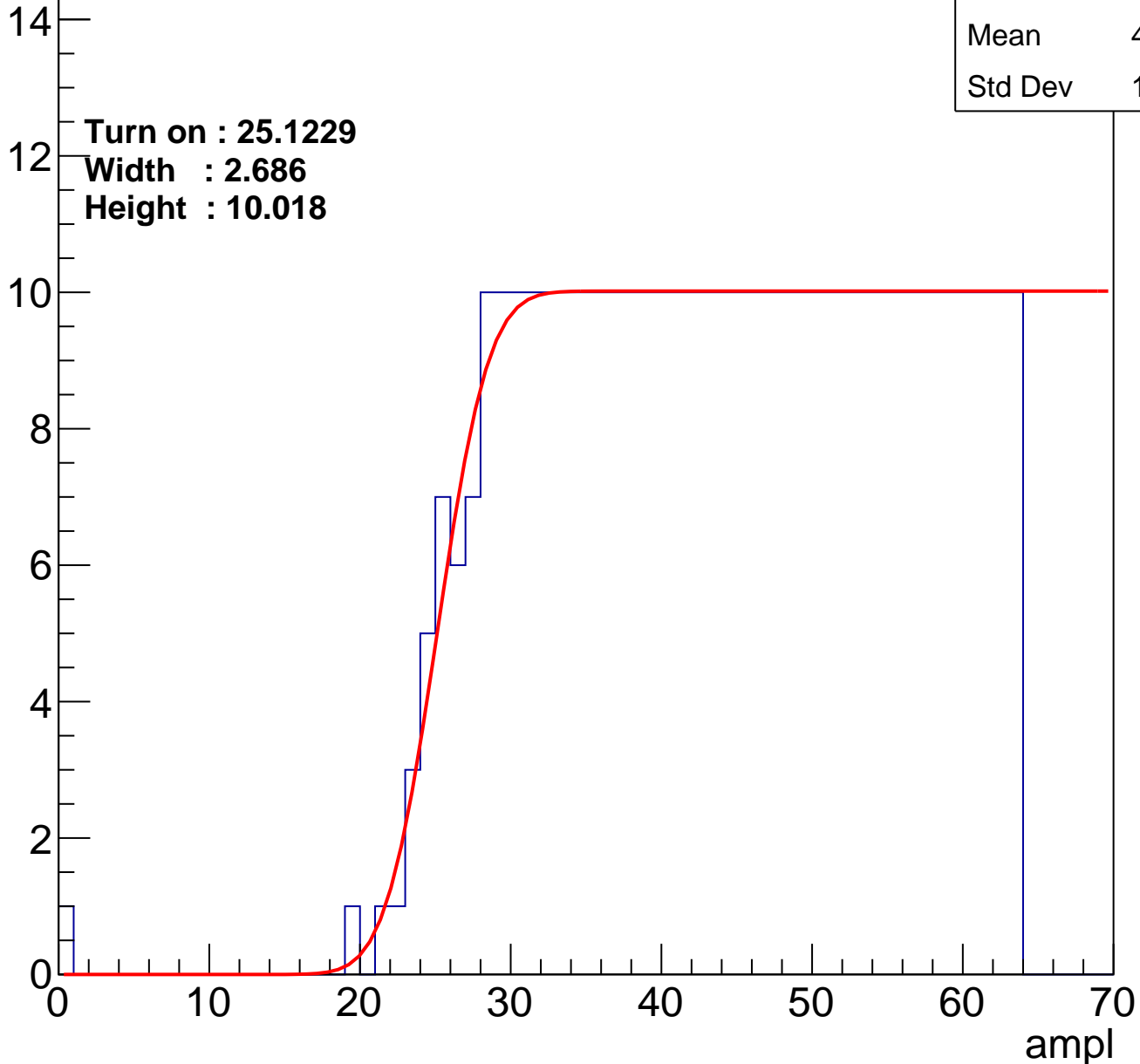
Entries	392
Mean	43.75
Std Dev	11.63

Turn on : 25.1229

Width : 2.686

Height : 10.018

Entry



B1L002S, U13-ch70

calib_packv5_042523_0143.root, FC#10, port B3

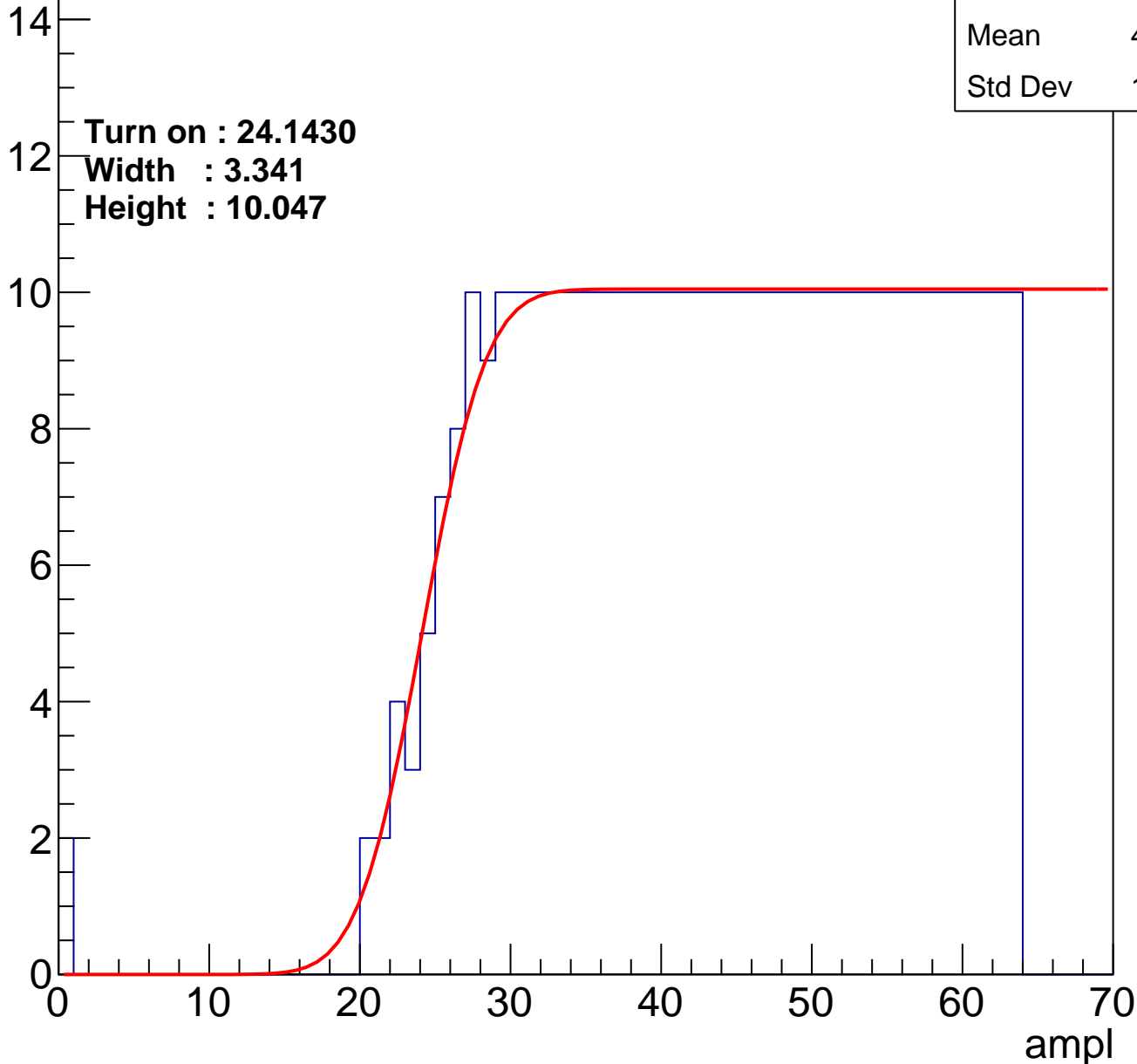
Entries	402
Mean	43.19
Std Dev	12.06

Turn on : 24.1430

Width : 3.341

Height : 10.047

Entry



B1L002S, U13-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.87
Std Dev	11.54

Turn on : 25.2482

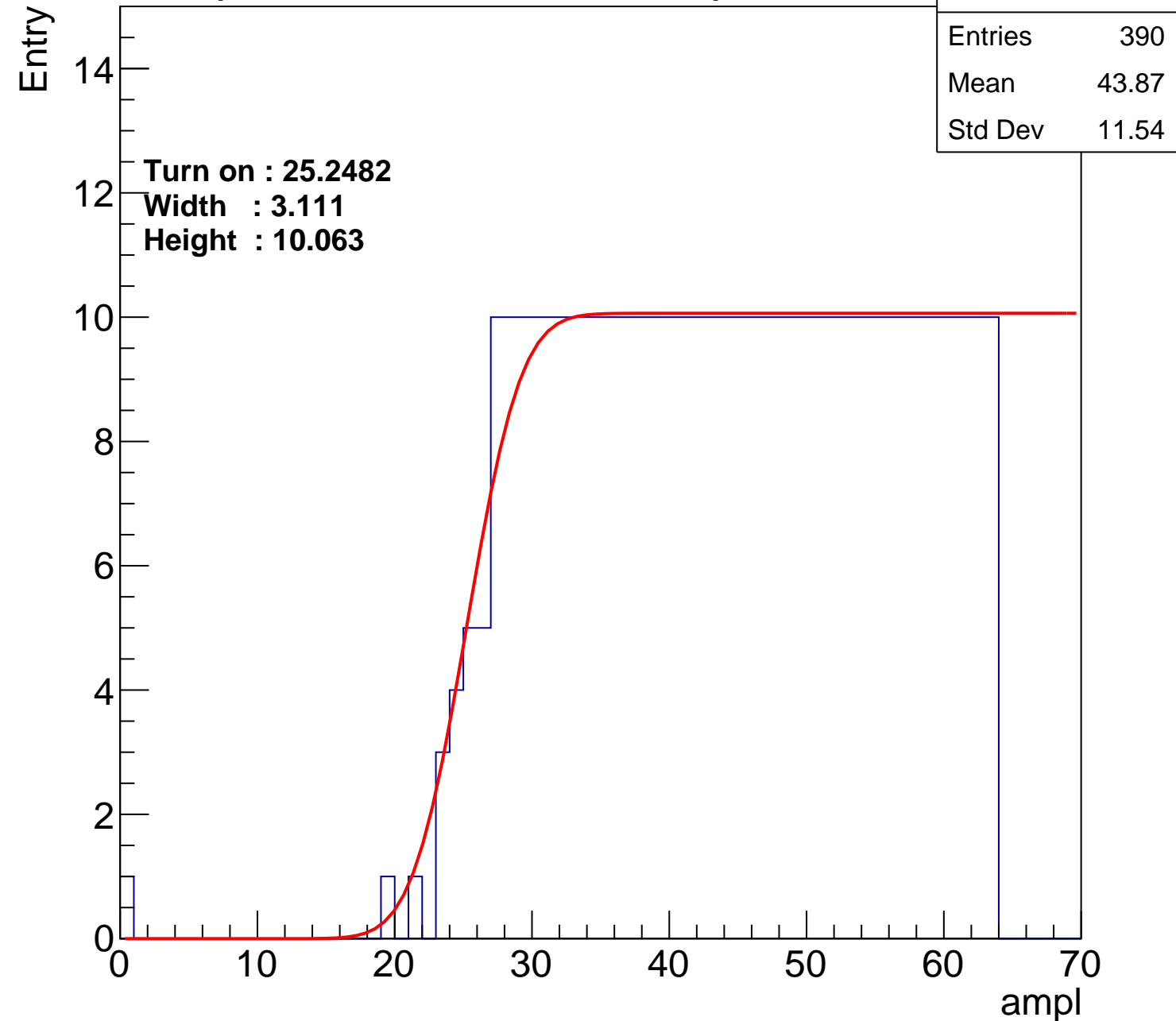
Width : 3.111

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 24.7765

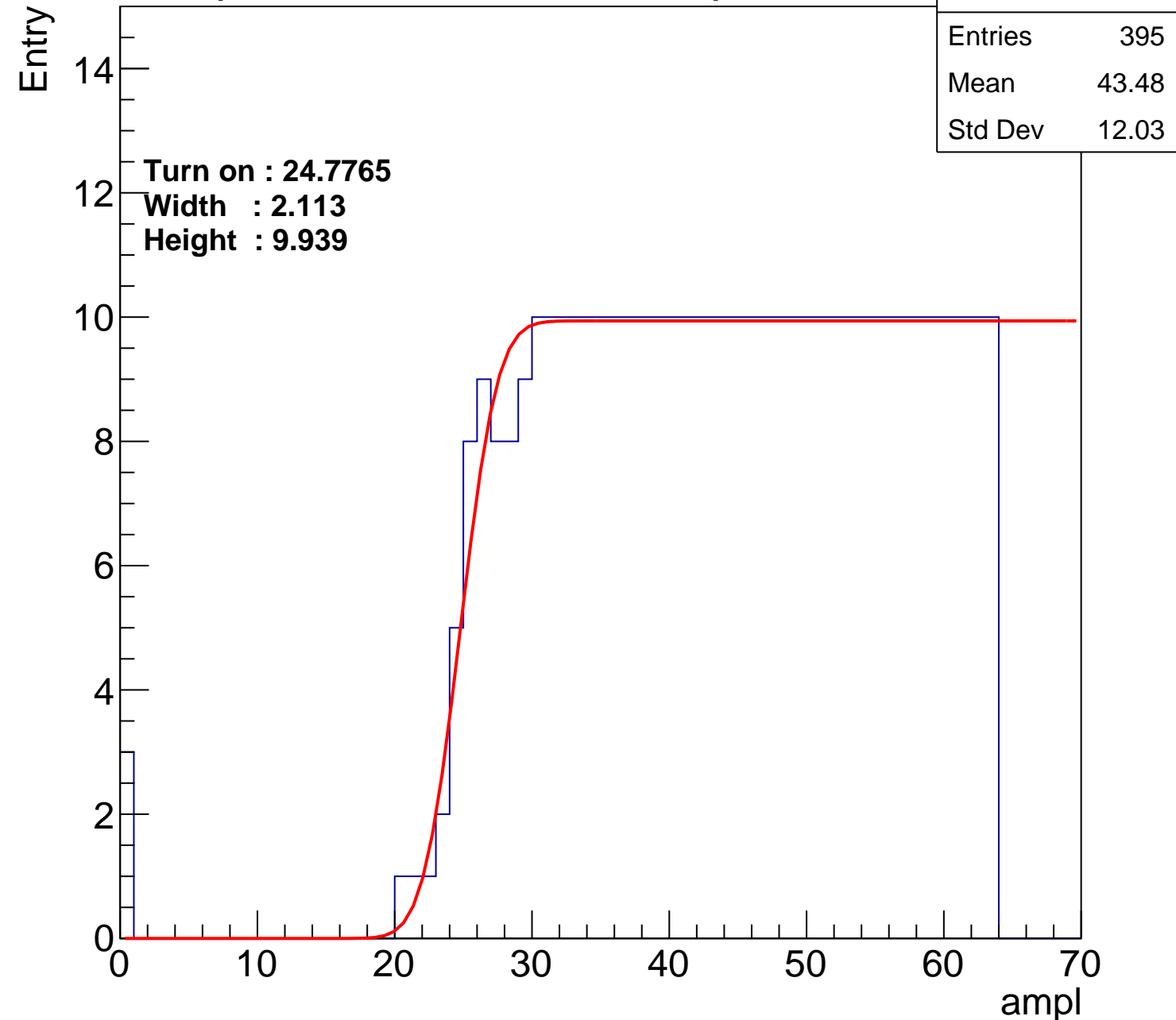
Width : 2.113

Height : 9.939

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.8317

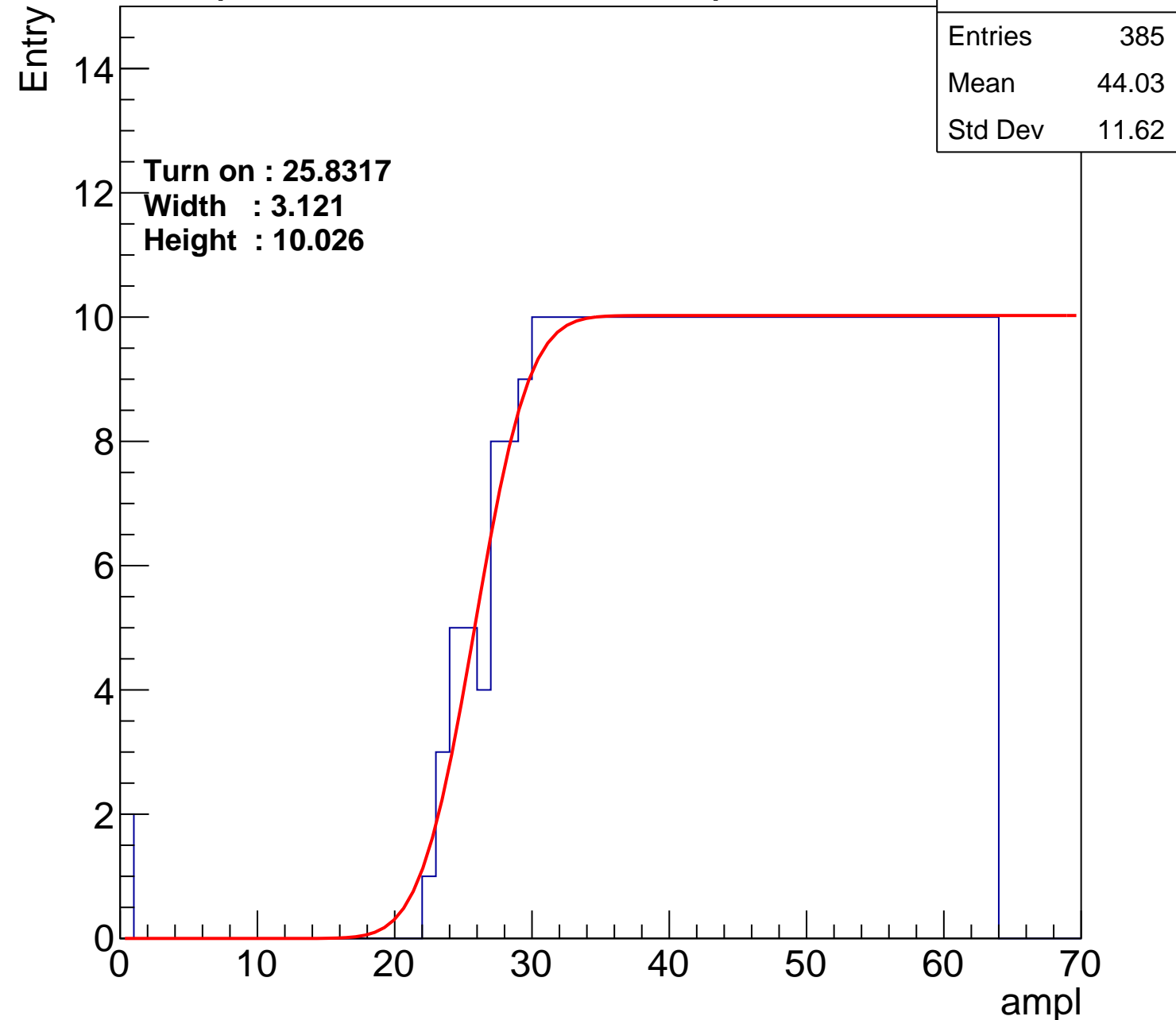
Width : 3.121

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.4406

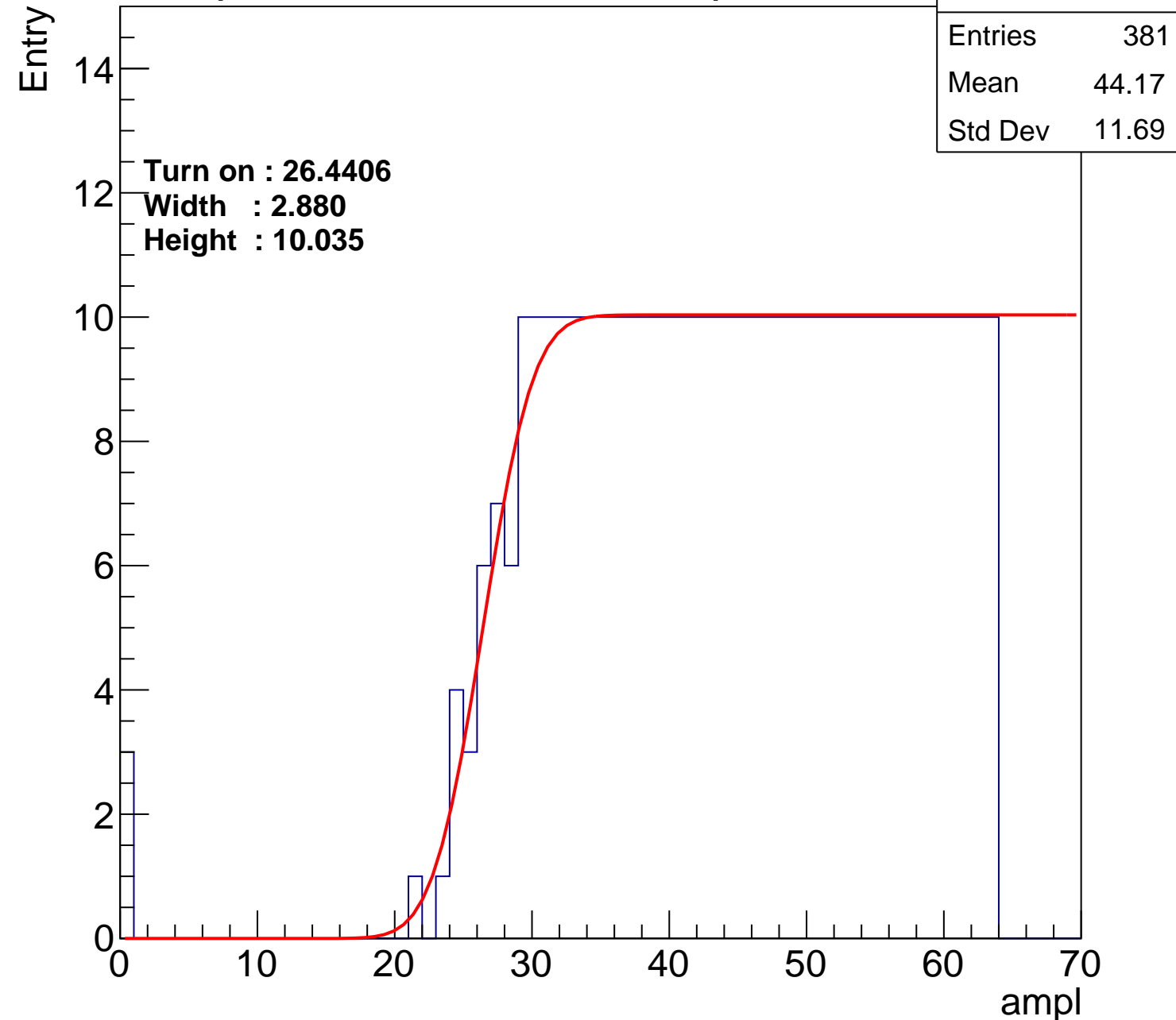
Width : 2.880

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.6181

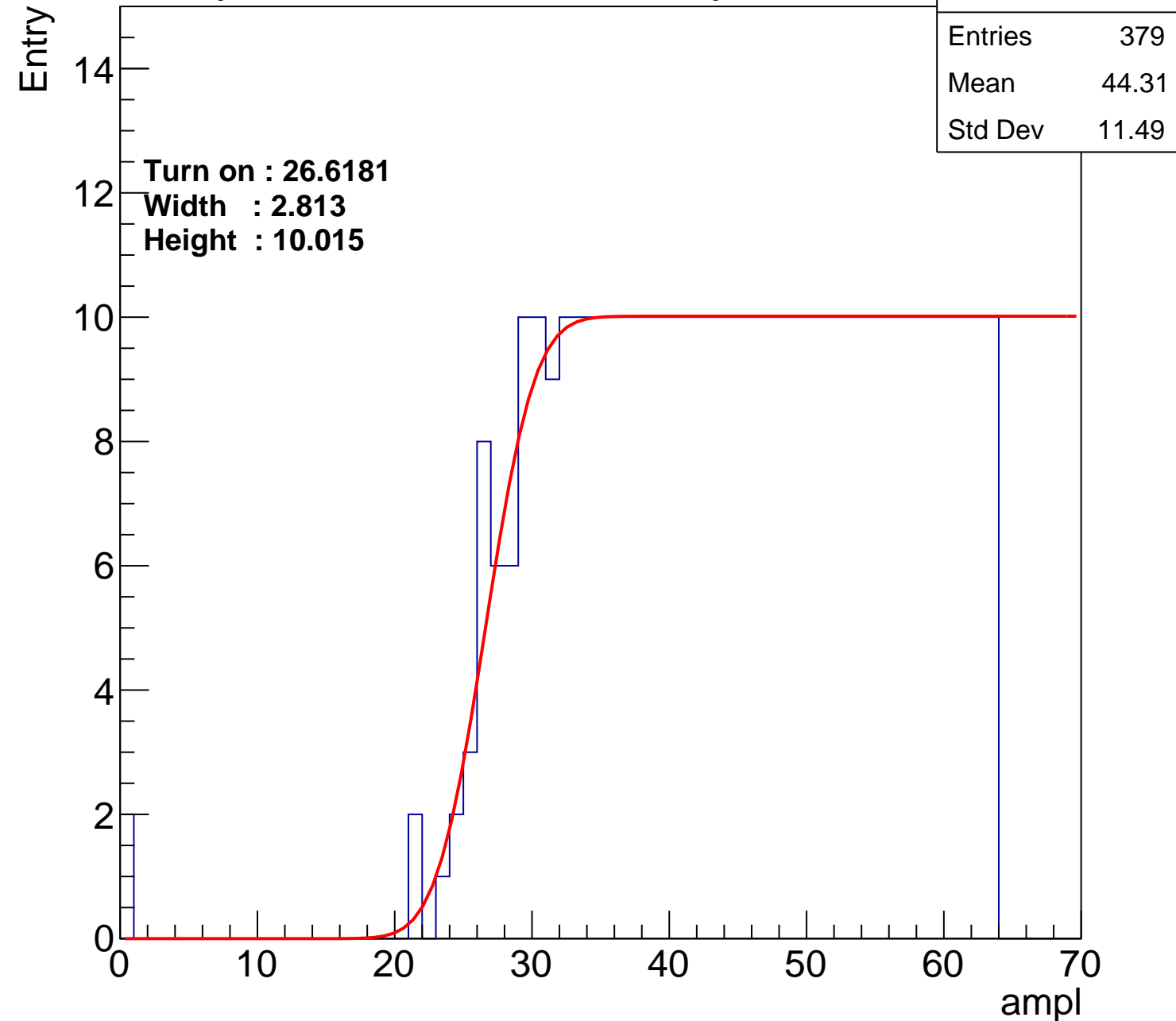
Width : 2.813

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.25
Std Dev	11.48

Turn on : 26.4171

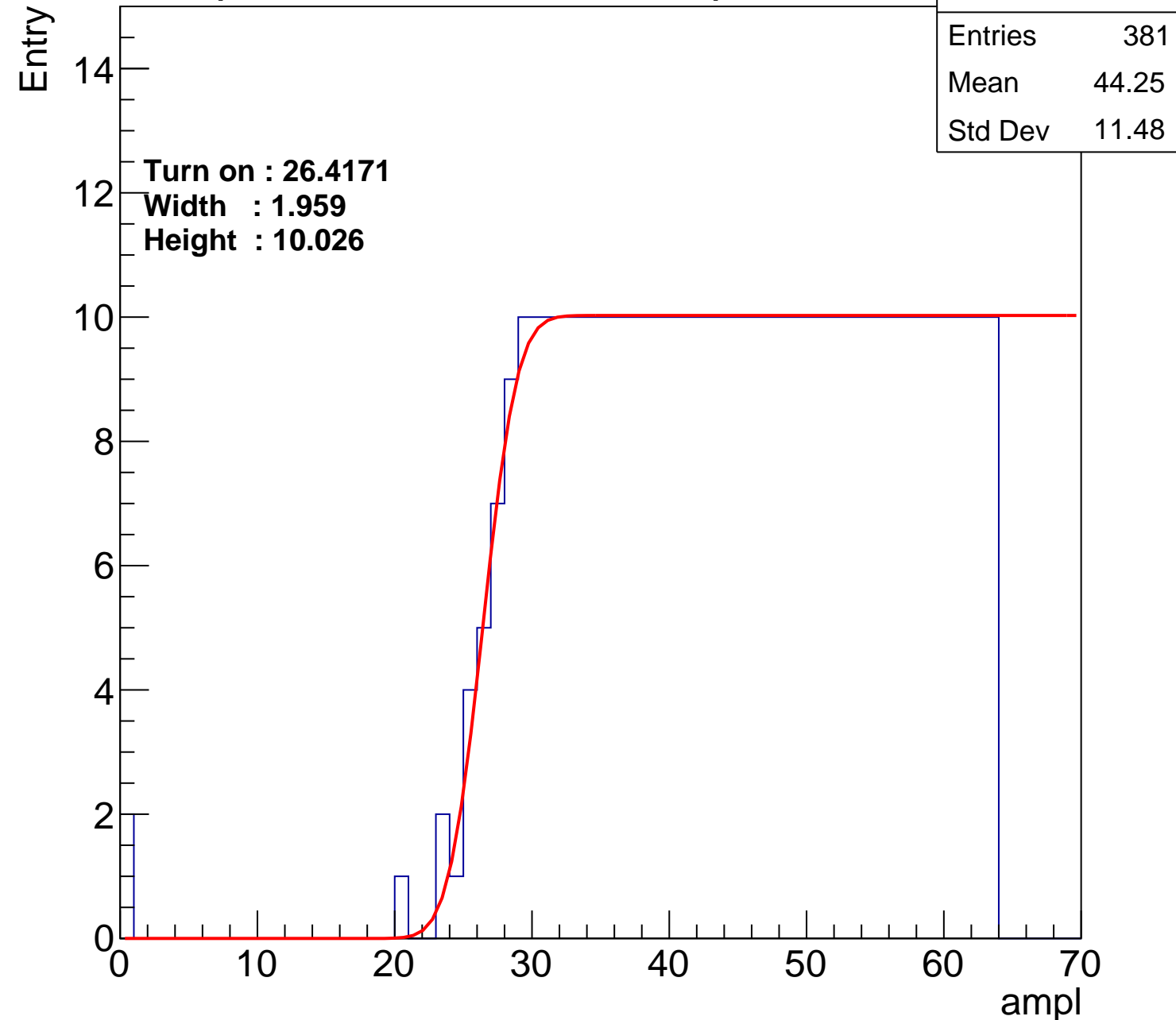
Width : 1.959

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch77

calib_packv5_042523_0143.root, FC#10, port B3

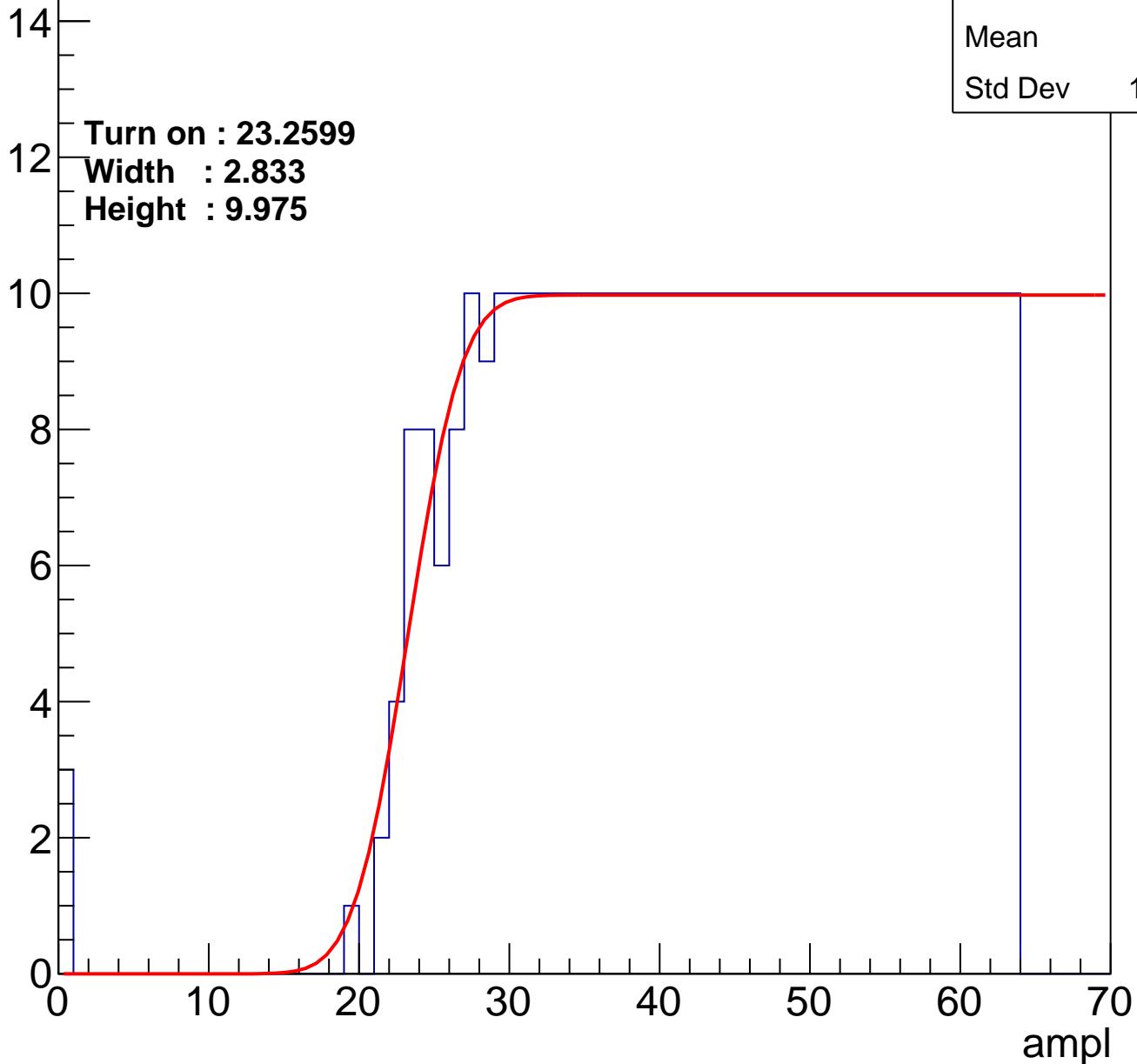
Entries	409
Mean	42.8
Std Dev	12.37

Turn on : 23.2599

Width : 2.833

Height : 9.975

Entry



B1L002S, U13-ch78

calib_packv5_042523_0143.root, FC#10, port B3

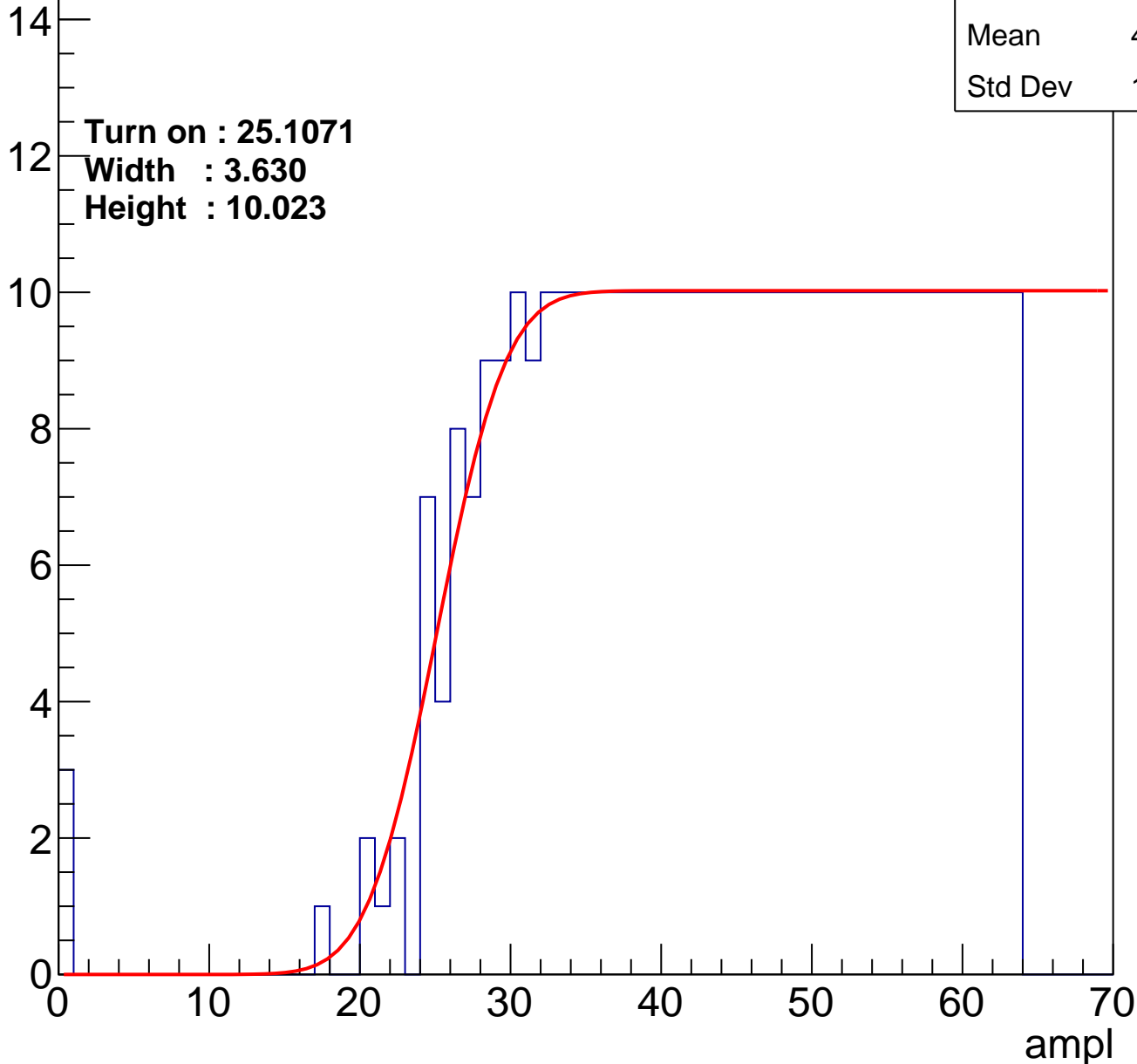
Entries	392
Mean	43.57
Std Dev	12.06

Turn on : 25.1071

Width : 3.630

Height : 10.023

Entry



B1L002S, U13-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.16
Std Dev	11.82

Turn on : 26.4595

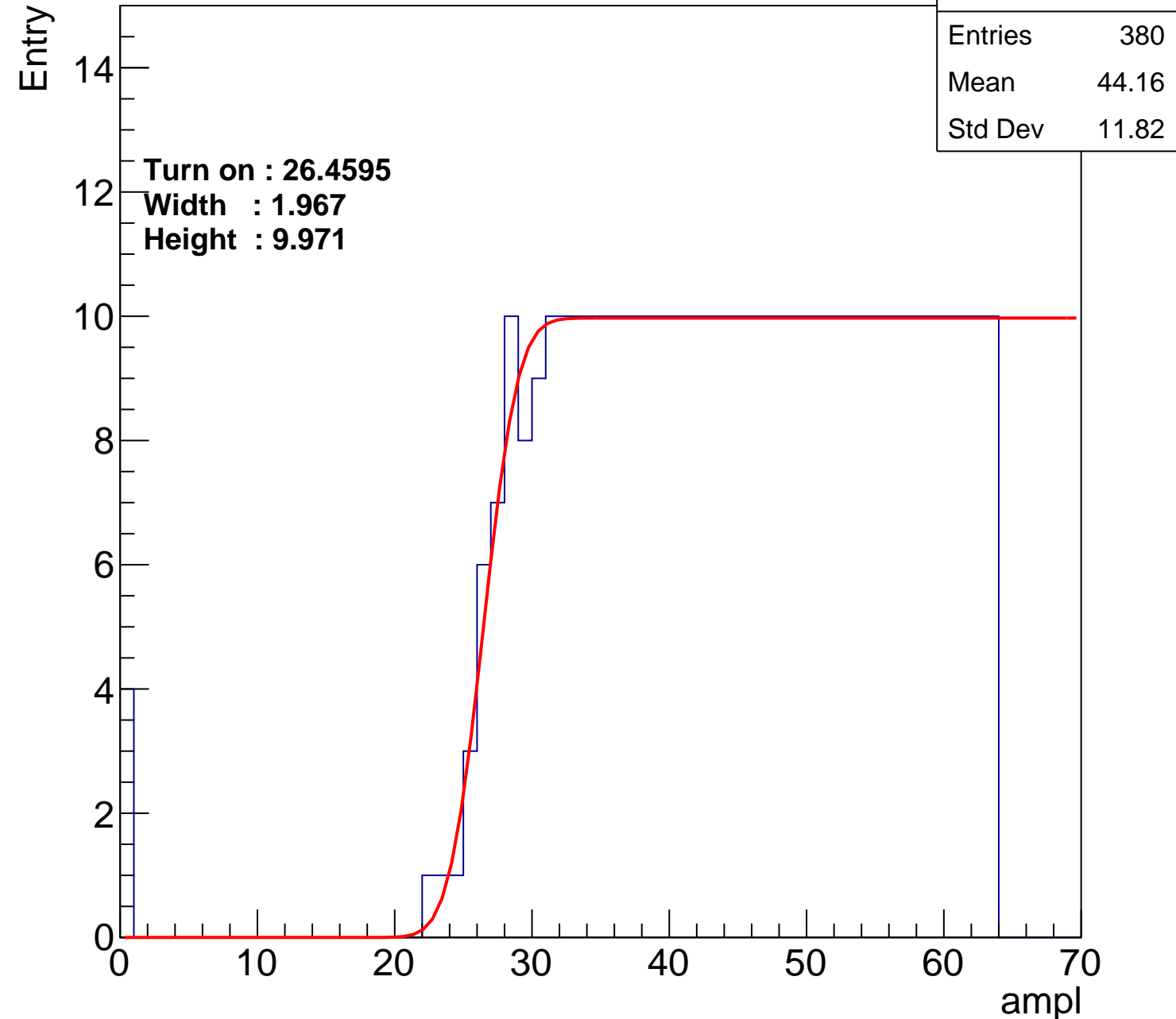
Width : 1.967

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch80

calib_packv5_042523_0143.root, FC#10, port B3

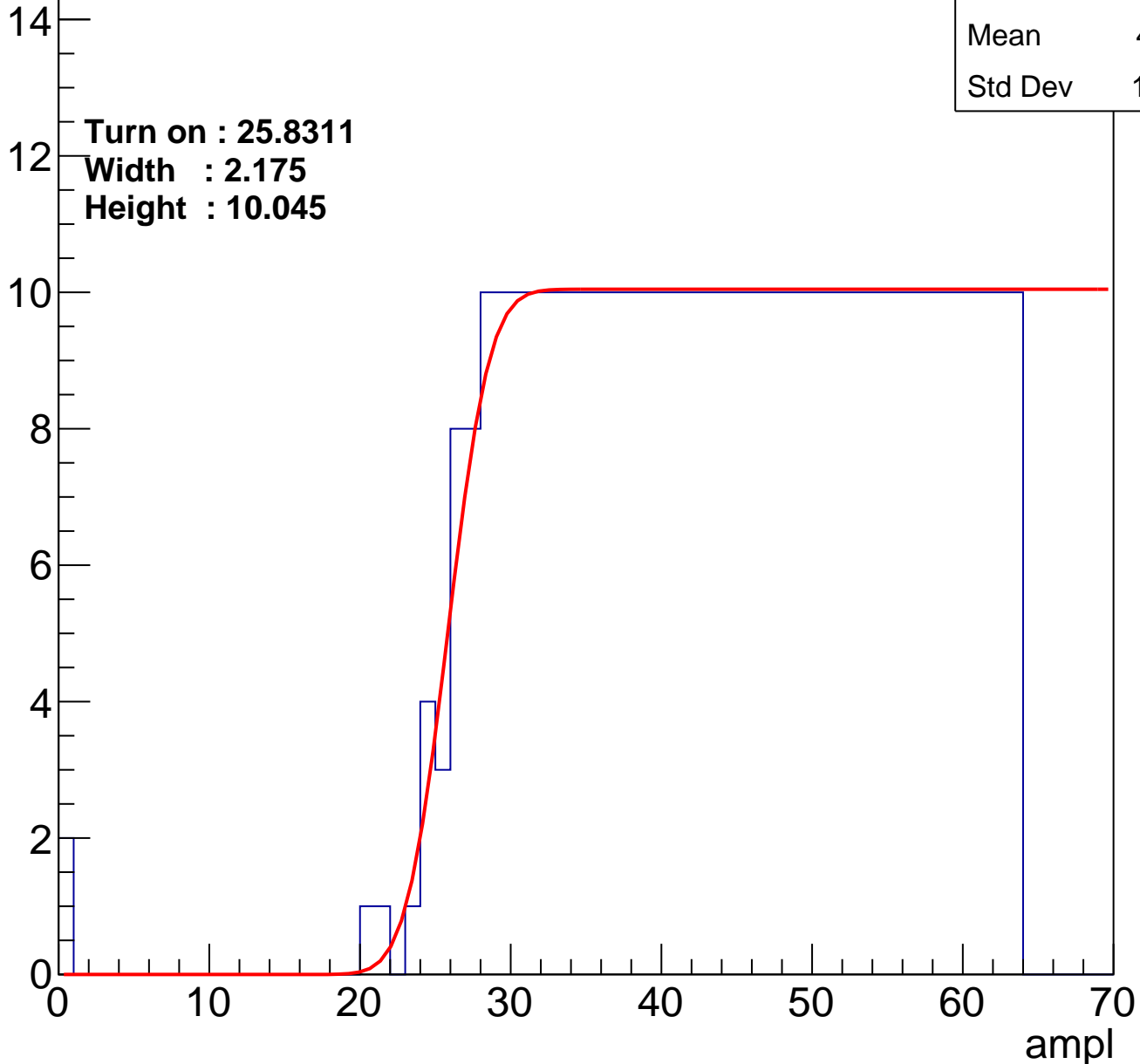
Entries	388
Mean	43.91
Std Dev	11.65

Turn on : 25.8311

Width : 2.175

Height : 10.045

Entry



B1L002S, U13-ch81

calib_packv5_042523_0143.root, FC#10, port B3

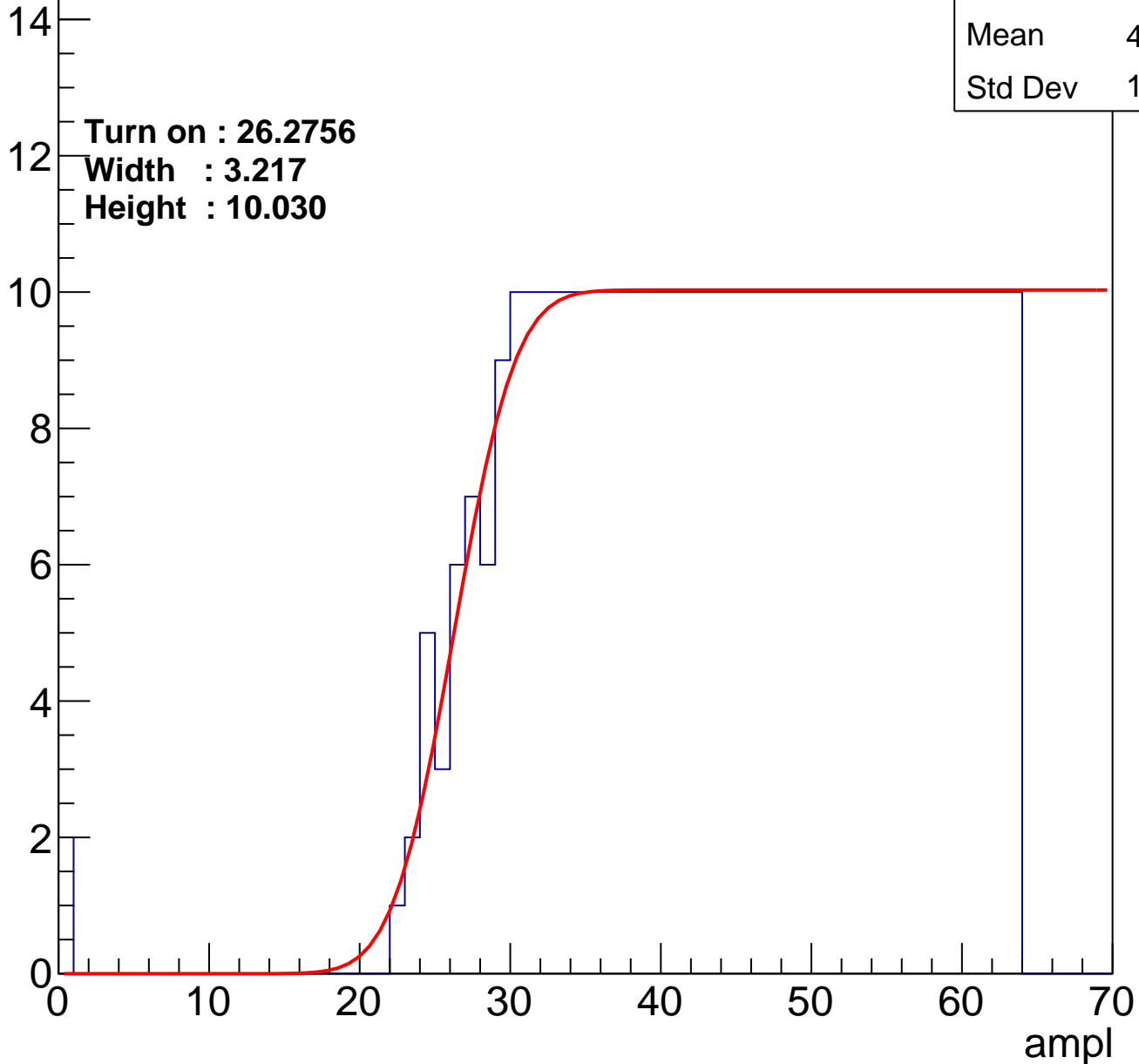
Entries	381
Mean	44.22
Std Dev	11.53

Turn on : 26.2756

Width : 3.217

Height : 10.030

Entry



B1L002S, U13-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.49
Std Dev	12.03

Turn on : 25.2567

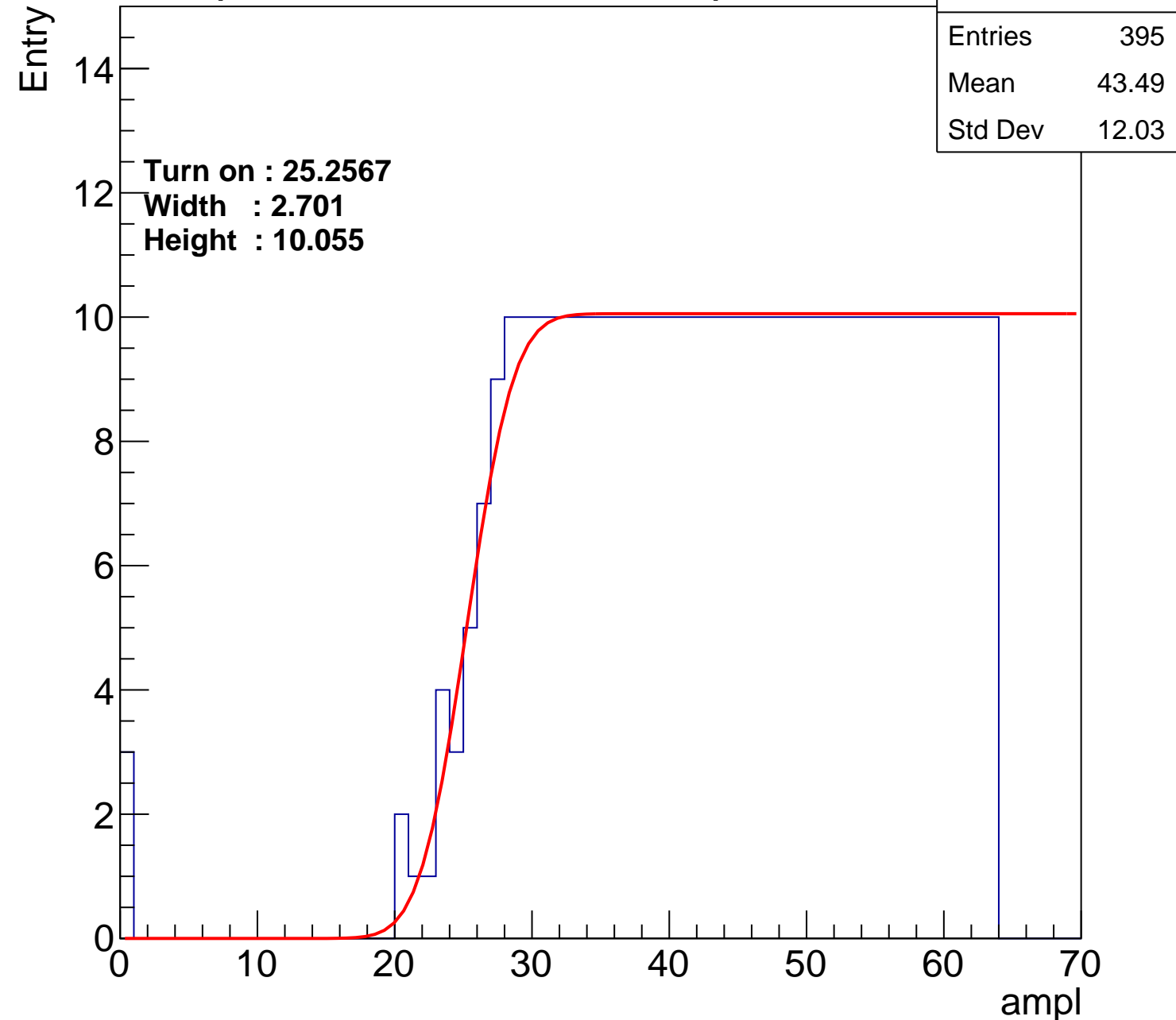
Width : 2.701

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.91
Std Dev	11.82

Turn on : 25.6354

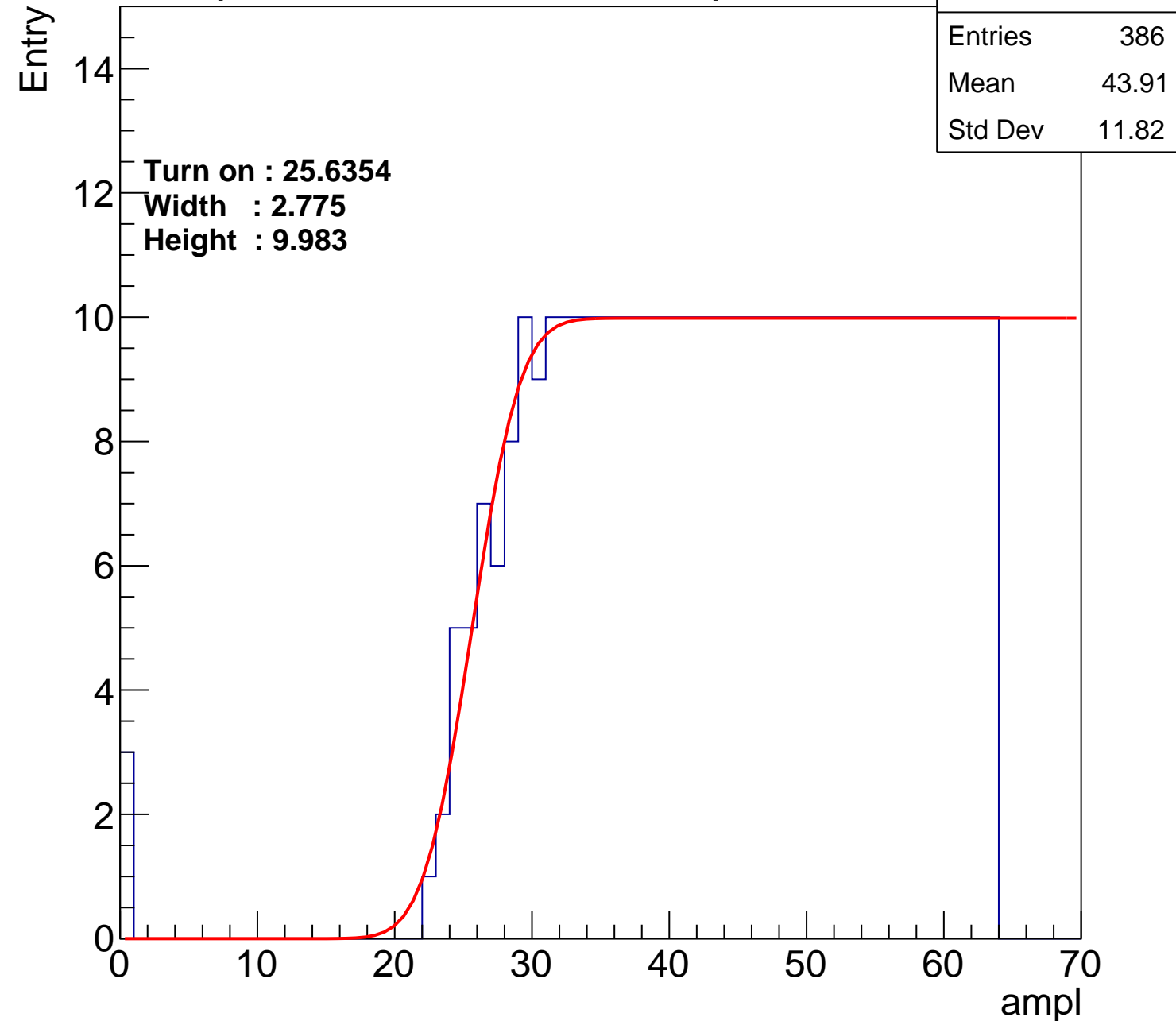
Width : 2.775

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.23
Std Dev	12.17

Turn on : 24.5643

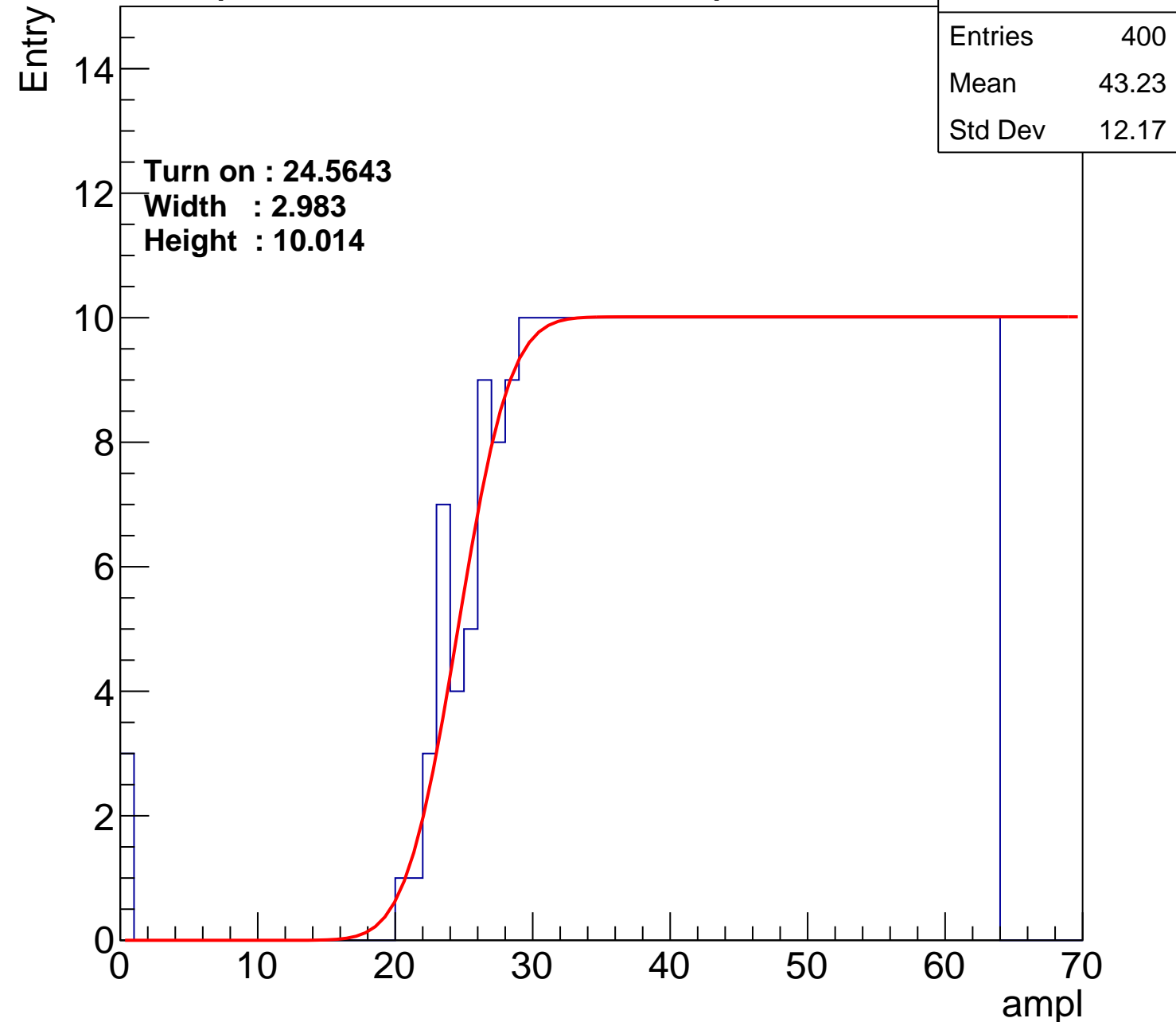
Width : 2.983

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.33
Std Dev	11.47

Turn on : 26.8132

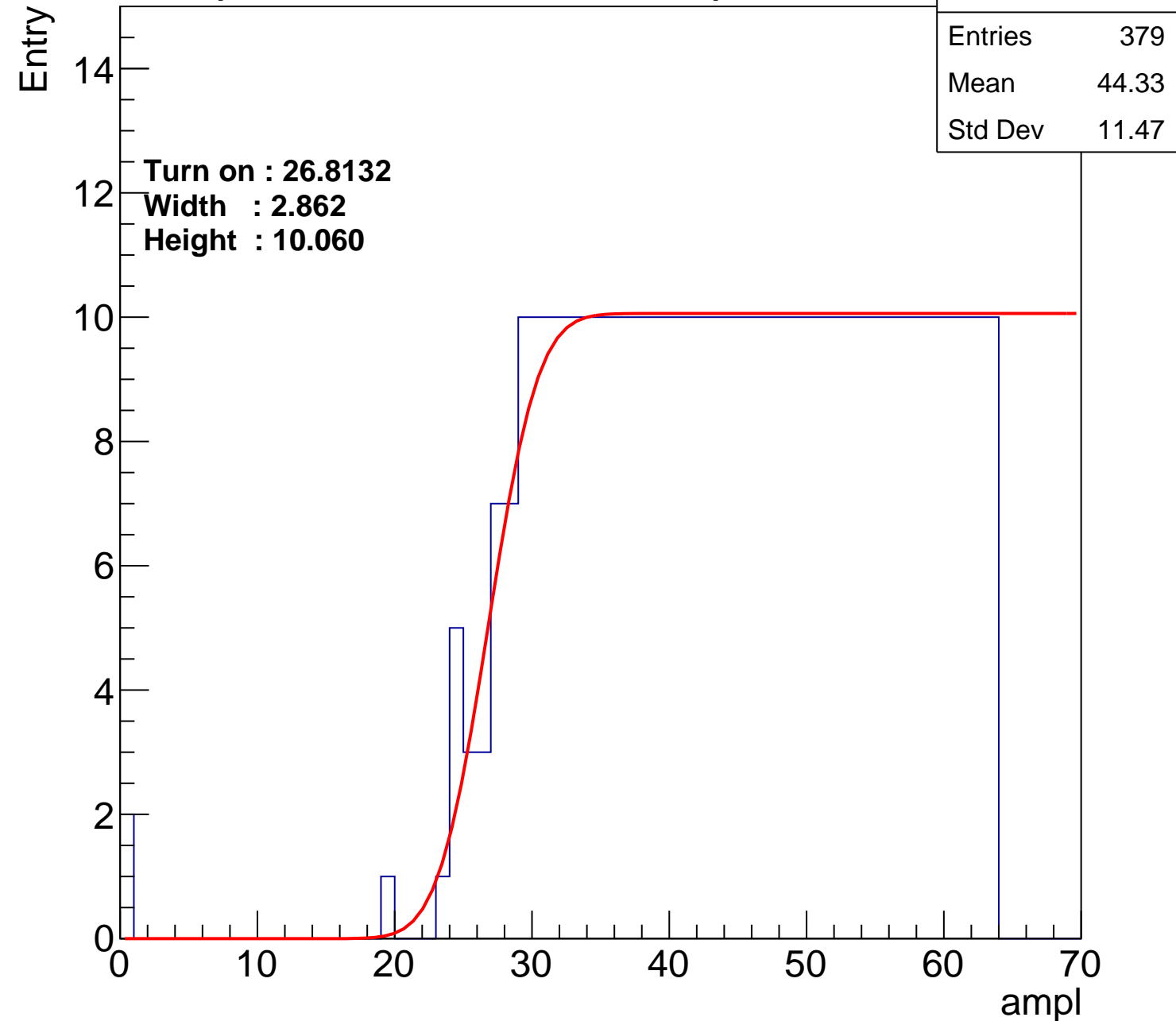
Width : 2.862

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.7896

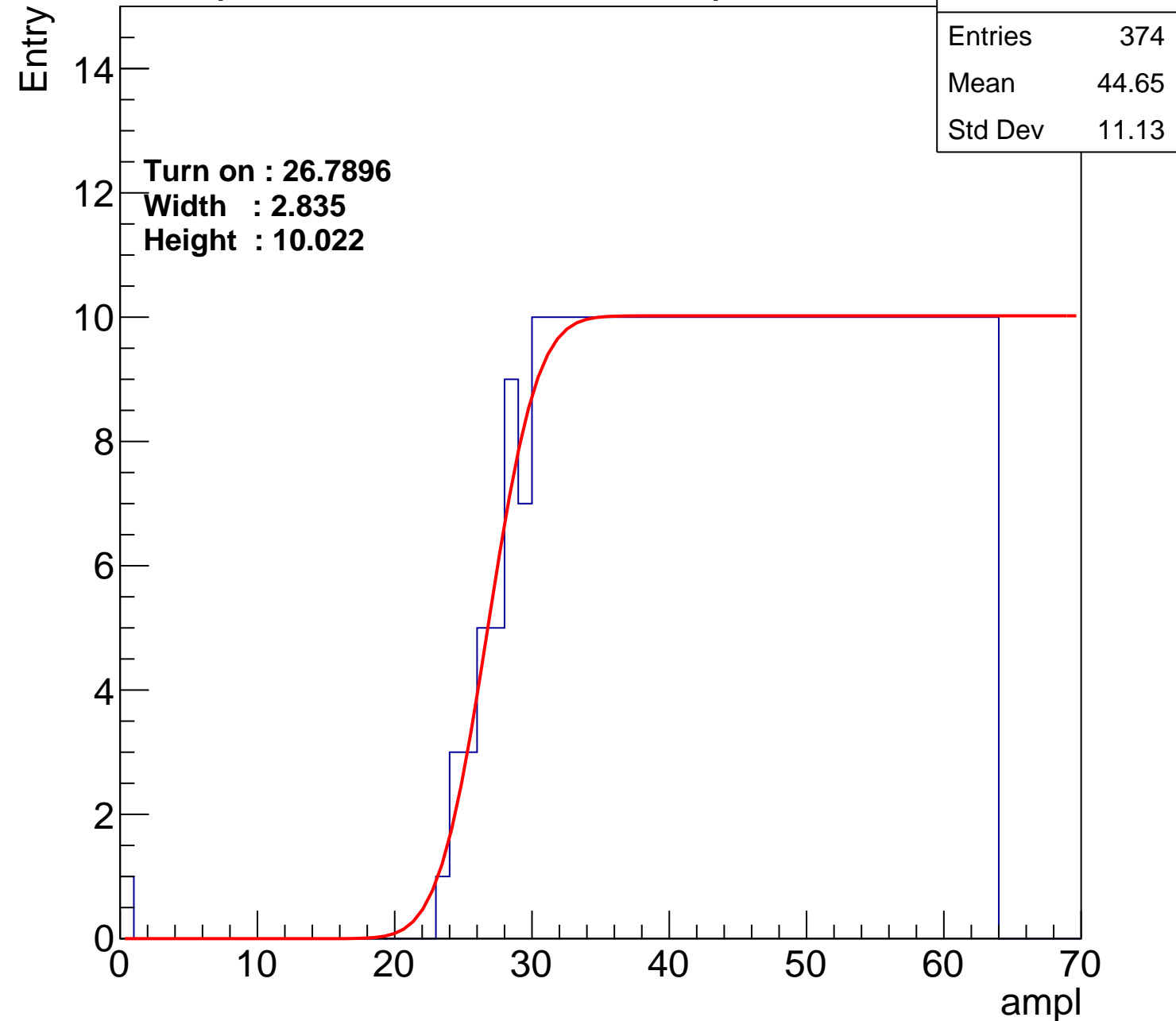
Width : 2.835

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.69
Std Dev	11.85

Turn on : 25.4427

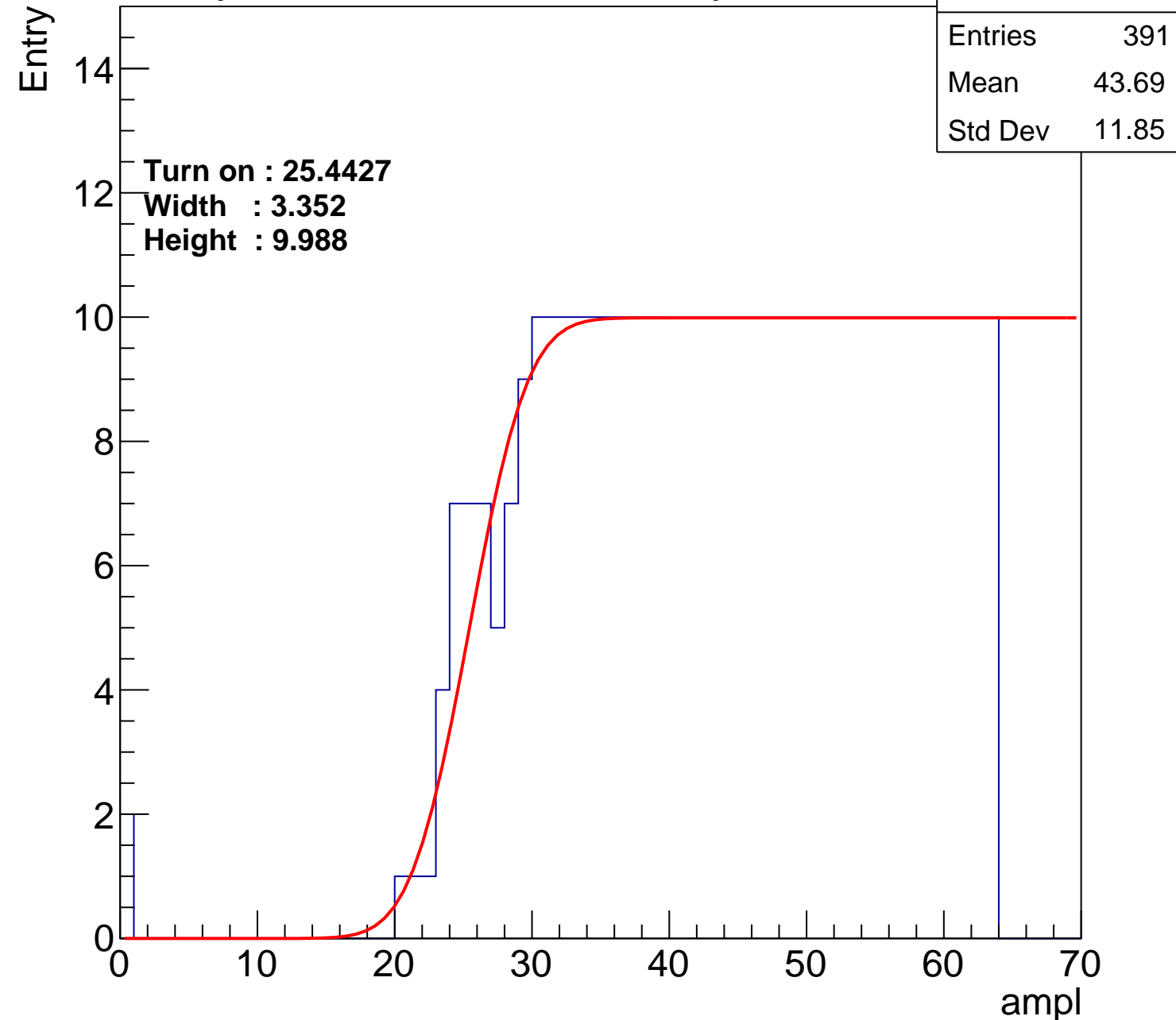
Width : 3.352

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.58
Std Dev	11.33

Turn on : 27.3424

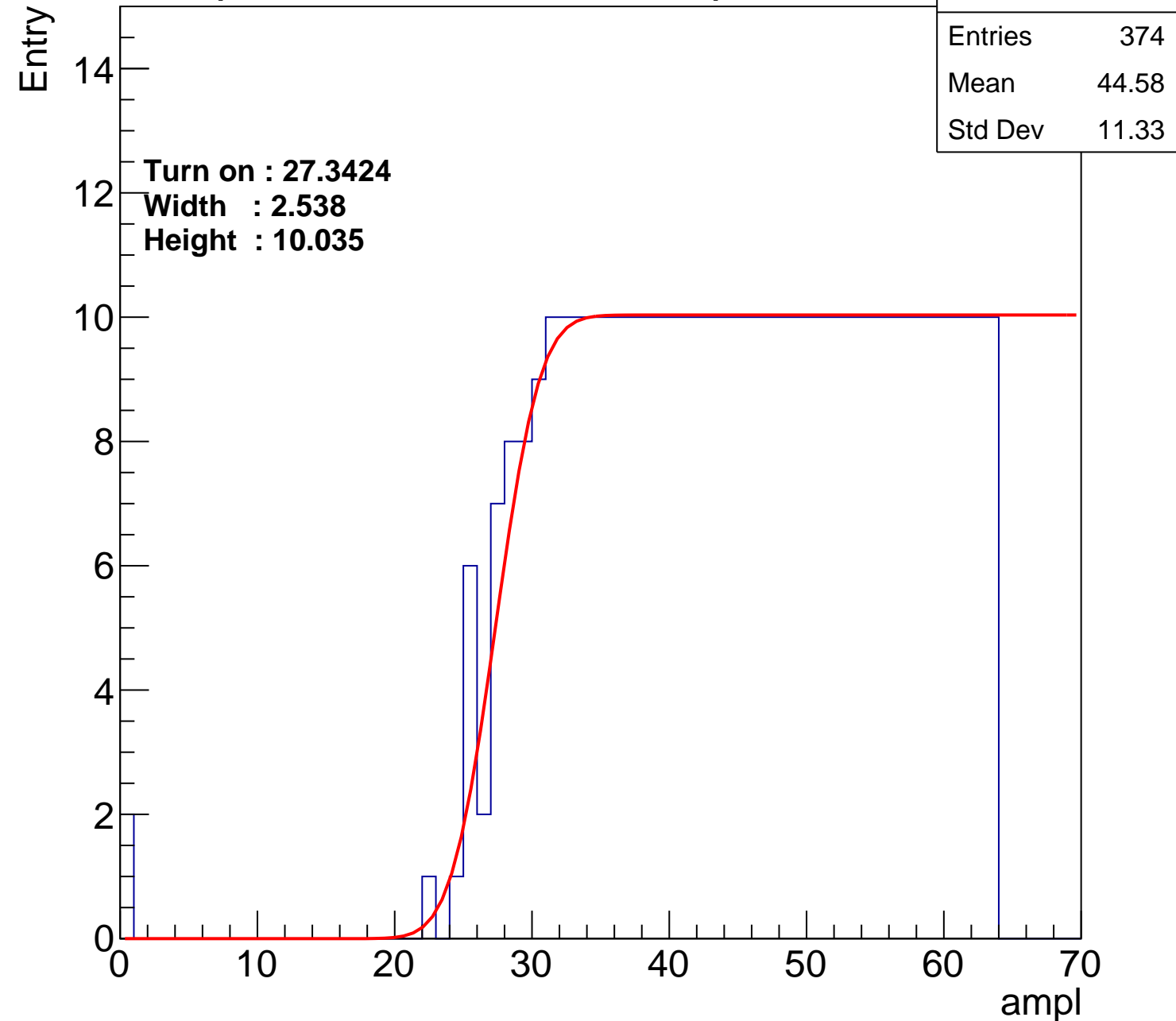
Width : 2.538

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.71
Std Dev	11.75

Turn on : 24.6183

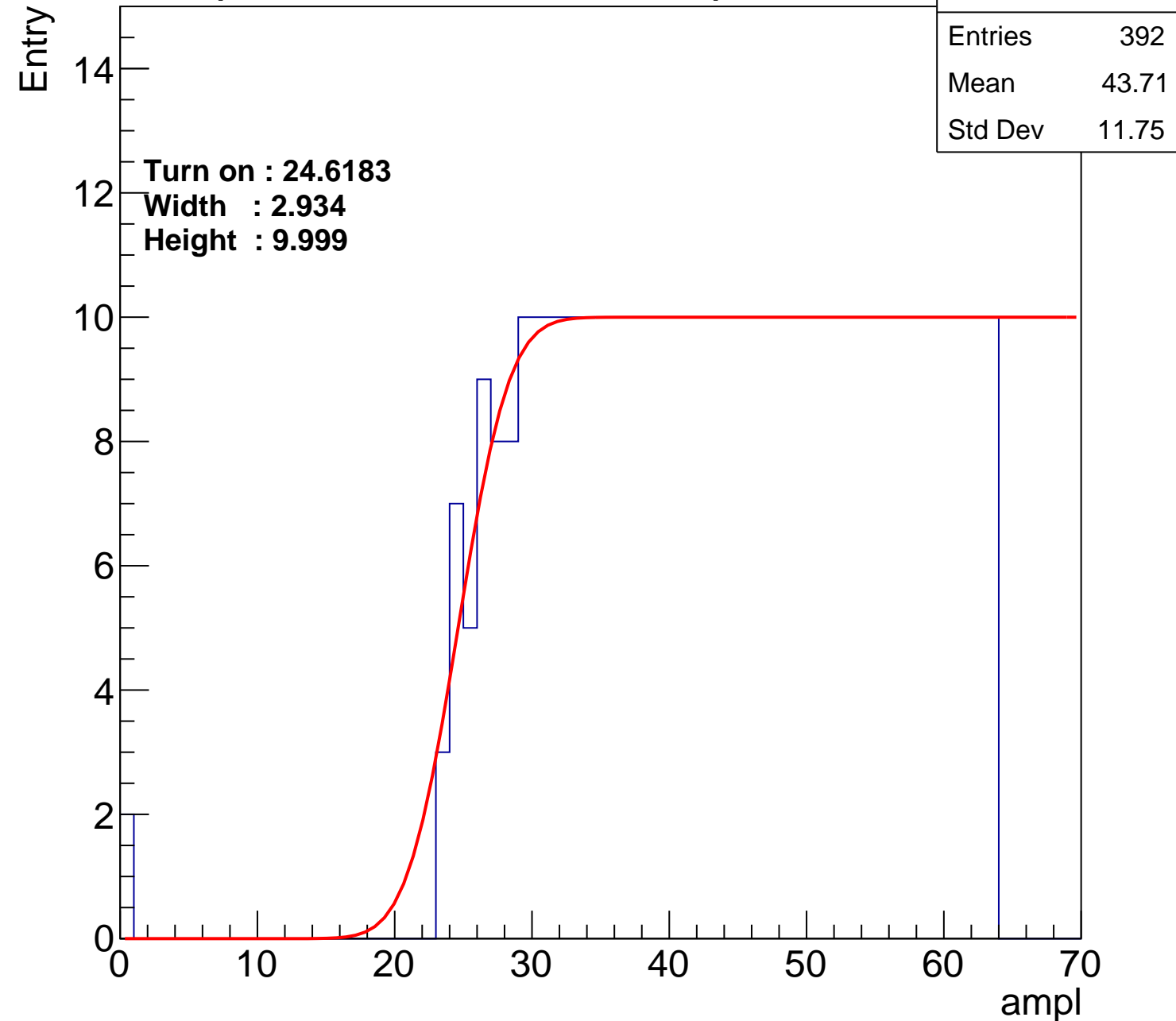
Width : 2.934

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.31
Std Dev	12.37

Turn on : 25.8045

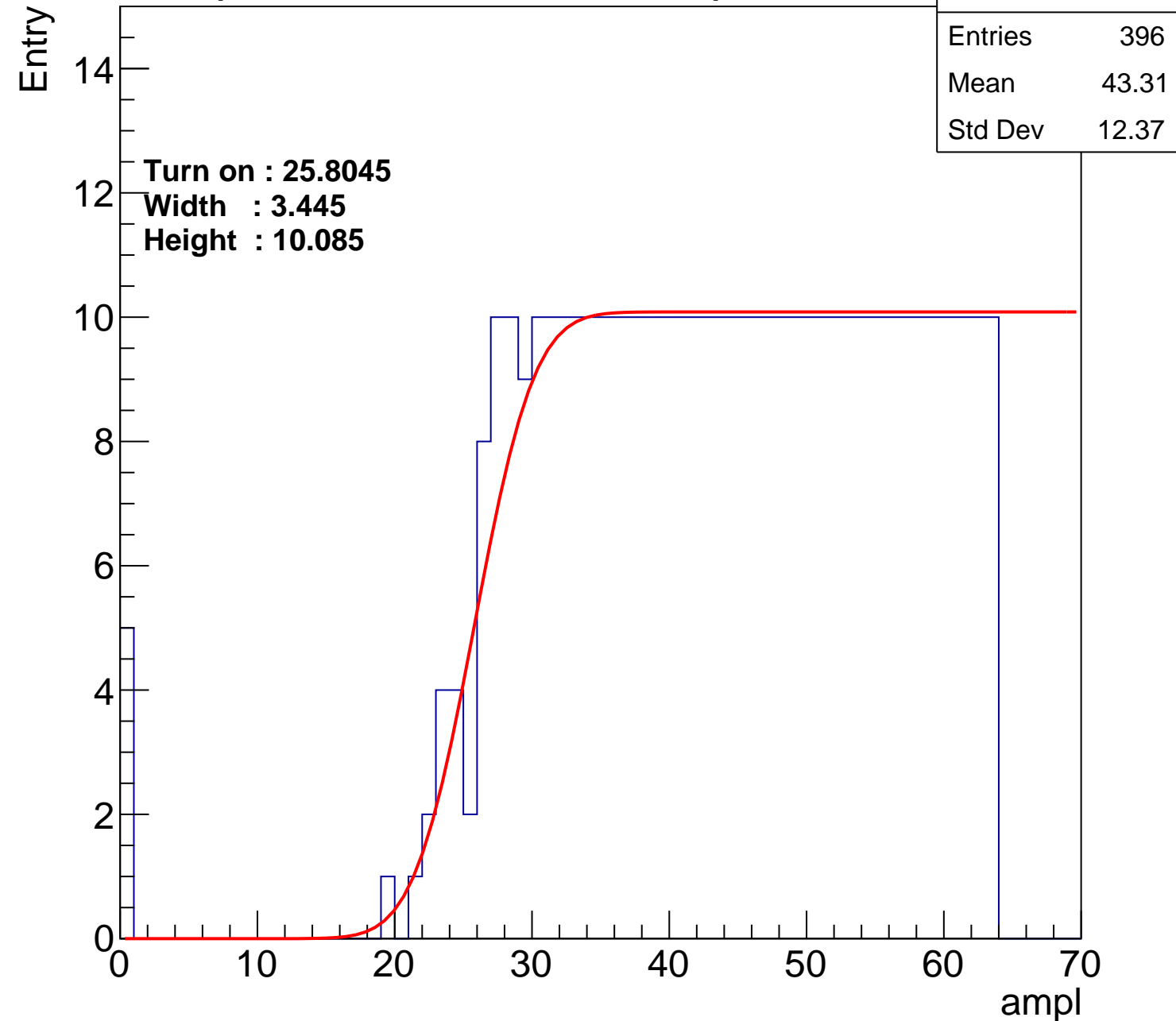
Width : 3.445

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.97
Std Dev	11.71

Turn on : 25.7777

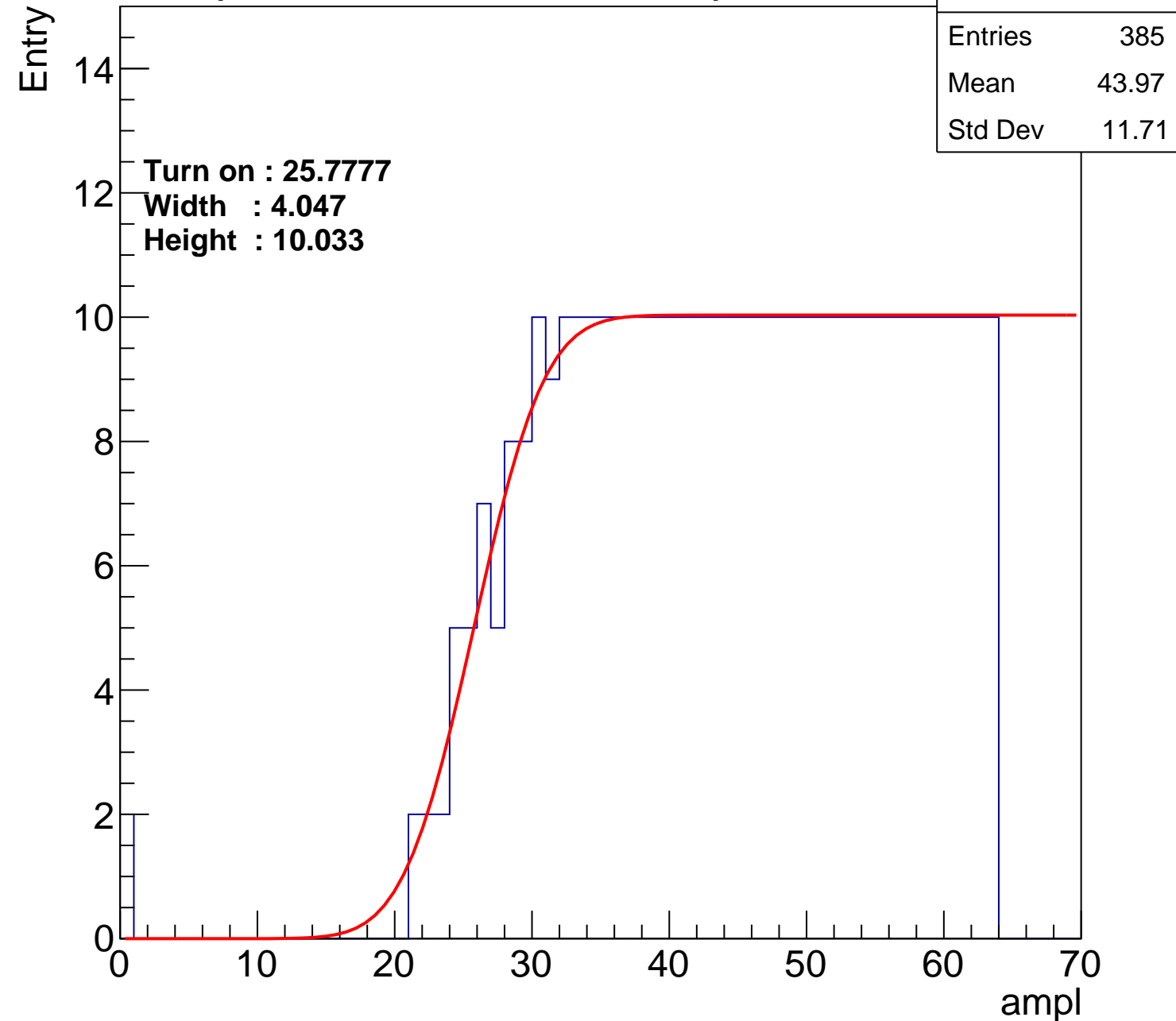
Width : 4.047

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.48
Std Dev	11.39

Turn on : 27.0608

Width : 3.180

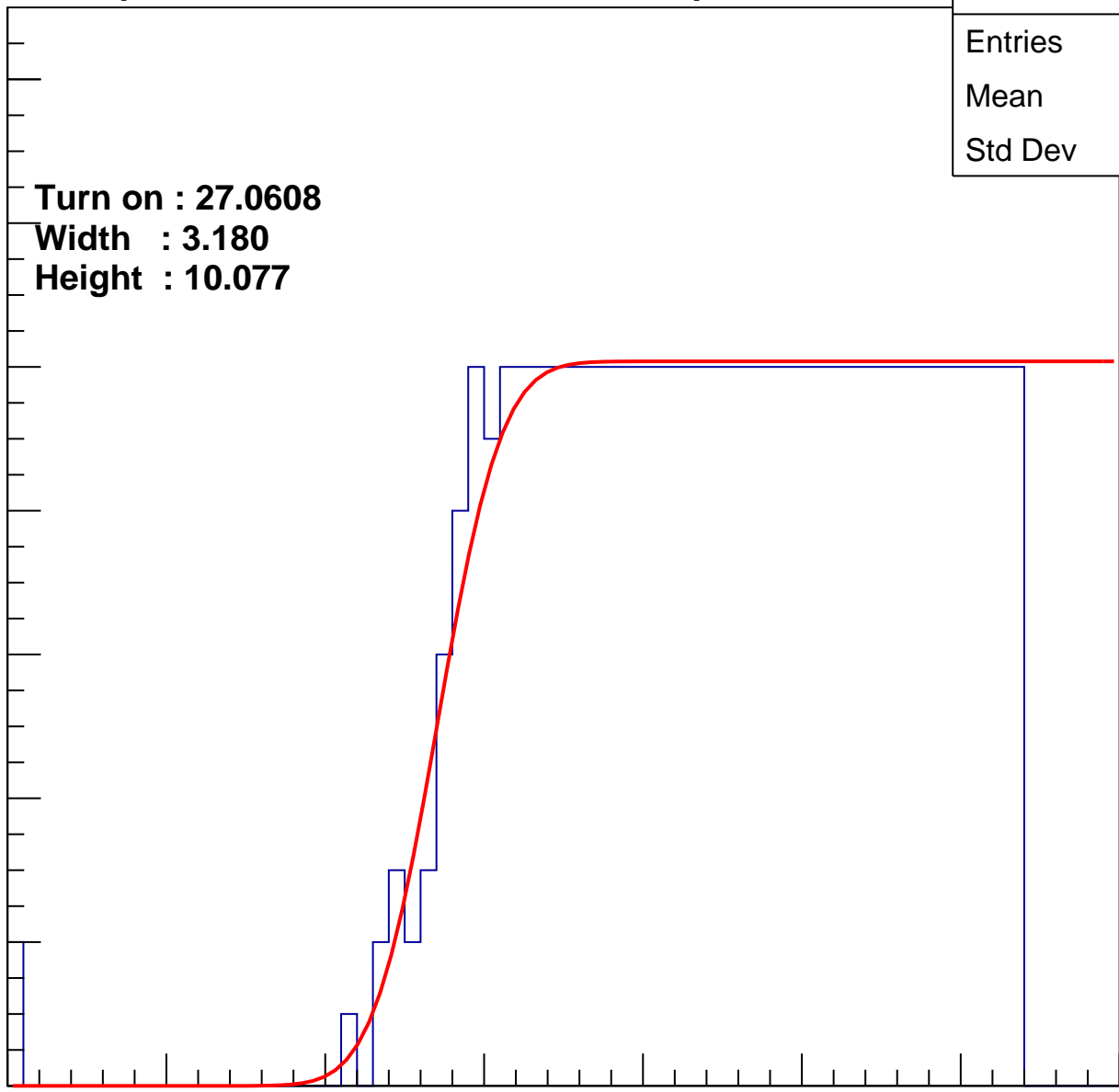
Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U13-ch93

calib_packv5_042523_0143.root, FC#10, port B3

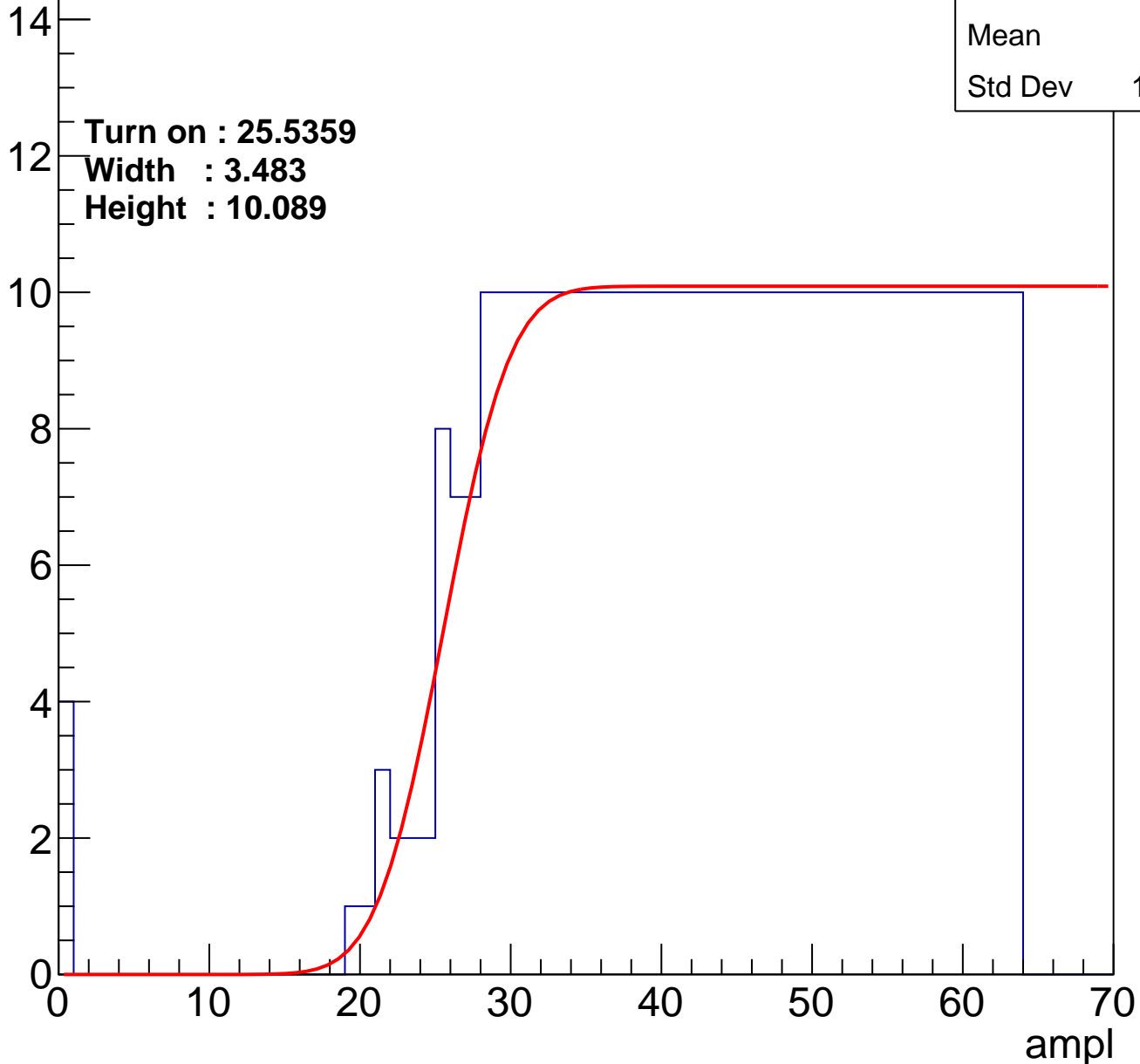
Entries	397
Mean	43.3
Std Dev	12.28

Turn on : 25.5359

Width : 3.483

Height : 10.089

Entry



B1L002S, U13-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.11
Std Dev	11.42

Turn on : 25.9357

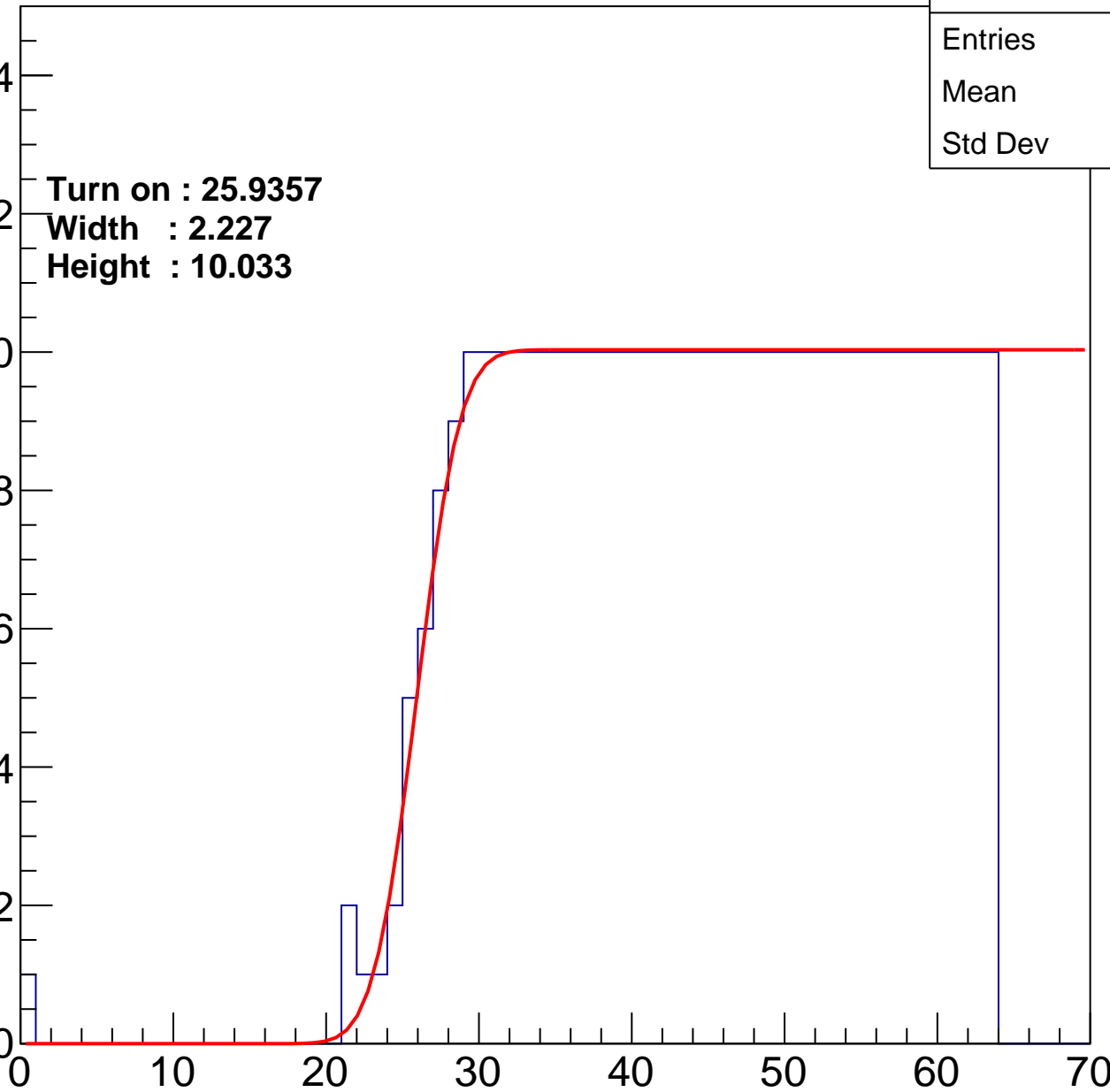
Width : 2.227

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.11
Std Dev	12.48

Turn on : 24.8528

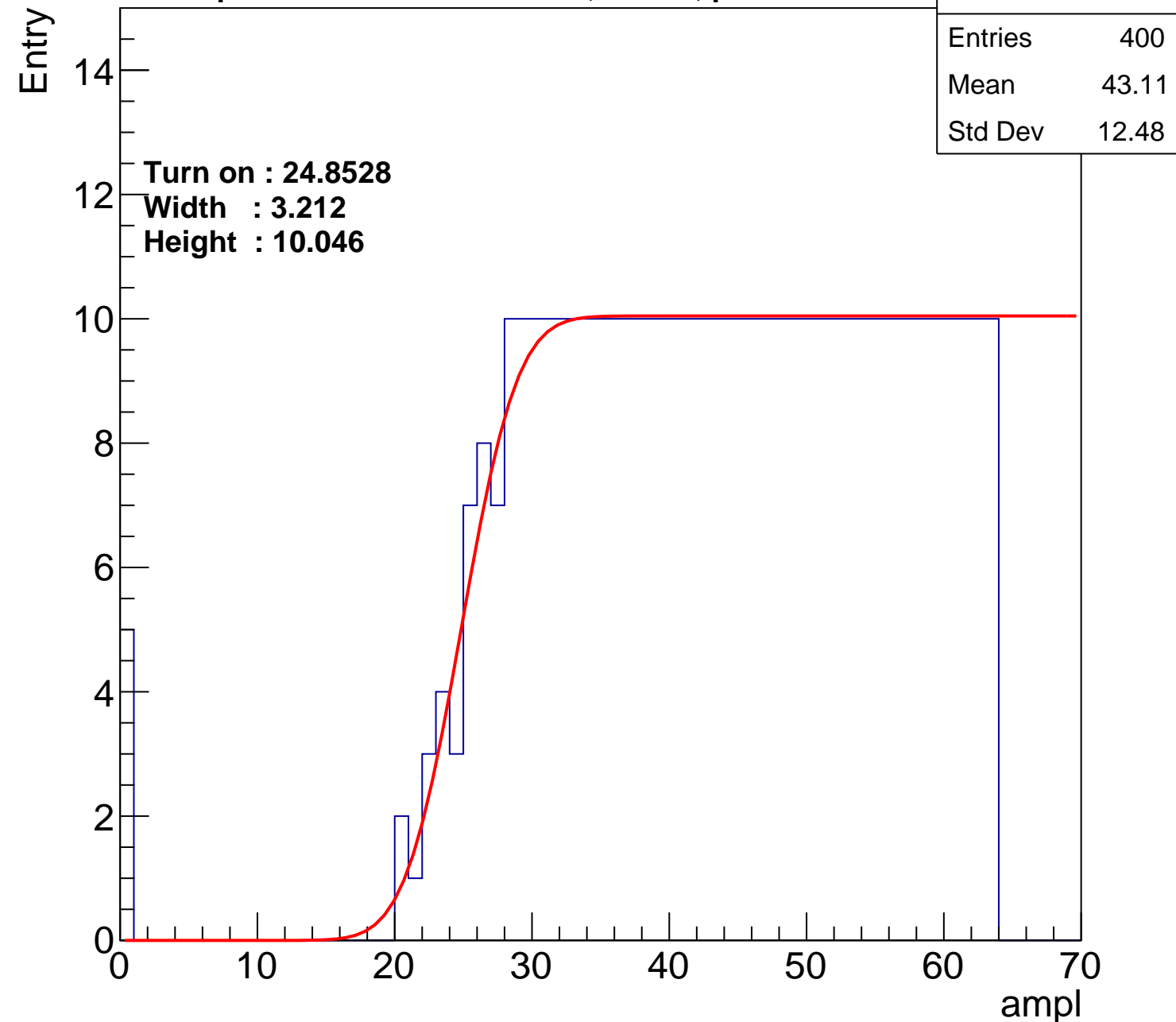
Width : 3.212

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch96

calib_packv5_042523_0143.root, FC#10, port B3

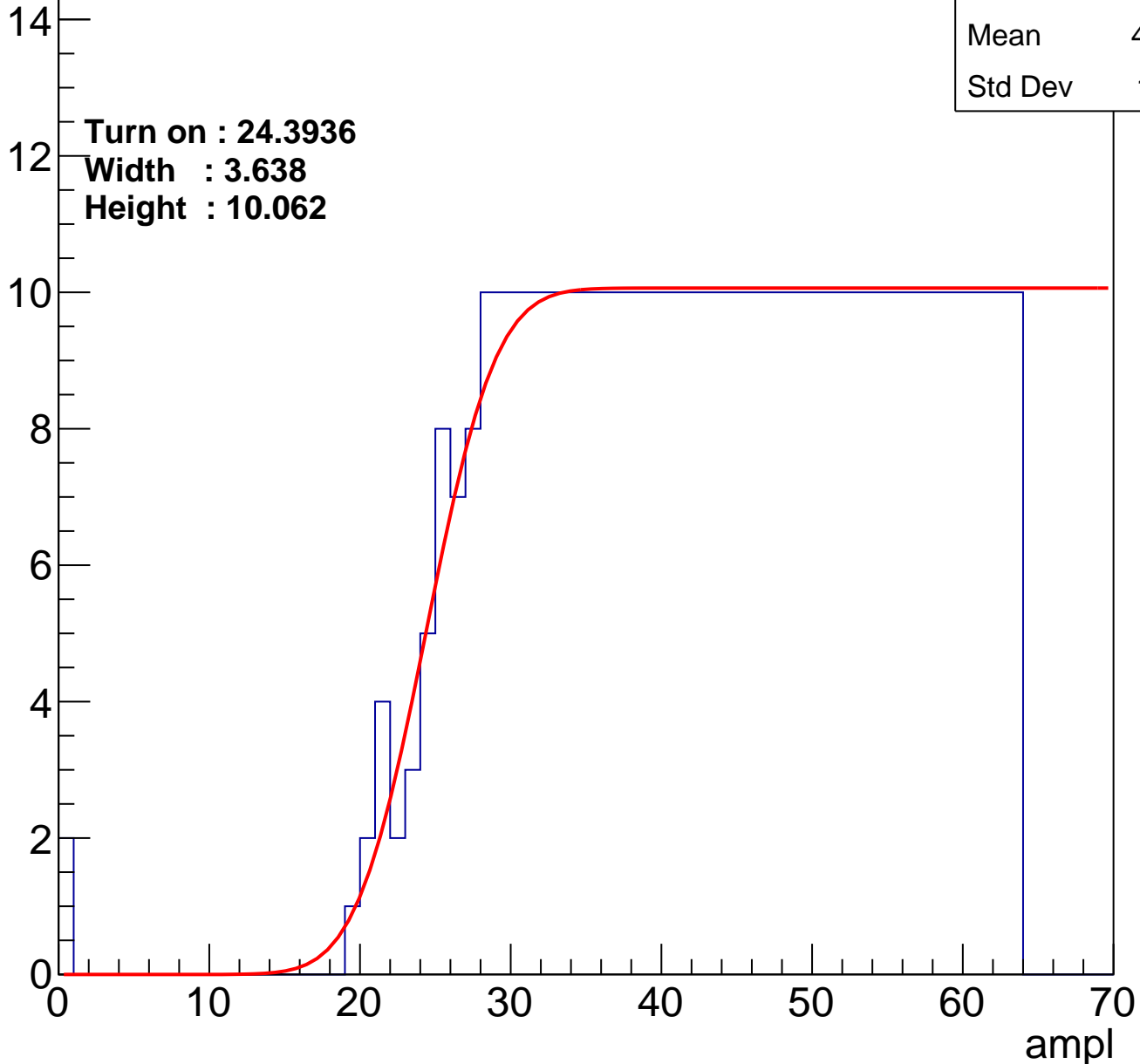
Entries	402
Mean	43.17
Std Dev	12.11

Turn on : 24.3936

Width : 3.638

Height : 10.062

Entry



B1L002S, U13-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.06
Std Dev	12.06

Turn on : 26.8987

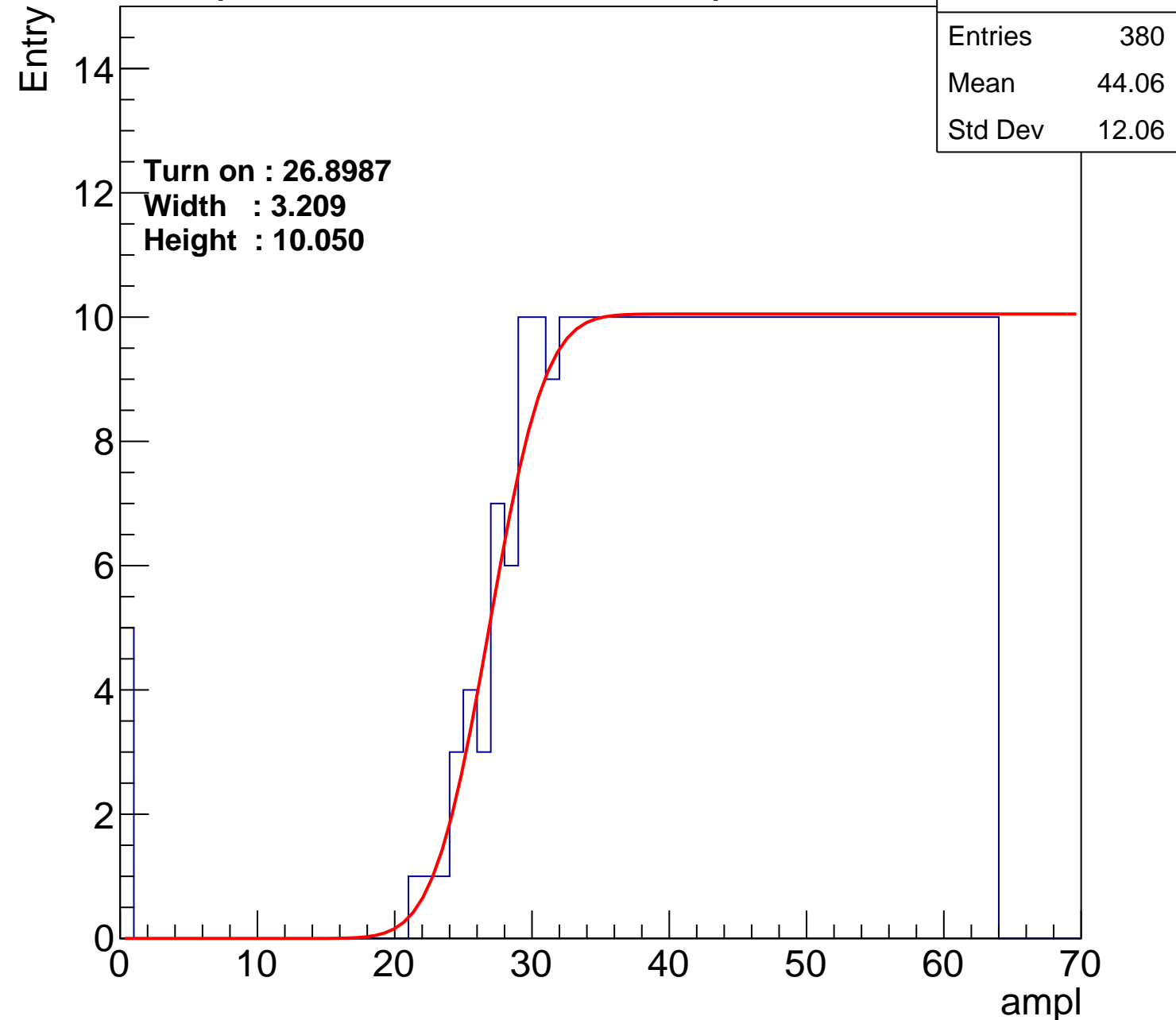
Width : 3.209

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.2507

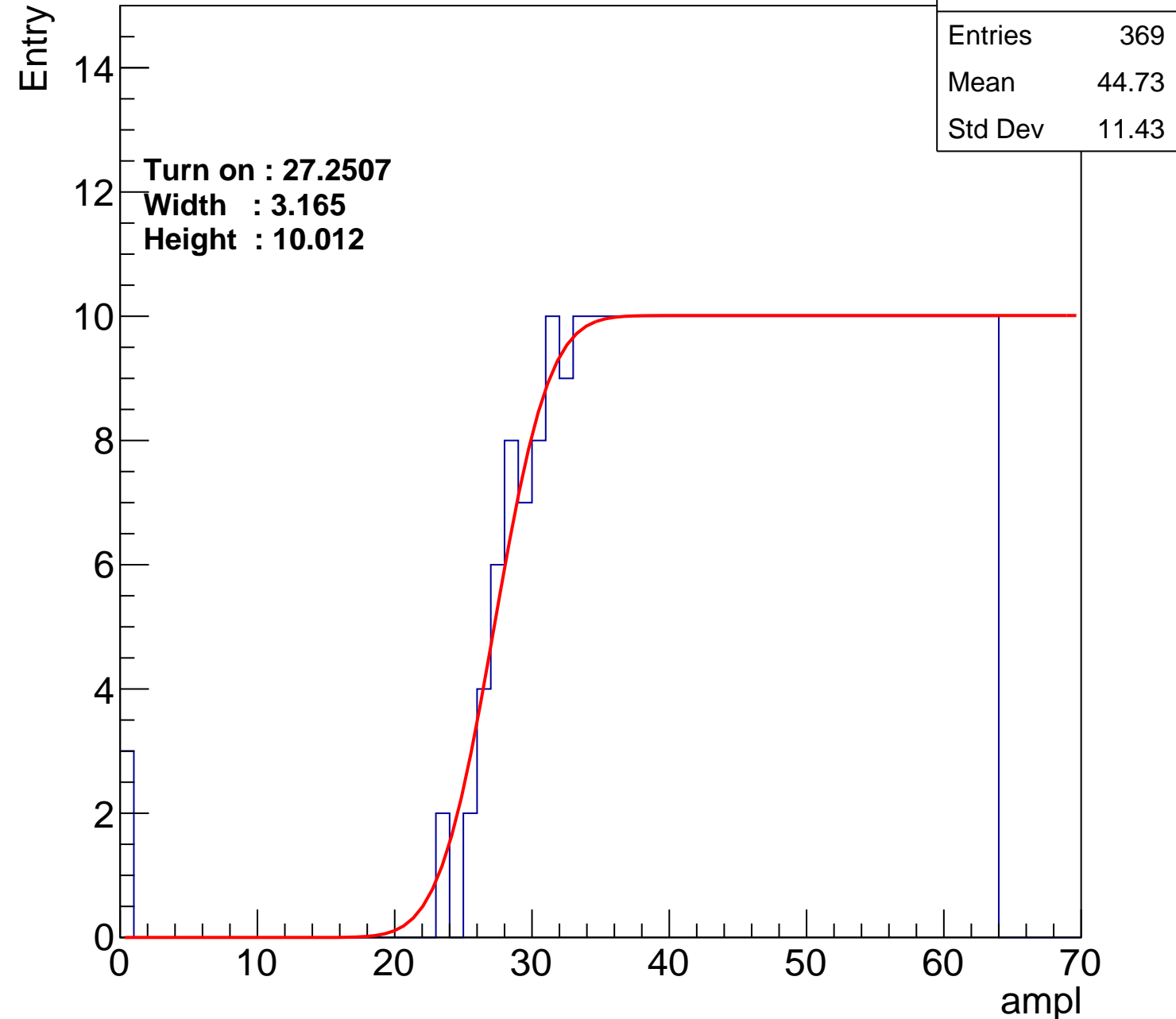
Width : 3.165

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.1
Std Dev	11.61

Turn on : 26.2365

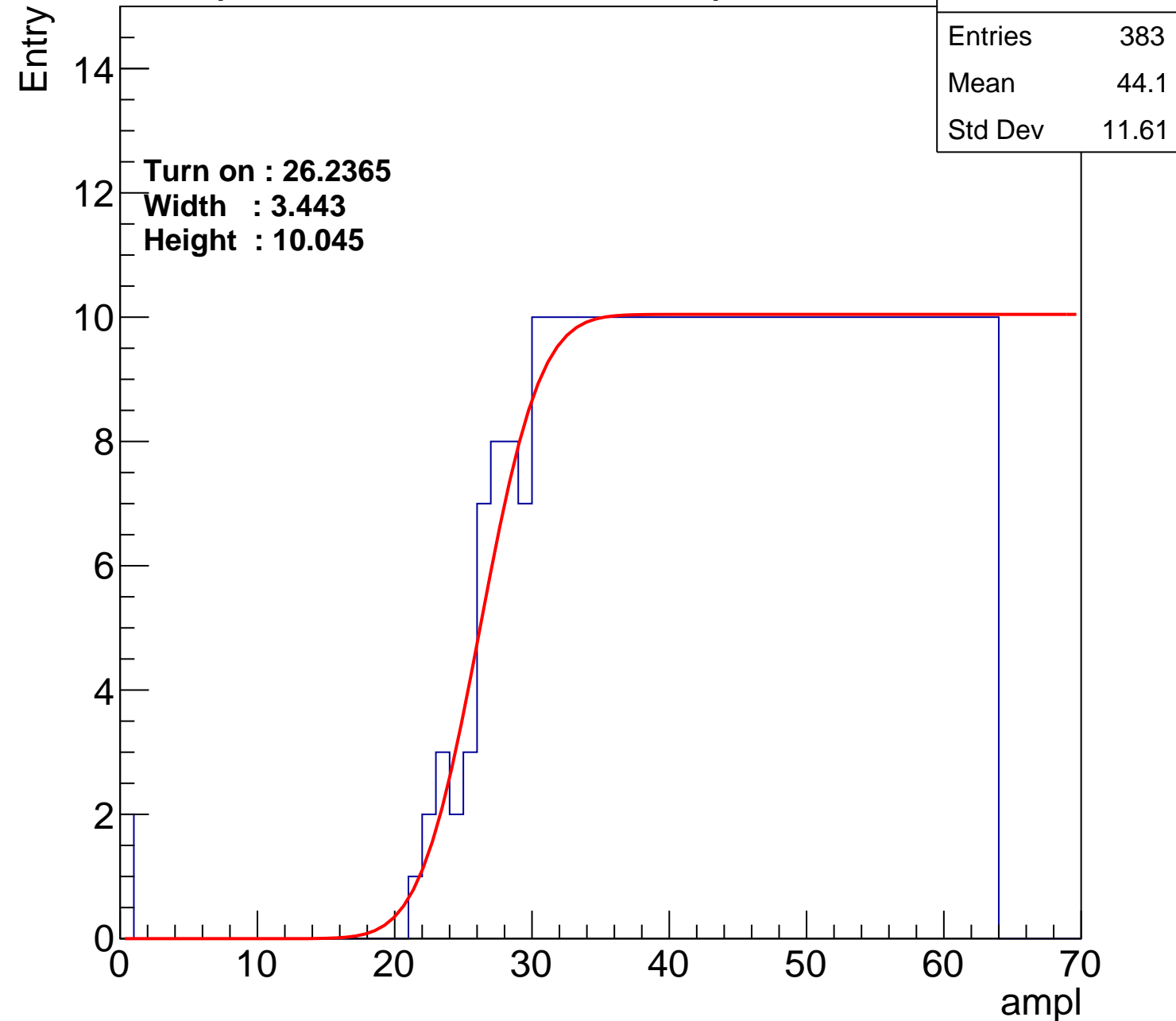
Width : 3.443

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch100

calib_packv5_042523_0143.root, FC#10, port B3

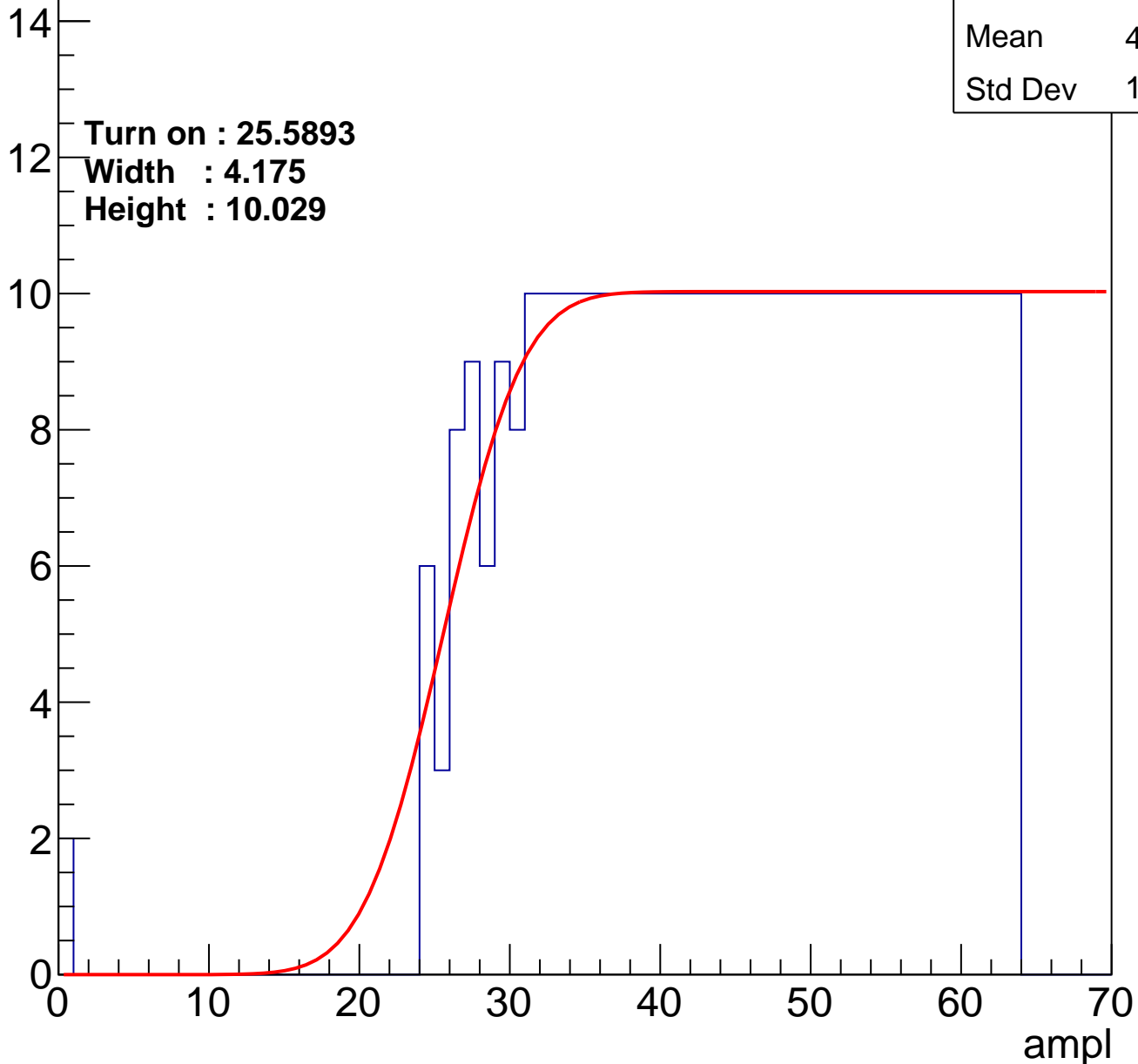
Entries	381
Mean	44.22
Std Dev	11.52

Turn on : 25.5893

Width : 4.175

Height : 10.029

Entry



B1L002S, U13-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.54

Turn on : 25.6790

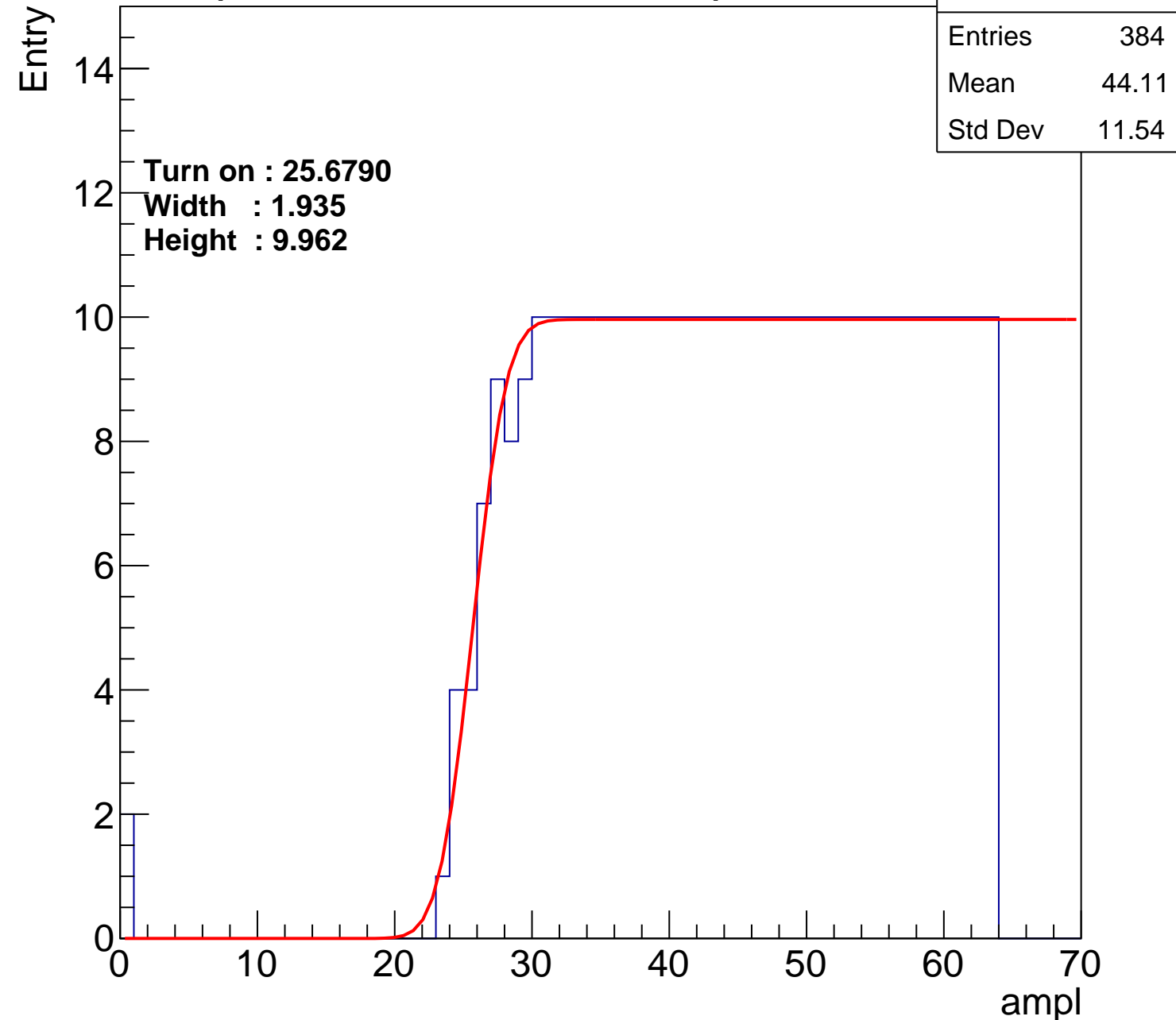
Width : 1.935

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch102

calib_packv5_042523_0143.root, FC#10, port B3

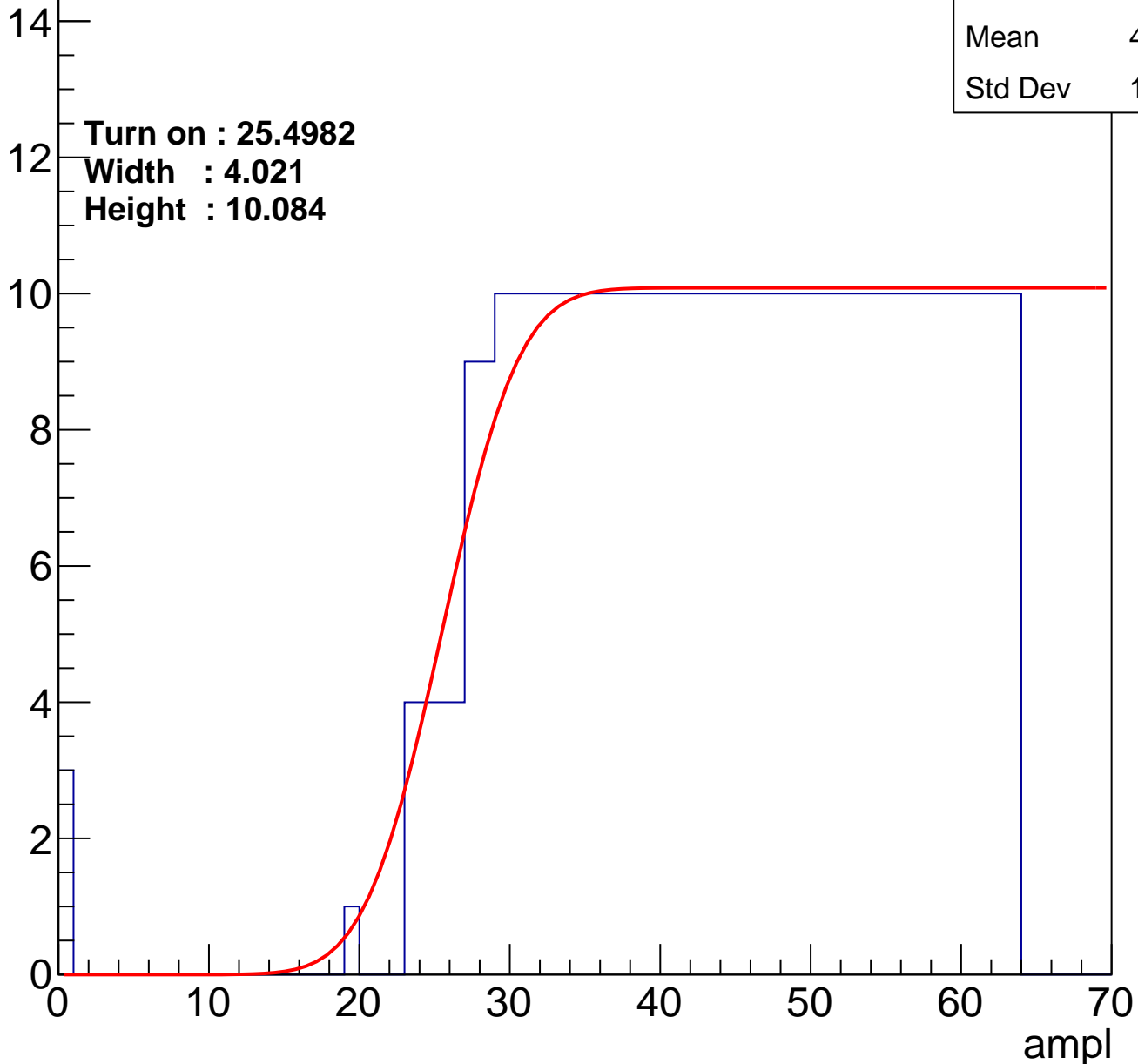
Entries	388
Mean	43.83
Std Dev	11.86

Turn on : 25.4982

Width : 4.021

Height : 10.084

Entry



B1L002S, U13-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.78
Std Dev	11.88

Turn on : 25.5680

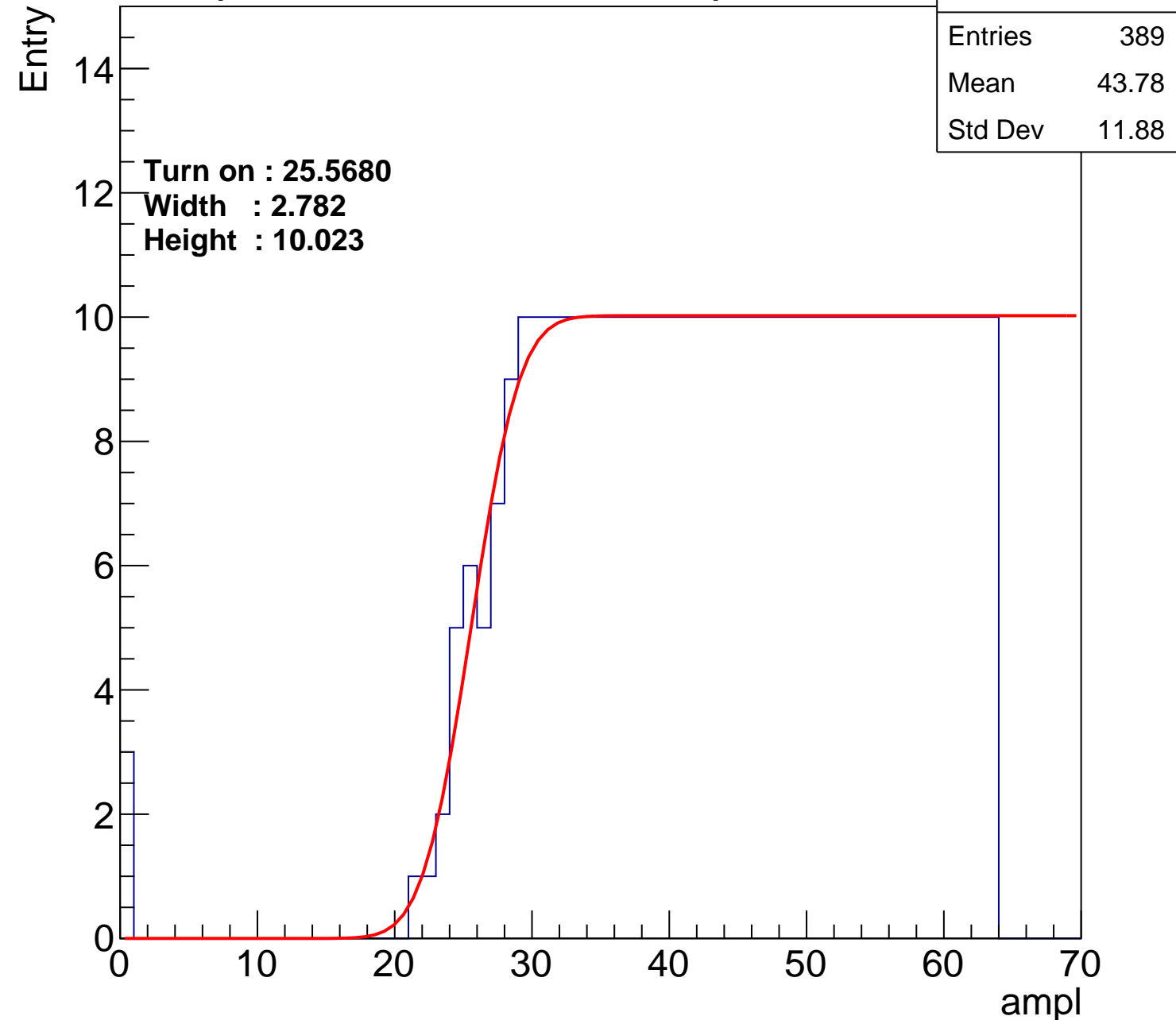
Width : 2.782

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch104

calib_packv5_042523_0143.root, FC#10, port B3

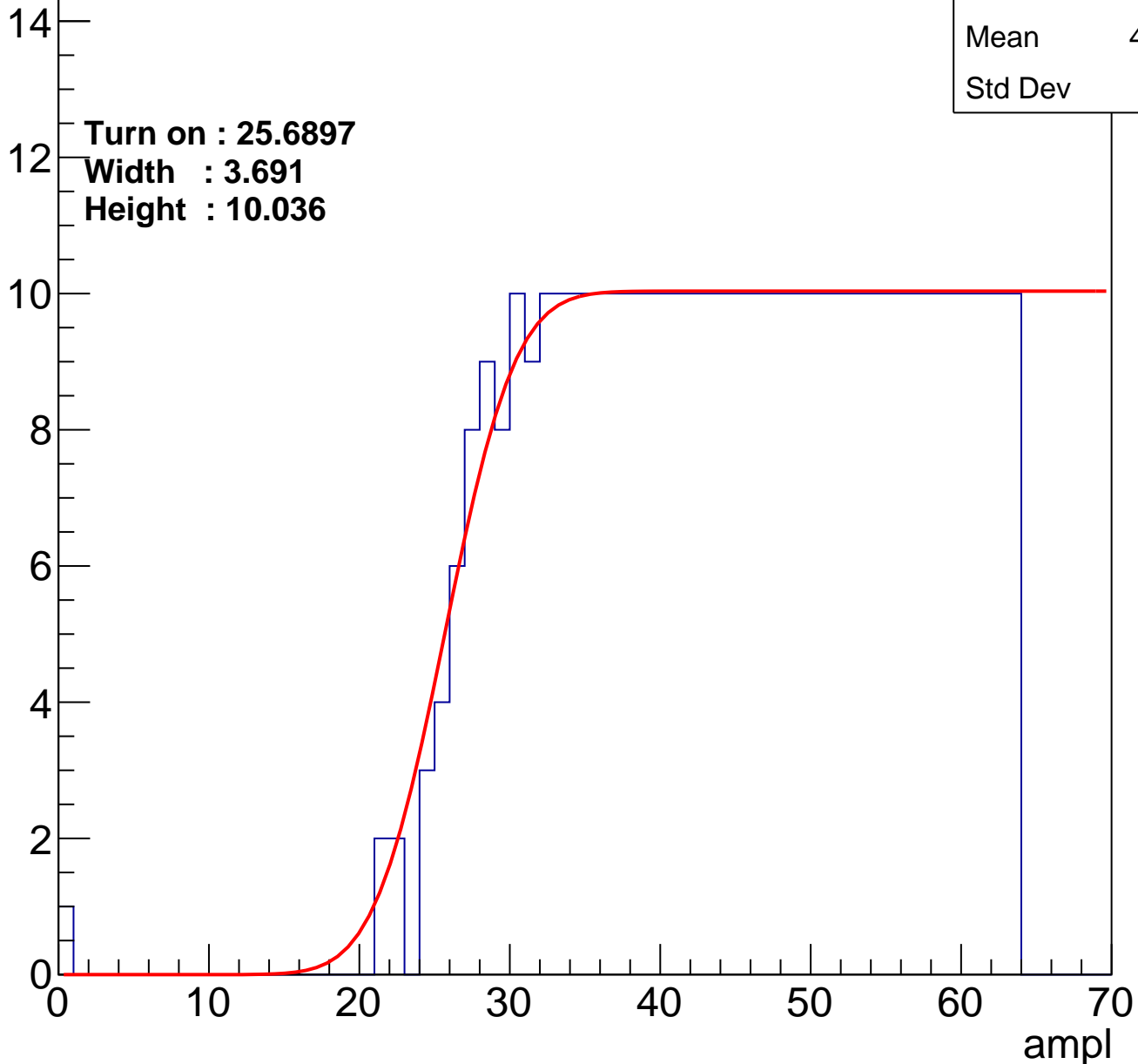
Entries	382
Mean	44.22
Std Dev	11.4

Turn on : 25.6897

Width : 3.691

Height : 10.036

Entry



B1L002S, U13-ch105

calib_packv5_042523_0143.root, FC#10, port B3

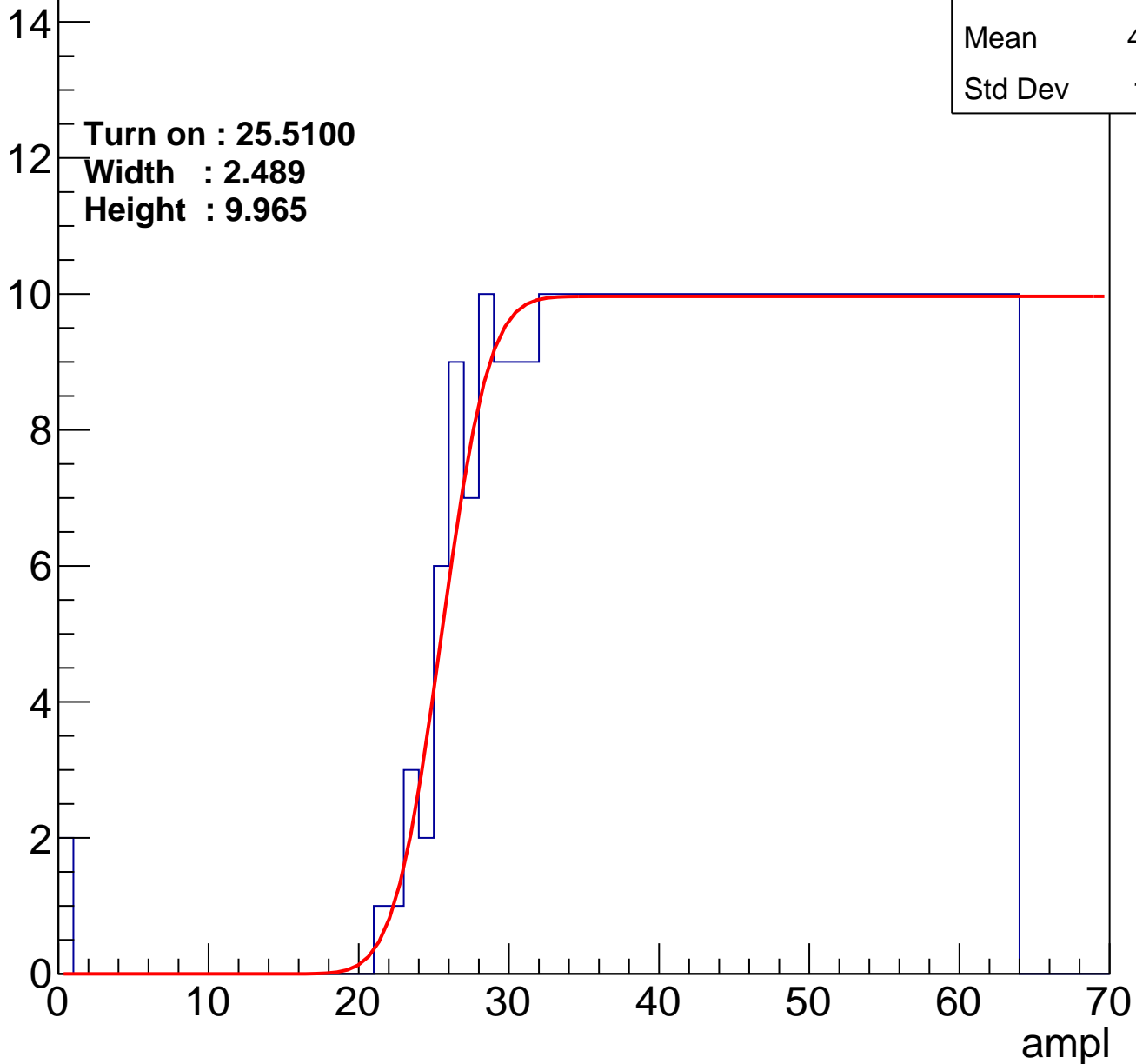
Entries	388
Mean	43.87
Std Dev	11.71

Turn on : 25.5100

Width : 2.489

Height : 9.965

Entry



B1L002S, U13-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.43
Std Dev	12.29

Turn on : 25.2710

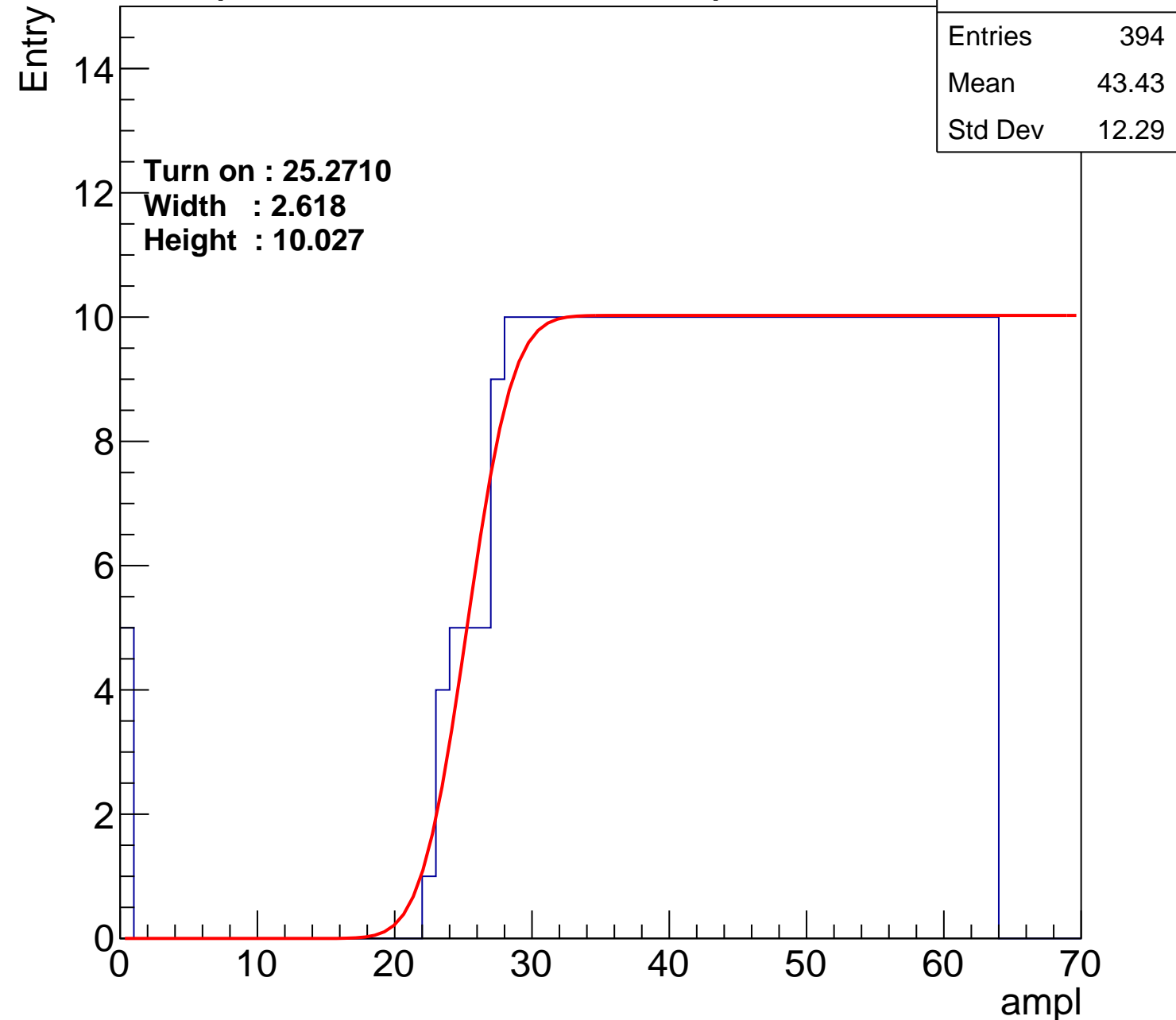
Width : 2.618

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	416
Mean	42.48
Std Dev	12.51

Turn on : 22.7077

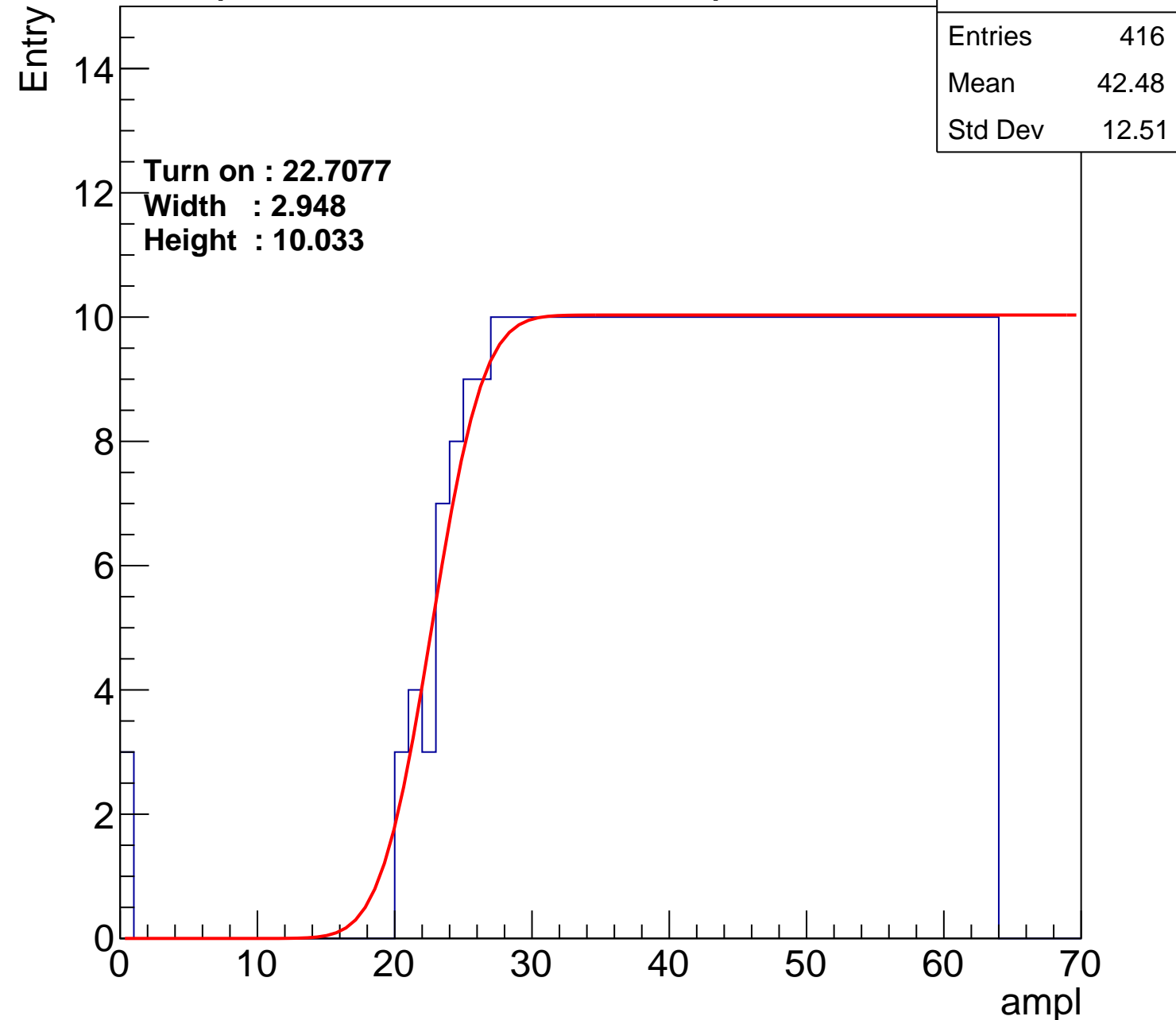
Width : 2.948

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.23
Std Dev	12.05

Turn on : 24.3206

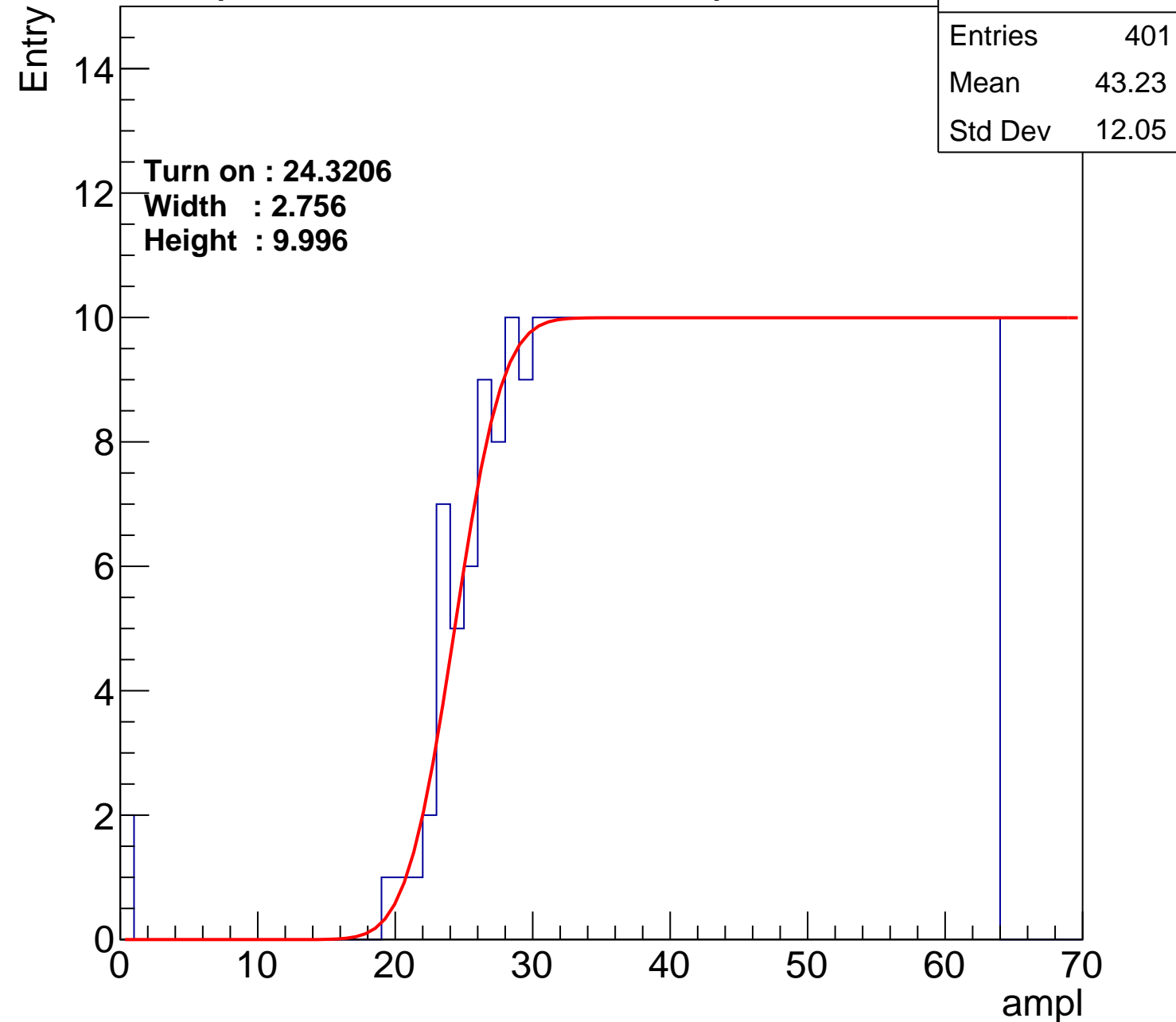
Width : 2.756

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.32
Std Dev	12.13

Turn on : 23.8855

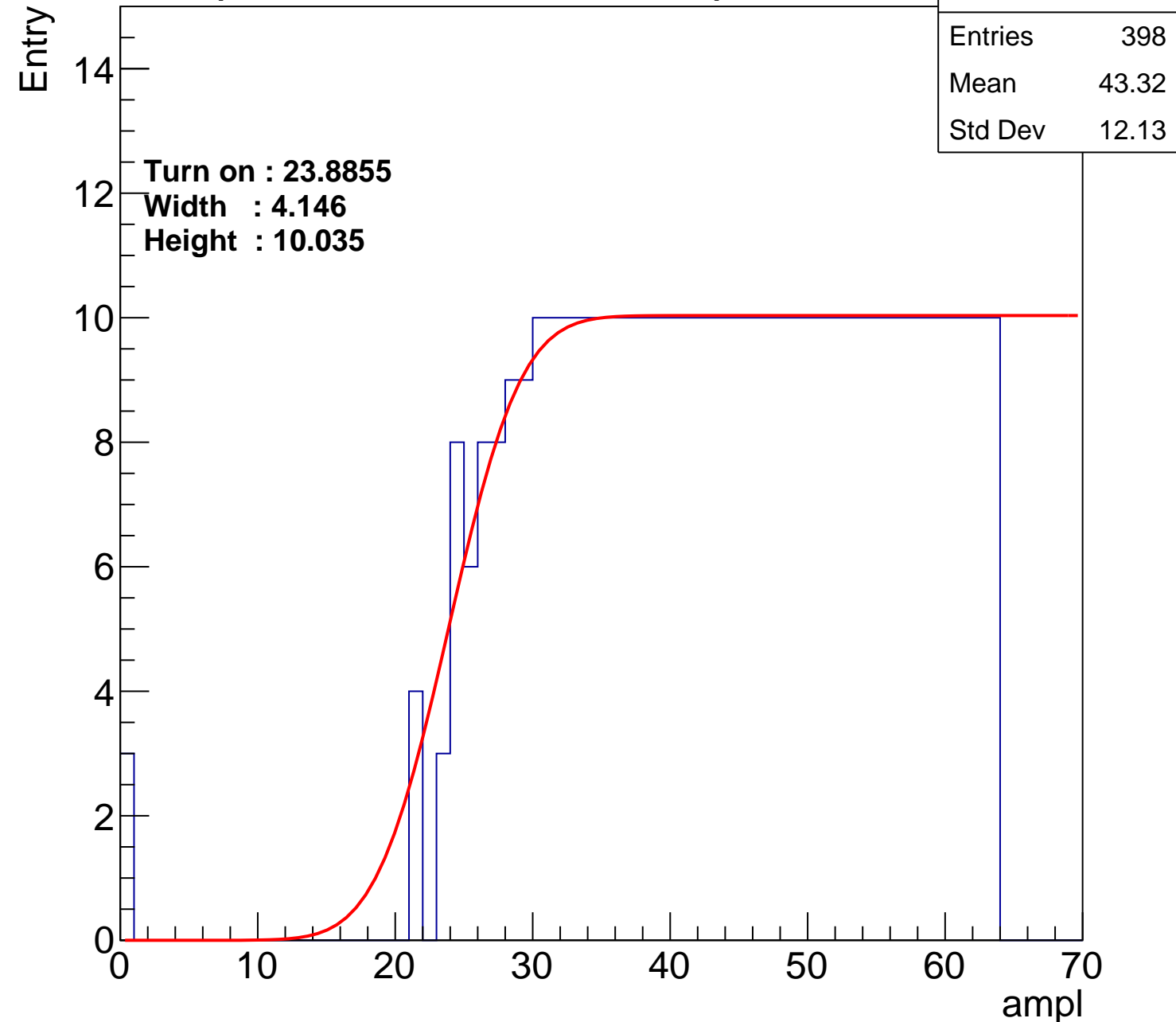
Width : 4.146

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	412
Mean	42.73
Std Dev	12.27

Turn on : 22.9696

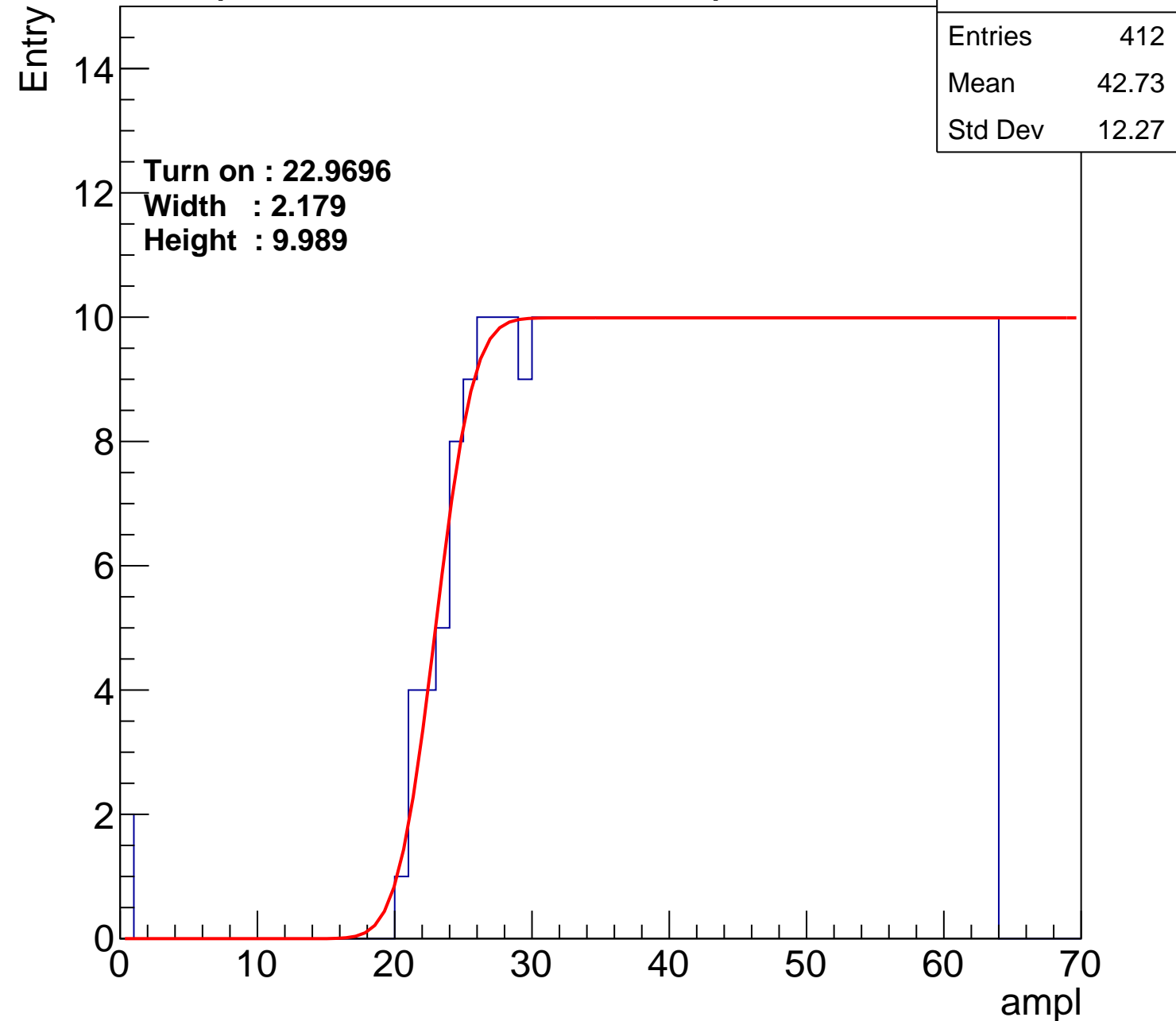
Width : 2.179

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.77
Std Dev	11.89

Turn on : 25.2688

Width : 2.623

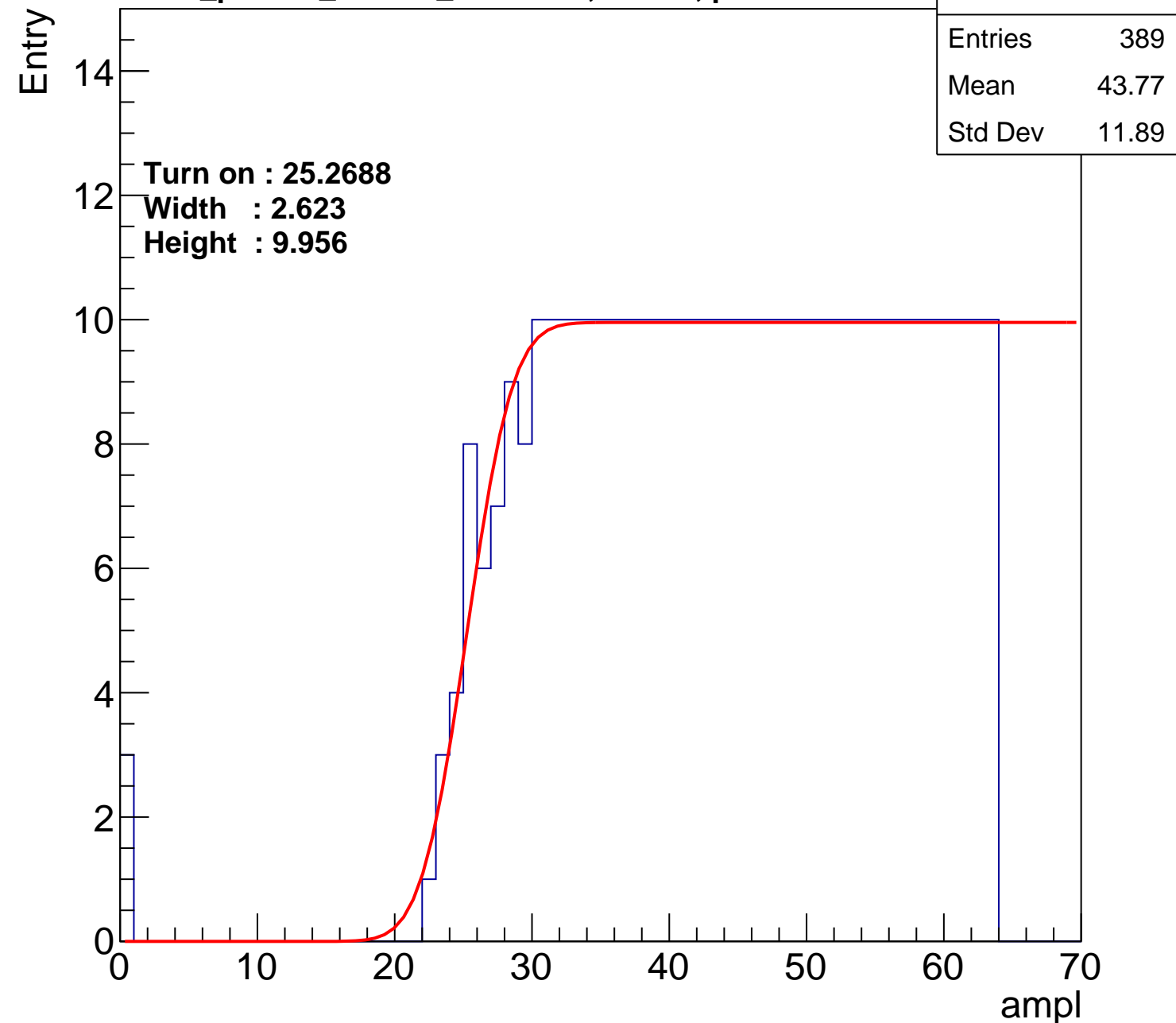
Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U13-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.27
Std Dev	12.04

Turn on : 24.3229

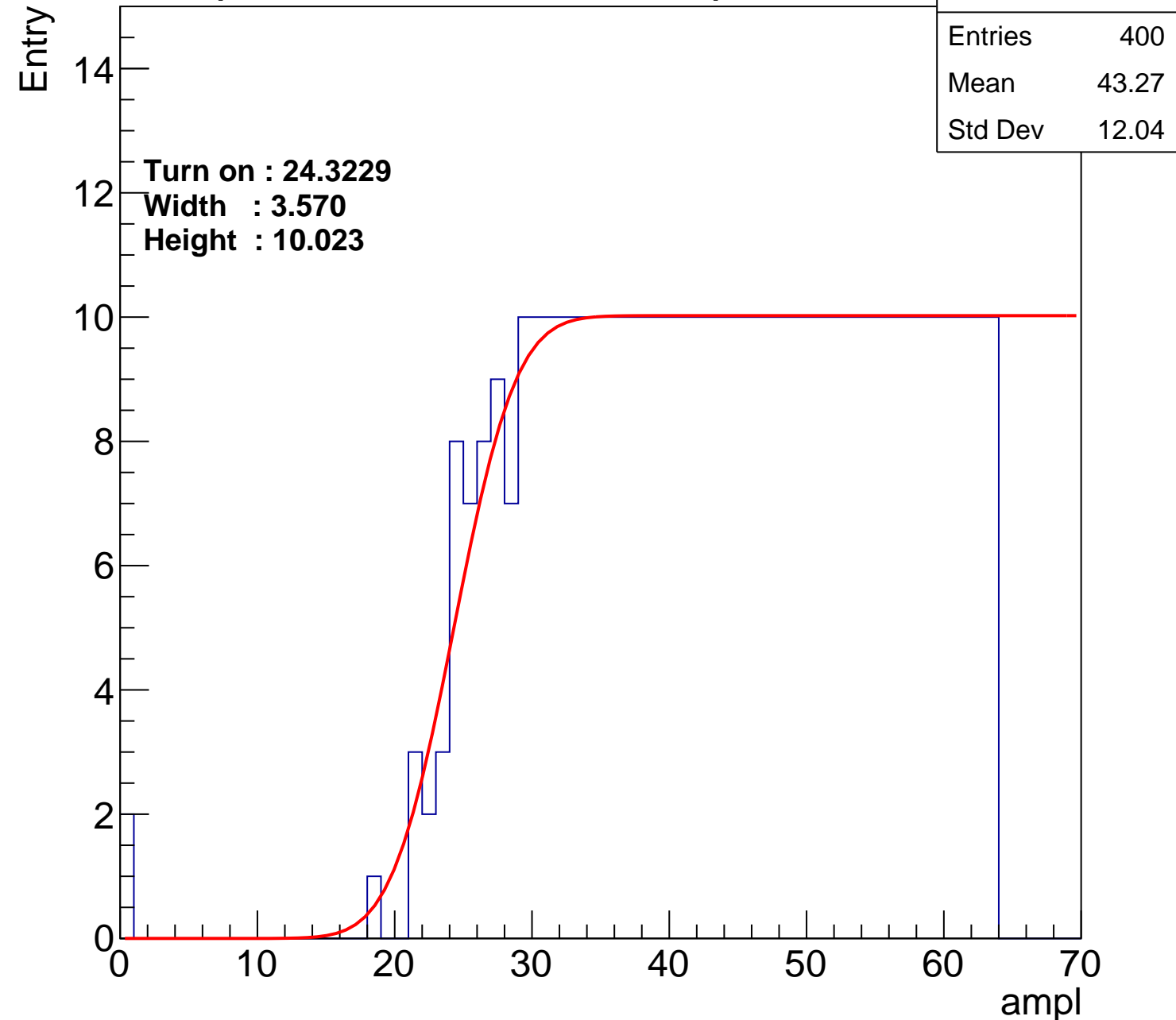
Width : 3.570

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	43.97
Std Dev	12.08

Turn on : 26.3515

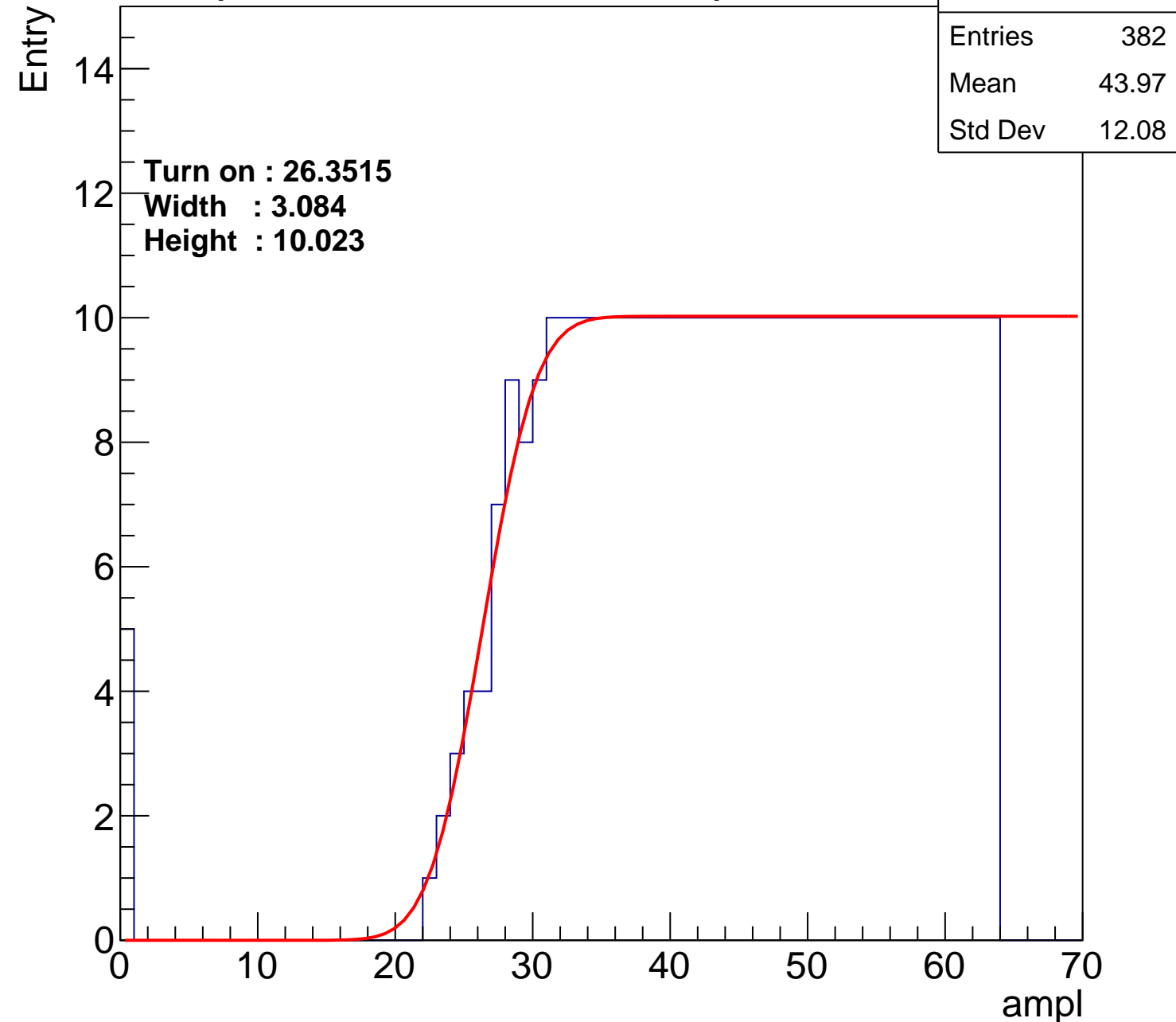
Width : 3.084

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.44
Std Dev	11.27

Turn on : 27.0969

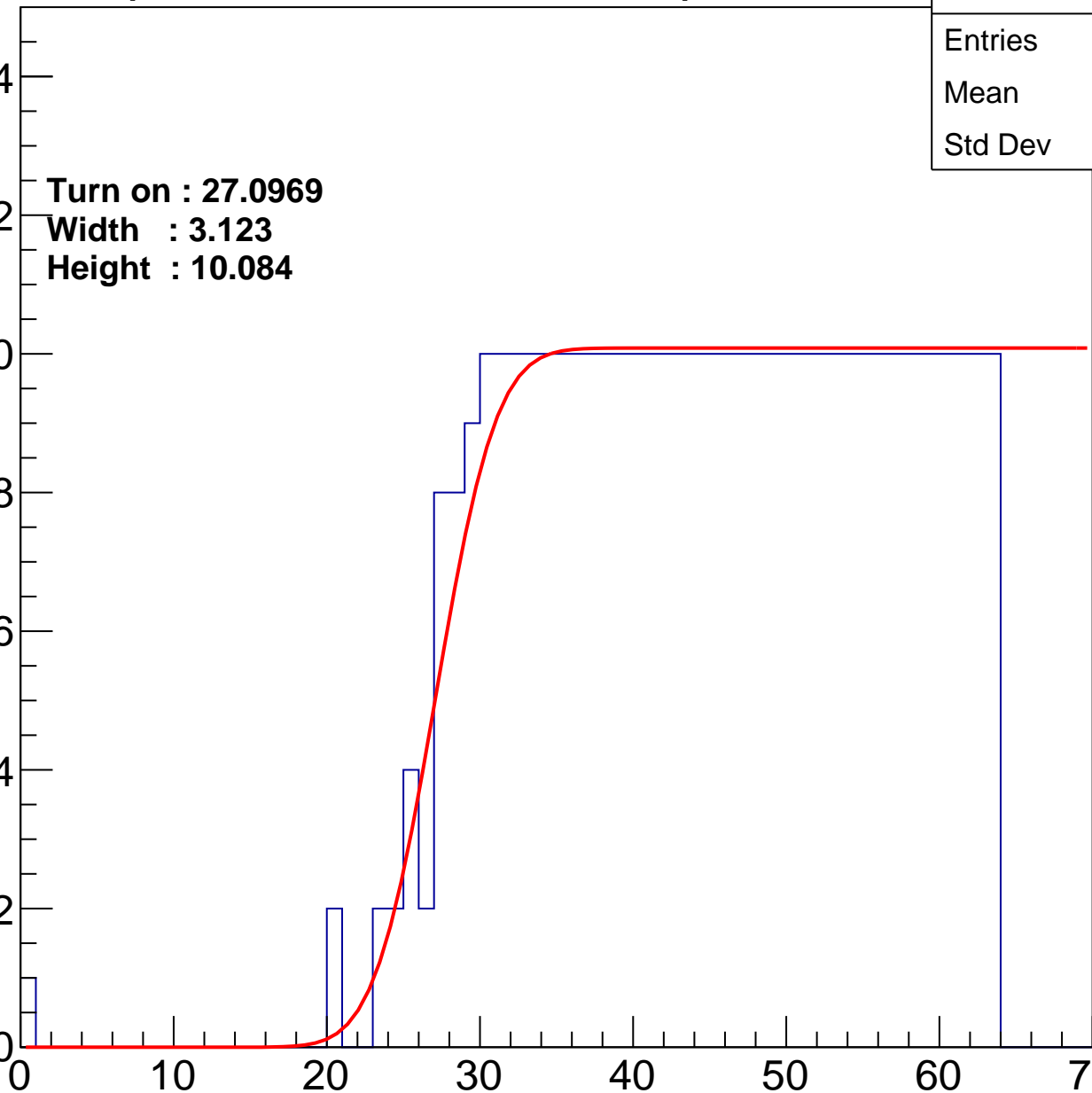
Width : 3.123

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.91
Std Dev	11.65

Turn on : 25.3589

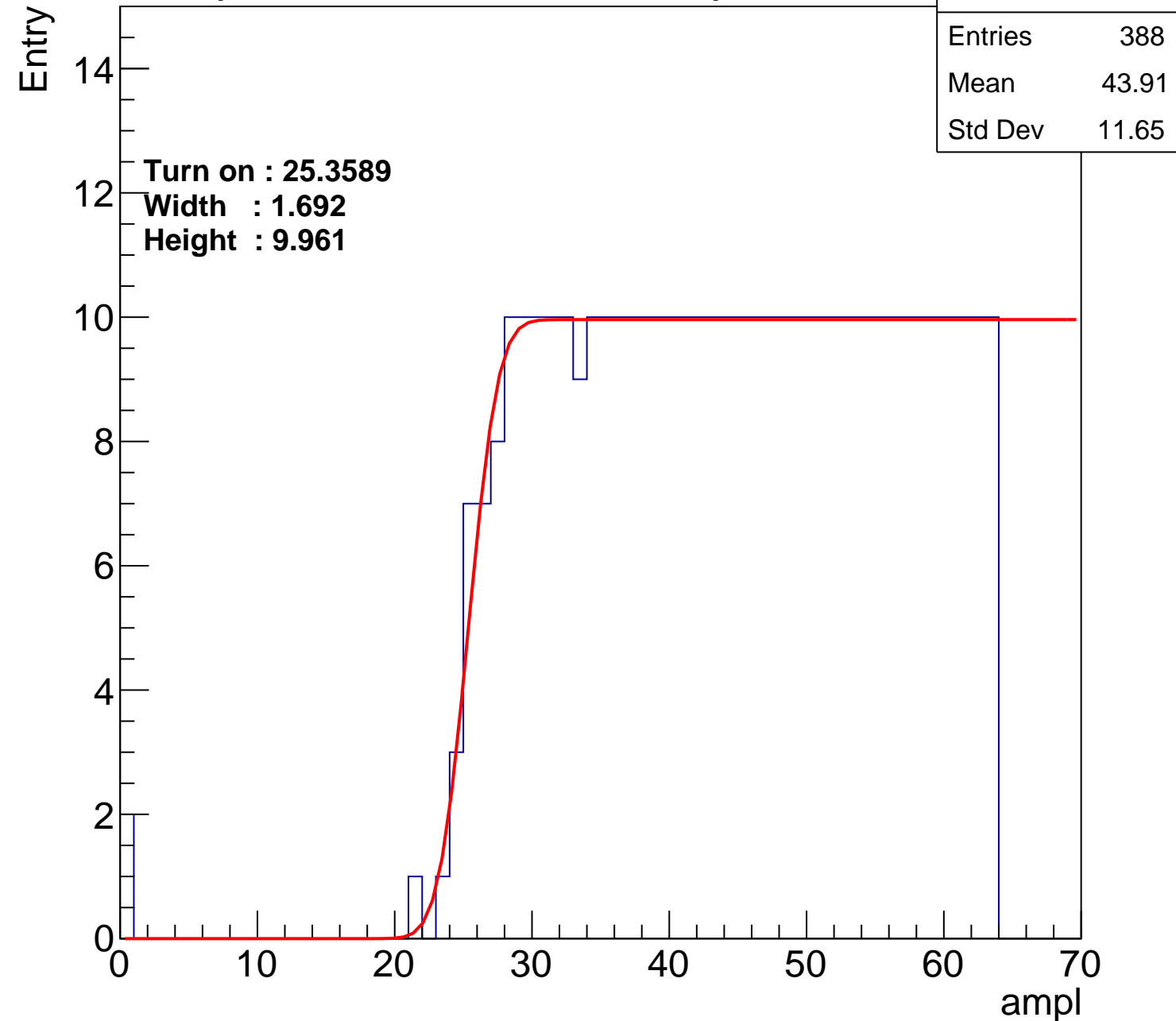
Width : 1.692

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.33
Std Dev	11.46

Turn on : 26.4320

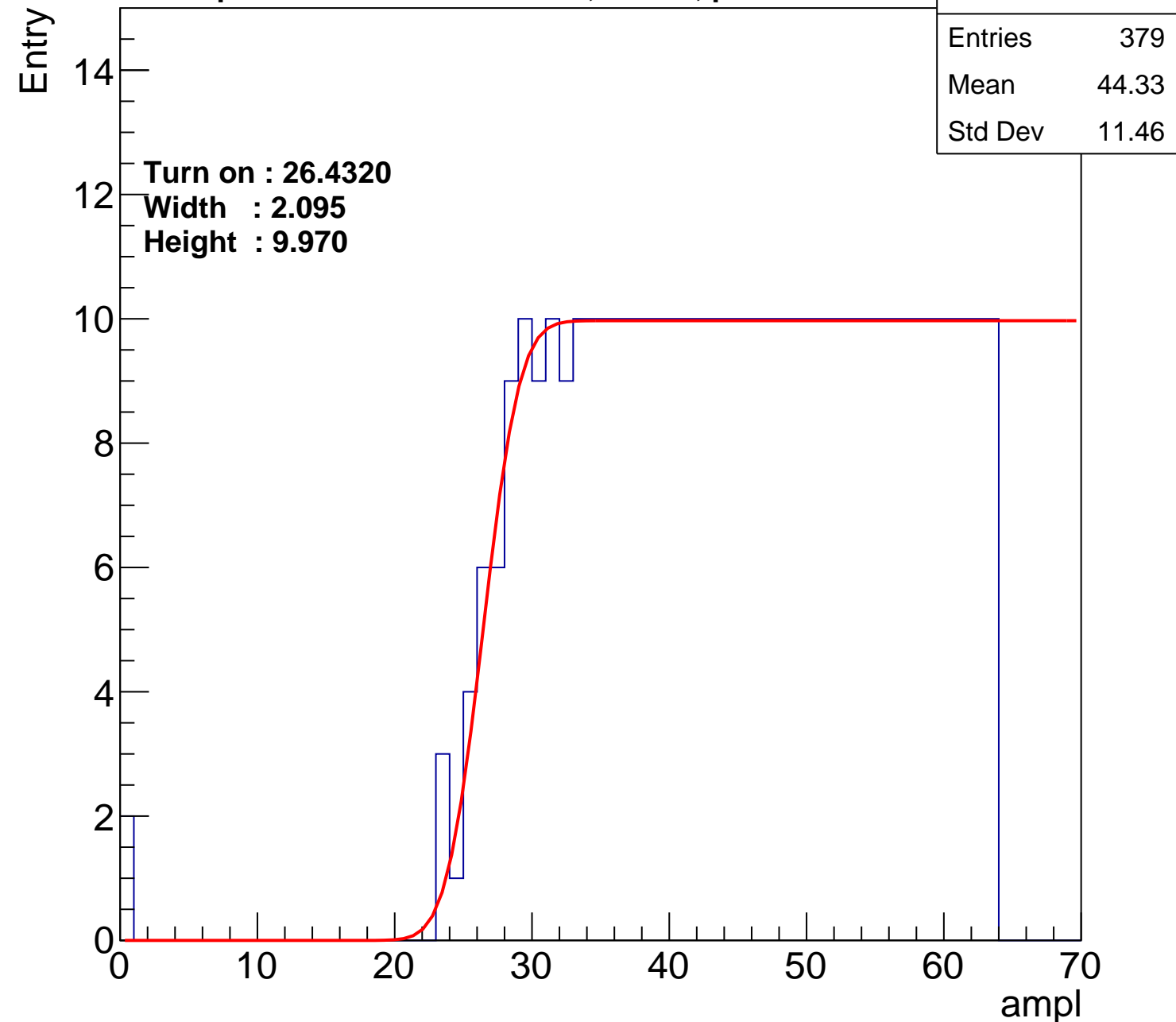
Width : 2.095

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	363
Mean	45.04
Std Dev	11.27

Turn on : 28.3420

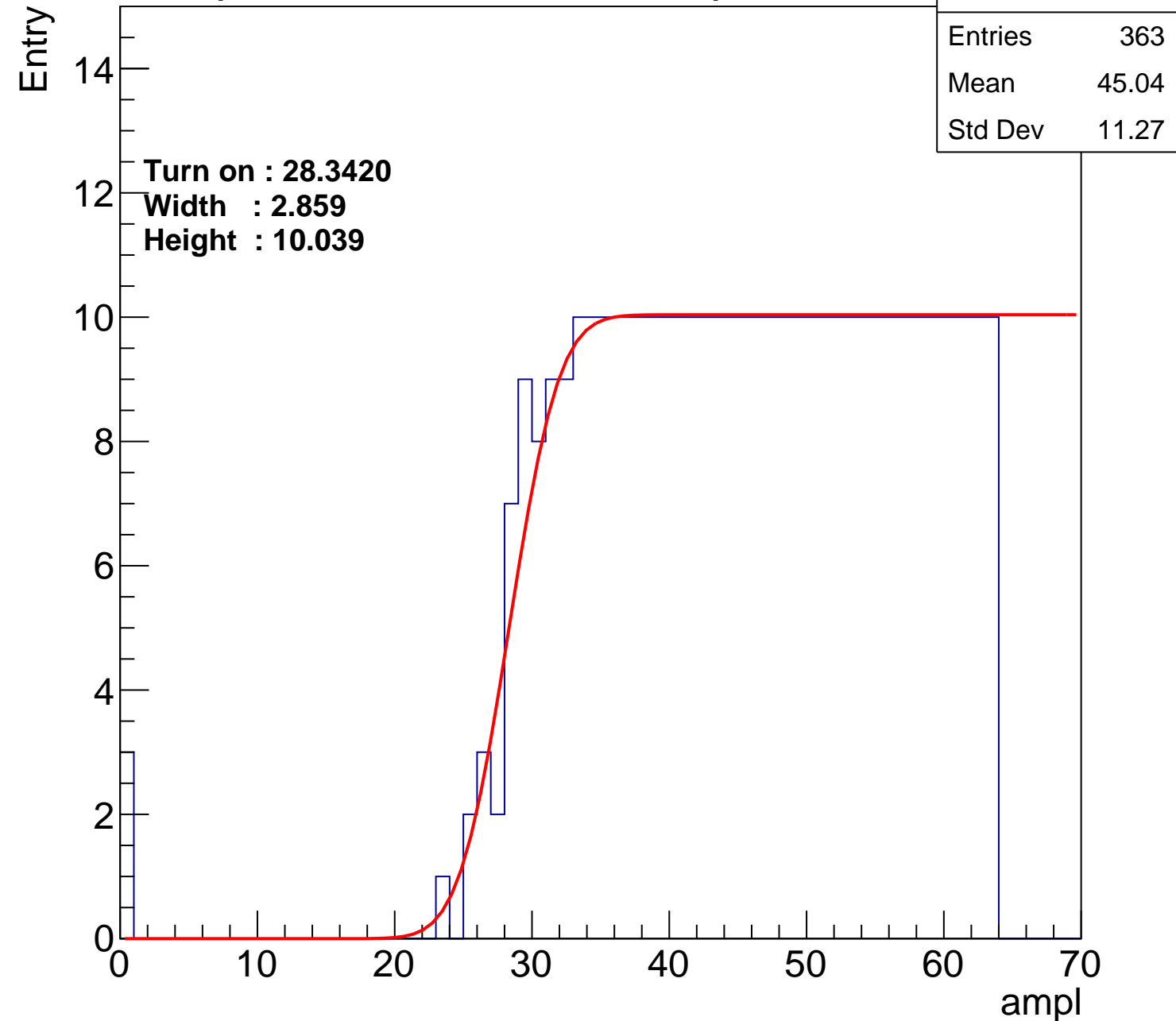
Width : 2.859

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.51
Std Dev	11.93

Turn on : 25.2146

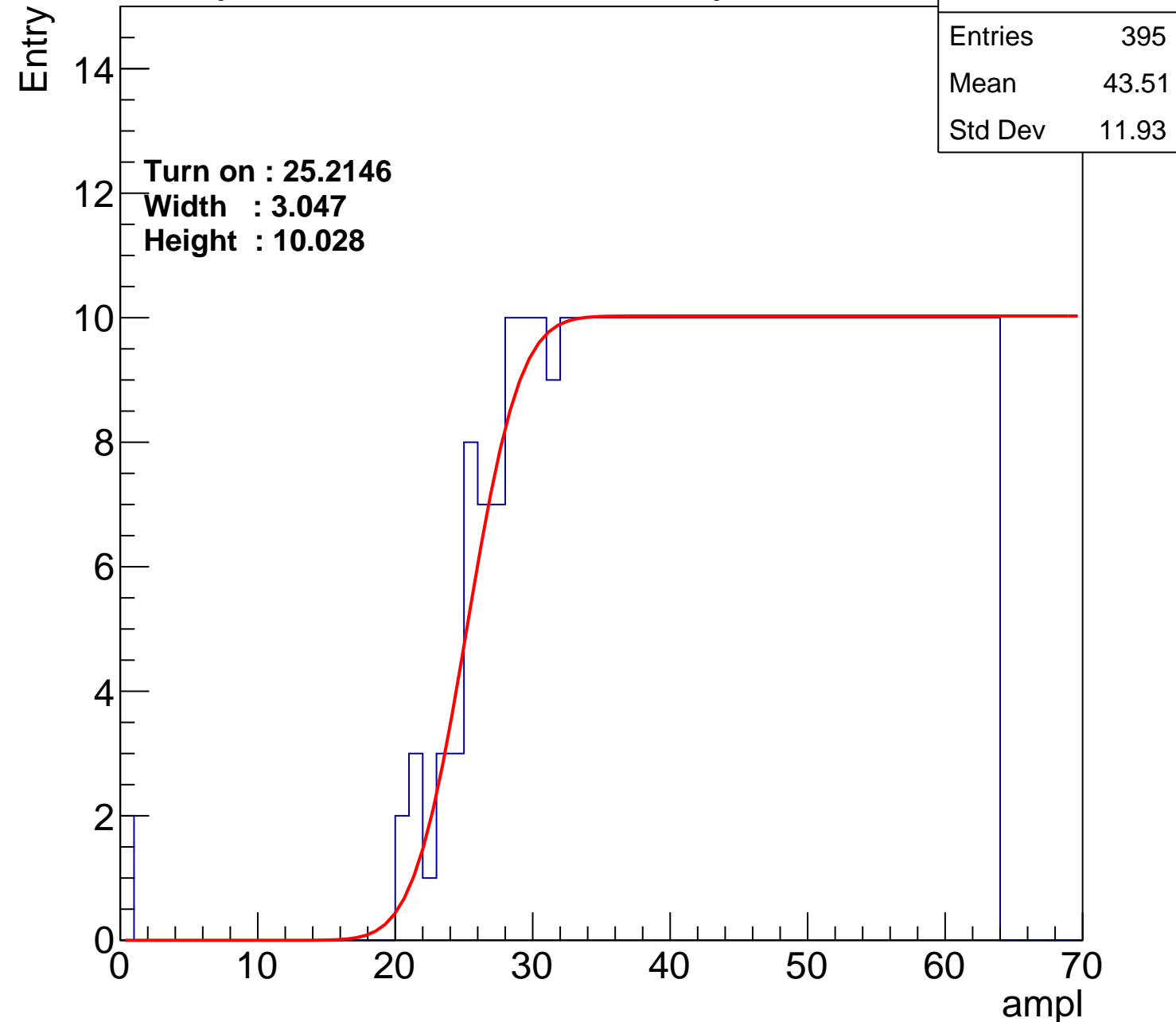
Width : 3.047

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch119

calib_packv5_042523_0143.root, FC#10, port B3

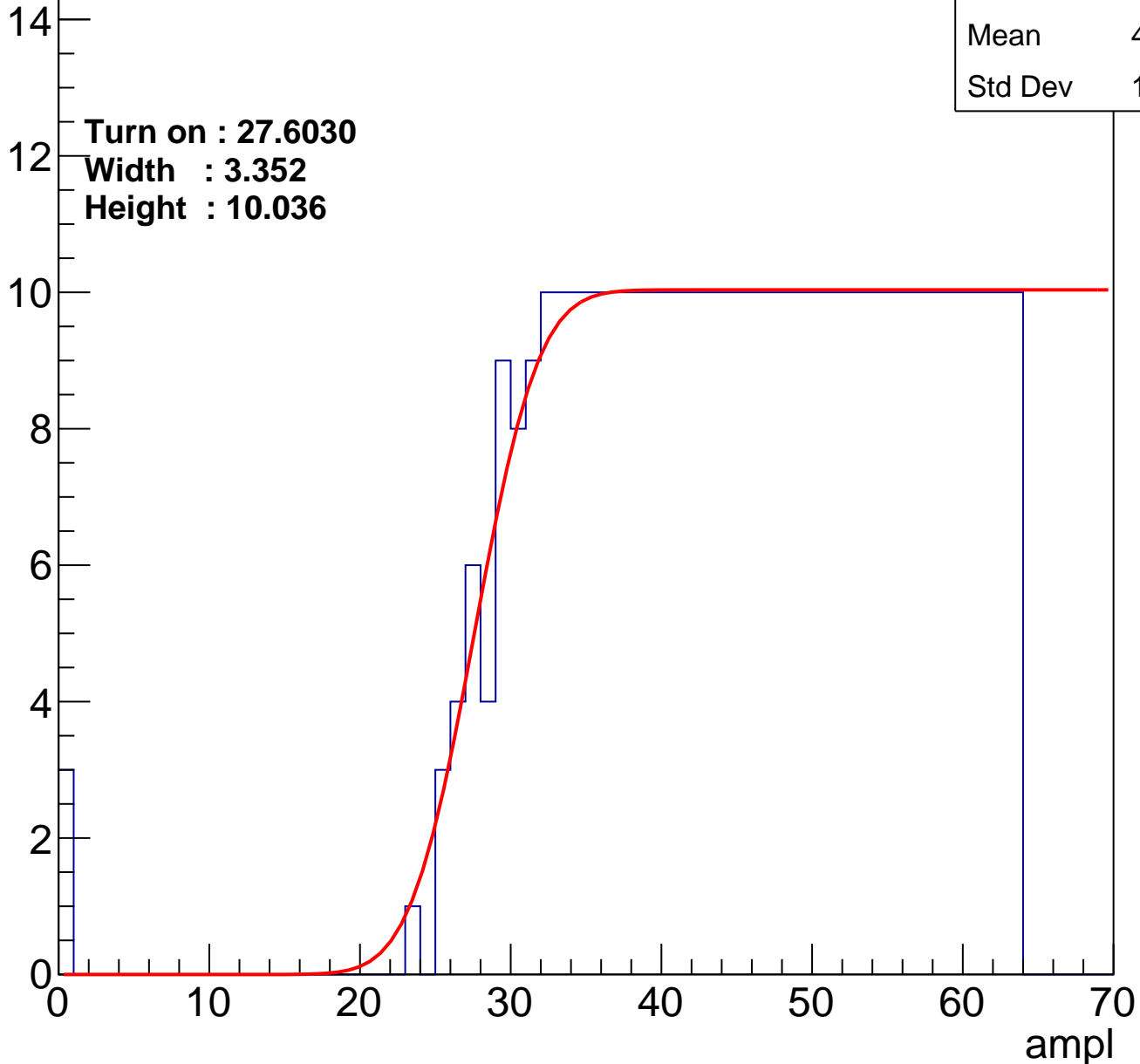
Entries	367
Mean	44.84
Std Dev	11.37

Turn on : 27.6030

Width : 3.352

Height : 10.036

Entry



B1L002S, U13-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.69
Std Dev	12.66

Turn on : 23.6191

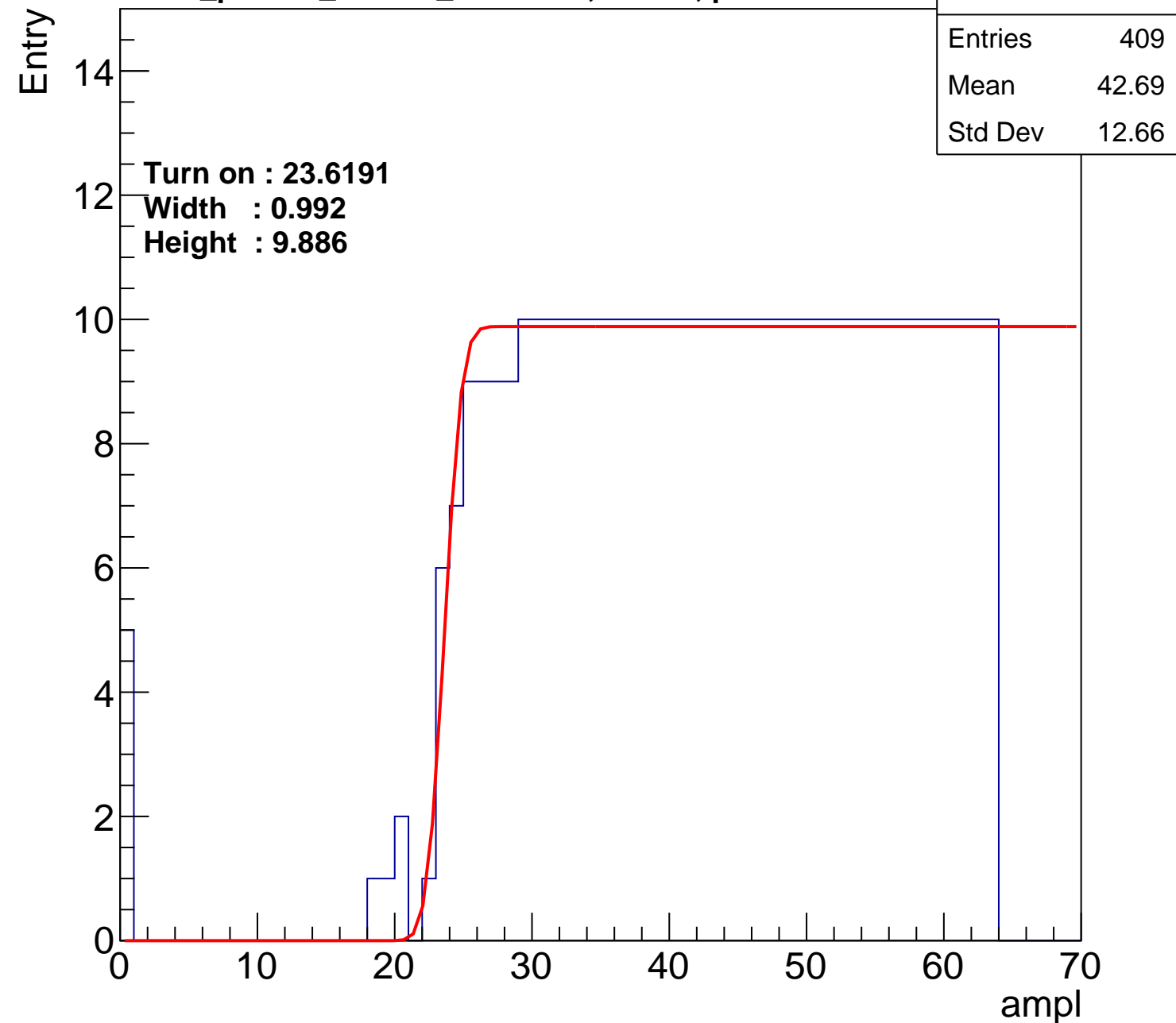
Width : 0.992

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	43.86
Std Dev	12.27

Turn on : 27.1148

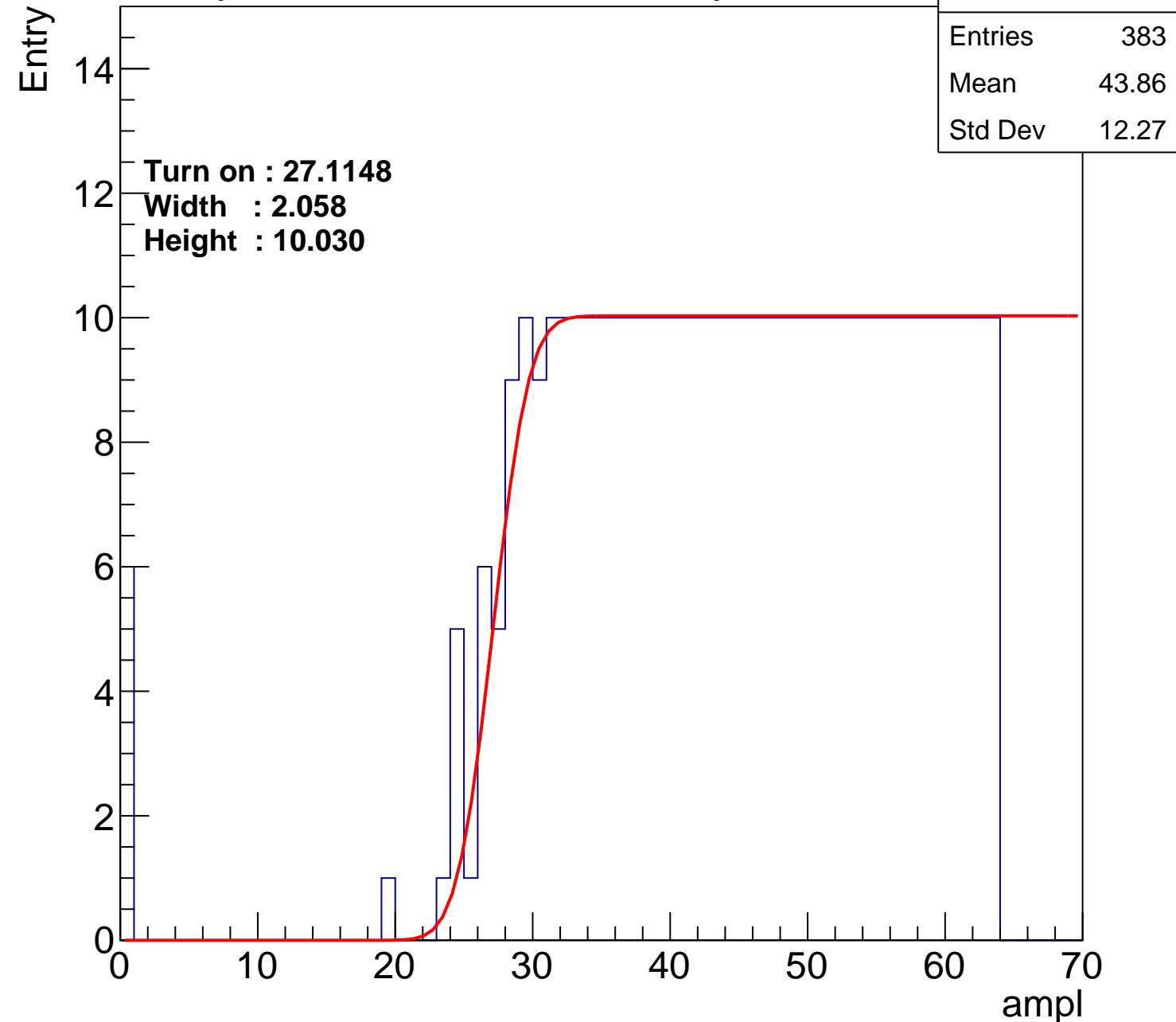
Width : 2.058

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch122

calib_packv5_042523_0143.root, FC#10, port B3

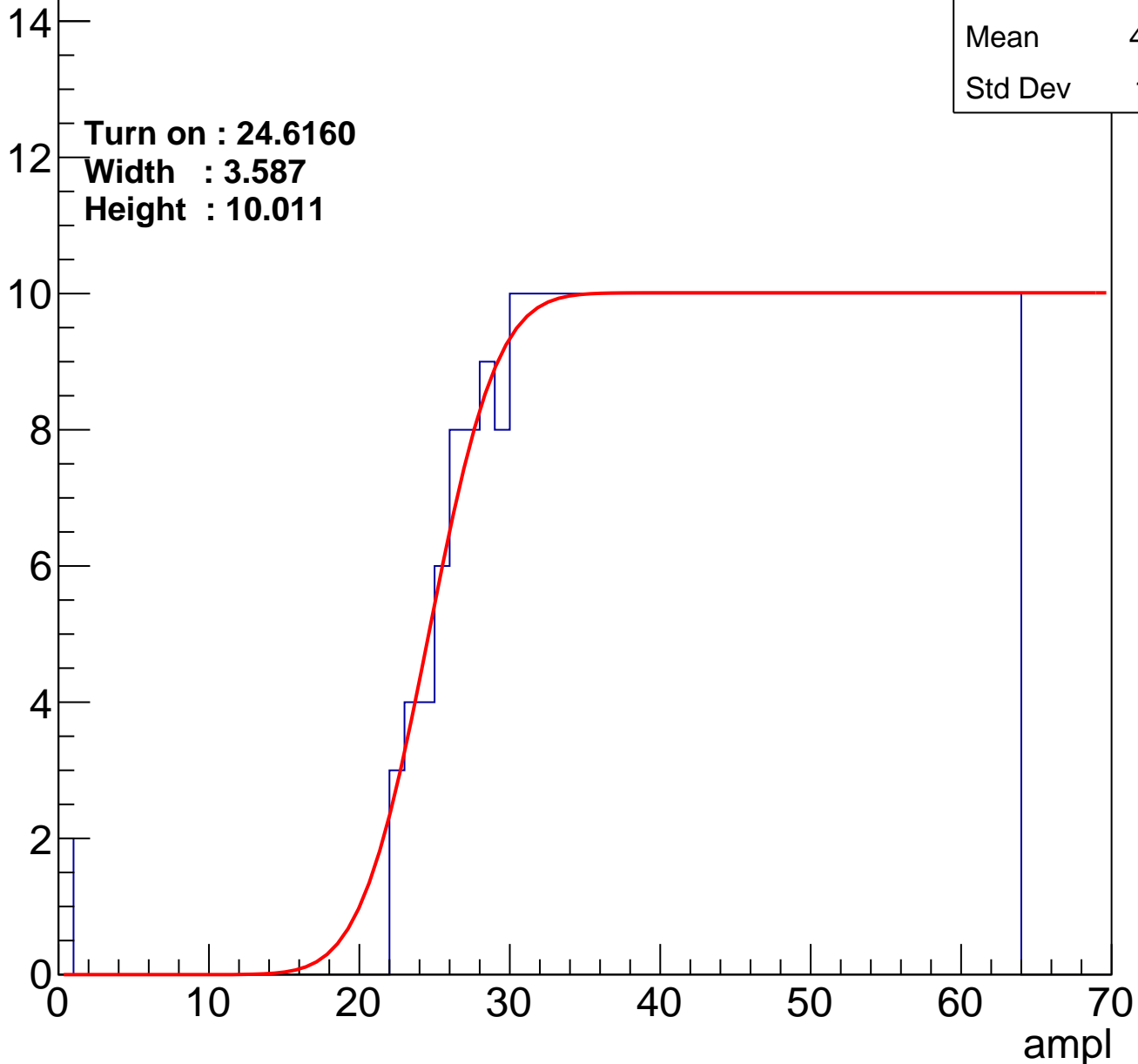
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 24.6160

Width : 3.587

Height : 10.011

Entry



B1L002S, U13-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.53
Std Dev	11.78

Turn on : 24.6964

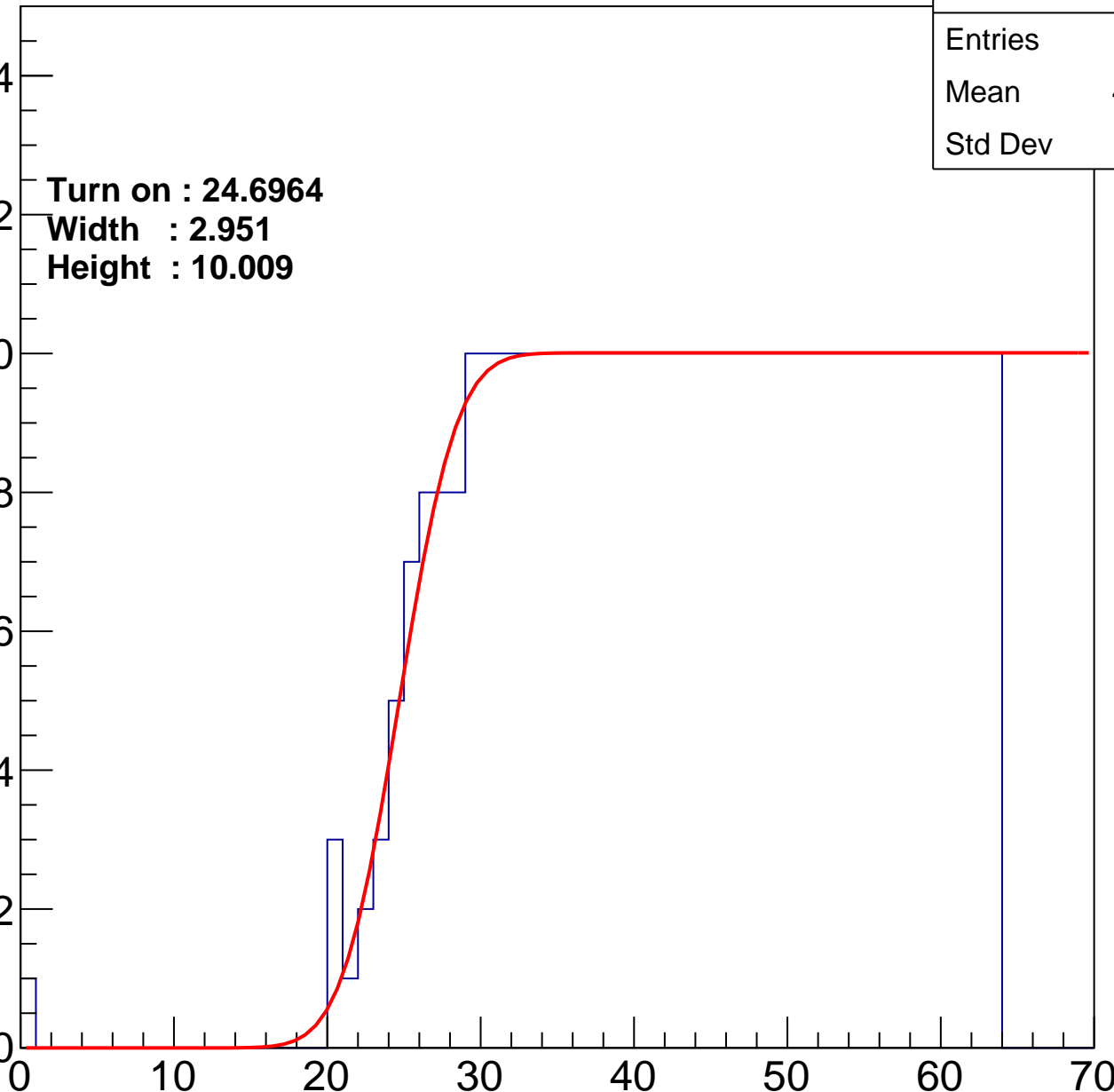
Width : 2.951

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.34
Std Dev	12.1

Turn on : 24.7253

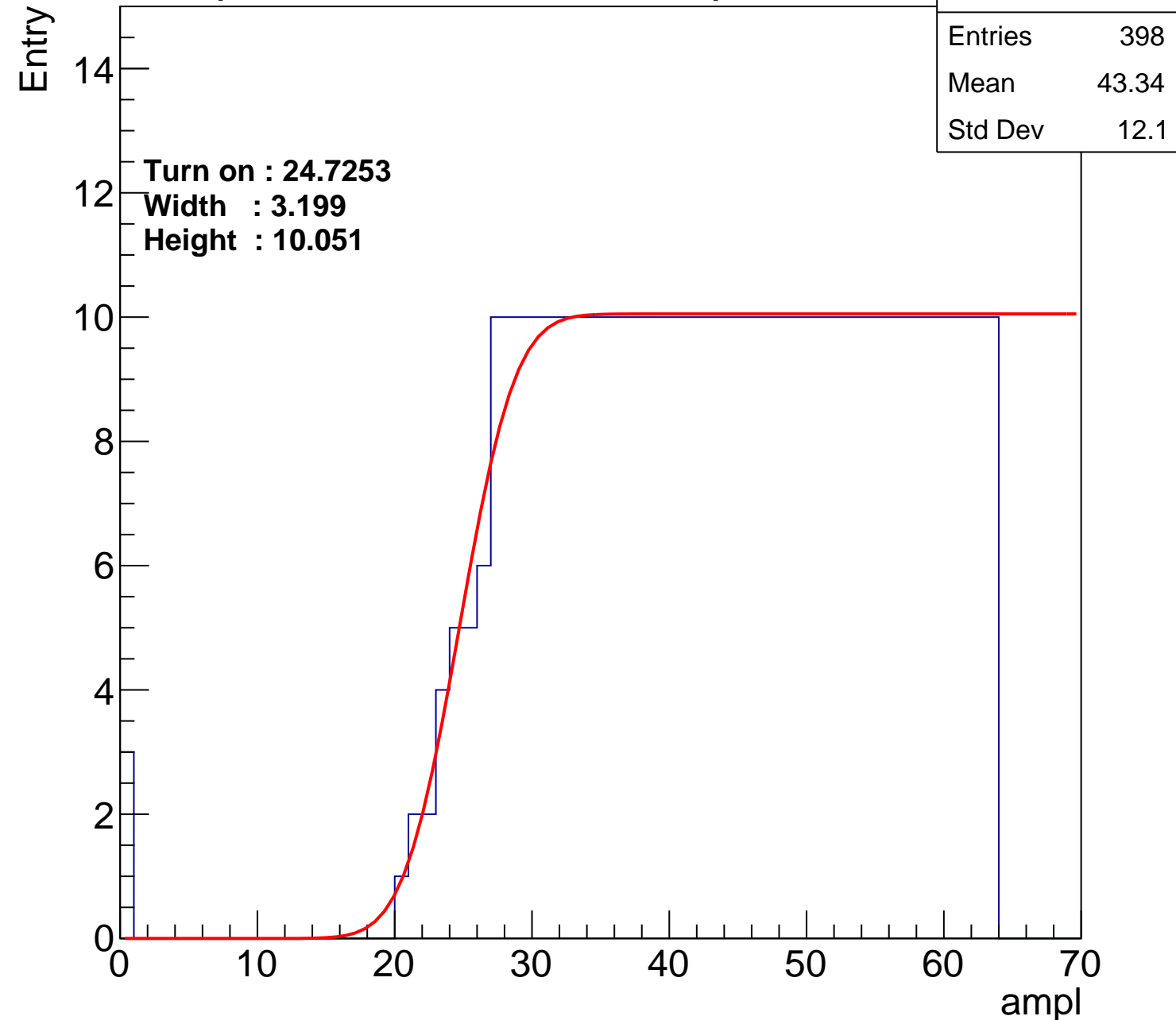
Width : 3.199

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.1
Std Dev	11.74

Turn on : 26.8071

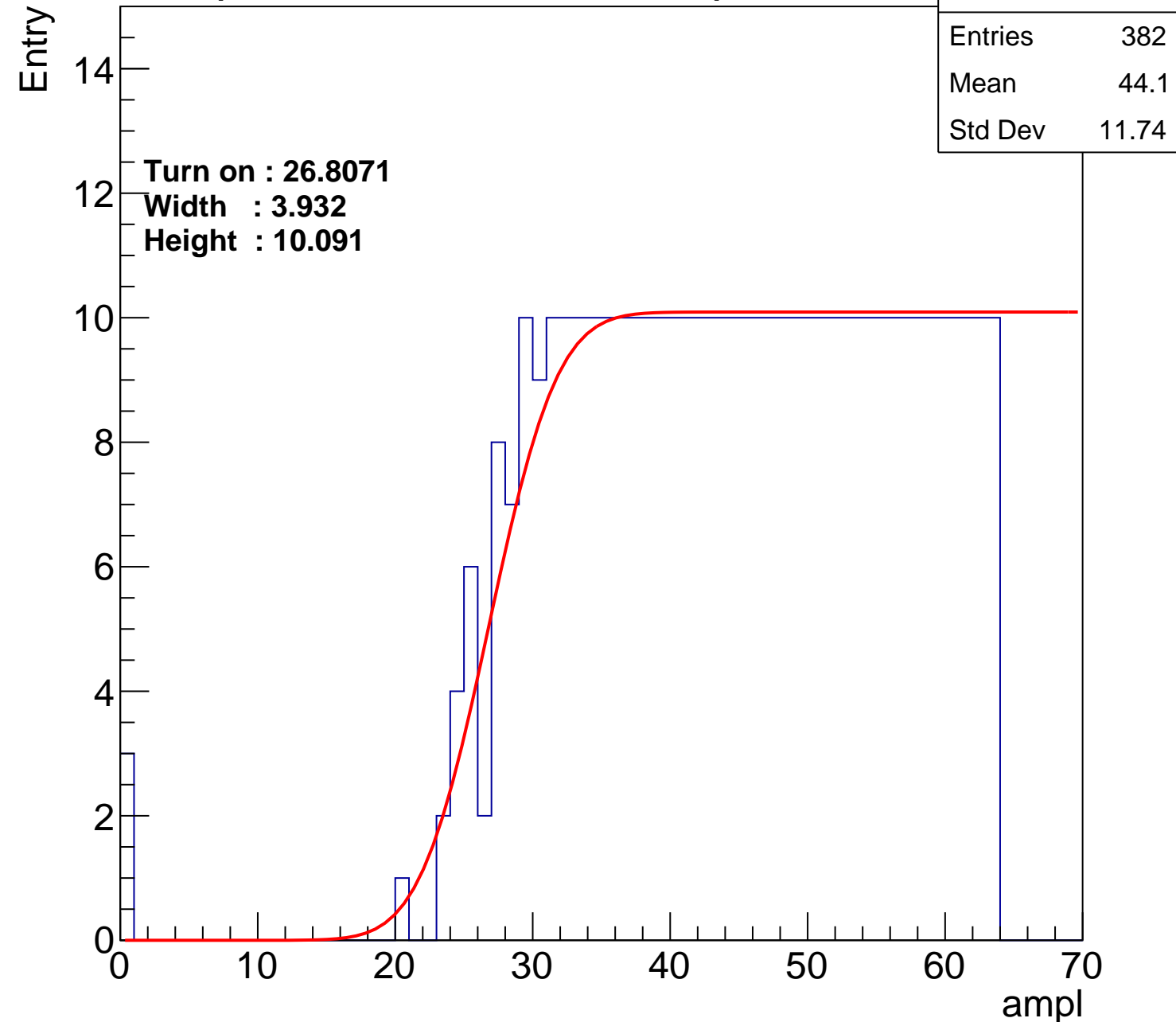
Width : 3.932

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.66
Std Dev	11.68

Turn on : 24.7859

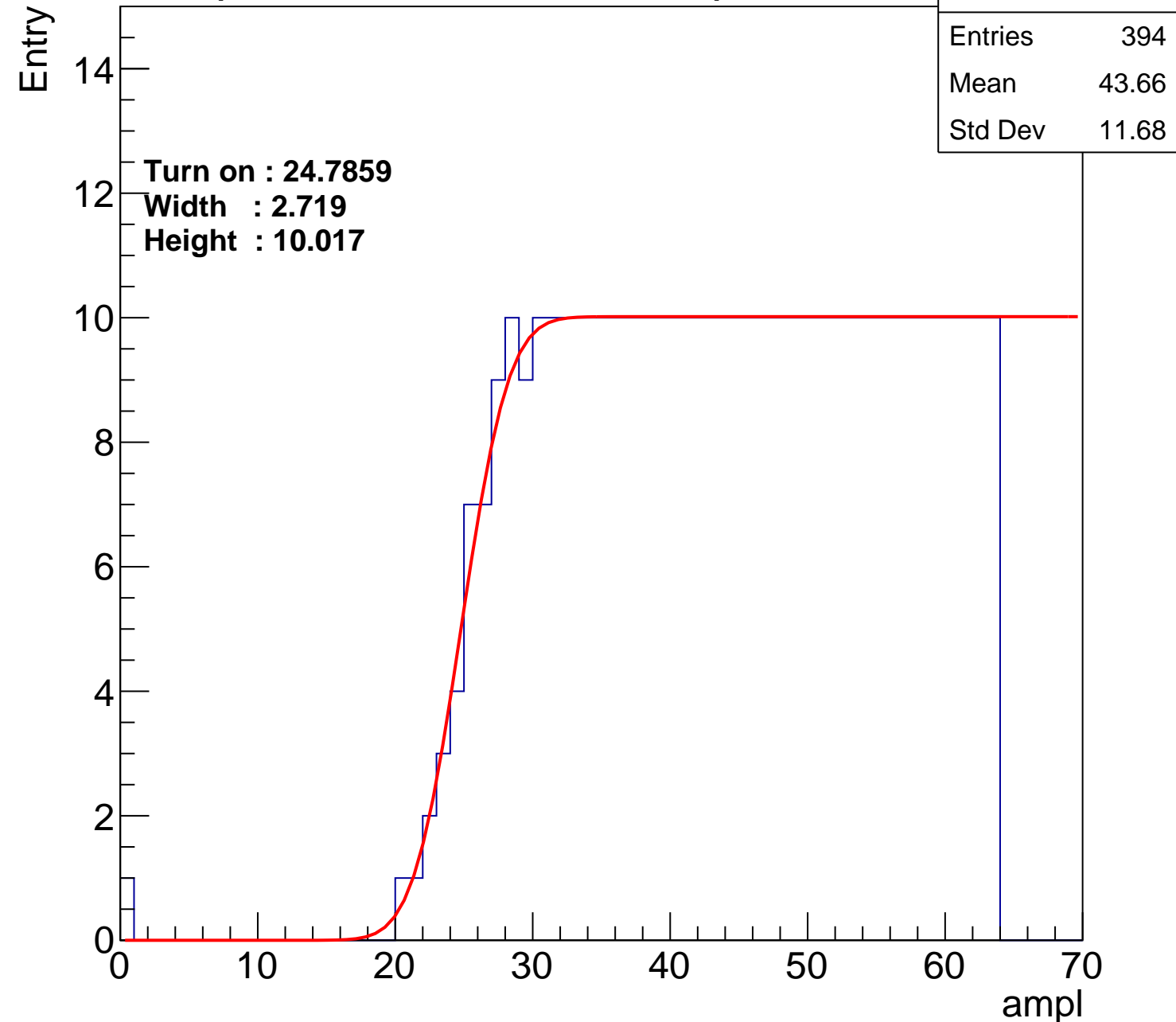
Width : 2.719

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U13-ch127

calib_packv5_042523_0143.root, FC#10, port B3

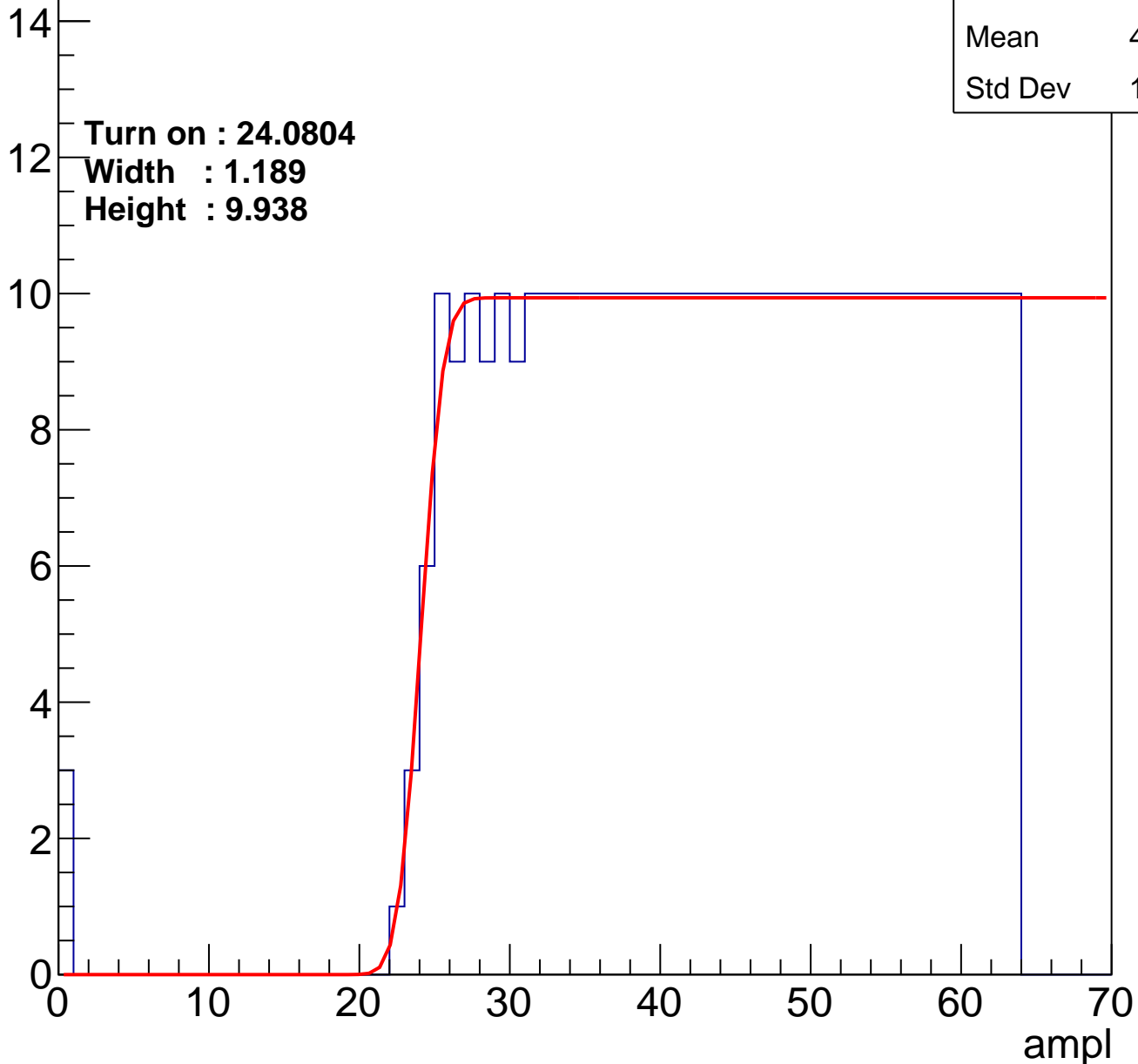
Entries	400
Mean	43.28
Std Dev	12.09

Turn on : 24.0804

Width : 1.189

Height : 9.938

Entry



B1L002S, U13-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.28
Std Dev	12.09

Turn on : 24.0804

Width : 1.189

Height : 9.938

Entry

14
12
10
8
6
4
2
0

ampl

