



# B0L103S, U23-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

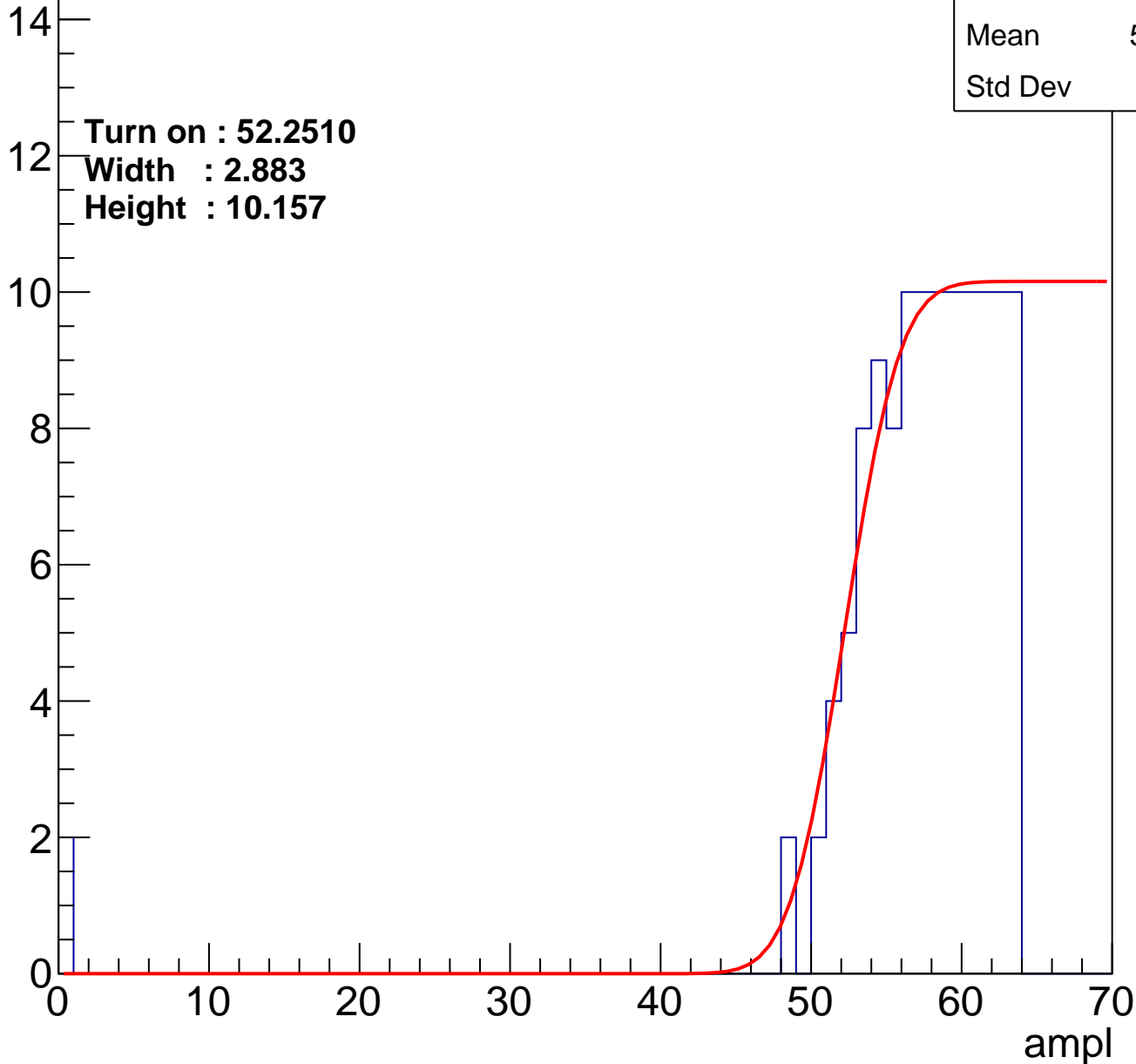
Entry

Entries	120
Mean	56.42
Std Dev	8.24

Turn on : 52.2510

Width : 2.883

Height : 10.157



# B0L103S, U23-ch1

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.22
Std Dev	10.7

**Turn on : 52.4669**

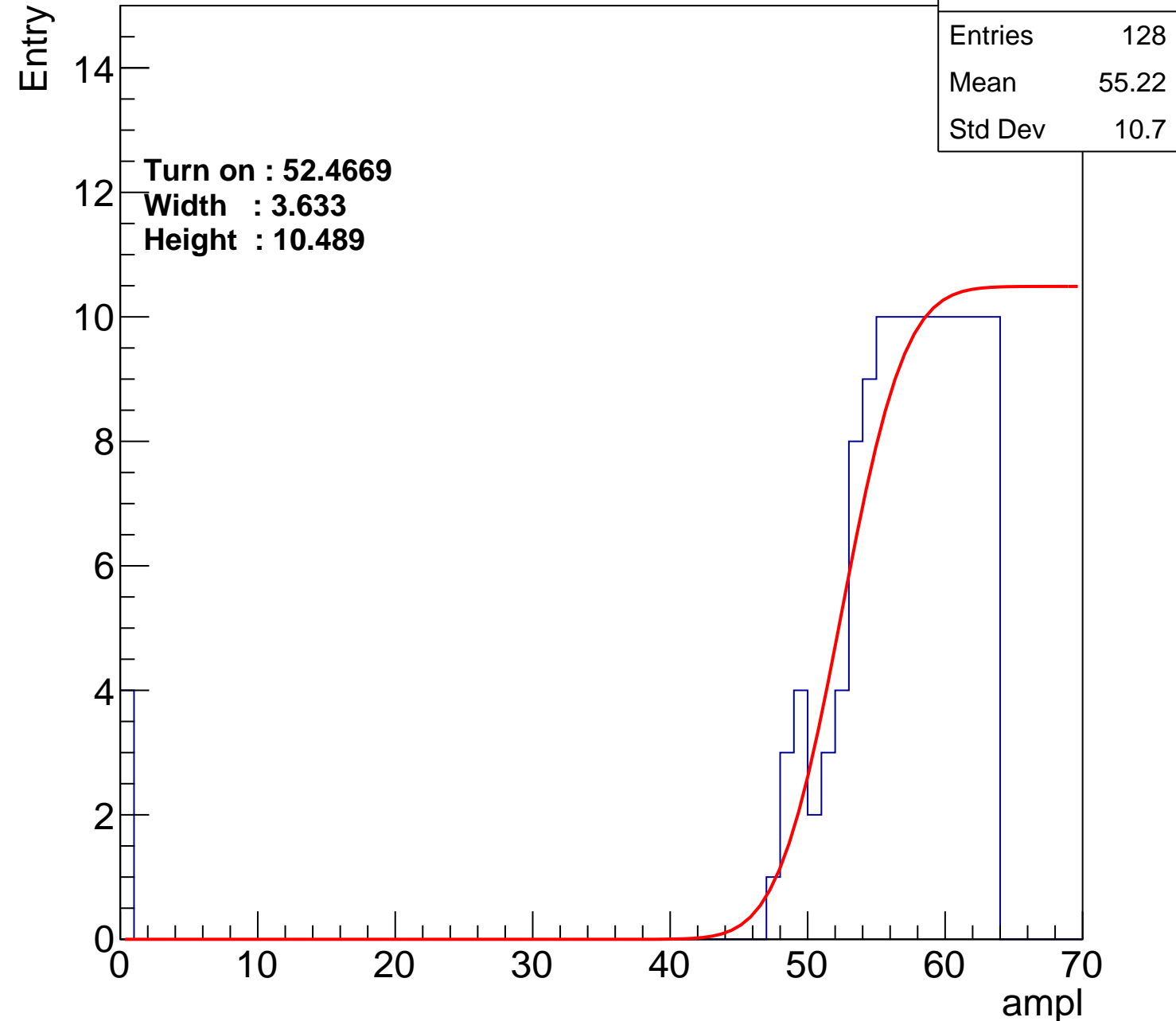
**Width : 3.633**

**Height : 10.489**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	56.23
Std Dev	10.02

Turn on : 53.6308

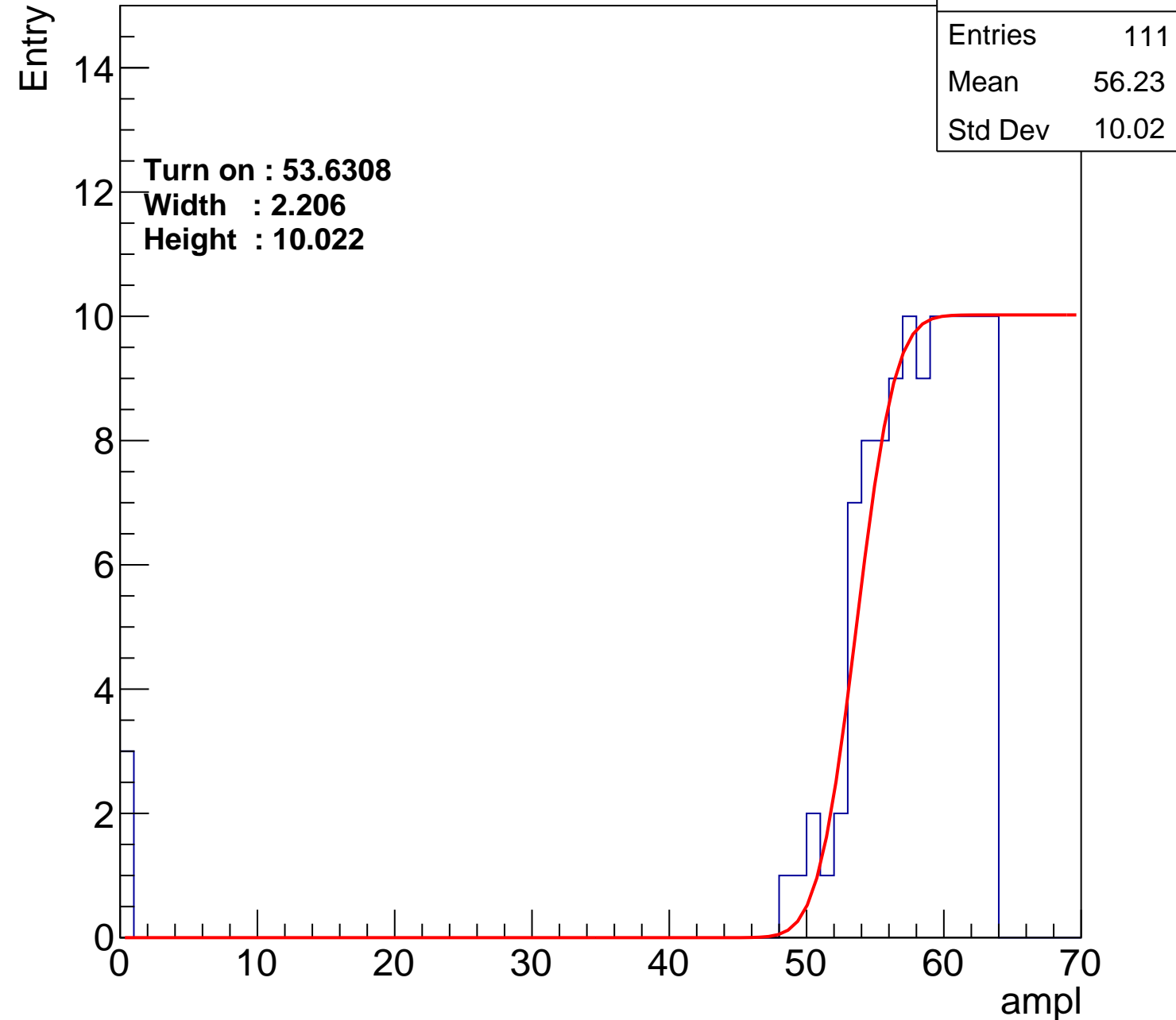
Width : 2.206

Height : 10.022

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

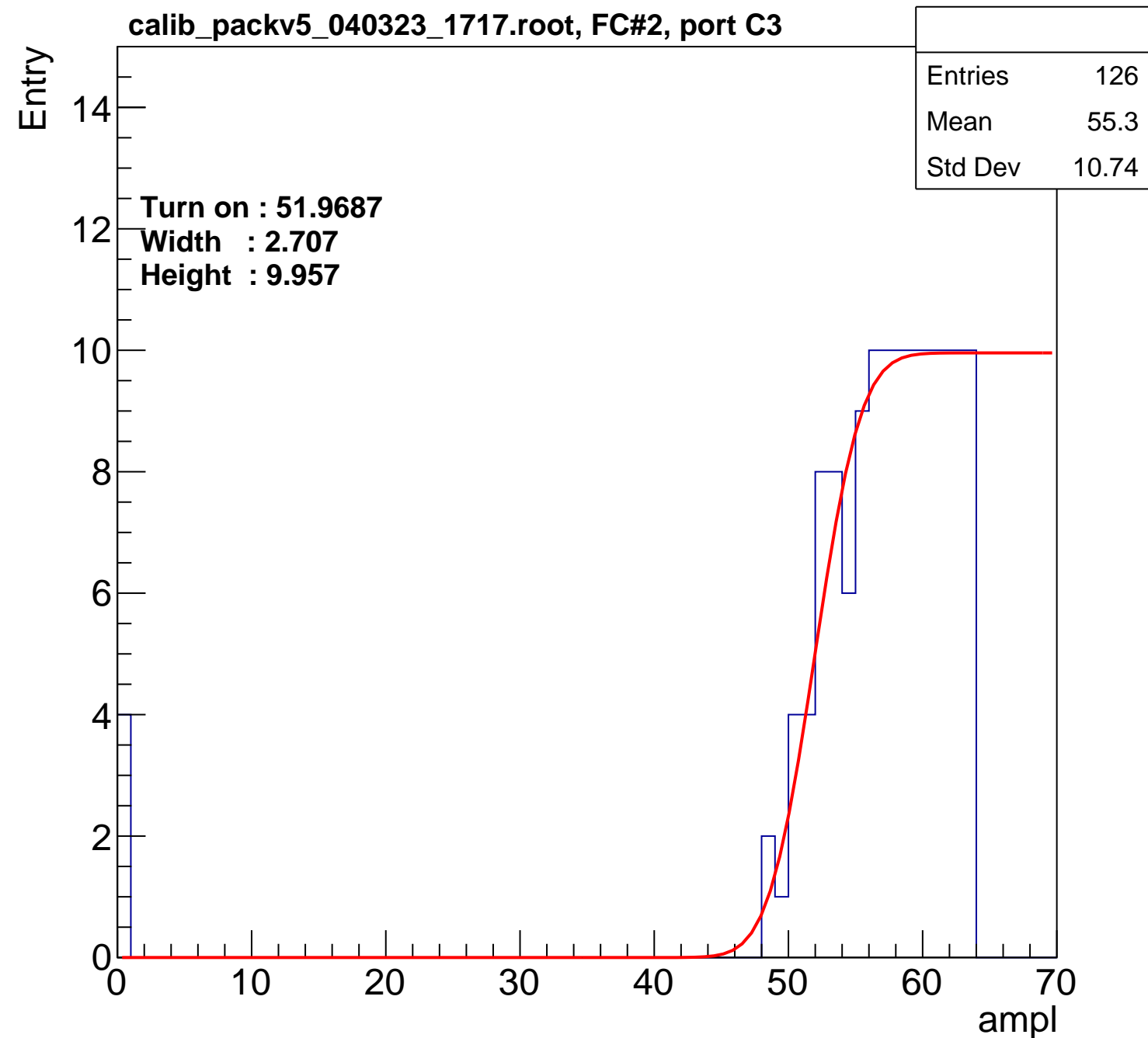
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 51.9687**  
**Width : 2.707**  
**Height : 9.957**

Entries	126
Mean	55.3
Std Dev	10.74

ampl



# B0L103S, U23-ch4

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.62
Std Dev	8.44

Turn on : 53.3061

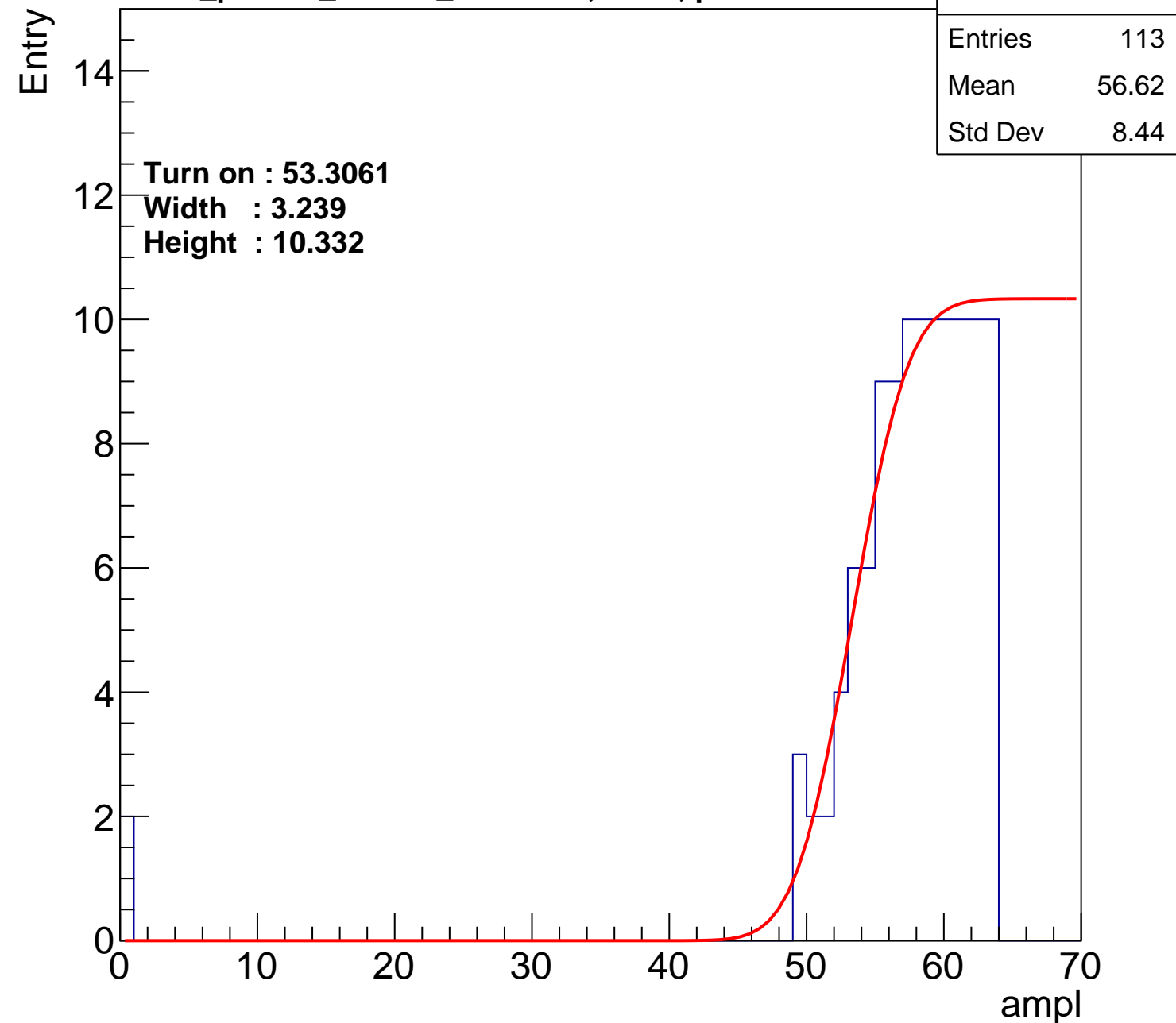
Width : 3.239

Height : 10.332

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch5

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.57
Std Dev	10.99

Turn on : 53.0981

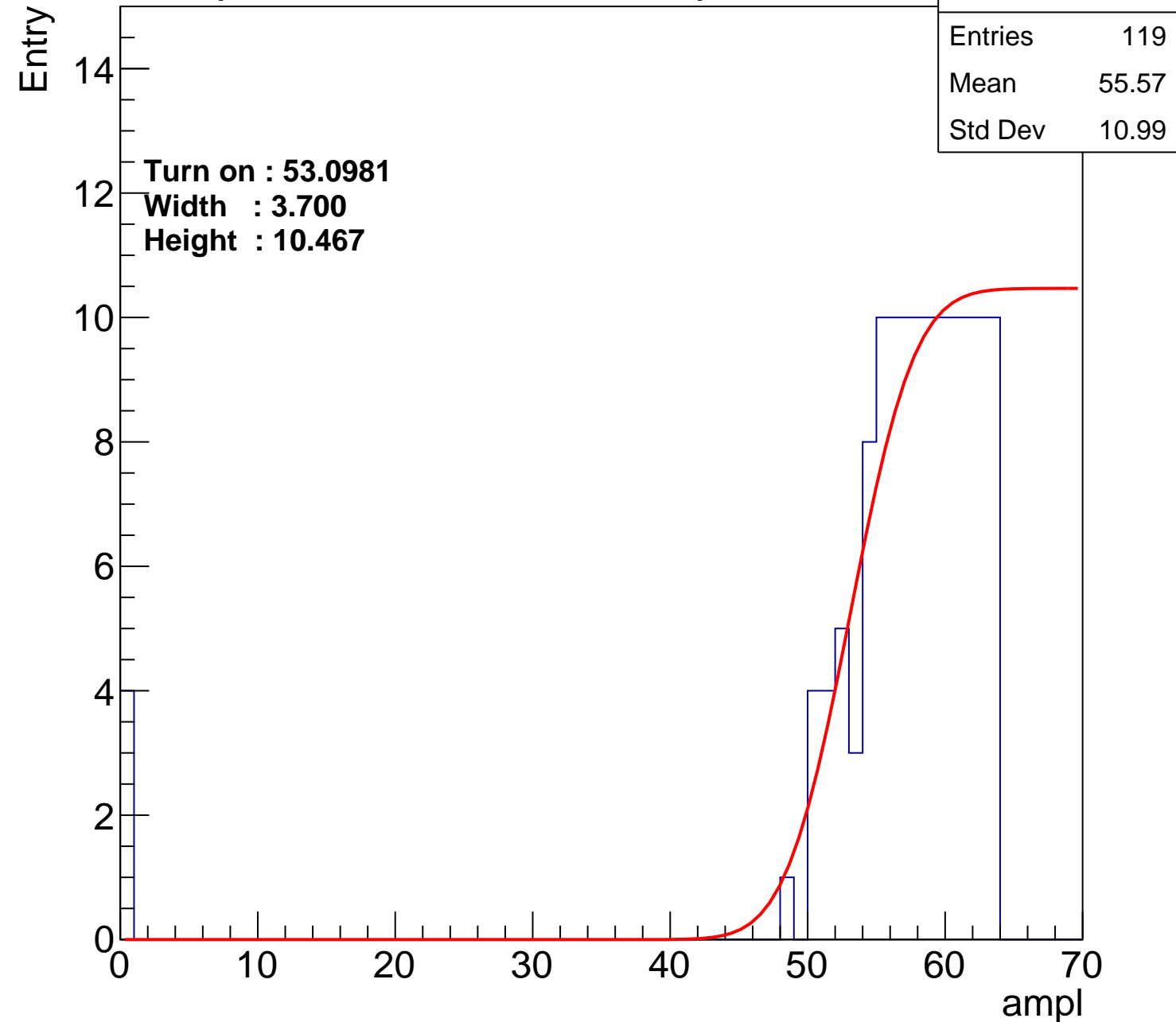
Width : 3.700

Height : 10.467

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	101
Mean	57.08
Std Dev	8.772

**Turn on : 53.9207**

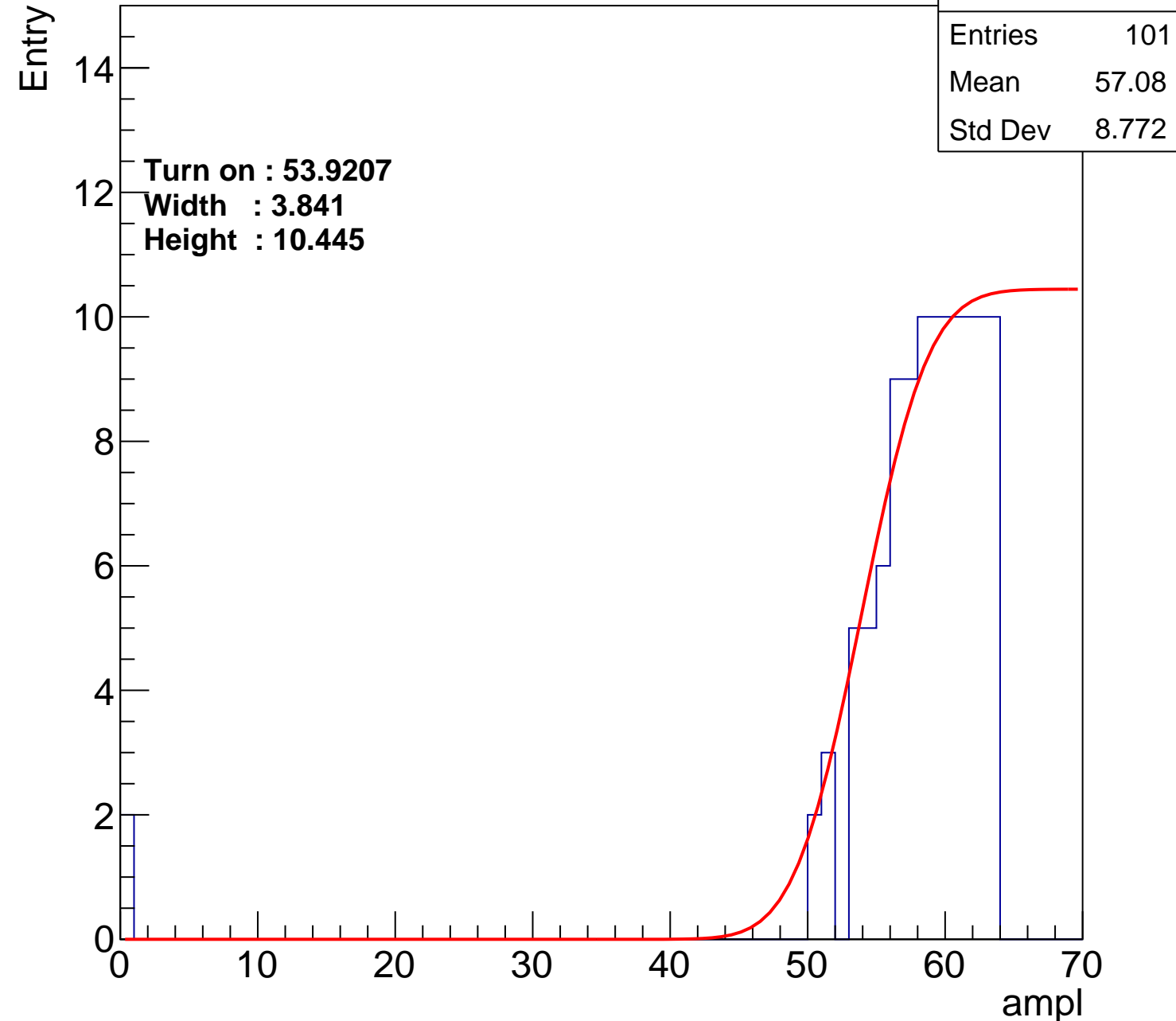
**Width : 3.841**

**Height : 10.445**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	57.27
Std Dev	6.485

Turn on : 53.4745

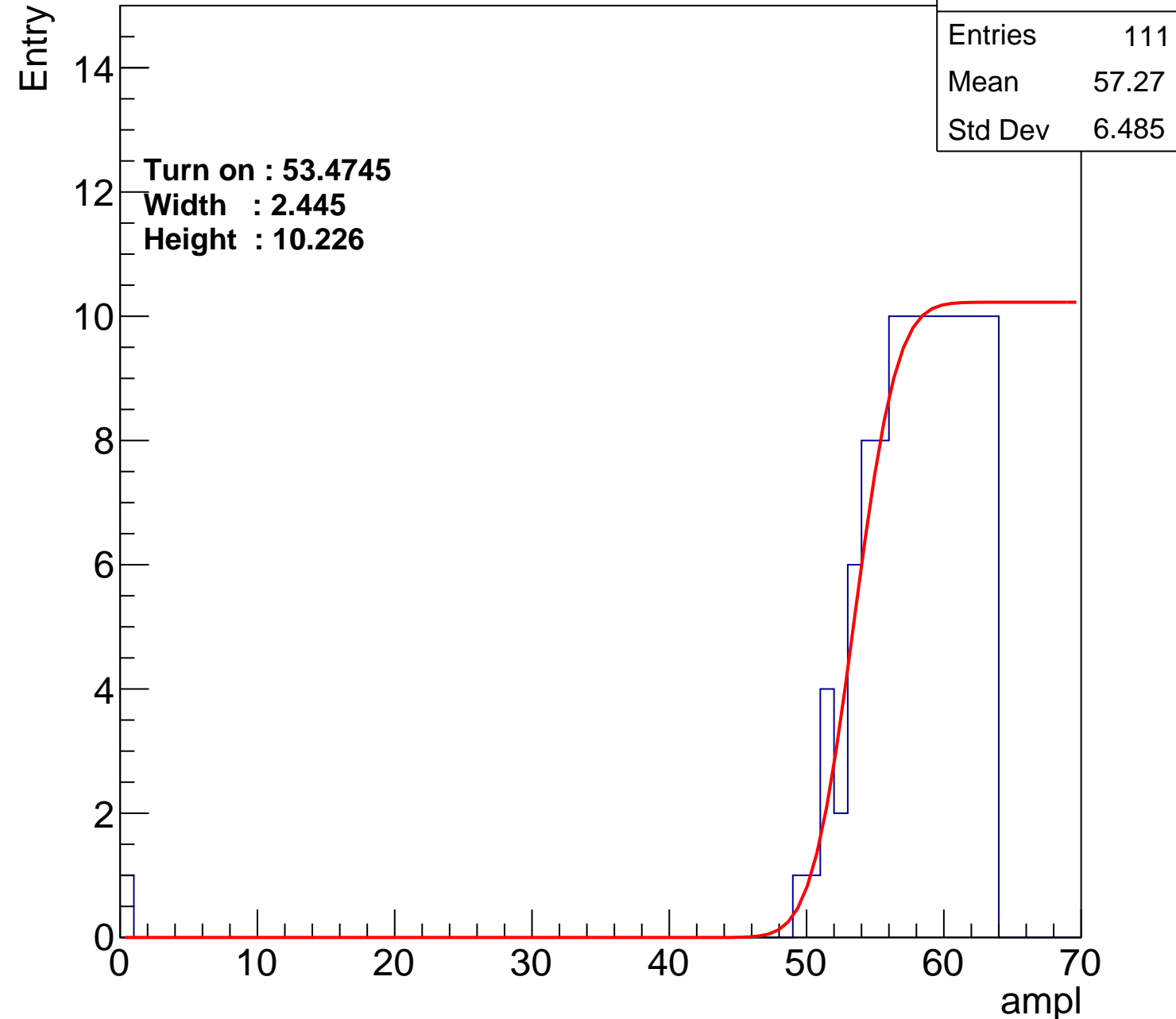
Width : 2.445

Height : 10.226

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch8

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.16
Std Dev	9.8

Turn on : 52.6832

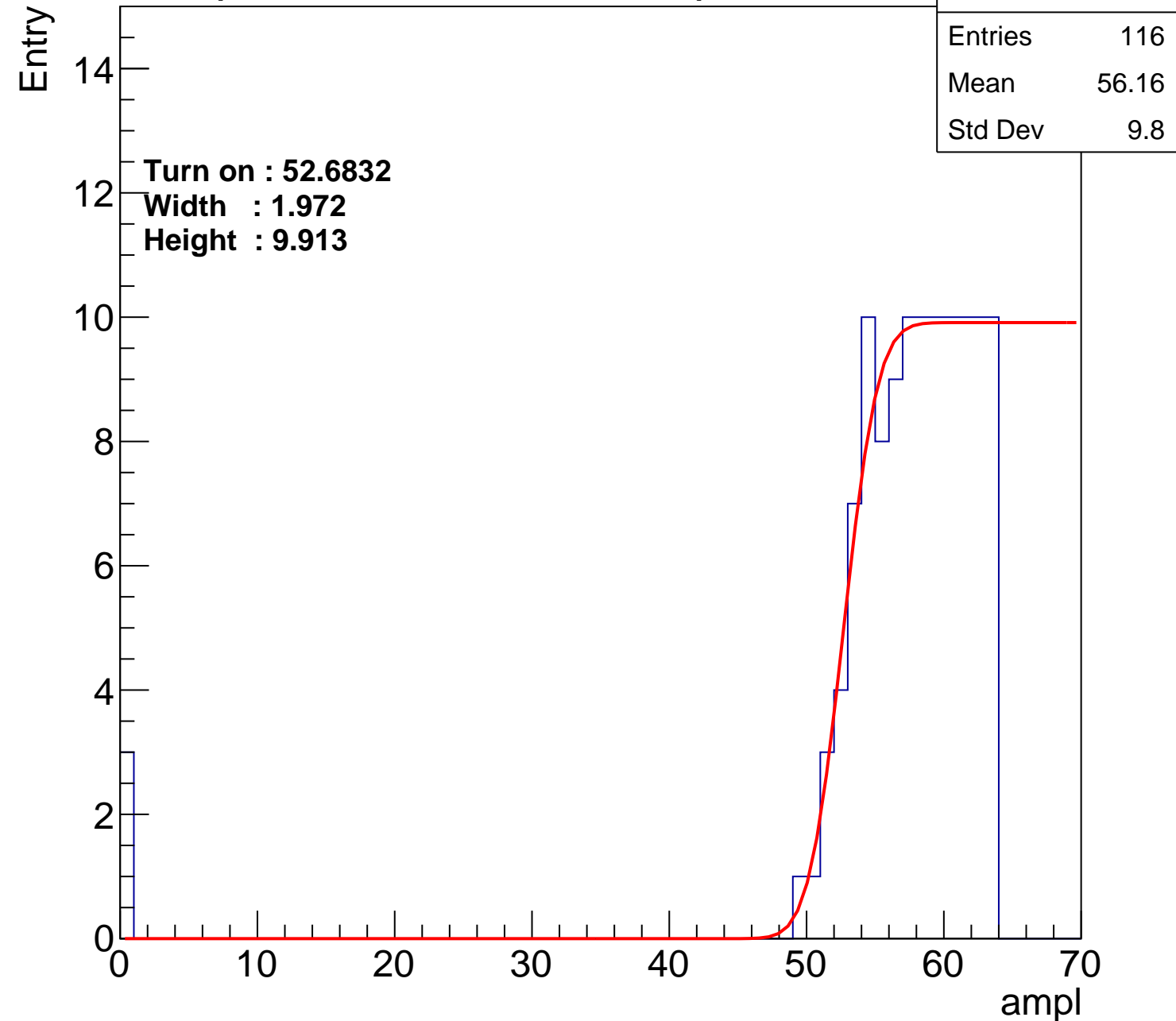
Width : 1.972

Height : 9.913

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

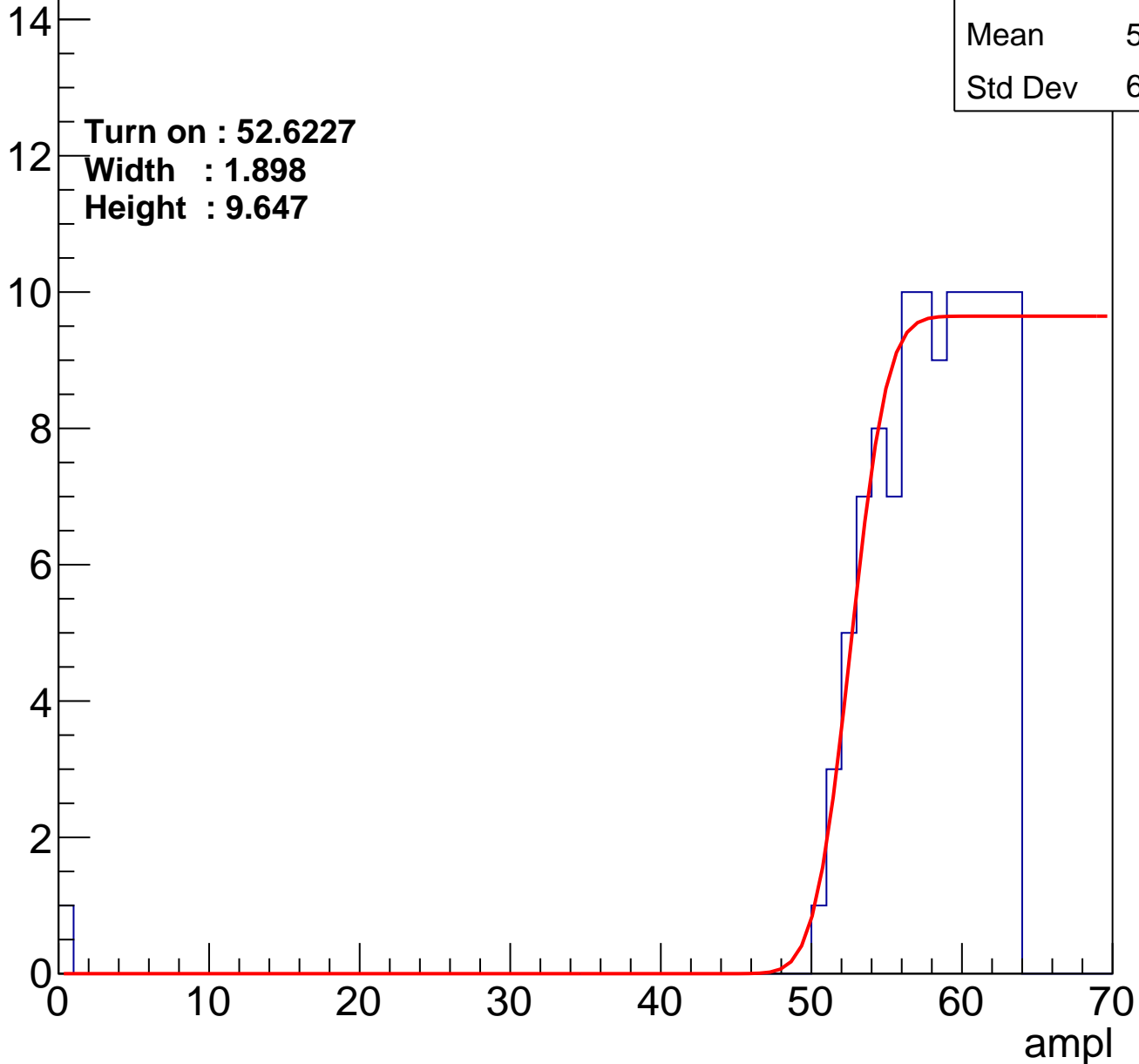
Entry

Entries	111
Mean	57.23
Std Dev	6.477

Turn on : 52.6227

Width : 1.898

Height : 9.647



# B0L103S, U23-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.99
Std Dev	10.47

Turn on : 51.0637

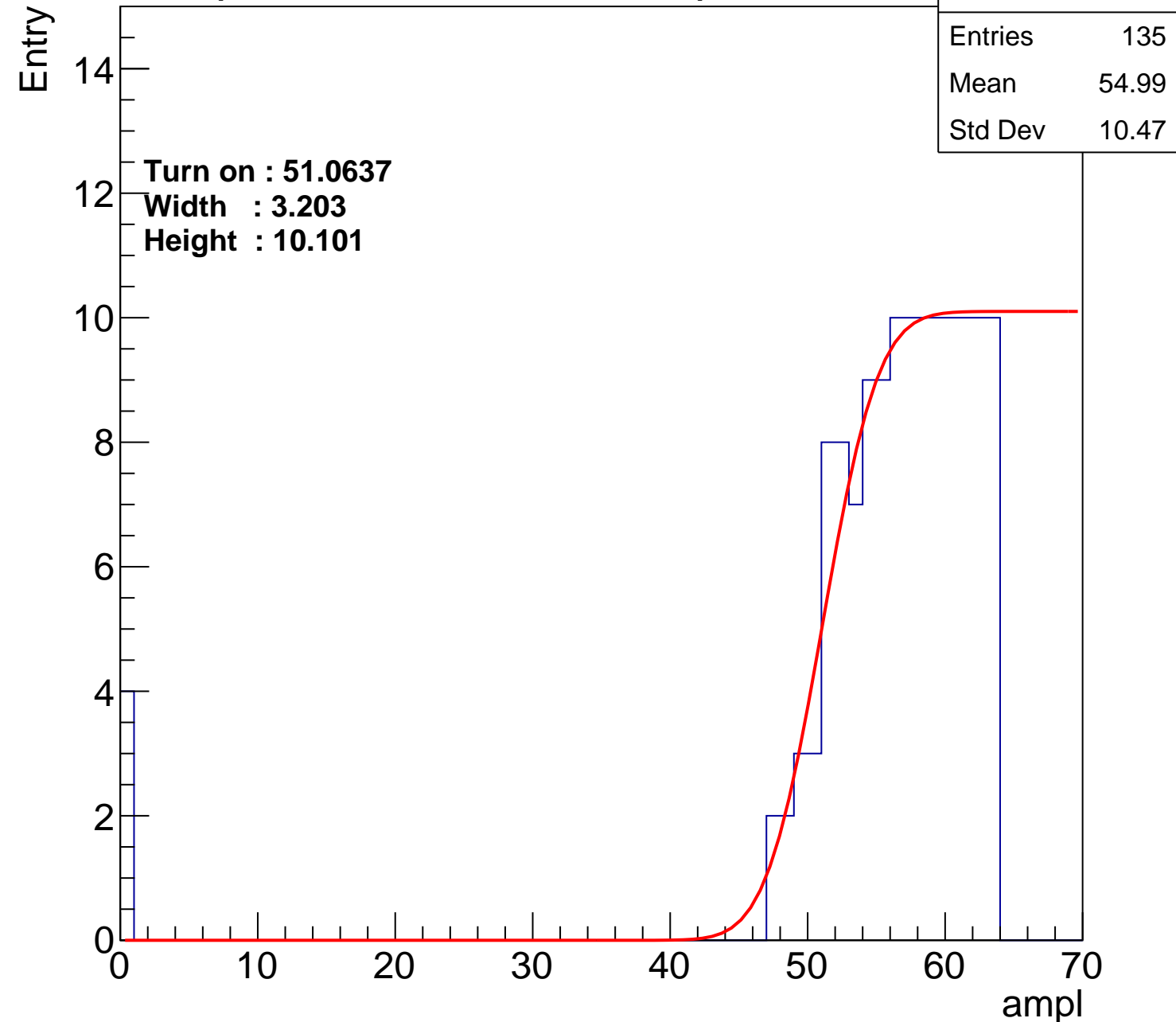
Width : 3.203

Height : 10.101

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

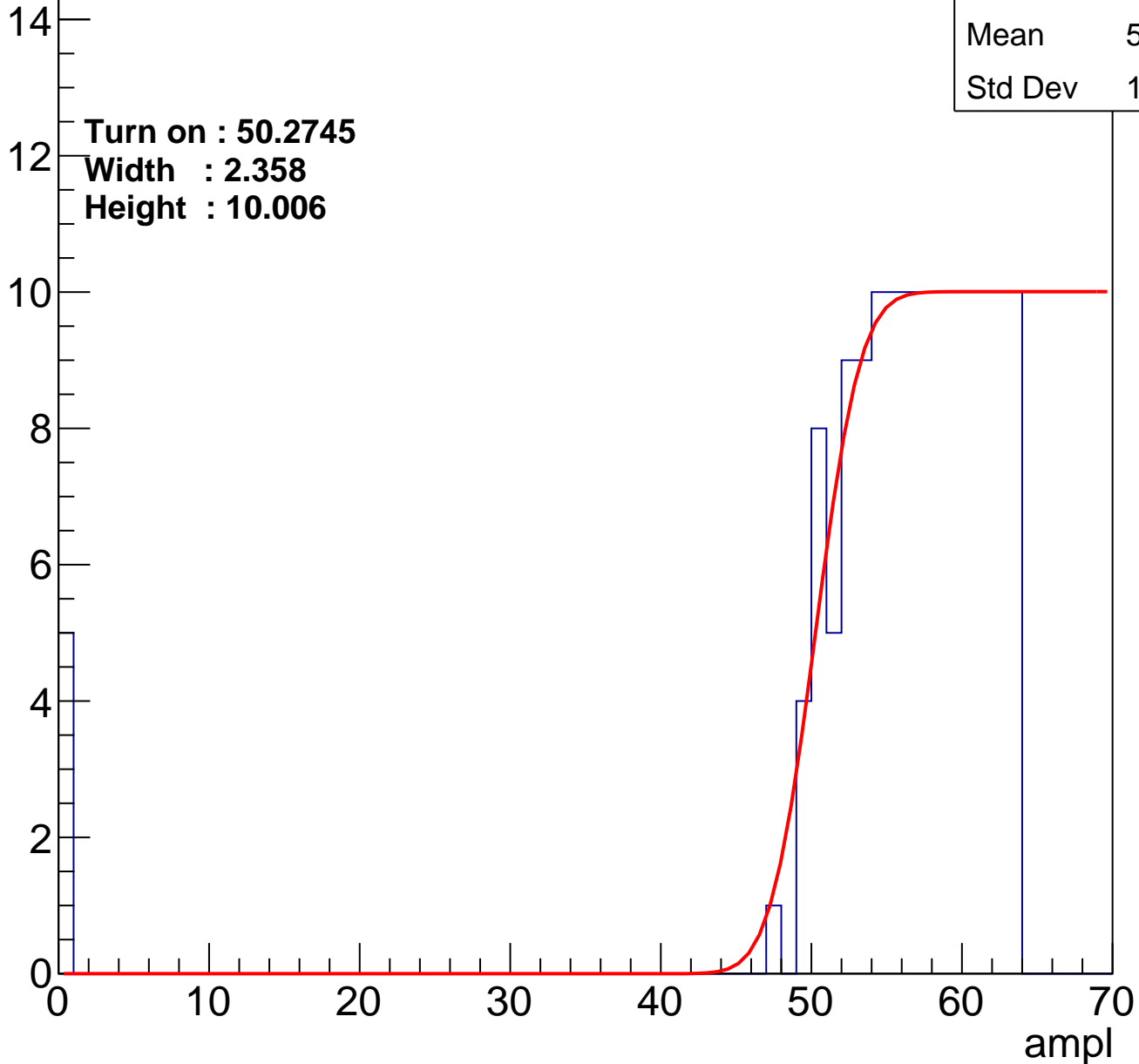
Entries	141
Mean	54.56
Std Dev	11.22

Turn on : 50.2745

Width : 2.358

Height : 10.006

Entry



# B0L103S, U23-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	56.9
Std Dev	6.463

Turn on : 52.4018

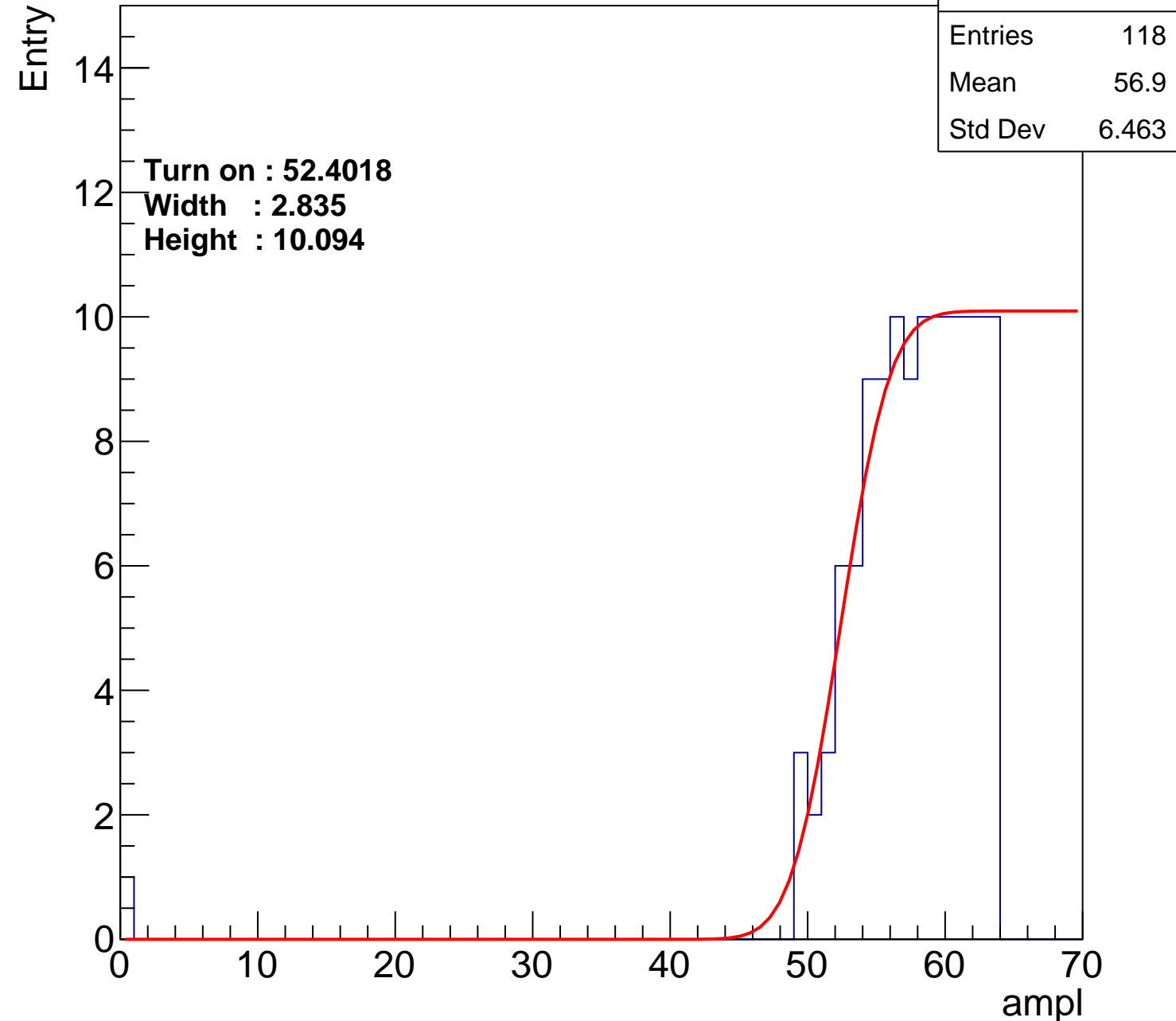
Width : 2.835

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch13

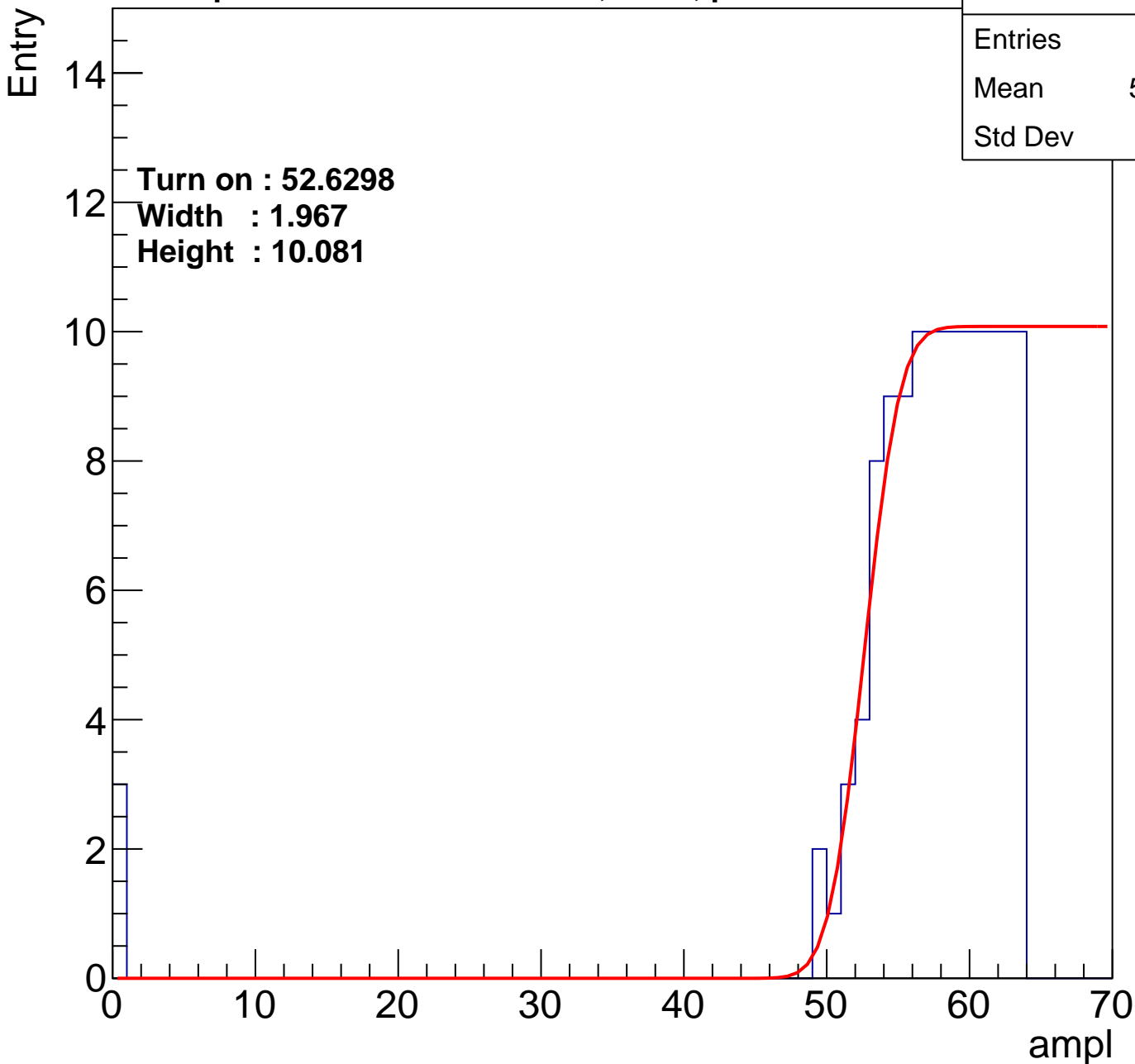
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	119
Mean	56.08
Std Dev	9.7

**Turn on : 52.6298**

**Width : 1.967**

**Height : 10.081**

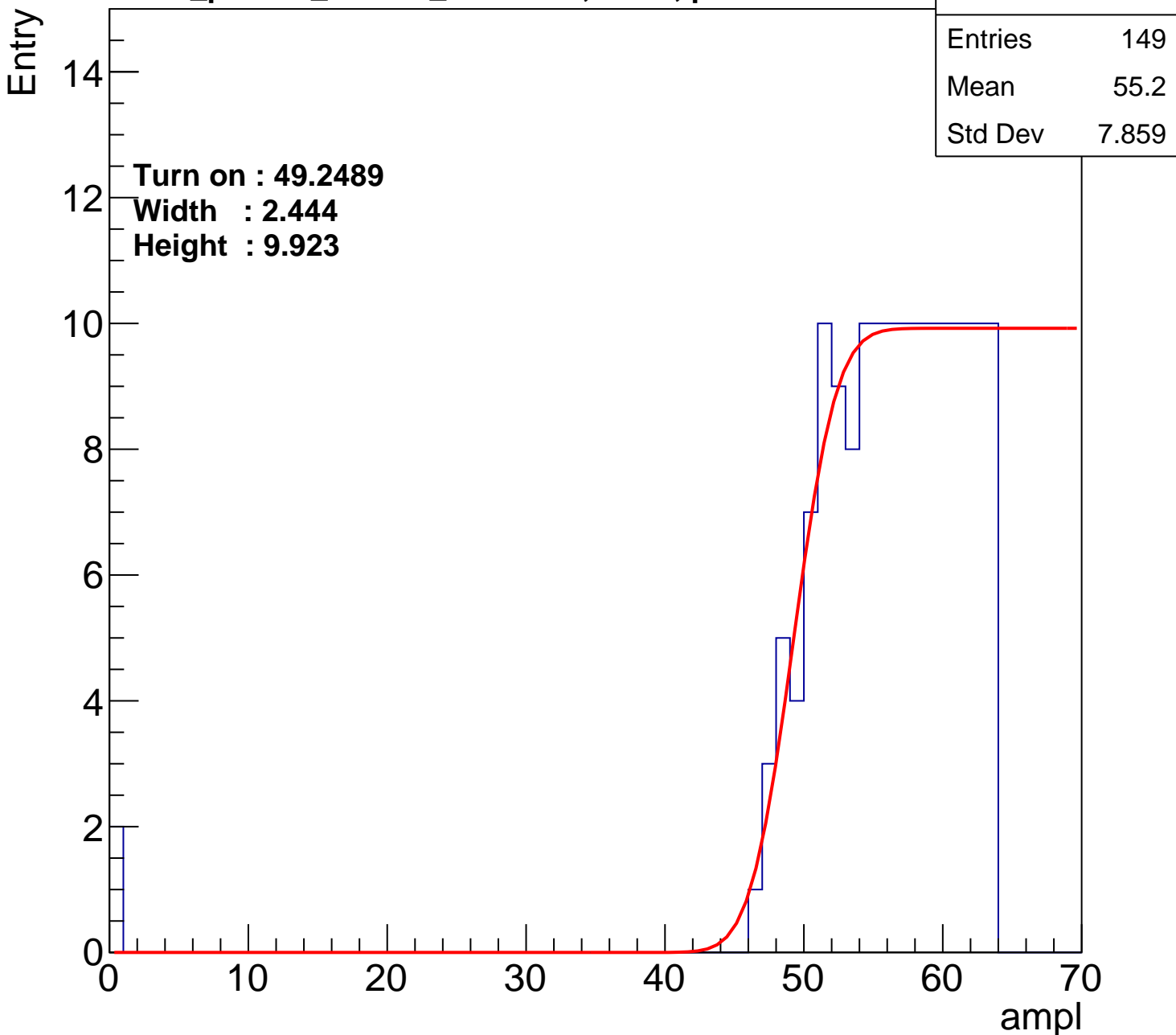


**calib\_packv5\_040323\_1717.root, FC#2, port C3**

**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	149
Mean	55.2
Std Dev	7.859

**Height : 9.923**





# B0L103S, U23-ch15

calib\_packv5\_040323\_1717.root, FC#2, port C3

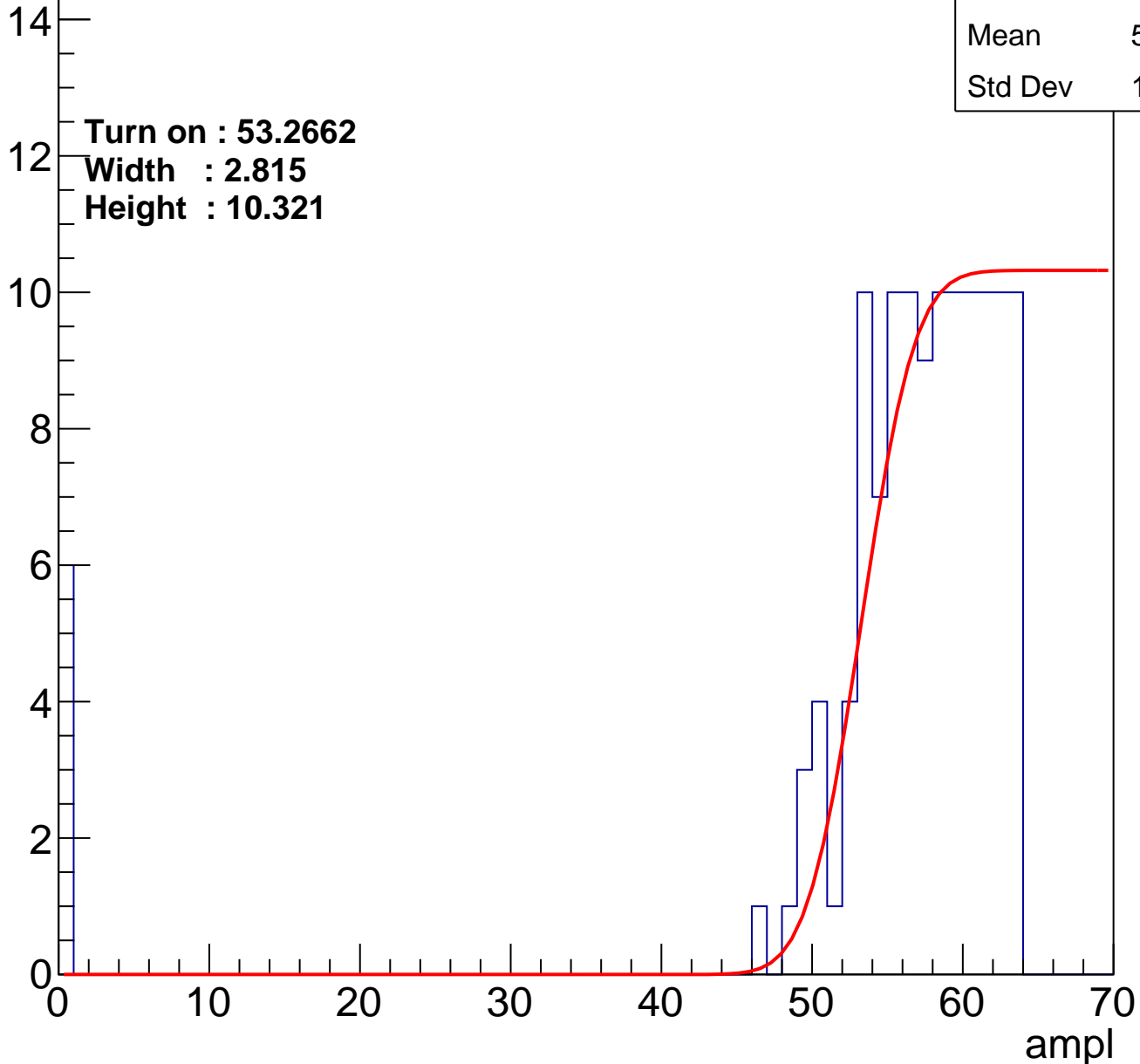
Entries	126
Mean	54.45
Std Dev	12.78

Turn on : 53.2662

Width : 2.815

Height : 10.321

Entry



# B0L103S, U23-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	56.29
Std Dev	9.961

Turn on : 53.8281

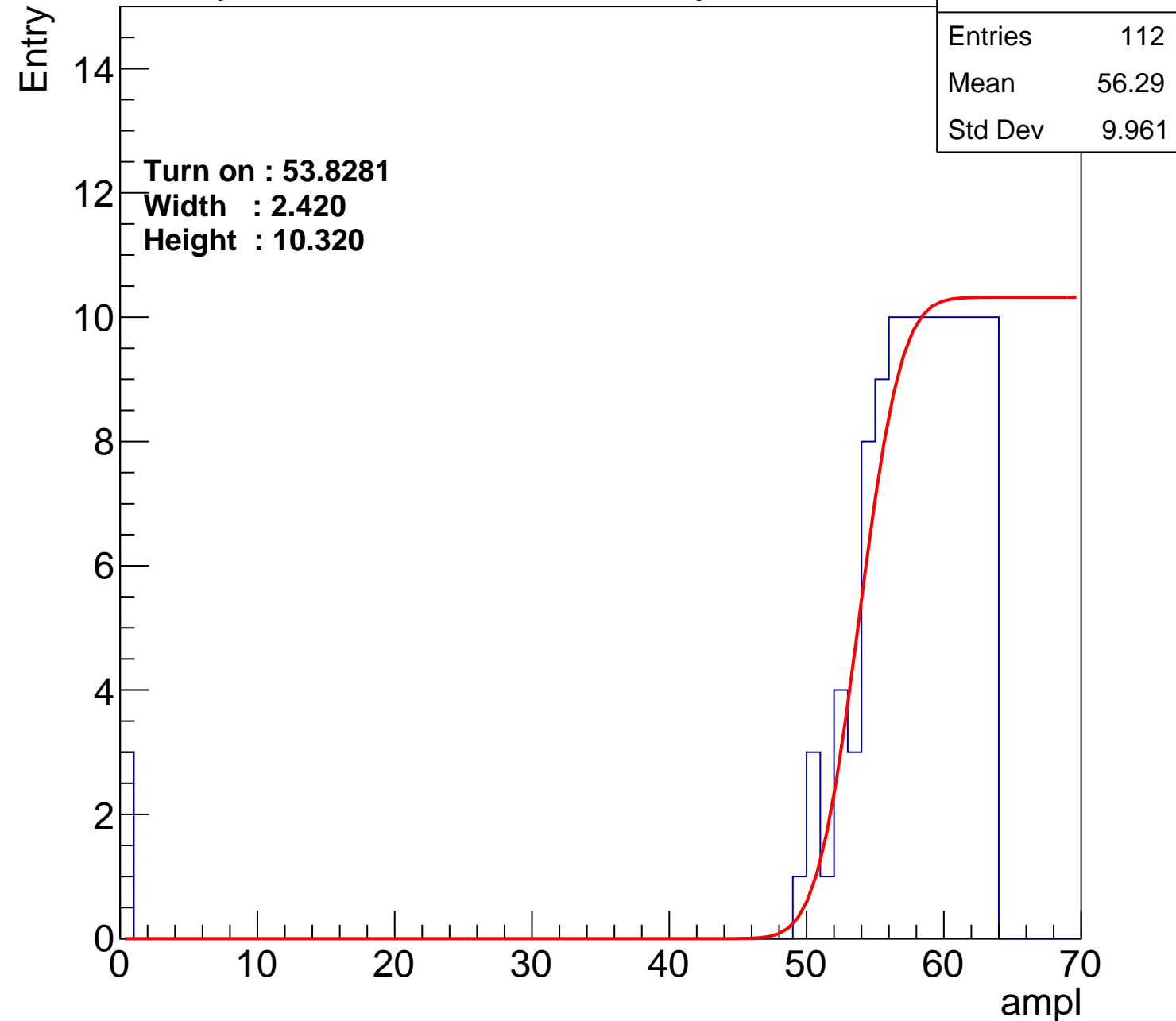
Width : 2.420

Height : 10.320

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.85
Std Dev	9.527

Turn on : 51.5952

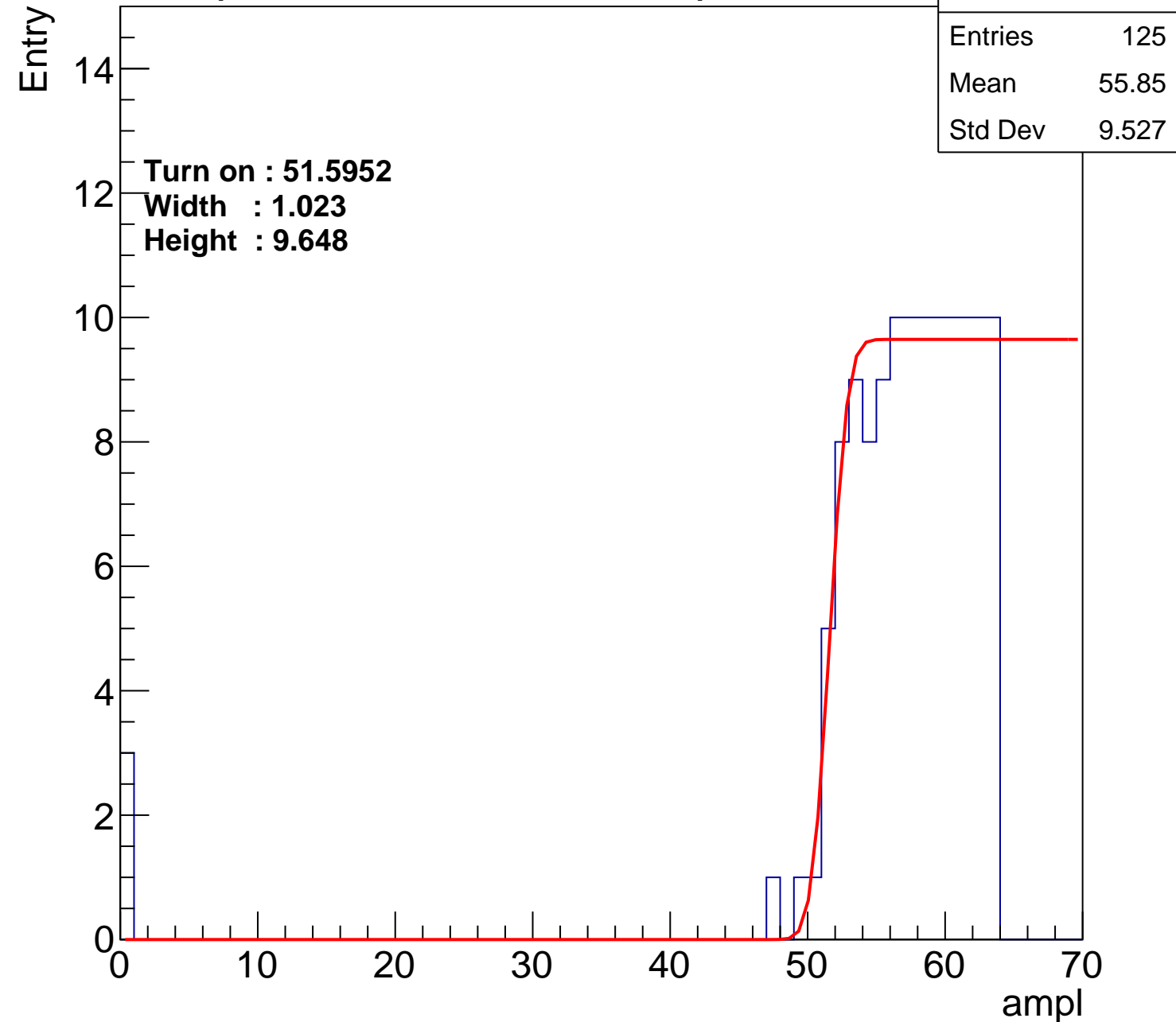
Width : 1.023

Height : 9.648

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch18

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	56.09
Std Dev	8.044

**Turn on : 51.2477**

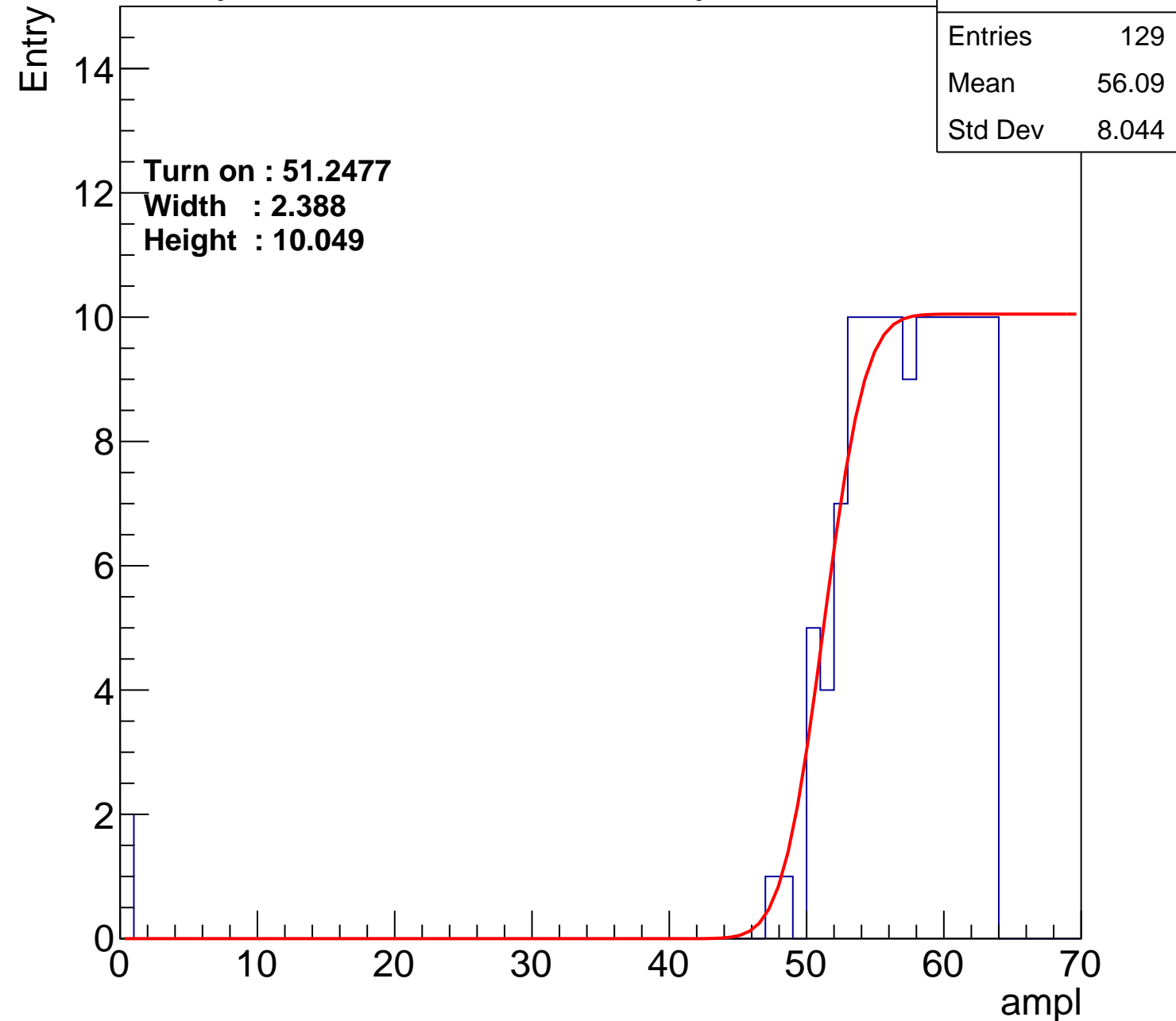
**Width : 2.388**

**Height : 10.049**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	56.42
Std Dev	8.311

Turn on : 52.8873

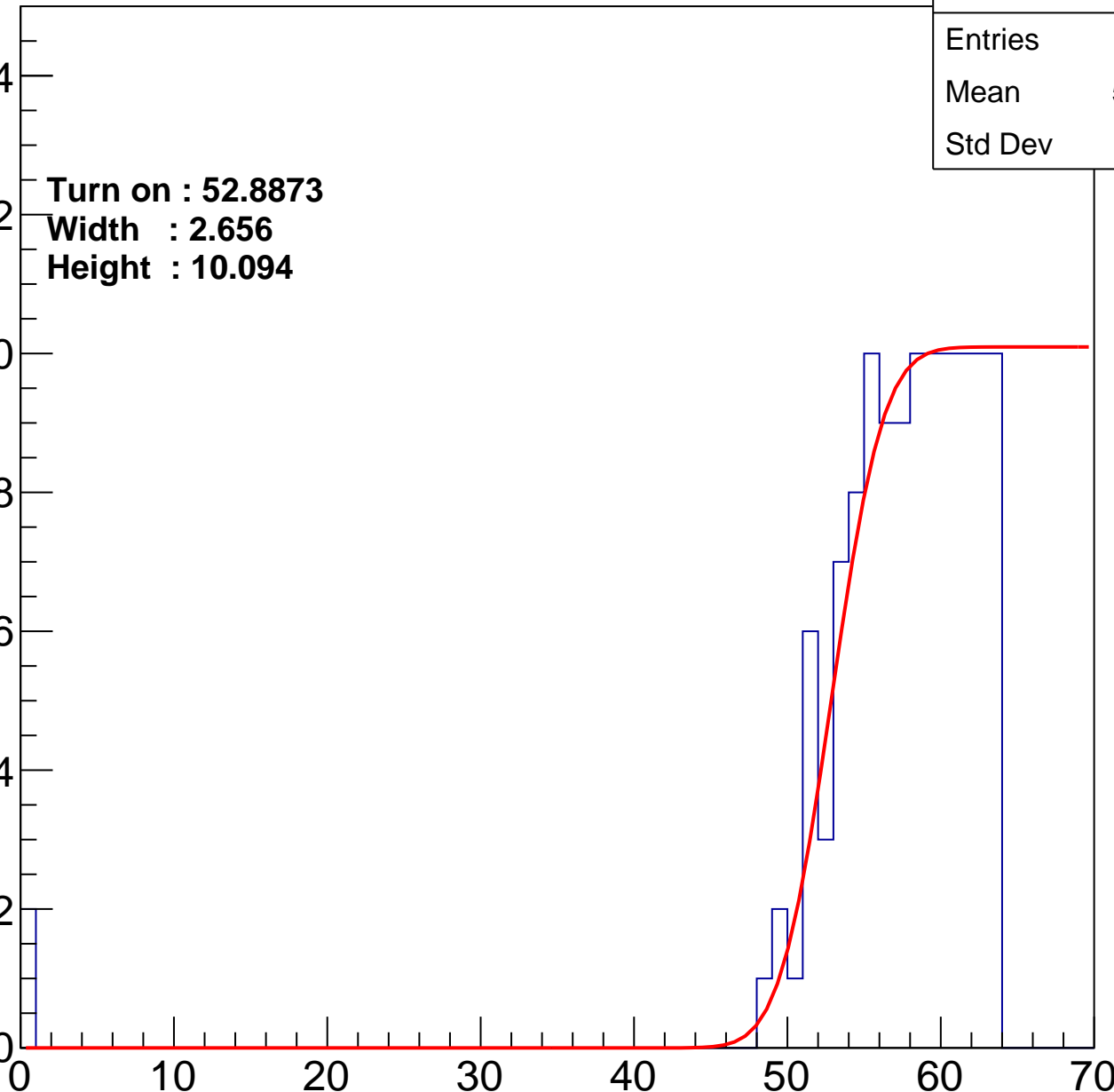
Width : 2.656

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

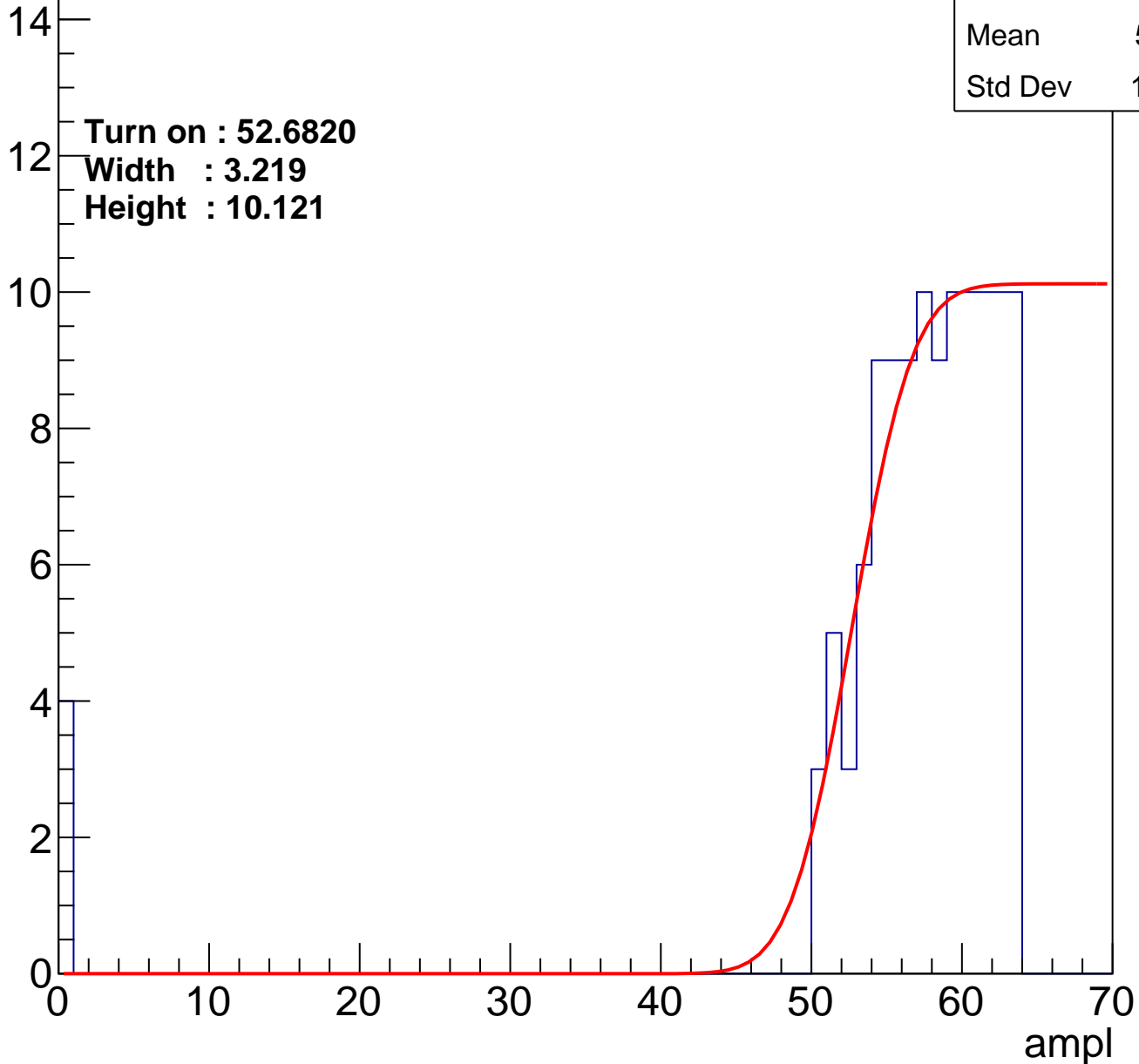
Entries	117
Mean	55.61
Std Dev	11.06

Turn on : 52.6820

Width : 3.219

Height : 10.121

Entry



# B0L103S, U23-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

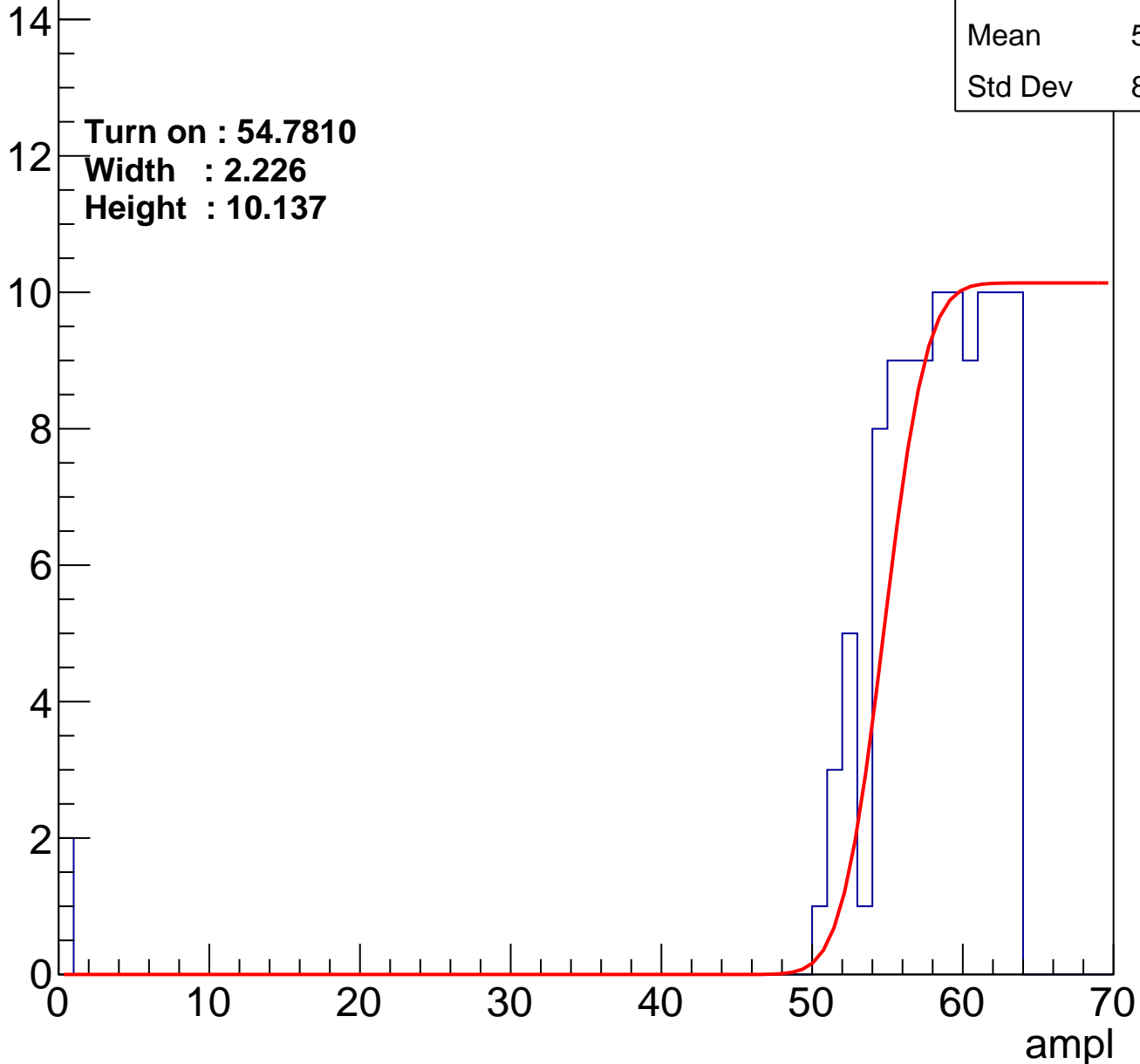
Entries	106
Mean	56.89
Std Dev	8.585

Turn on : 54.7810

Width : 2.226

Height : 10.137

Entry



# B0L103S, U23-ch22

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.89
Std Dev	10.26

Turn on : 51.0874

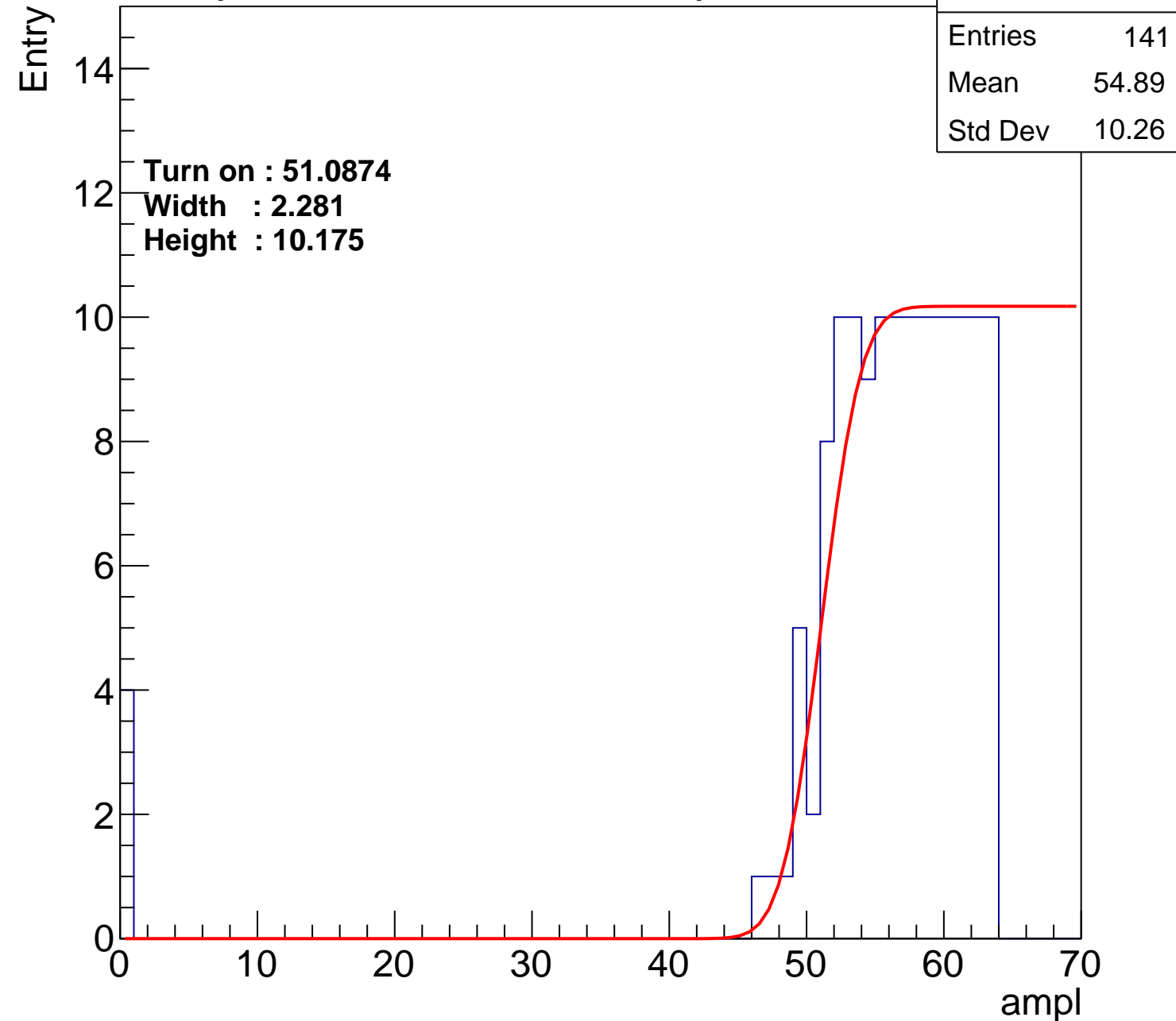
Width : 2.281

Height : 10.175

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

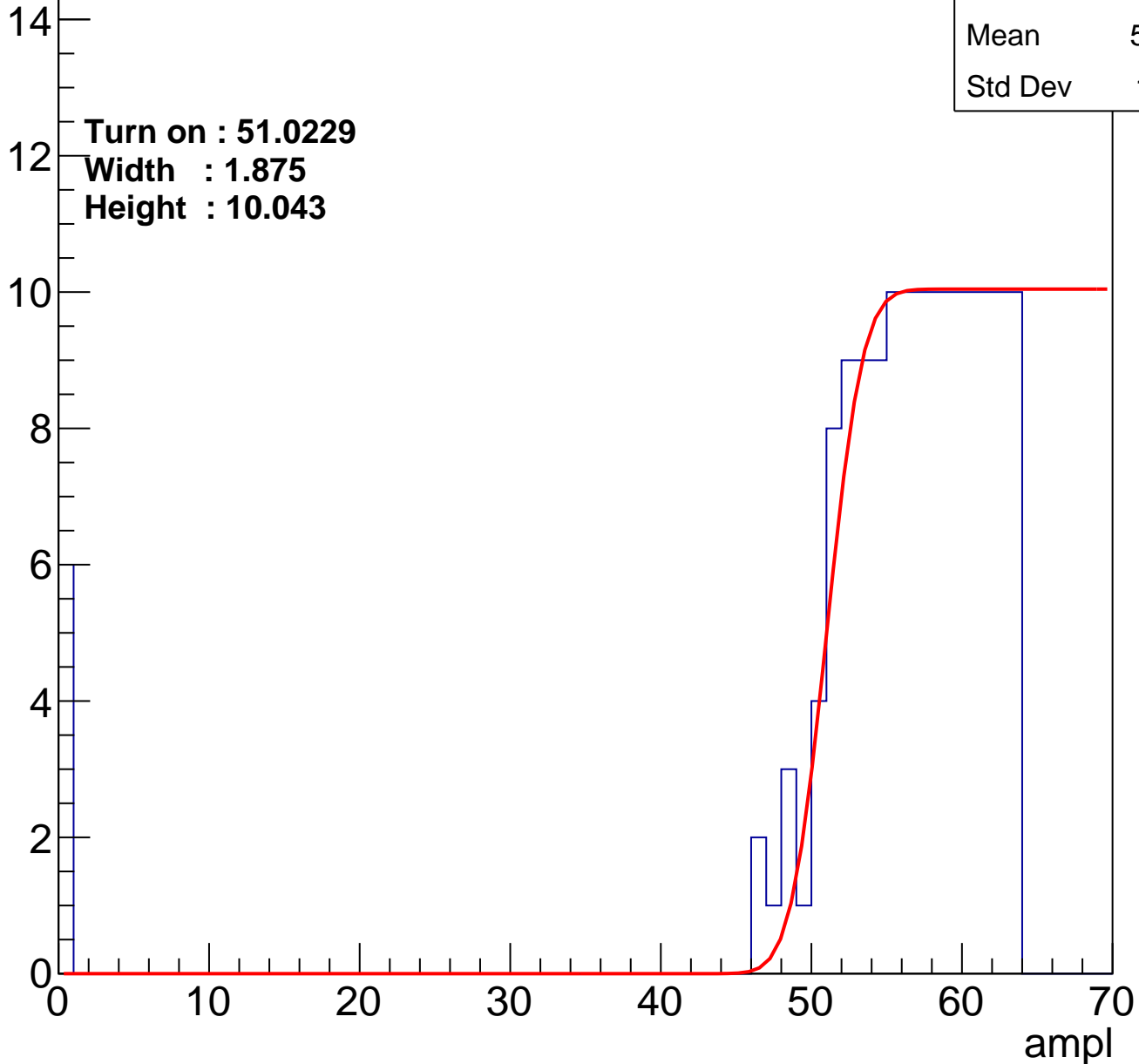
Entries	142
Mean	54.09
Std Dev	12.11

Turn on : 51.0229

Width : 1.875

Height : 10.043

Entry



# B0L103S, U23-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	56.46
Std Dev	8.238

Turn on : 52.2306

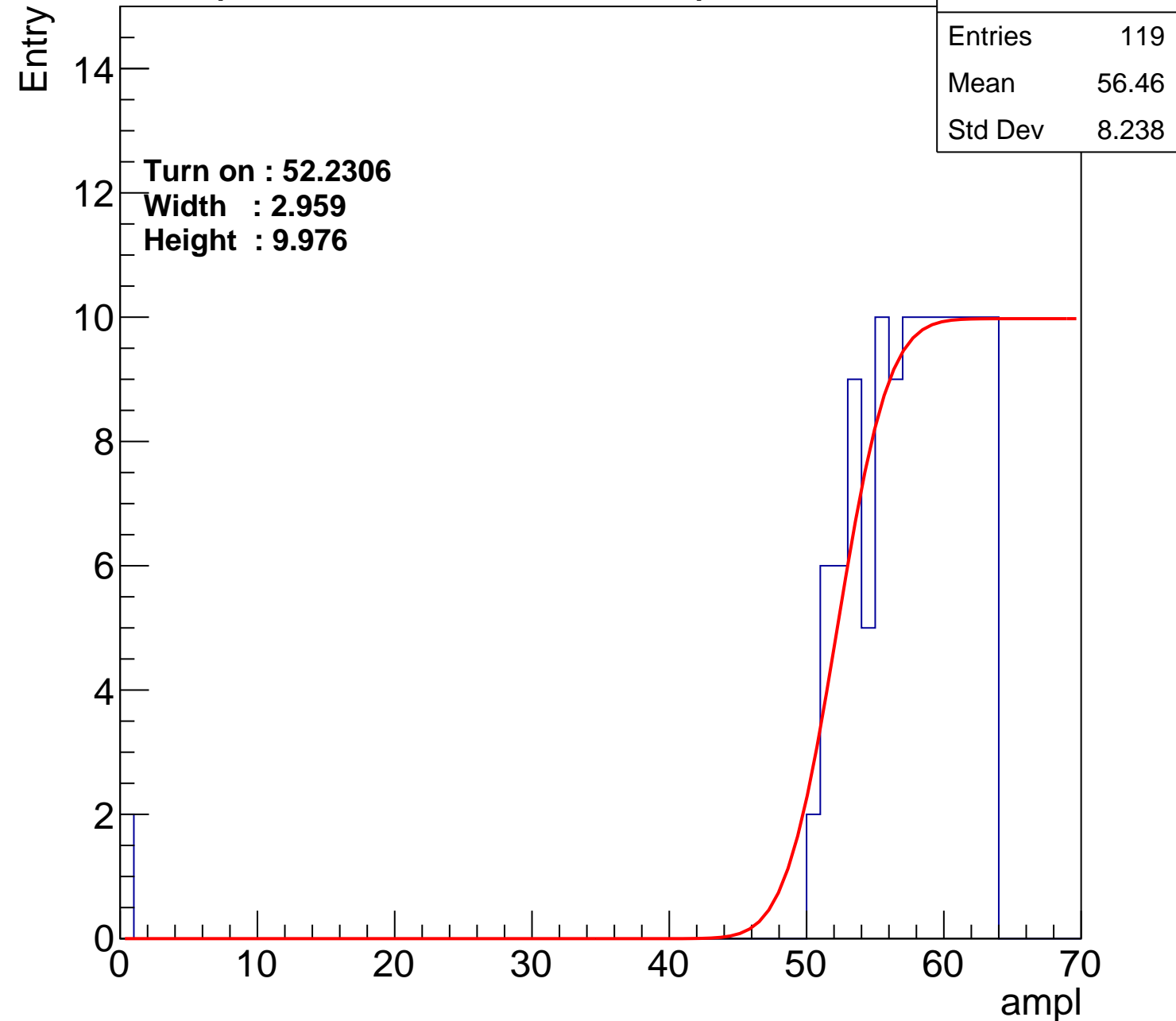
Width : 2.959

Height : 9.976

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.92
Std Dev	6.442

Turn on : 50.5655

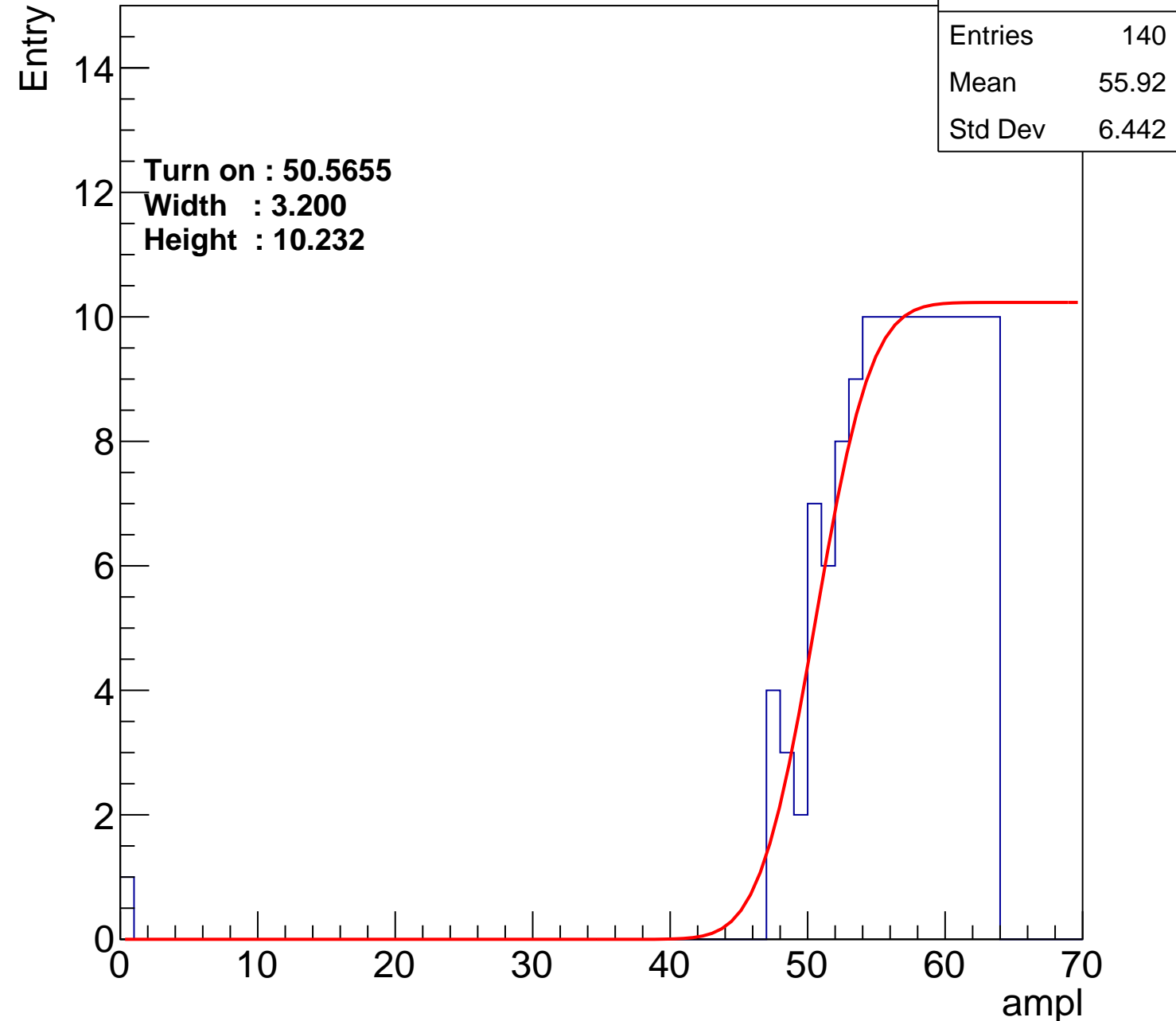
Width : 3.200

Height : 10.232

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.31
Std Dev	10.94

Turn on : 53.0374

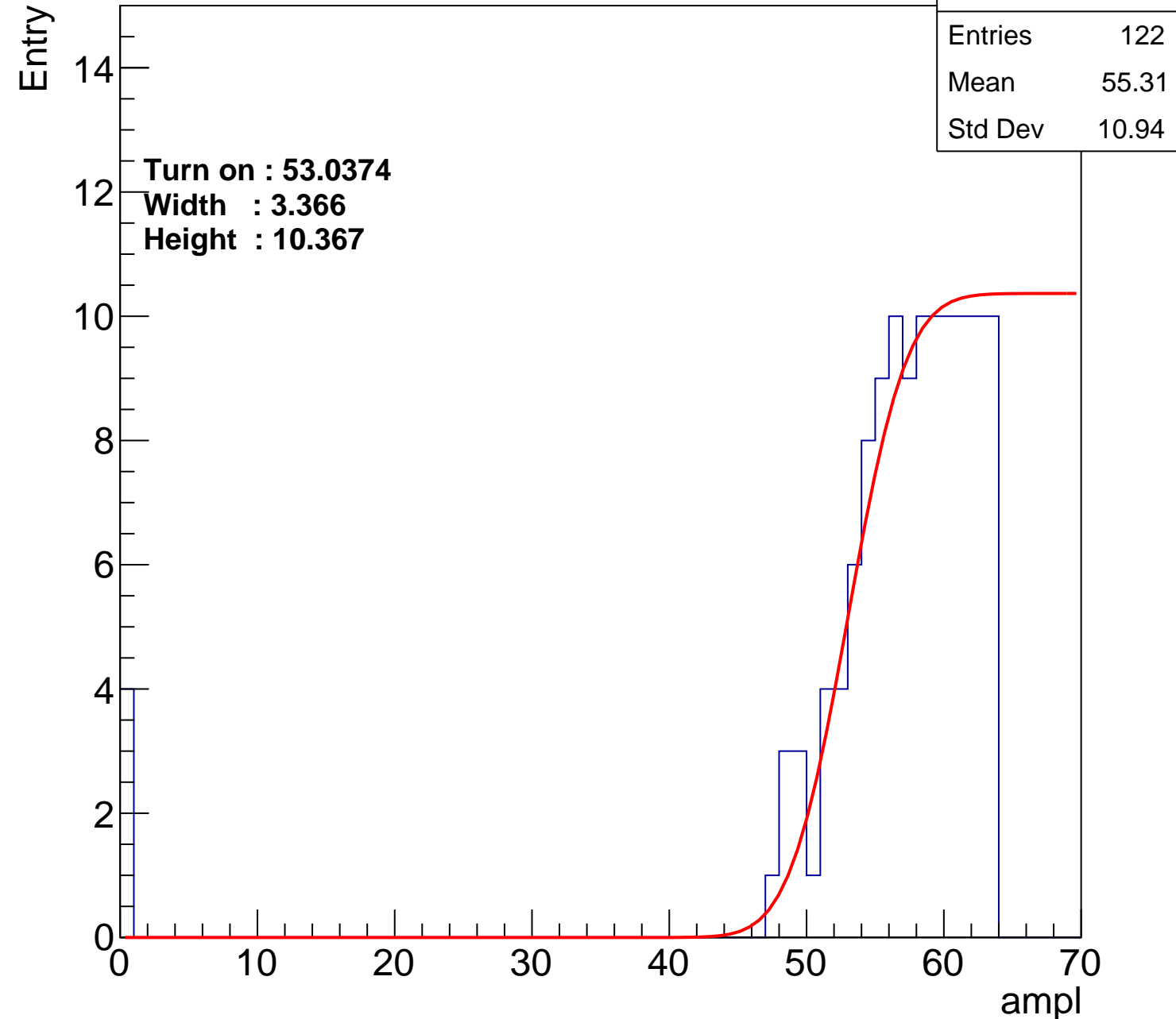
Width : 3.366

Height : 10.367

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	56.11
Std Dev	9.728

Turn on : 52.2217

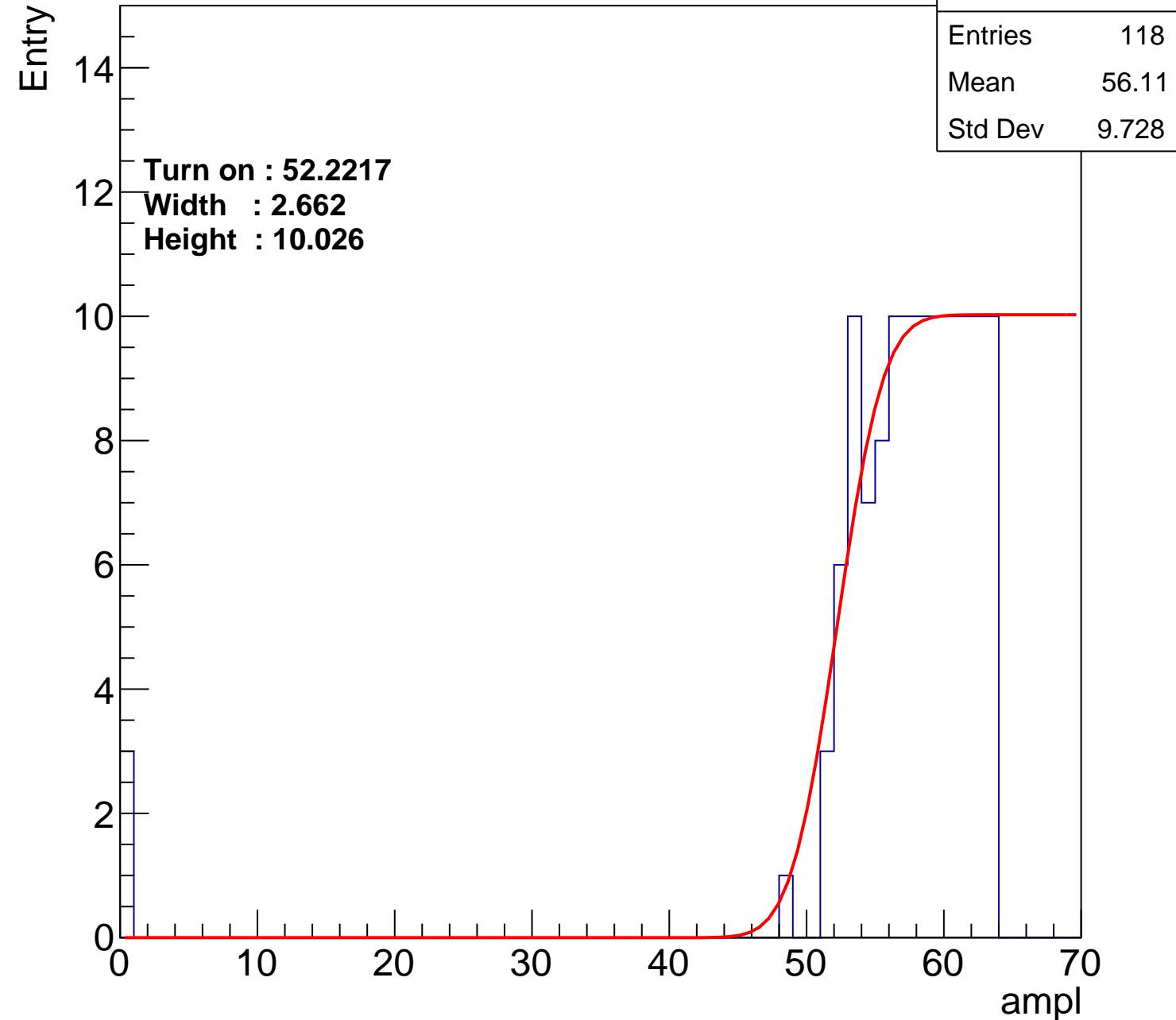
Width : 2.662

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.63
Std Dev	9.374

Turn on : 51.4450

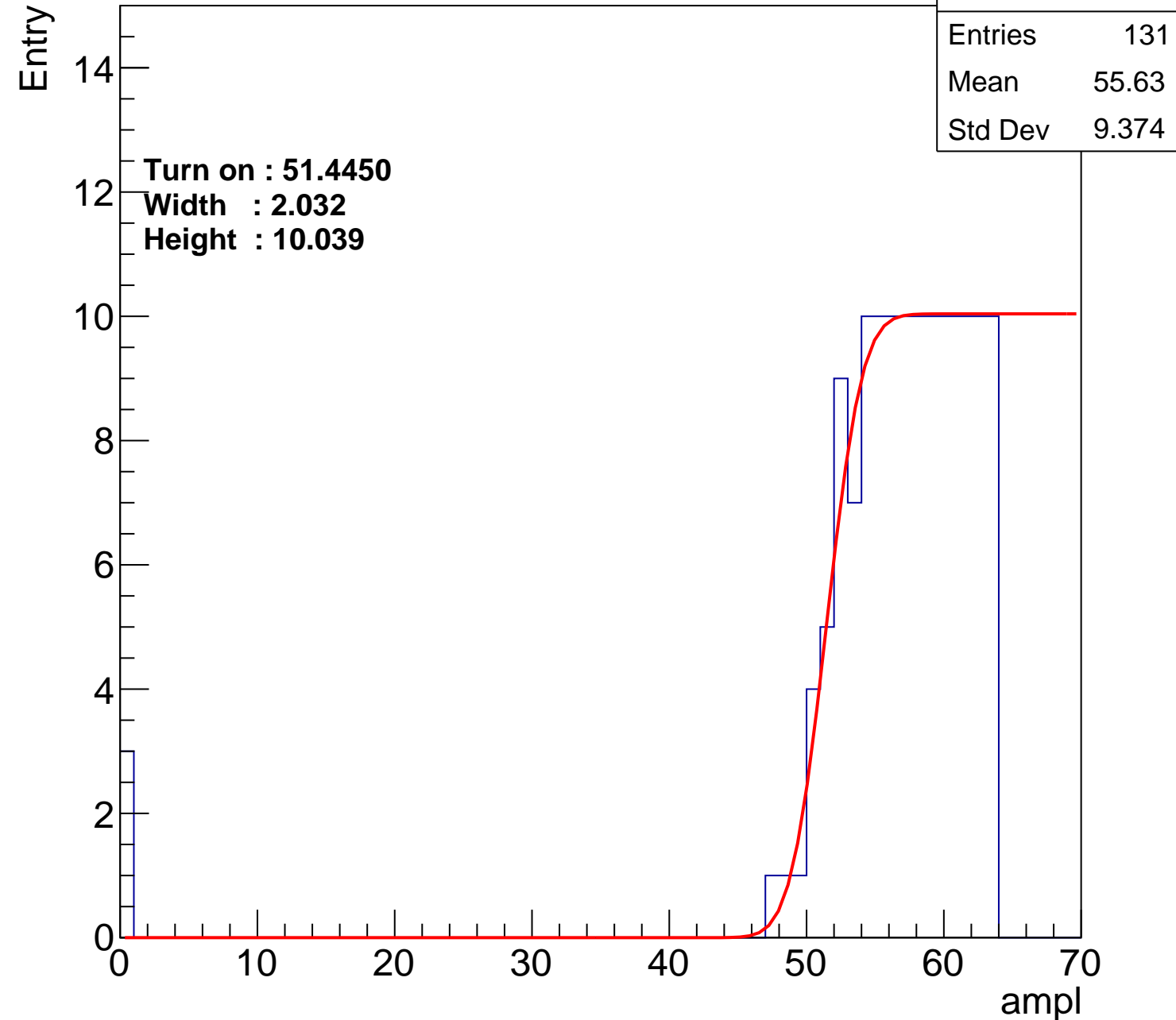
Width : 2.032

Height : 10.039

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.67
Std Dev	6.453

Turn on : 51.9524

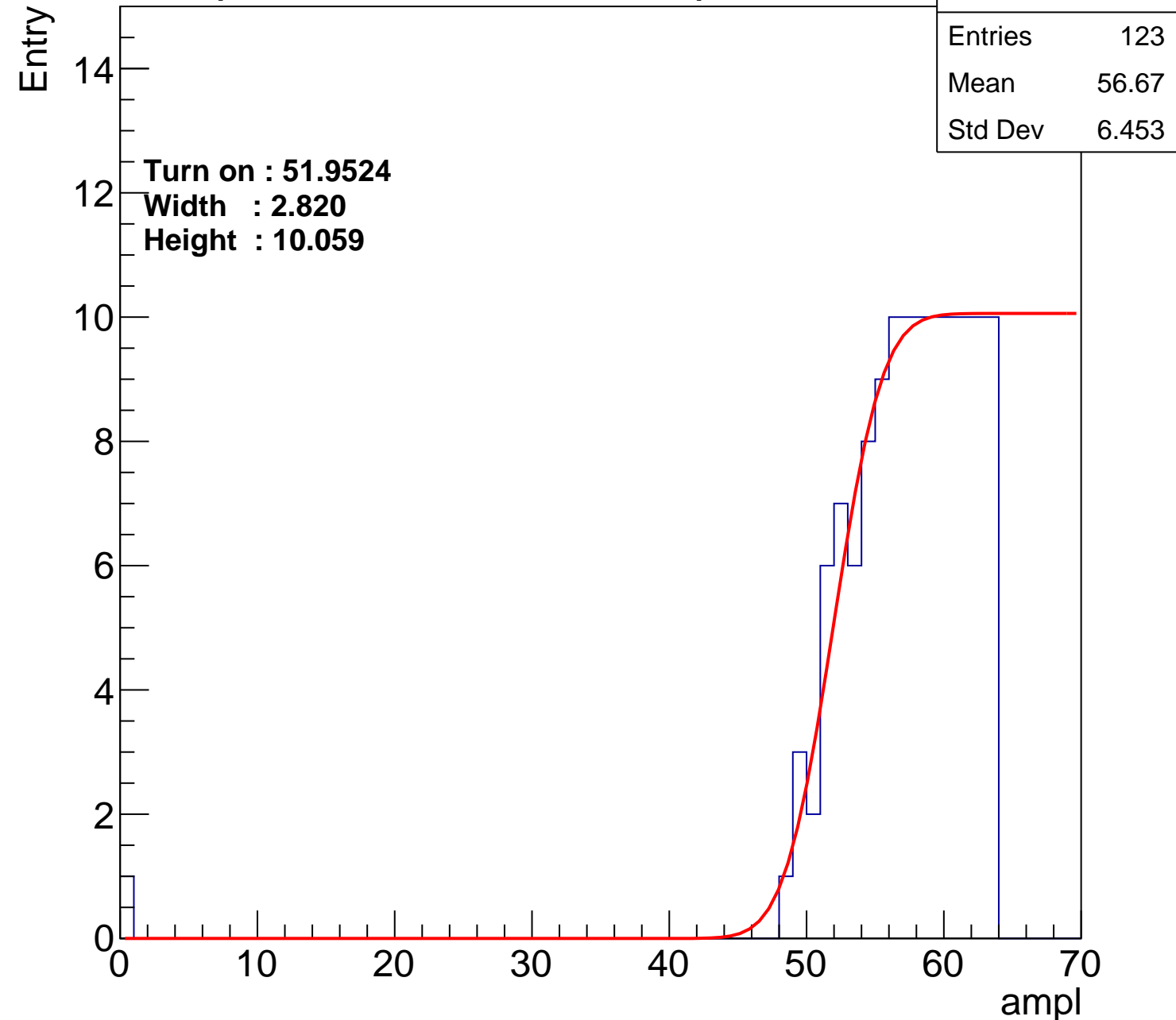
Width : 2.820

Height : 10.059

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.44
Std Dev	10.75

Turn on : 51.9650

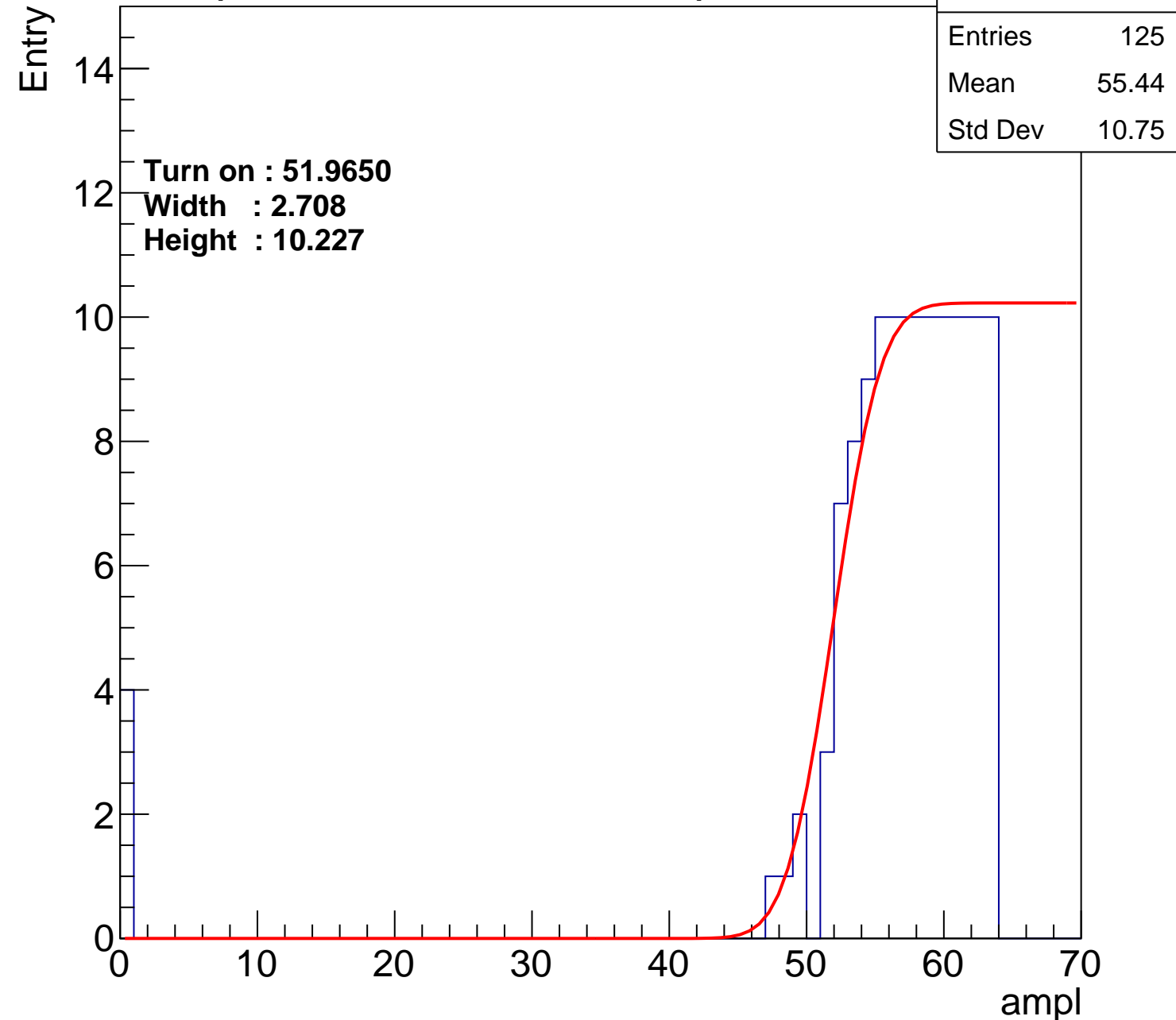
Width : 2.708

Height : 10.227

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.6
Std Dev	11.44

Turn on : 51.3087

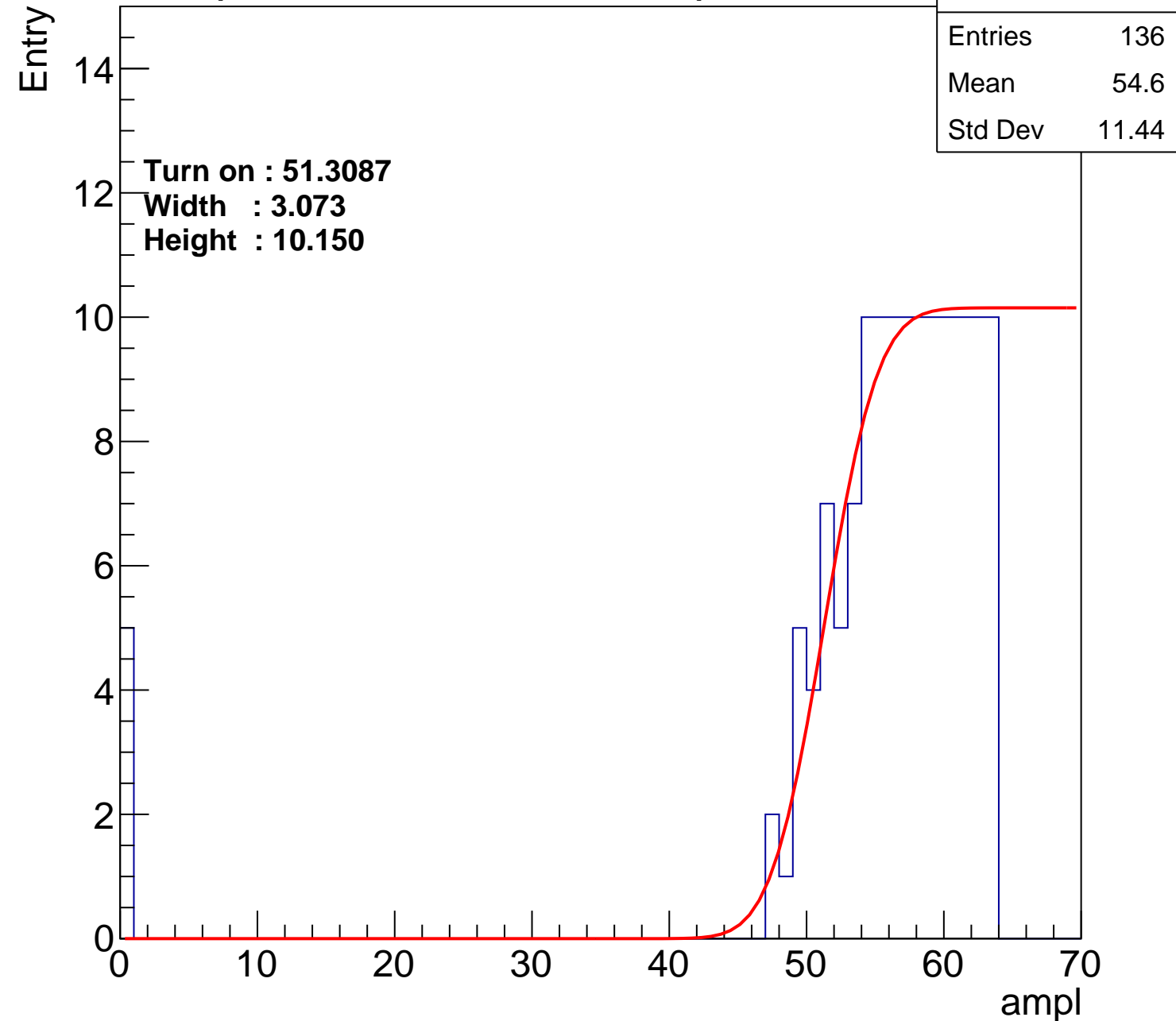
Width : 3.073

Height : 10.150

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.27
Std Dev	10.67

Turn on : 52.0131

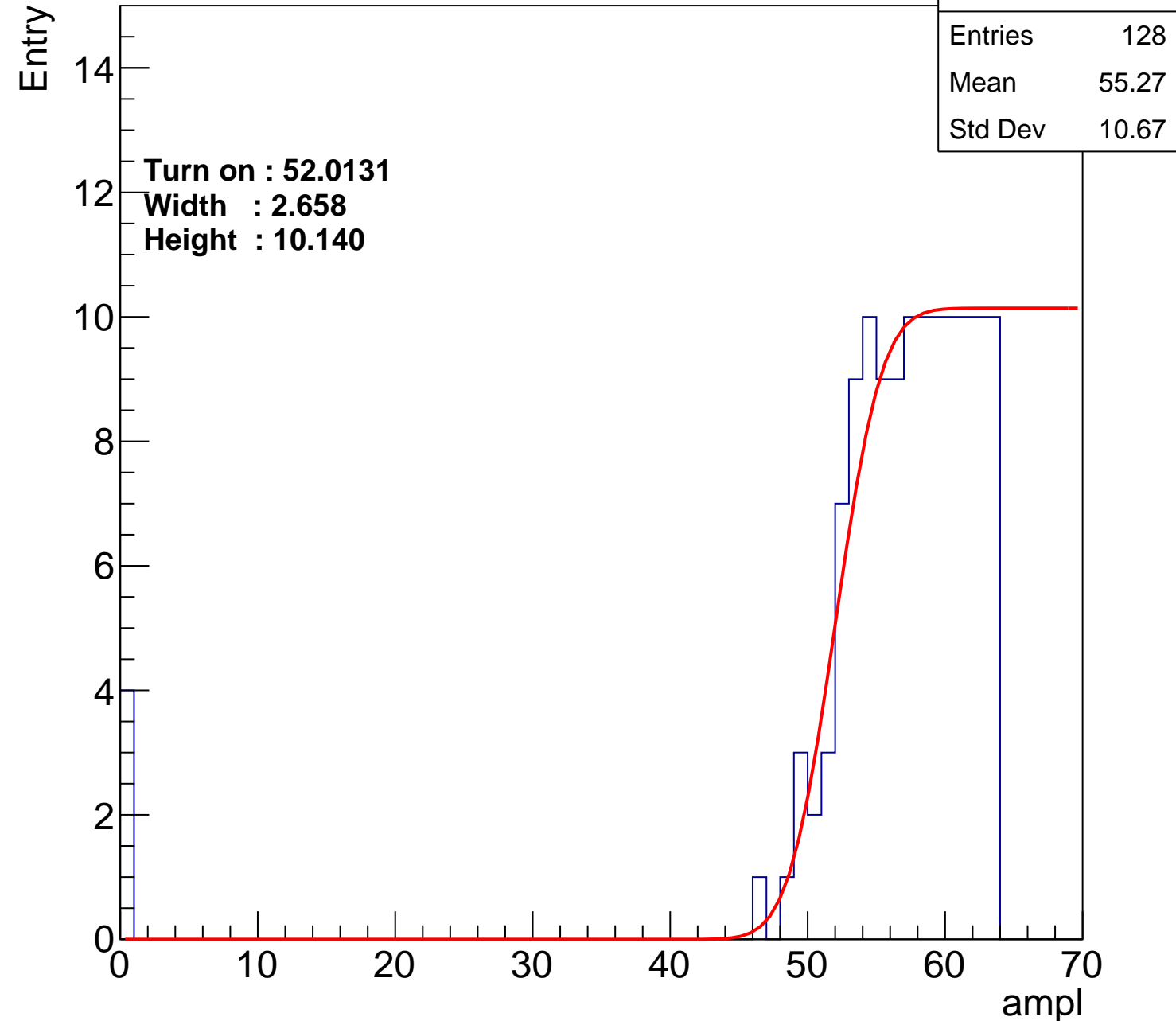
Width : 2.658

Height : 10.140

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch33

calib\_packv5\_040323\_1717.root, FC#2, port C3

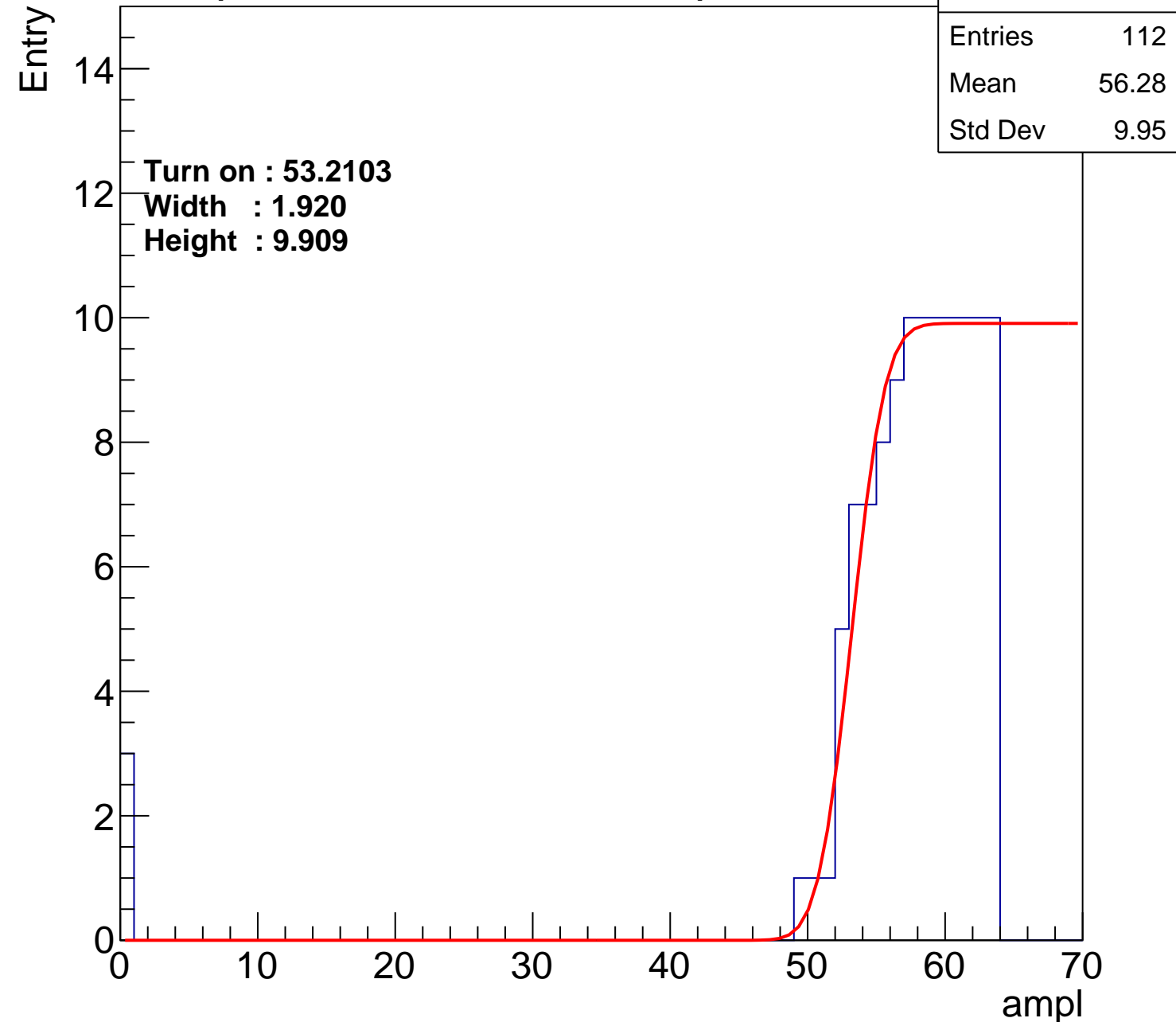
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 53.2103**  
**Width : 1.920**  
**Height : 9.909**

Entries	112
Mean	56.28
Std Dev	9.95

ampl



# B0L103S, U23-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

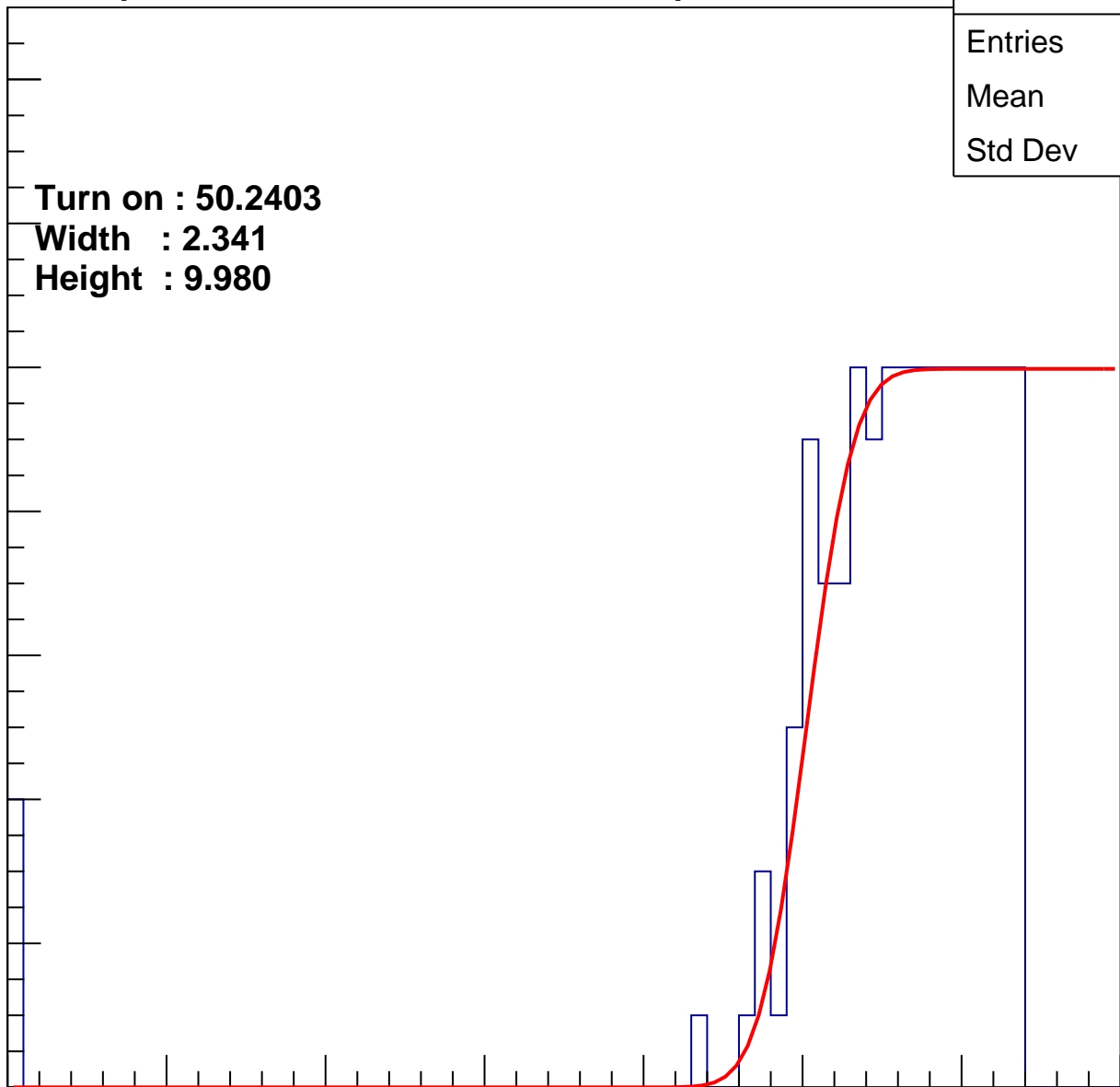
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 50.2403  
Width : 2.341  
Height : 9.980

Entries	147
Mean	54.56
Std Dev	10.17

ampl



# B0L103S, U23-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.61
Std Dev	10.98

Turn on : 52.8641

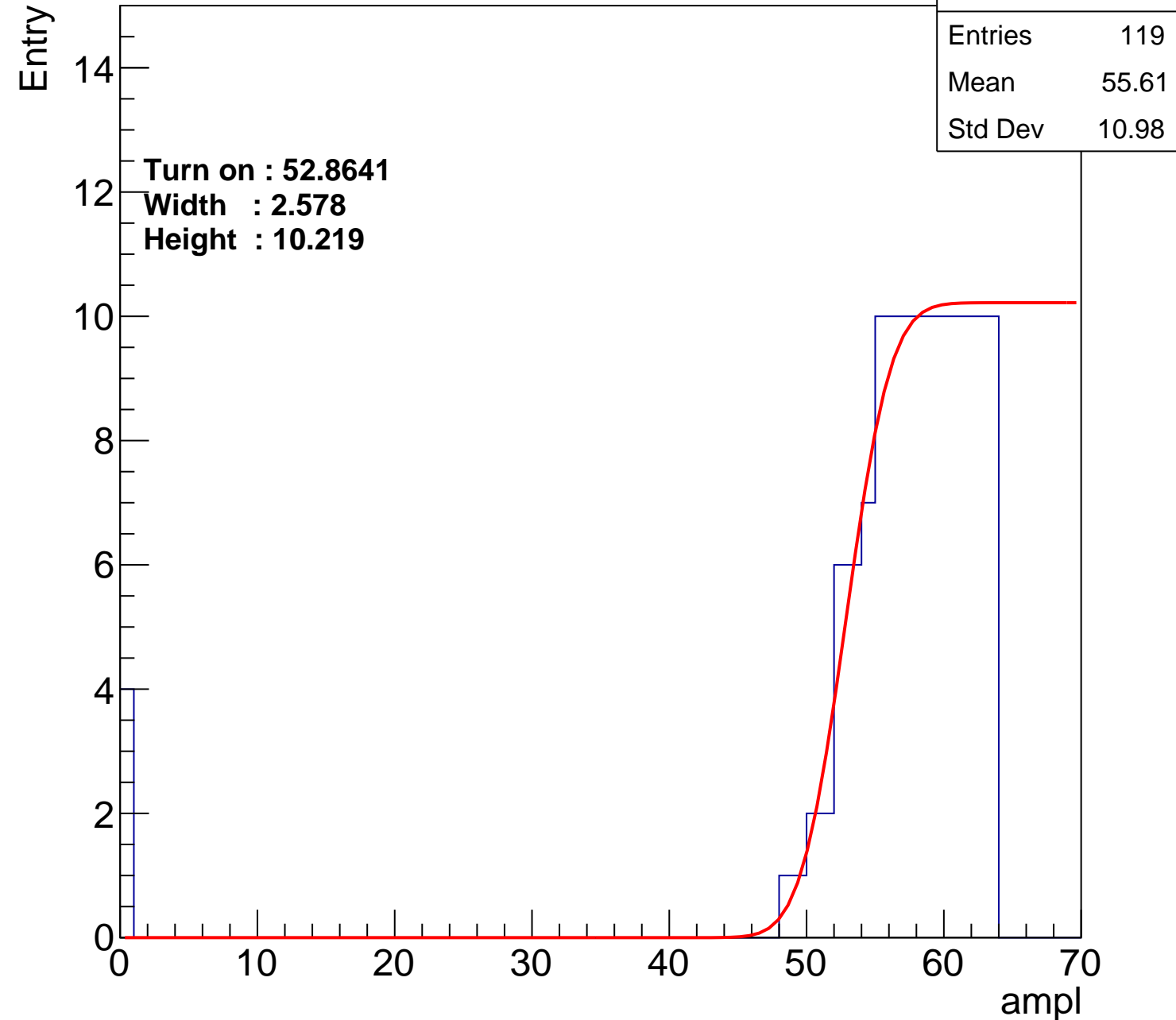
Width : 2.578

Height : 10.219

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	56.09
Std Dev	6.398

Turn on : 50.6318

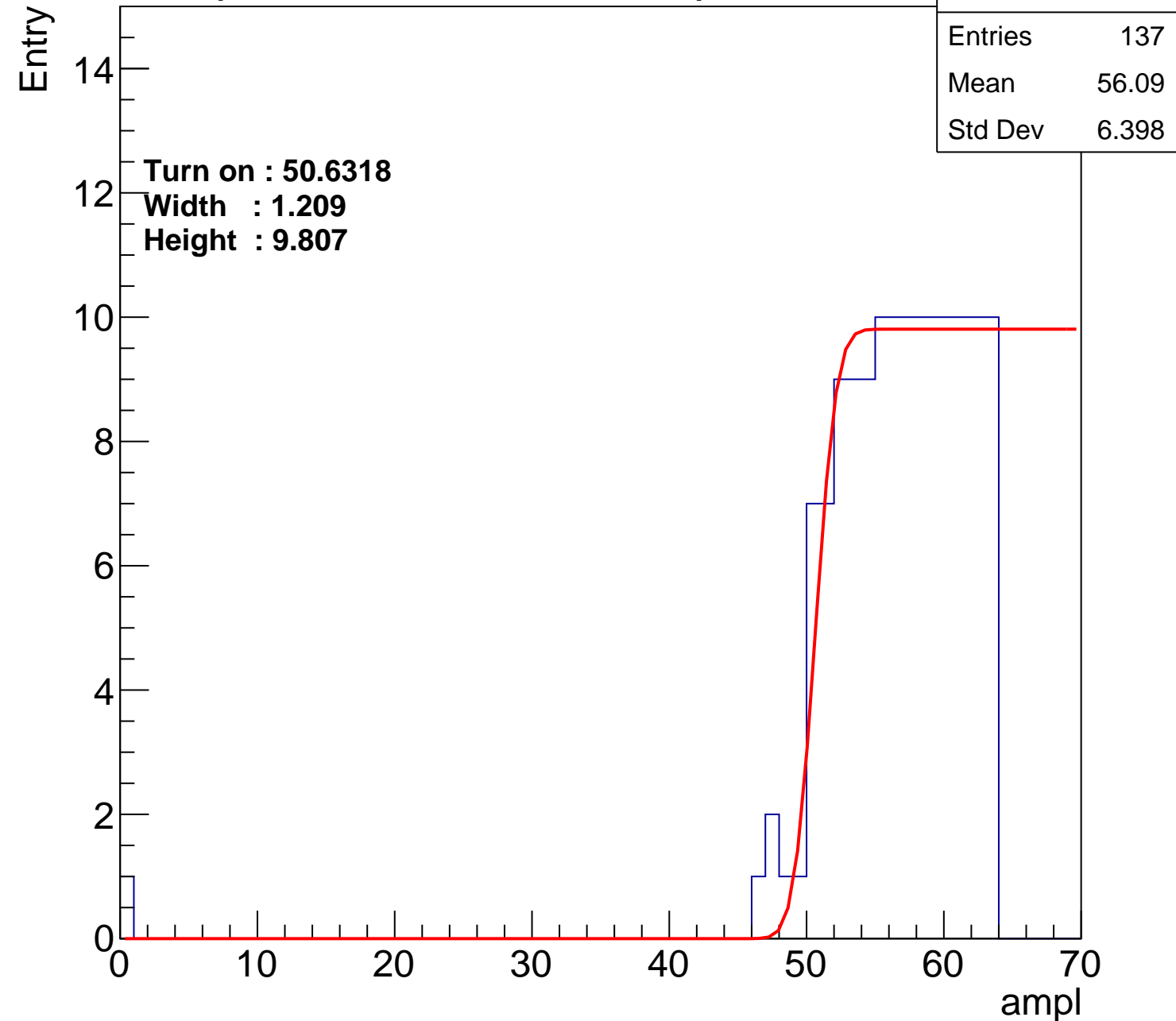
Width : 1.209

Height : 9.807

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.64
Std Dev	8.407

Turn on : 52.8194

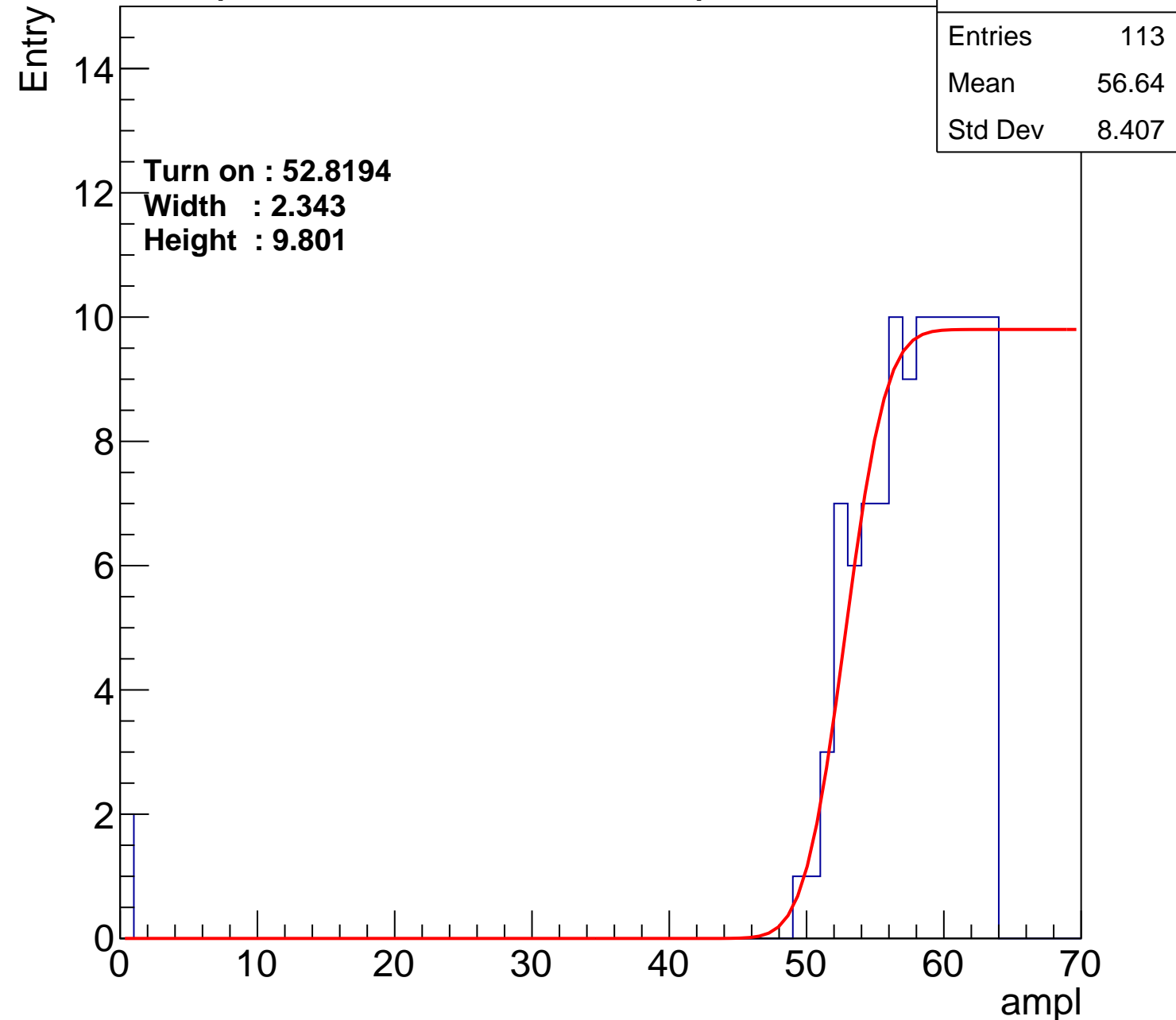
Width : 2.343

Height : 9.801

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.26
Std Dev	8.186

Turn on : 51.8485

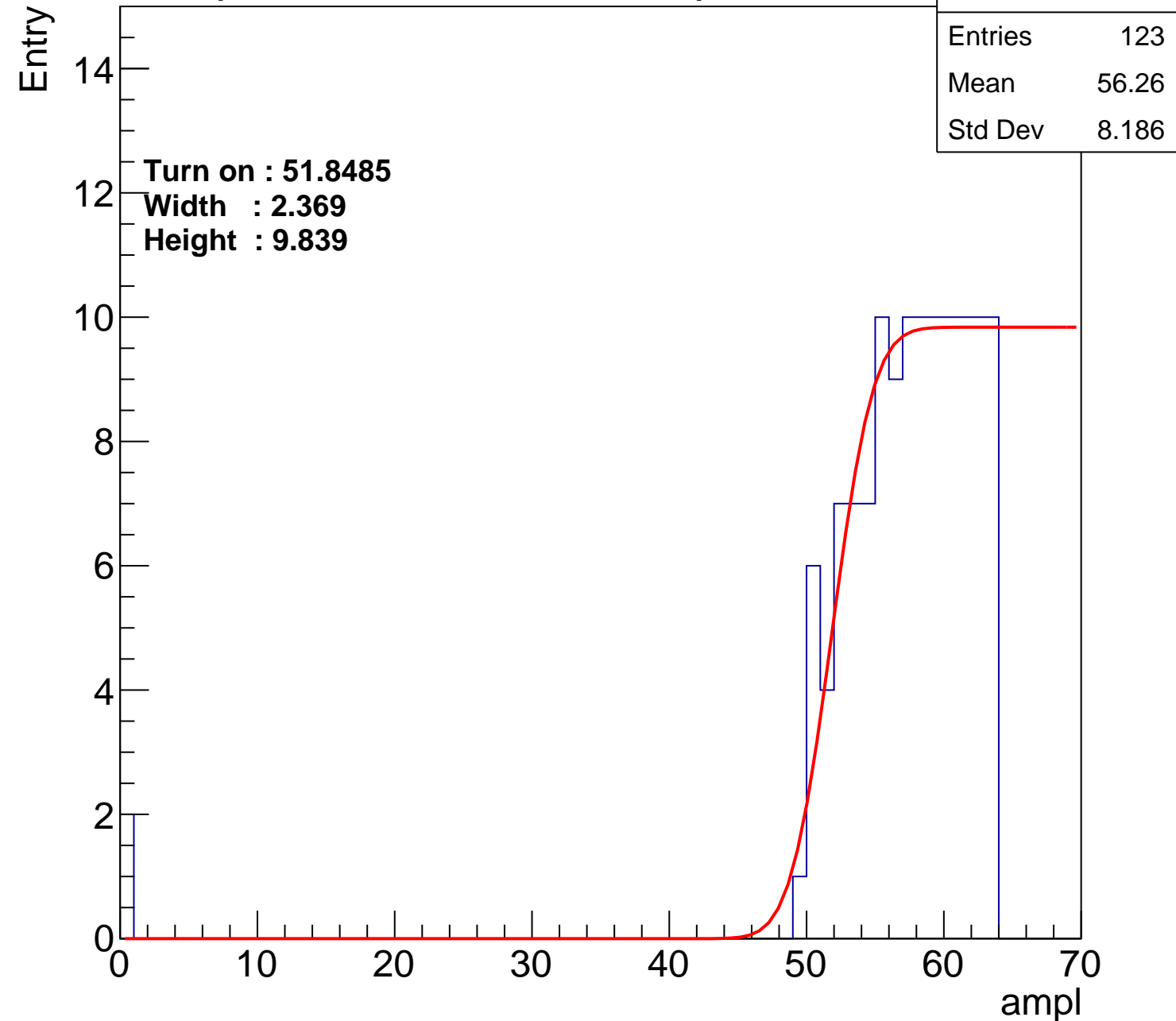
Width : 2.369

Height : 9.839

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.97
Std Dev	9.793

Turn on : 52.5512

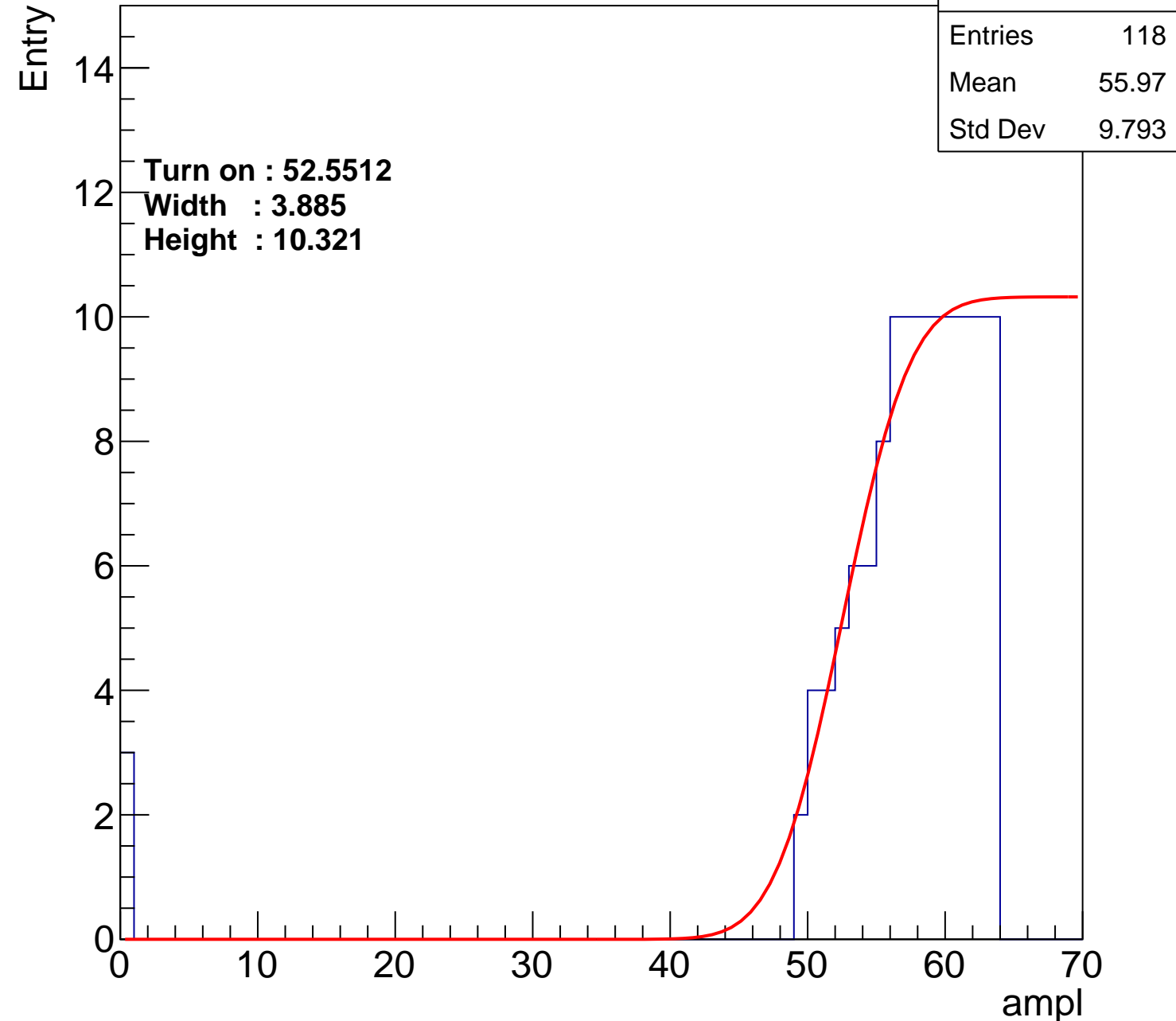
Width : 3.885

Height : 10.321

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.93
Std Dev	9.656

Turn on : 52.2455

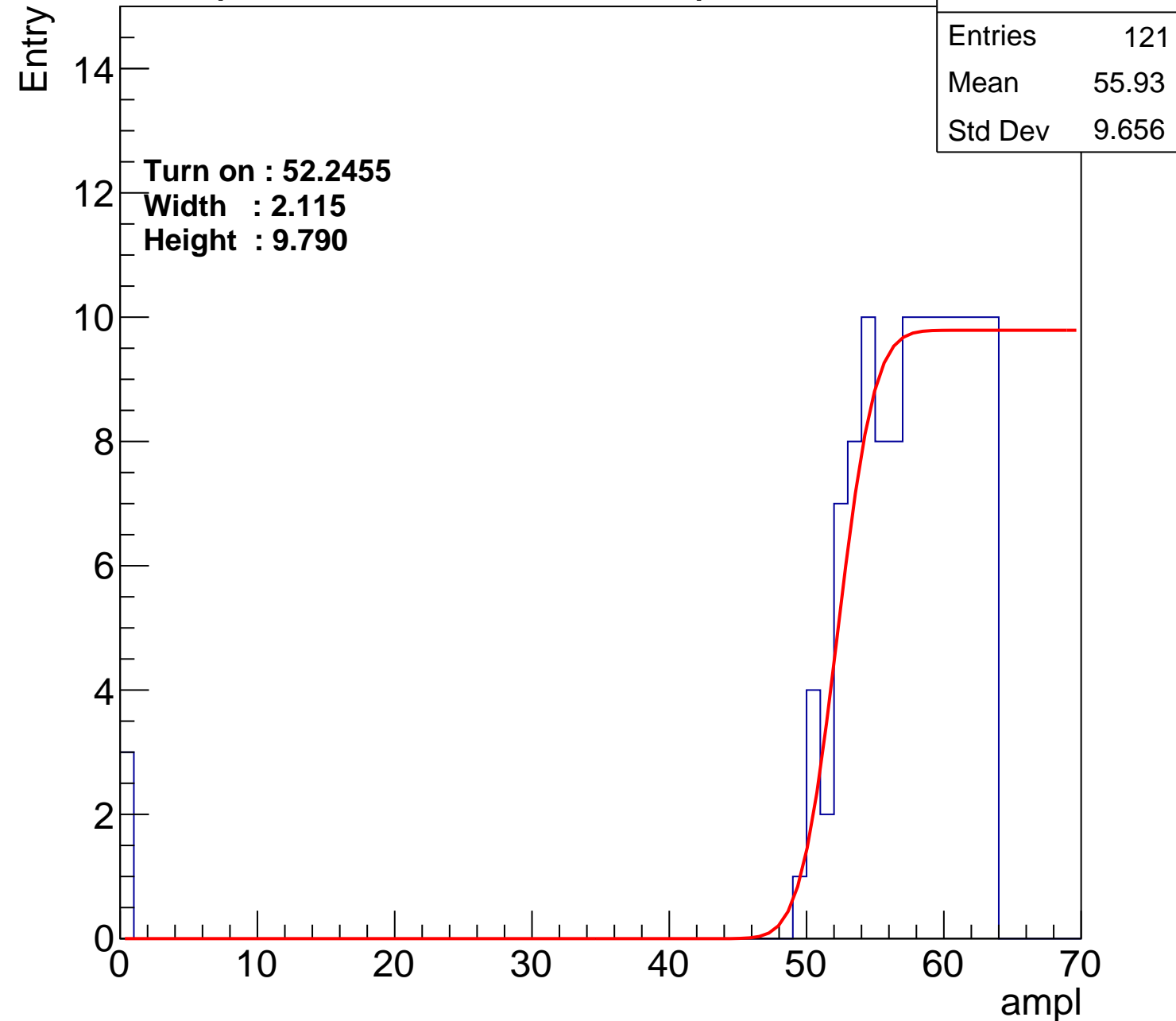
Width : 2.115

Height : 9.790

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

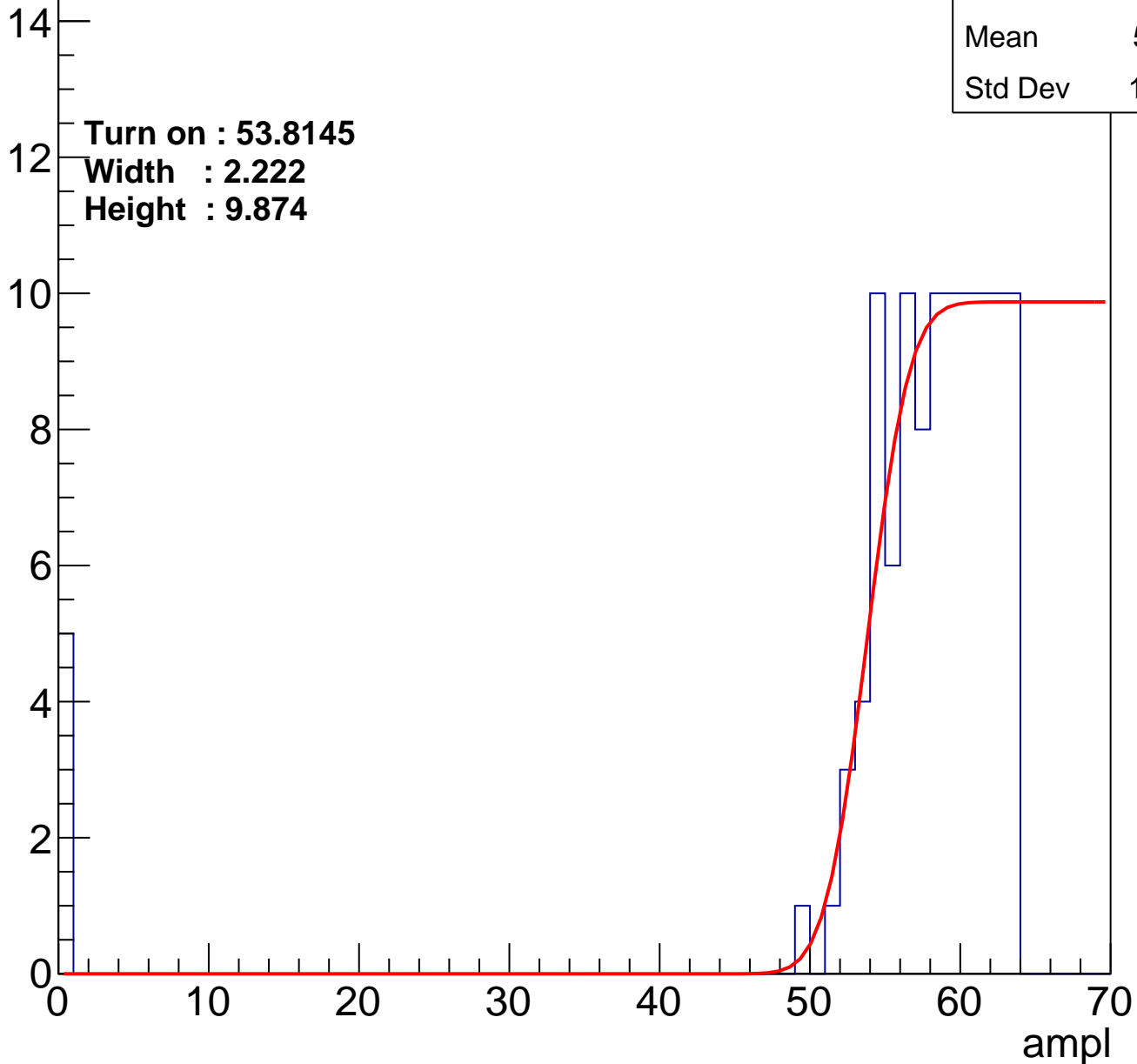
Entries	108
Mean	55.41
Std Dev	12.64

Turn on : 53.8145

Width : 2.222

Height : 9.874

Entry



# B0L103S, U23-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.68
Std Dev	9.389

Turn on : 51.2271

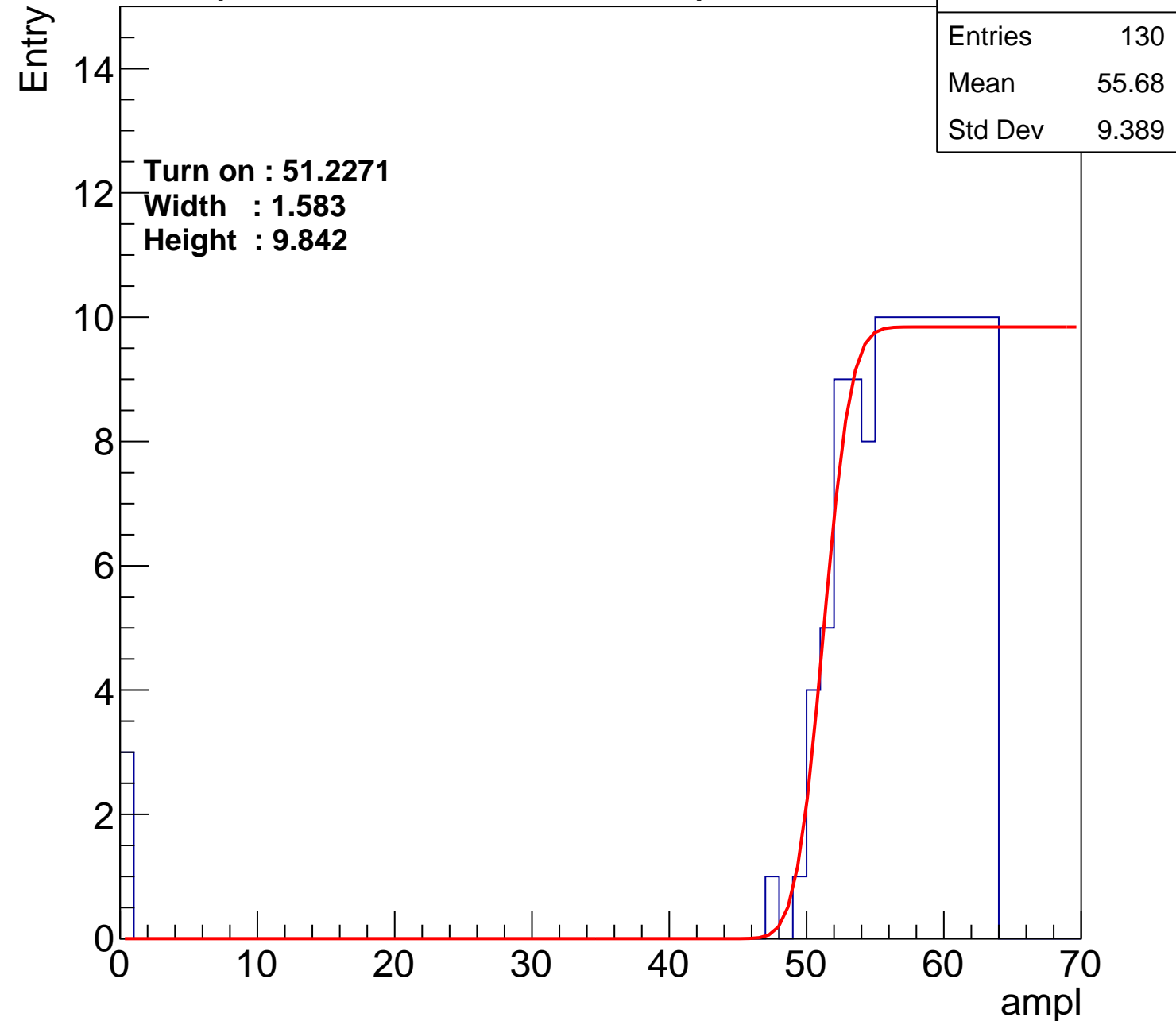
Width : 1.583

Height : 9.842

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	56.29
Std Dev	10.04

Turn on : 53.2825

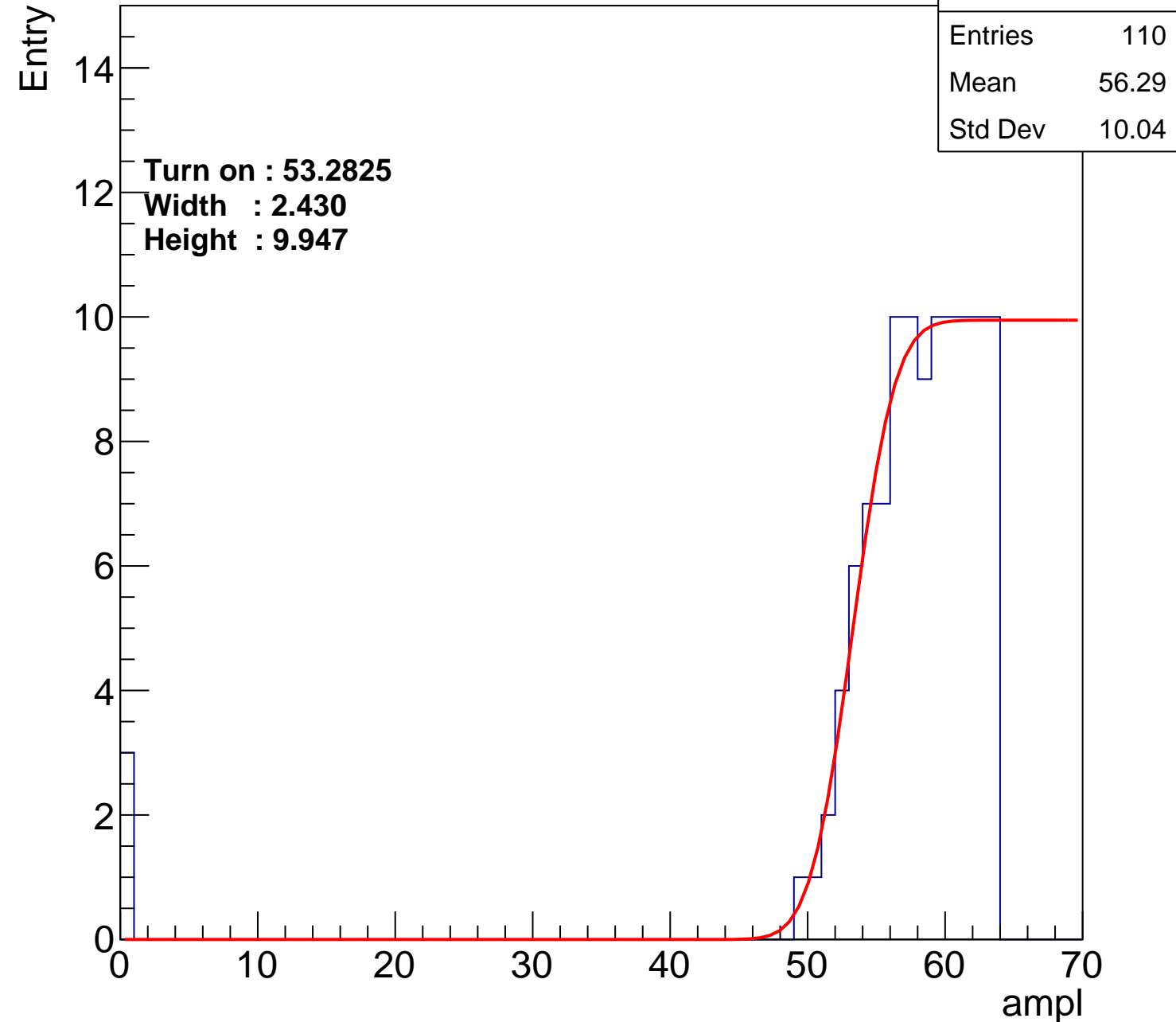
Width : 2.430

Height : 9.947

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch44

calib\_packv5\_040323\_1717.root, FC#2, port C3

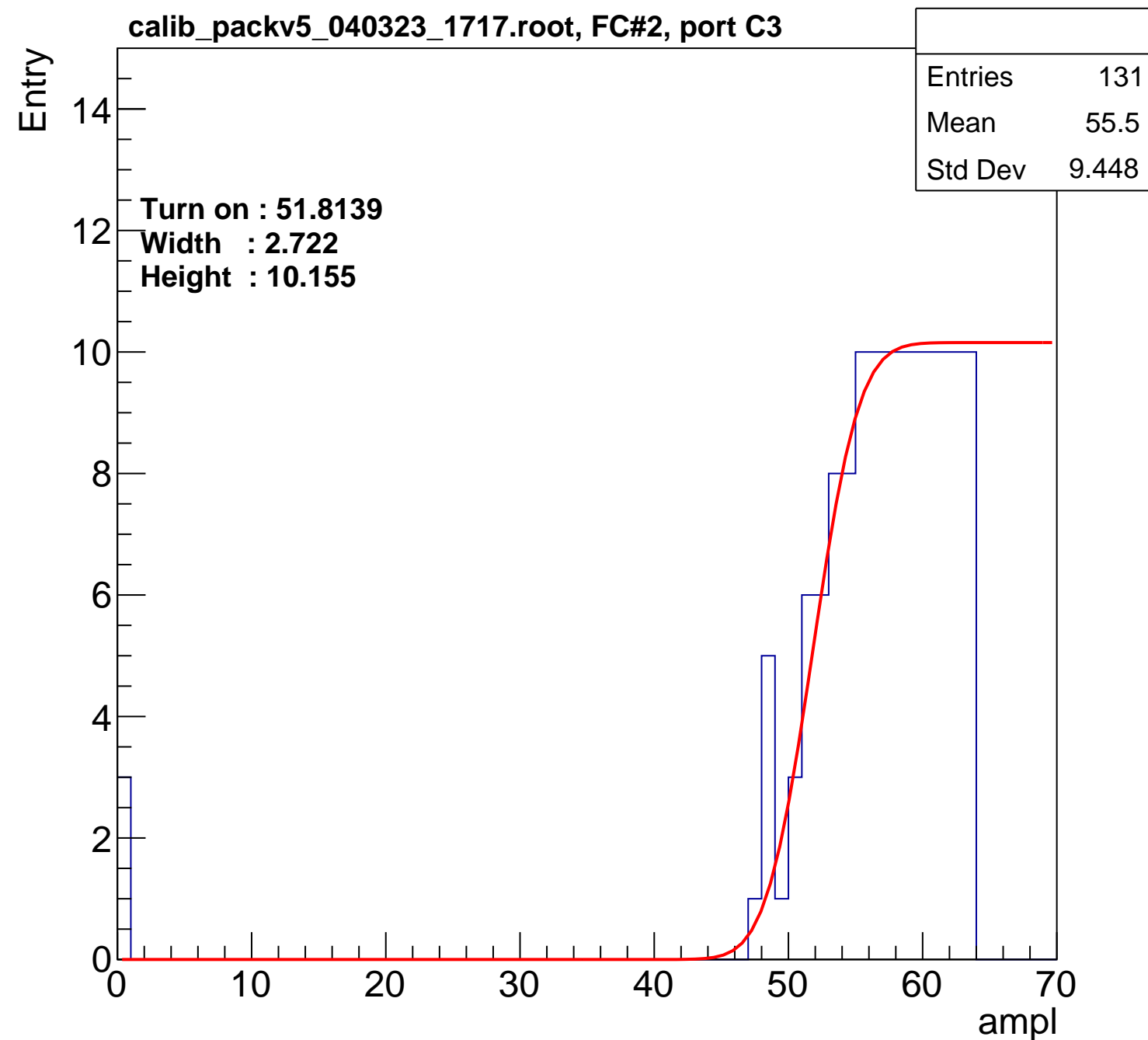
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 51.8139  
Width : 2.722  
Height : 10.155

Entries	131
Mean	55.5
Std Dev	9.448

ampl



# B0L103S, U23-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	56.52
Std Dev	6.399

Turn on : 51.1124

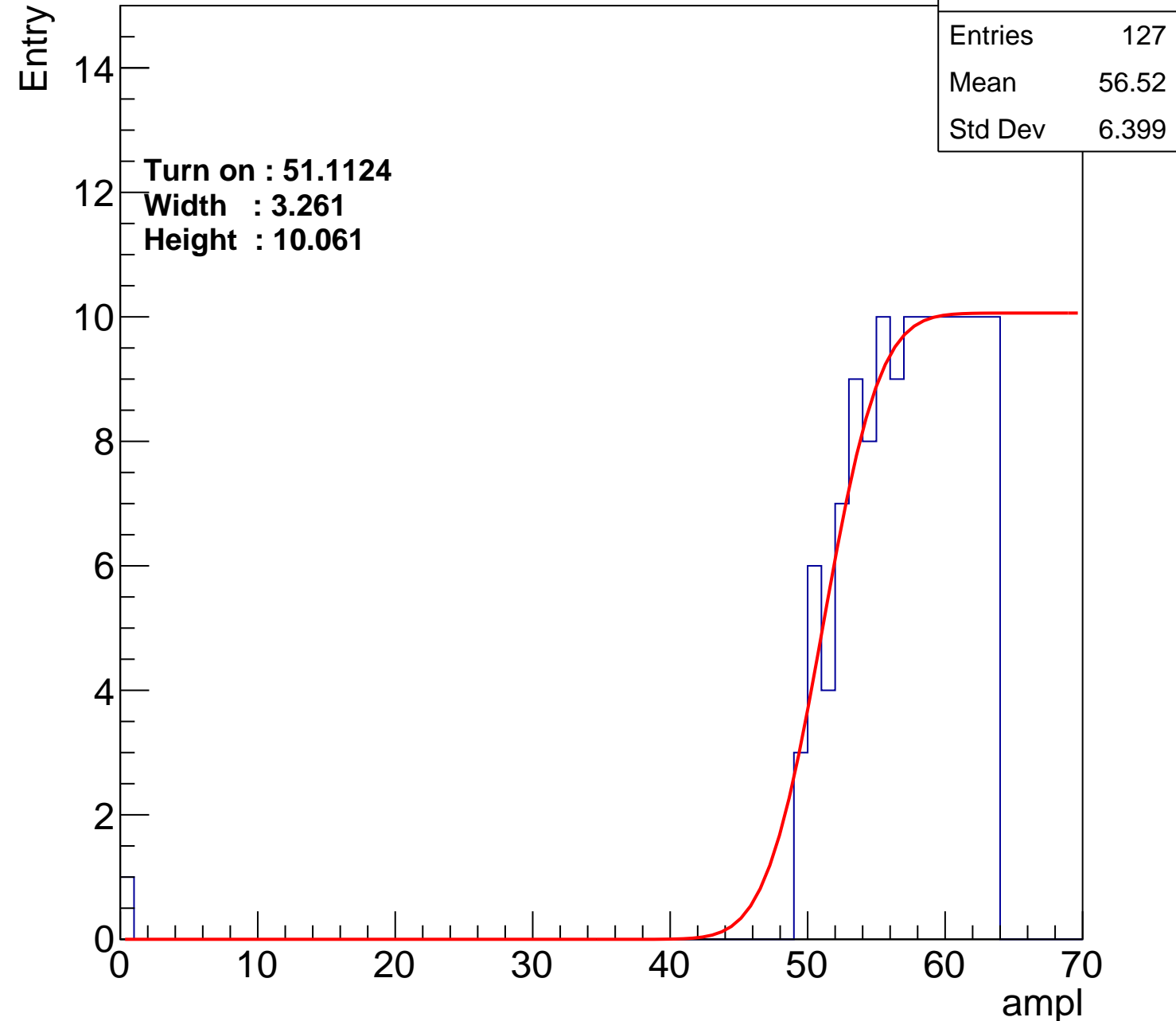
Width : 3.261

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	53.71
Std Dev	14.59

Turn on : 52.7144

Width : 4.351

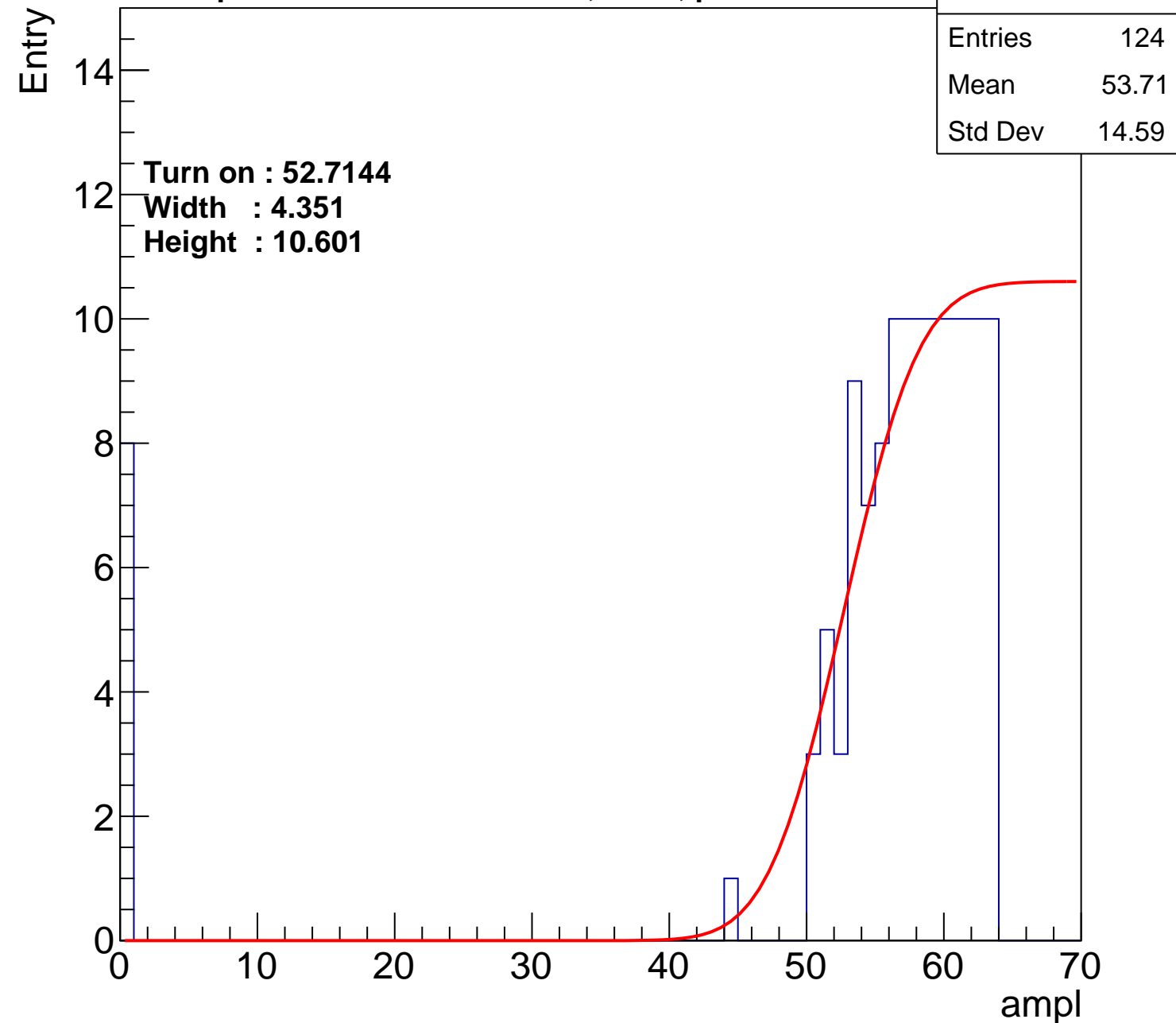
Height : 10.601

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





# B0L103S, U23-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

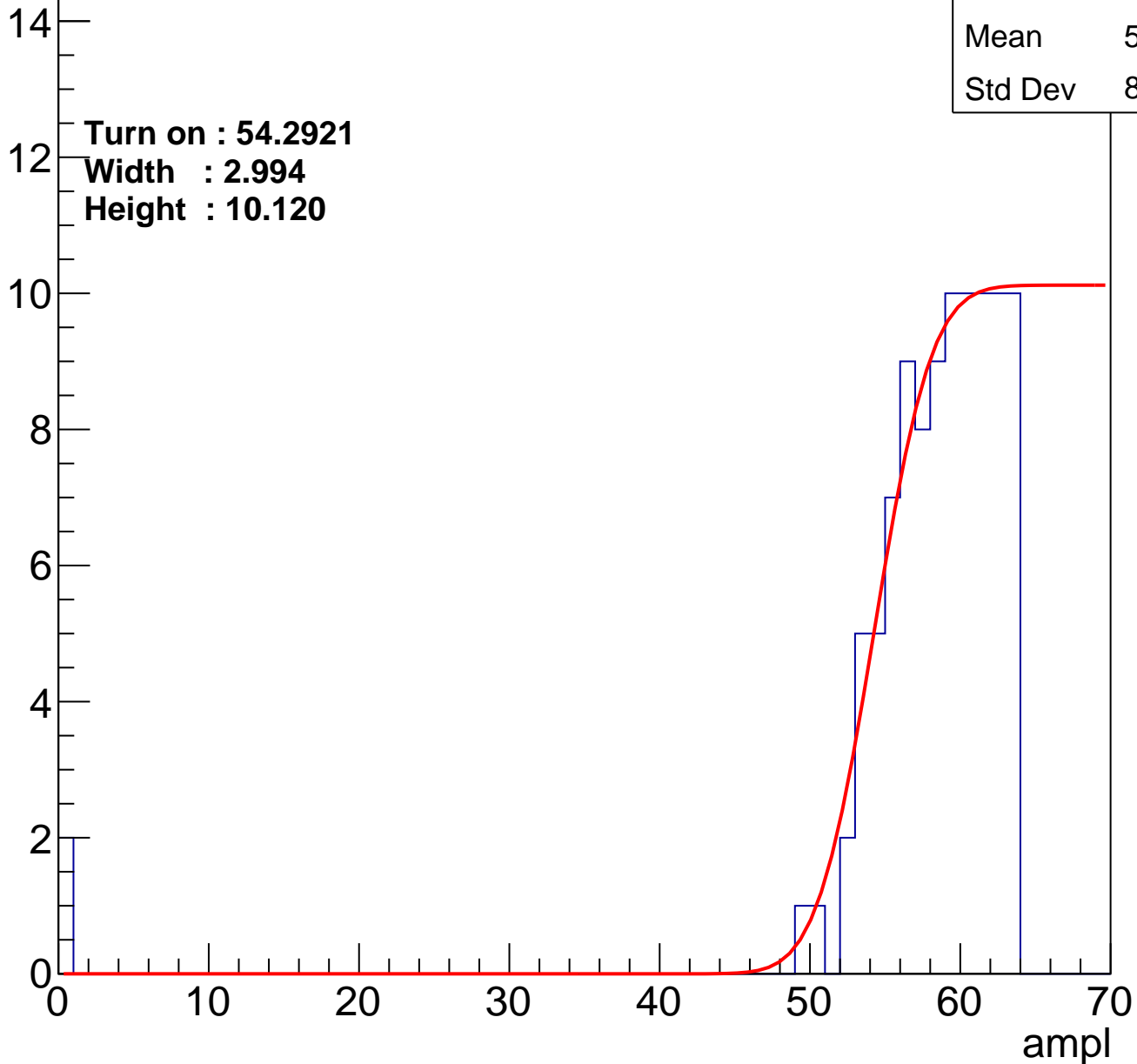
Entries	99
Mean	57.12
Std Dev	8.837

Turn on : 54.2921

Width : 2.994

Height : 10.120

Entry



# B0L103S, U23-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	54.45
Std Dev	12.67

Turn on : 51.8008

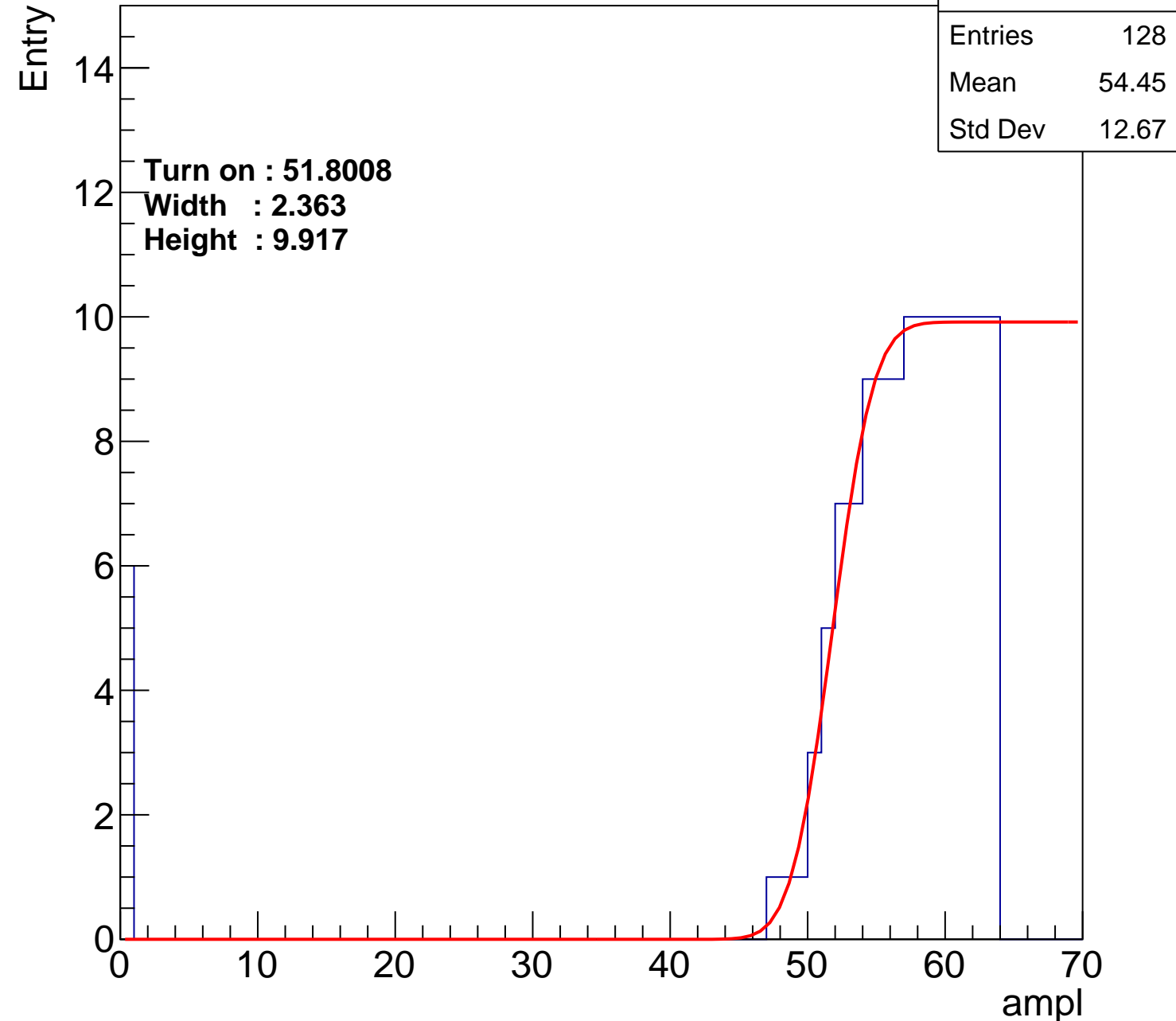
Width : 2.363

Height : 9.917

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

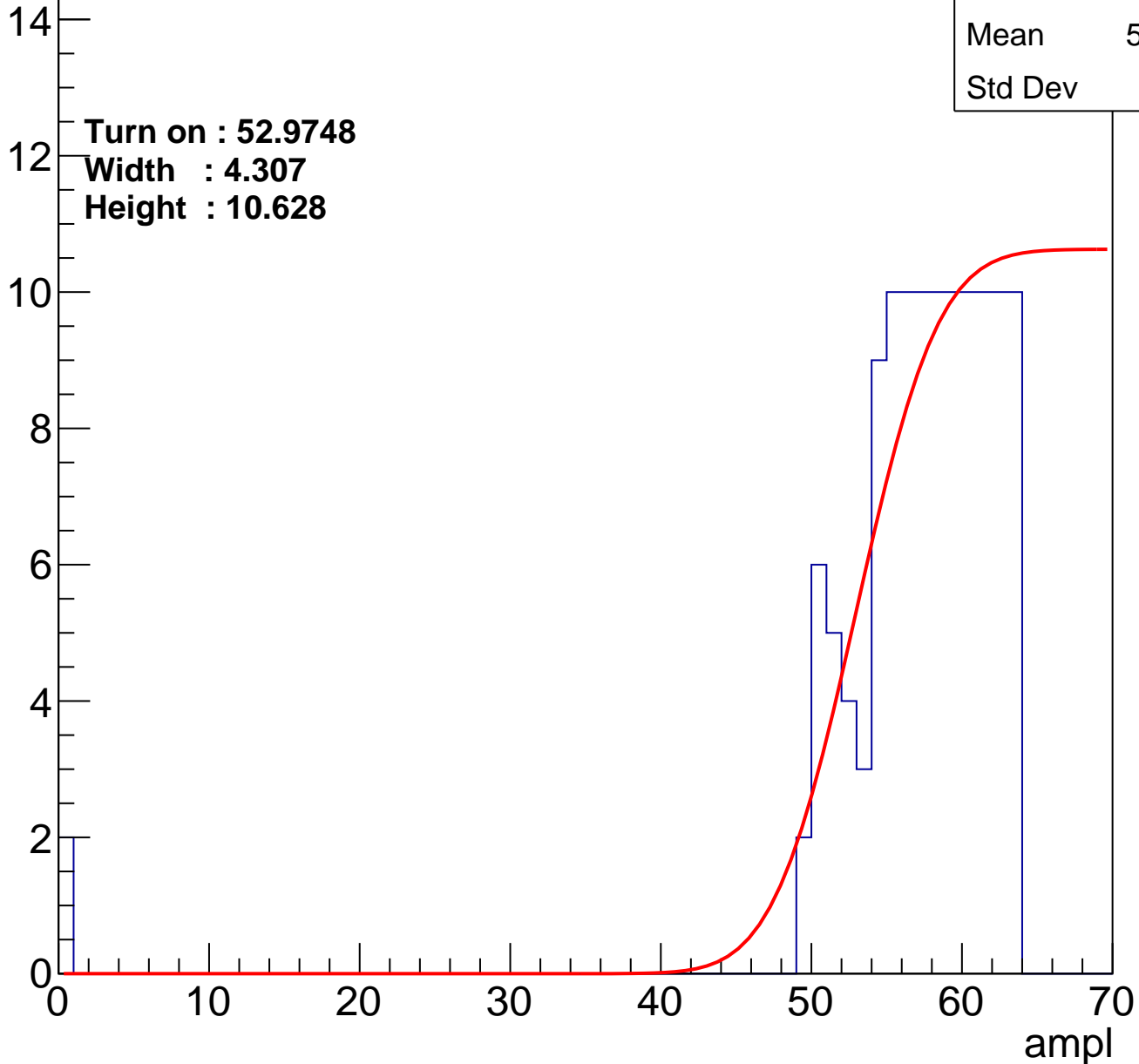
Entries	121
Mean	56.33
Std Dev	8.25

**Turn on : 52.9748**

**Width : 4.307**

**Height : 10.628**

Entry



# B0L103S, U23-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.43
Std Dev	10.9

Turn on : 52.5245

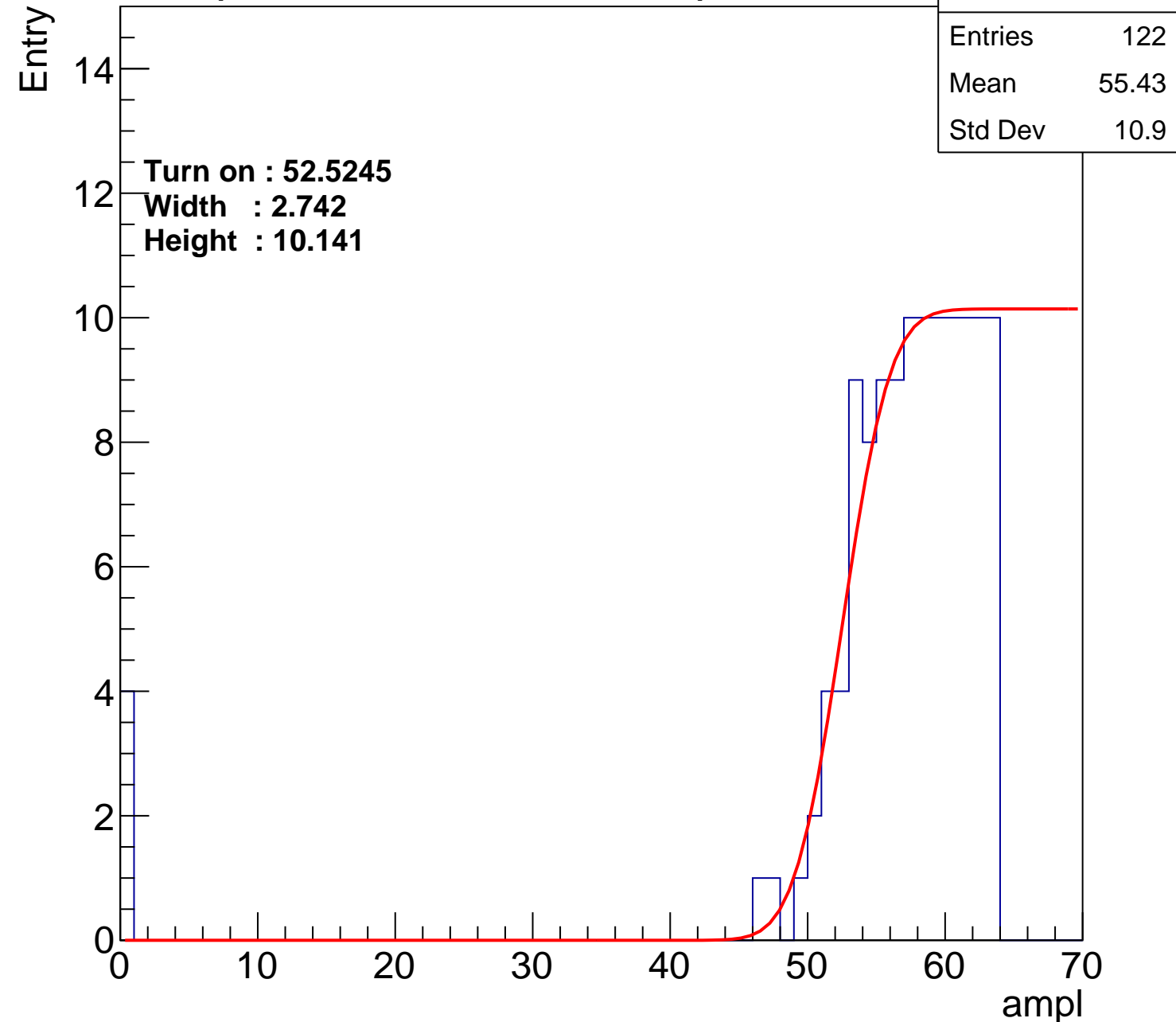
Width : 2.742

Height : 10.141

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch51

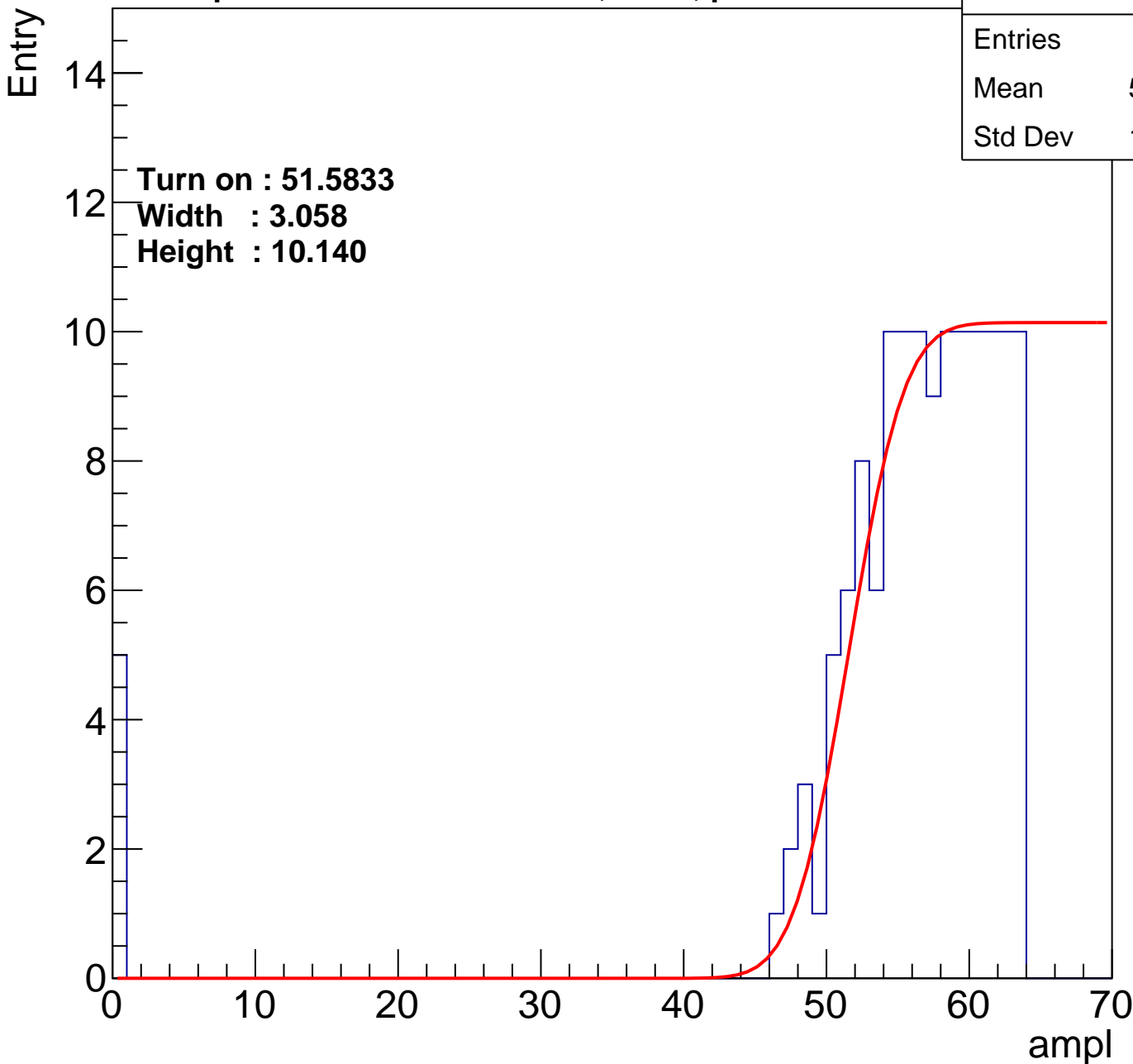
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	136
Mean	54.53
Std Dev	11.46

**Turn on : 51.5833**

**Width : 3.058**

**Height : 10.140**



# B0L103S, U23-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

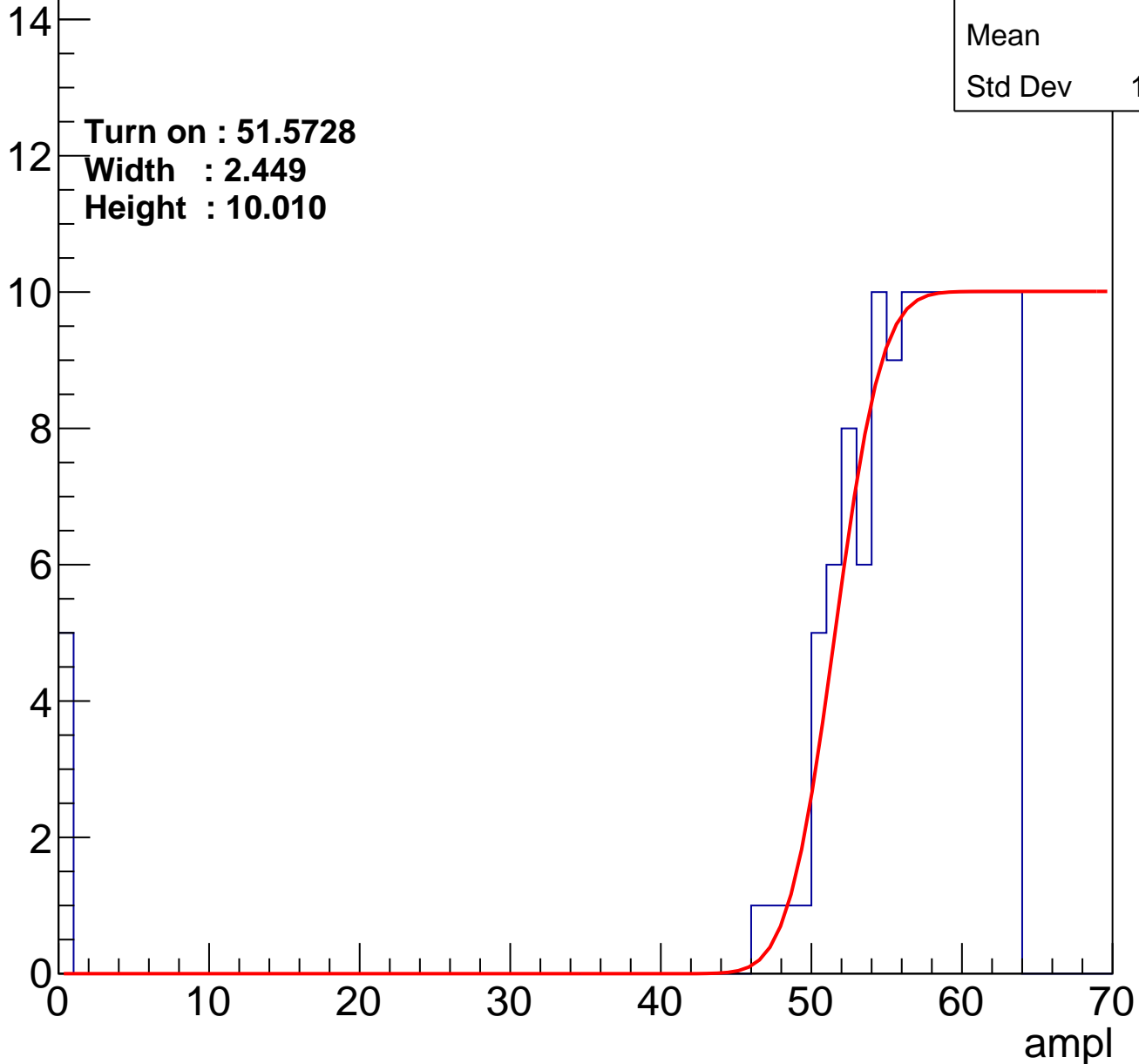
Entries	133
Mean	54.7
Std Dev	11.54

Turn on : 51.5728

Width : 2.449

Height : 10.010

Entry



# B0L103S, U23-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	56.06
Std Dev	9.847

Turn on : 53.0624

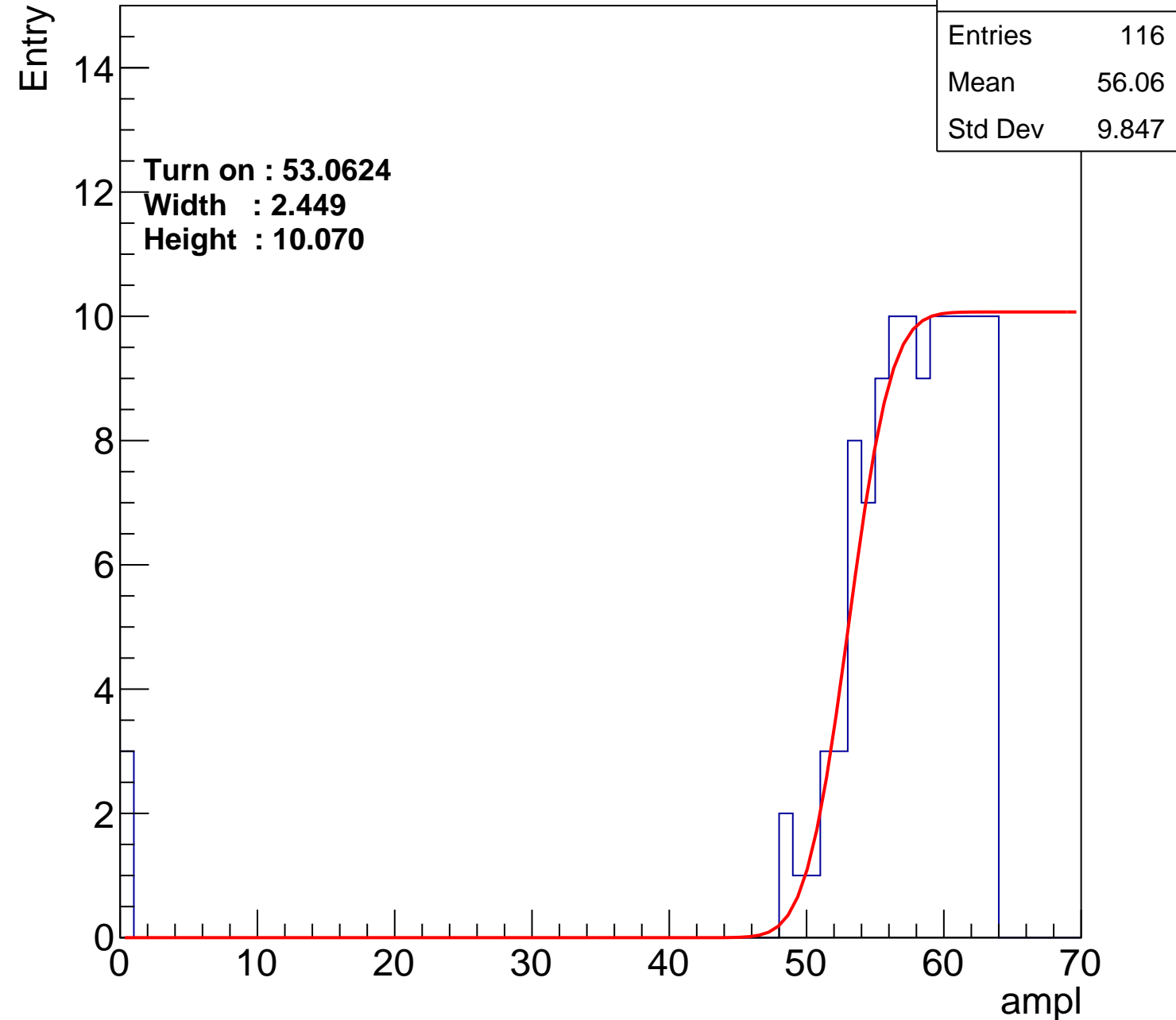
Width : 2.449

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	56.01
Std Dev	6.49

Turn on : 50.6931

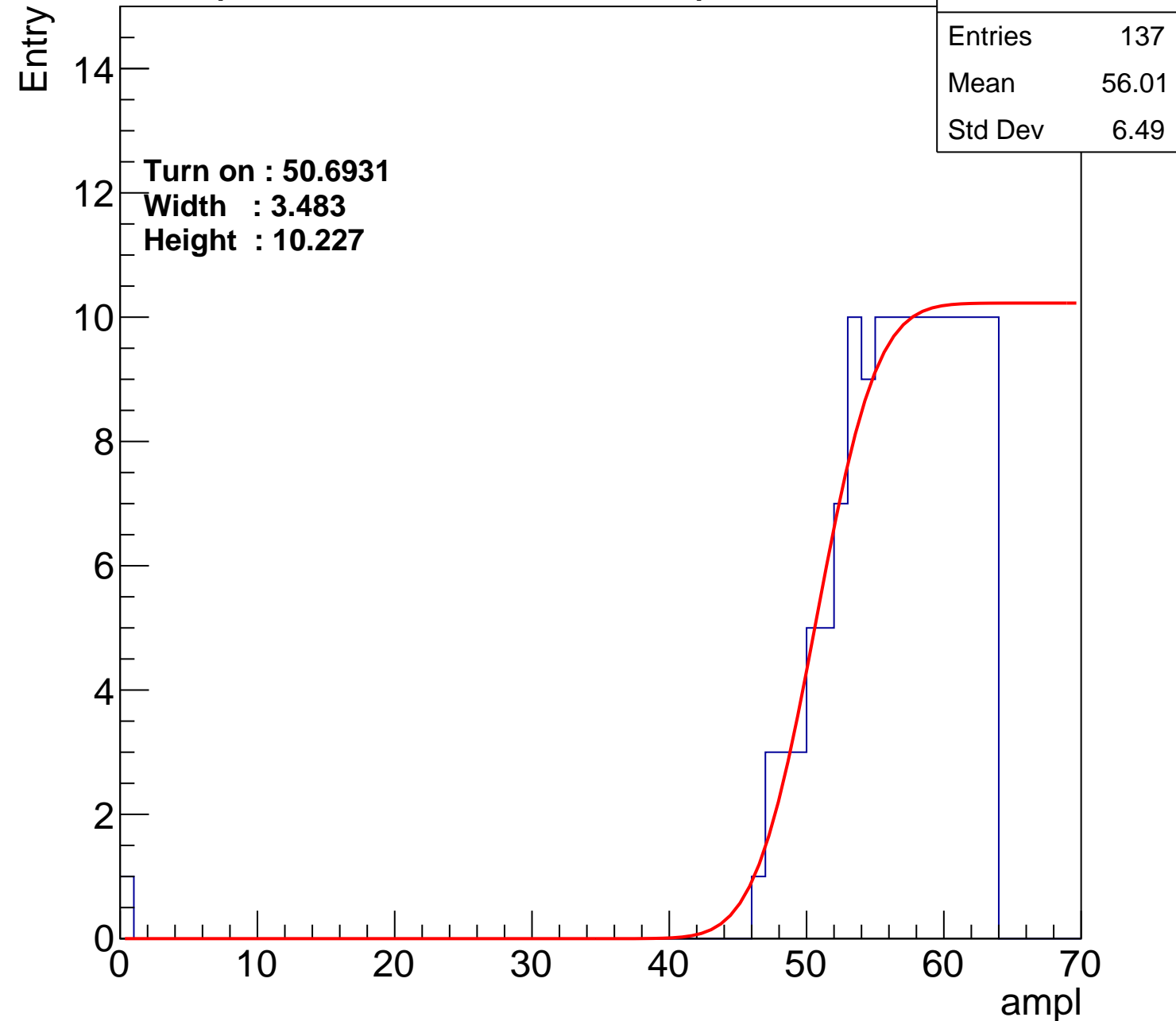
Width : 3.483

Height : 10.227

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

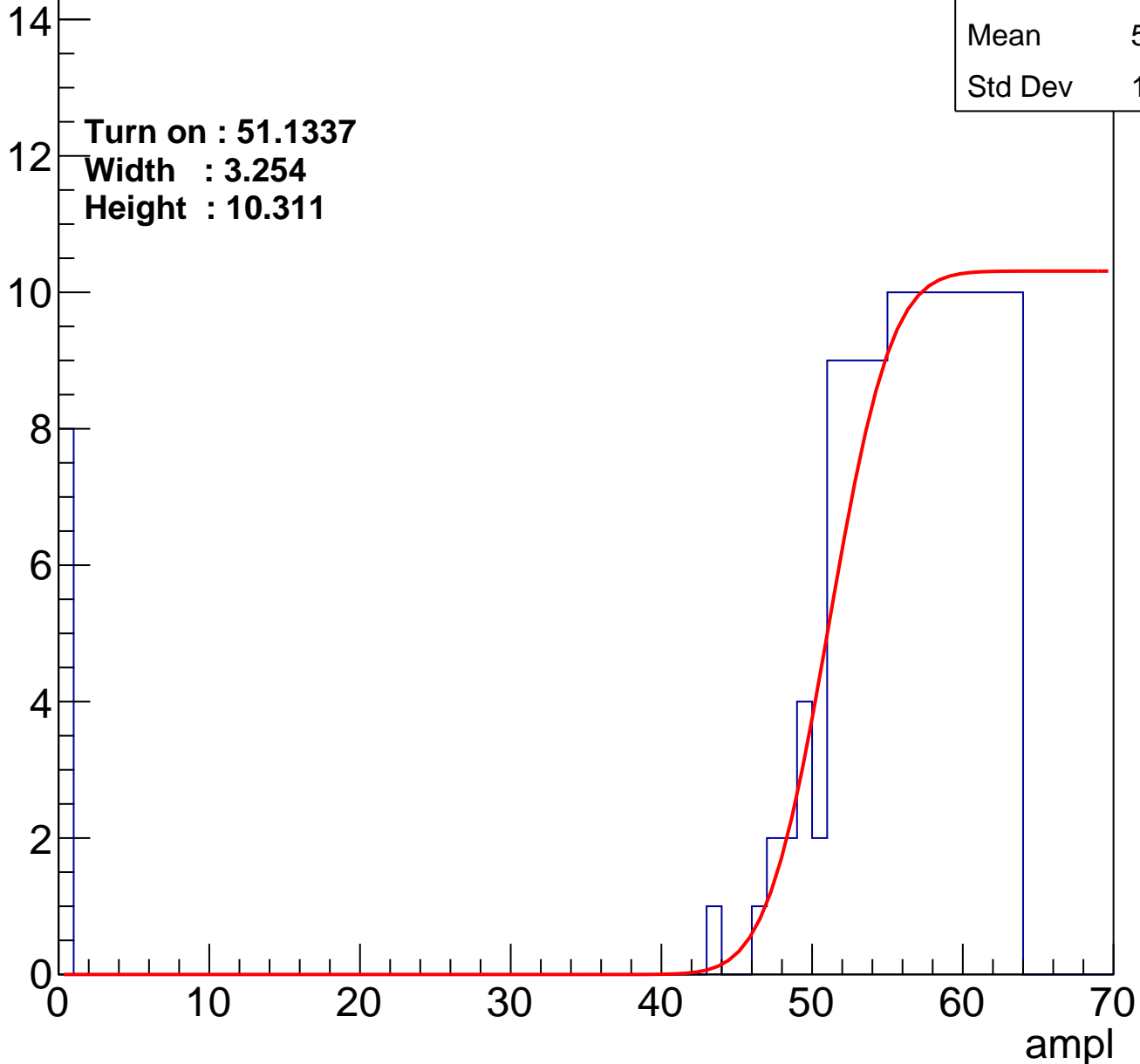
Entries	146
Mean	53.25
Std Dev	13.53

Turn on : 51.1337

Width : 3.254

Height : 10.311

Entry



# B0L103S, U23-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	56.27
Std Dev	6.417

Turn on : 50.7042

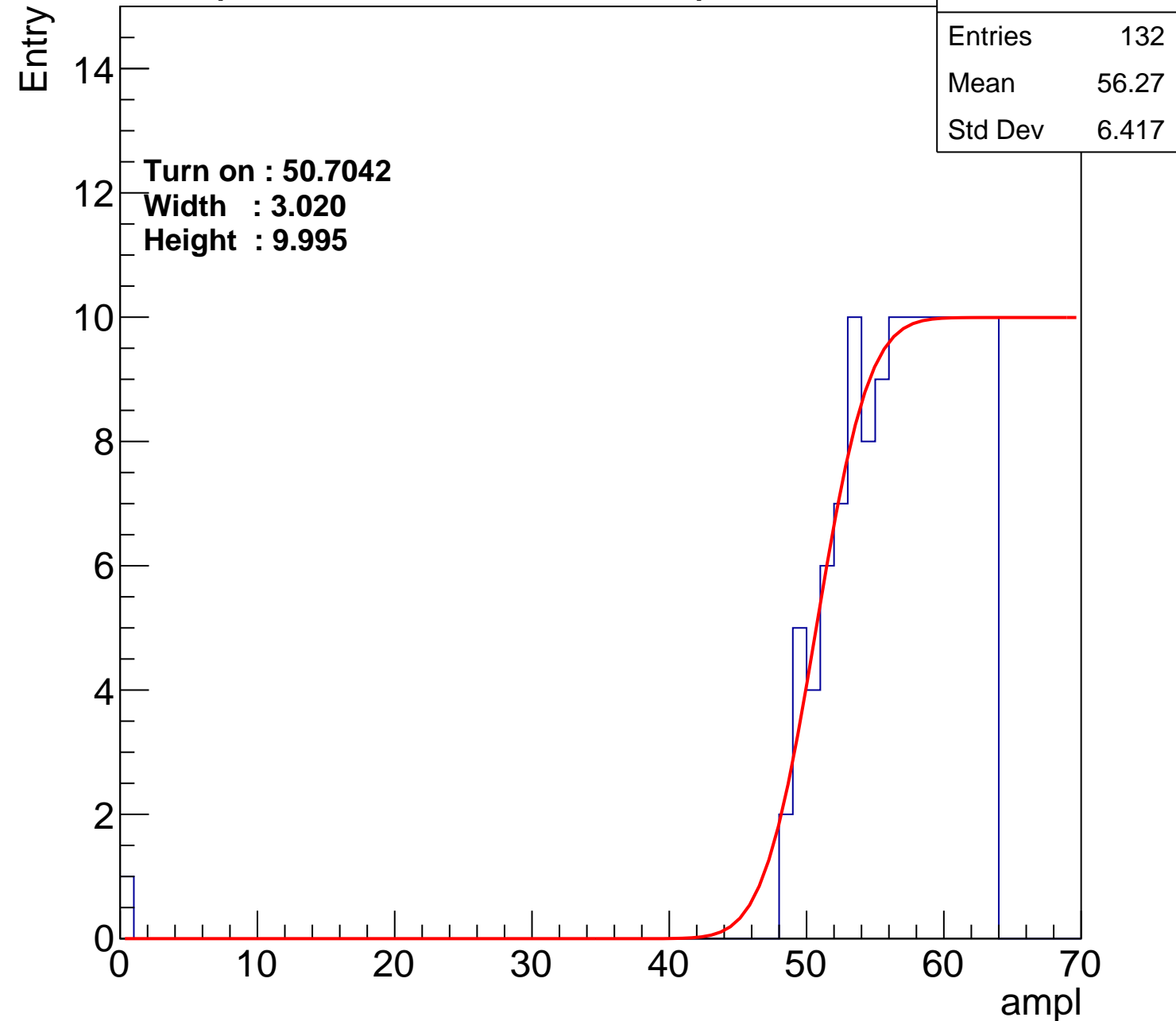
Width : 3.020

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

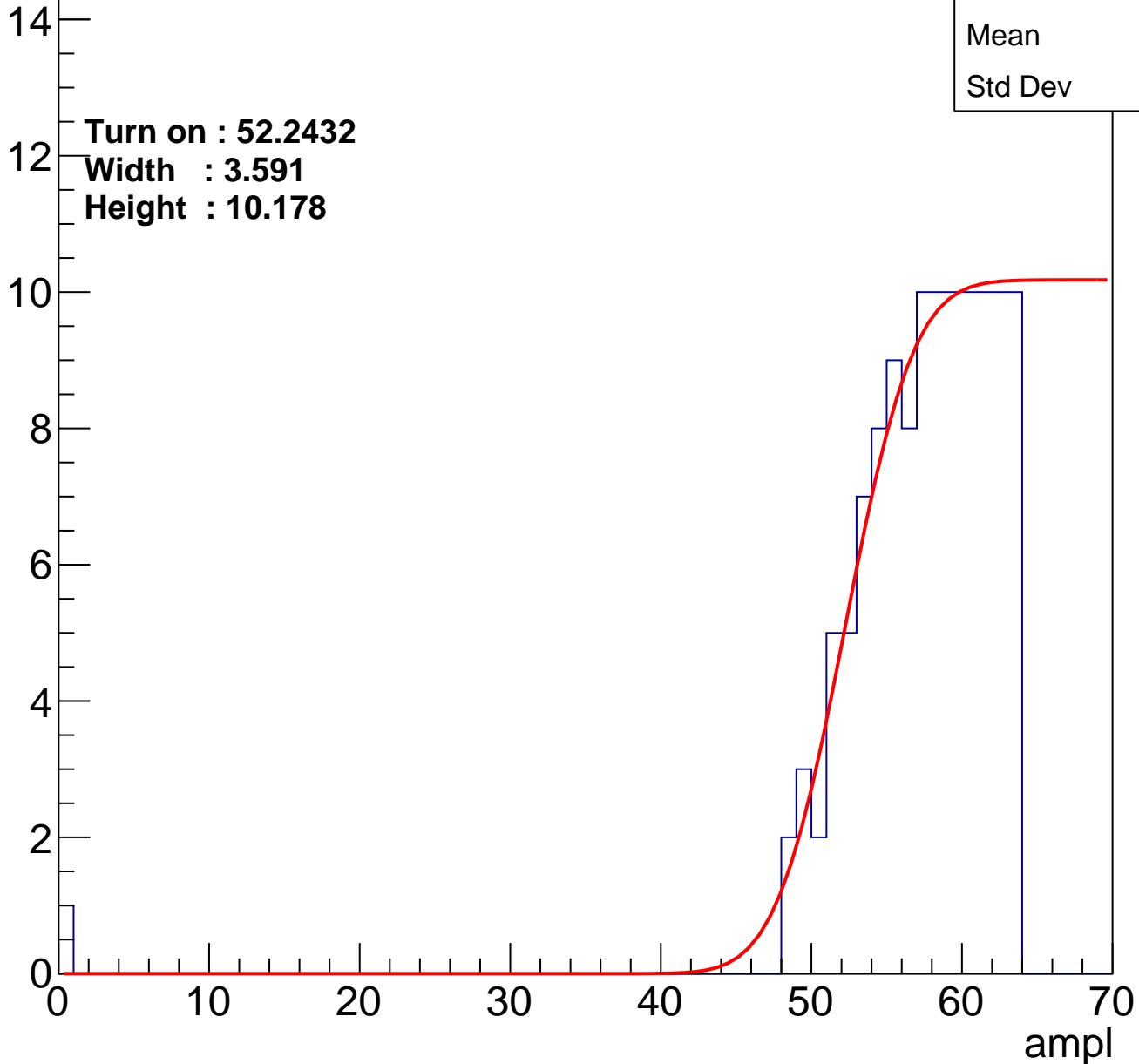
Entries	120
Mean	56.7
Std Dev	6.54

Turn on : 52.2432

Width : 3.591

Height : 10.178

Entry



# B0L103S, U23-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.92
Std Dev	8.166

Turn on : 51.3865

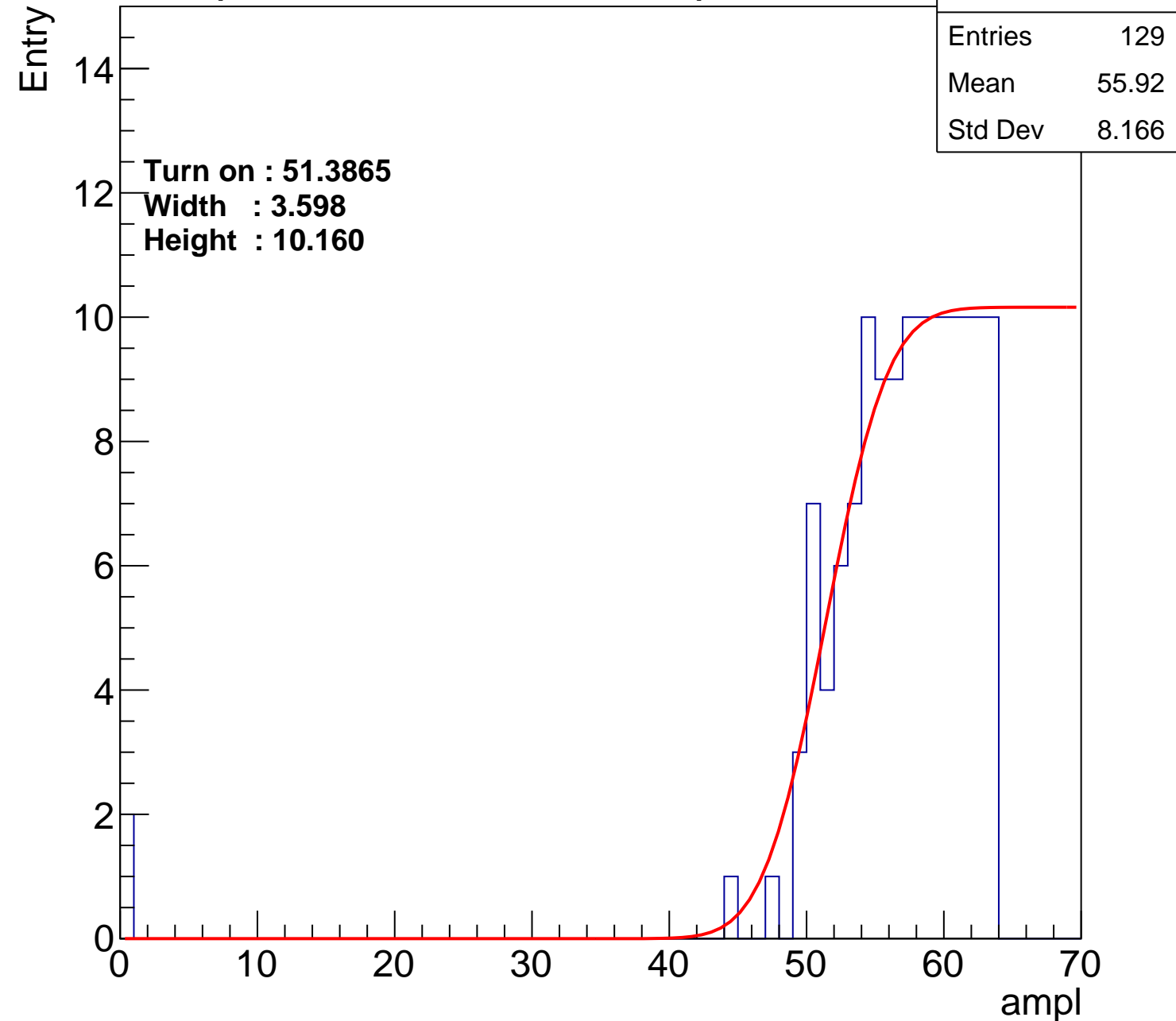
Width : 3.598

Height : 10.160

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

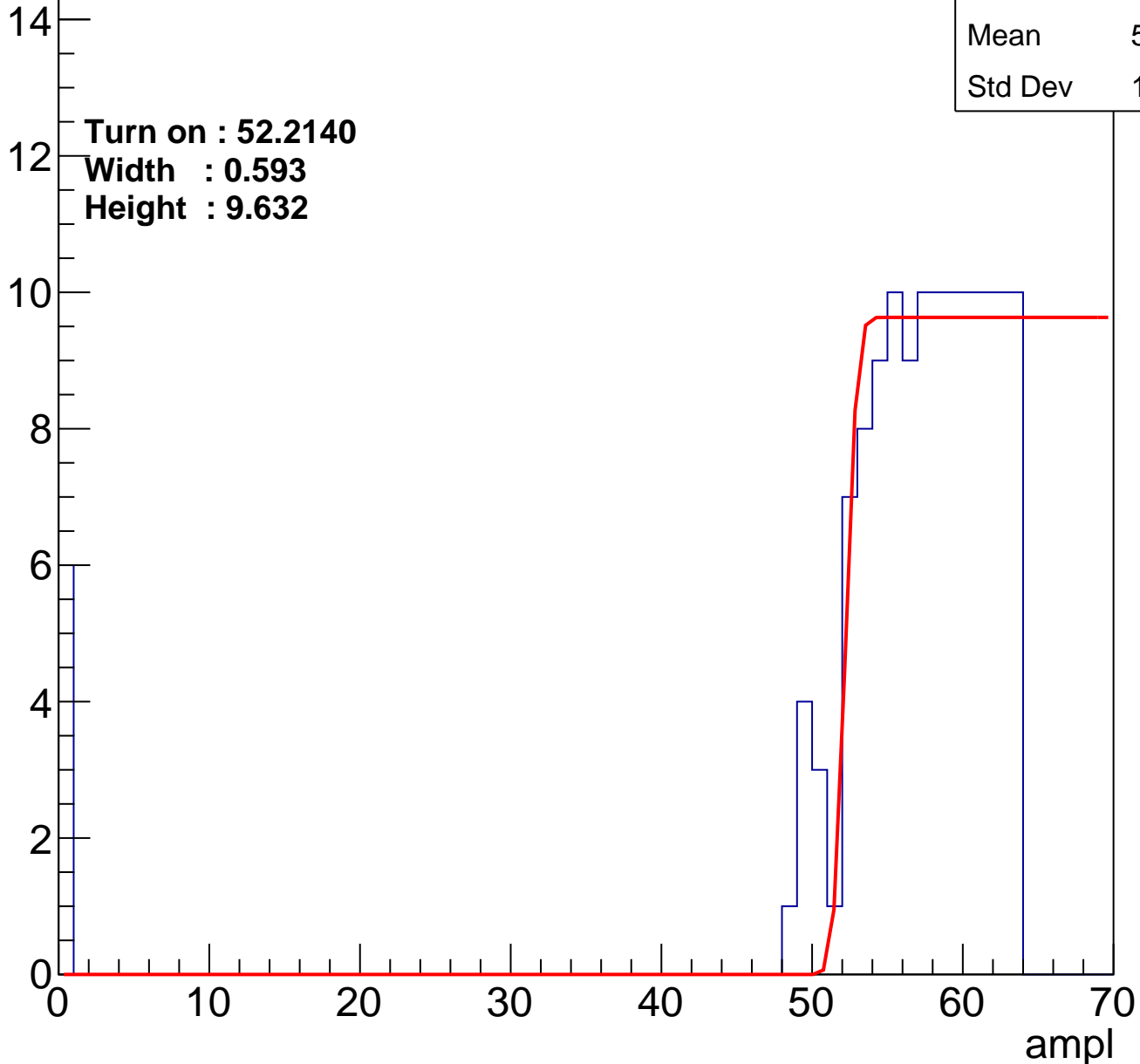
Entries	128
Mean	54.48
Std Dev	12.67

Turn on : 52.2140

Width : 0.593

Height : 9.632

Entry



# B0L103S, U23-ch60

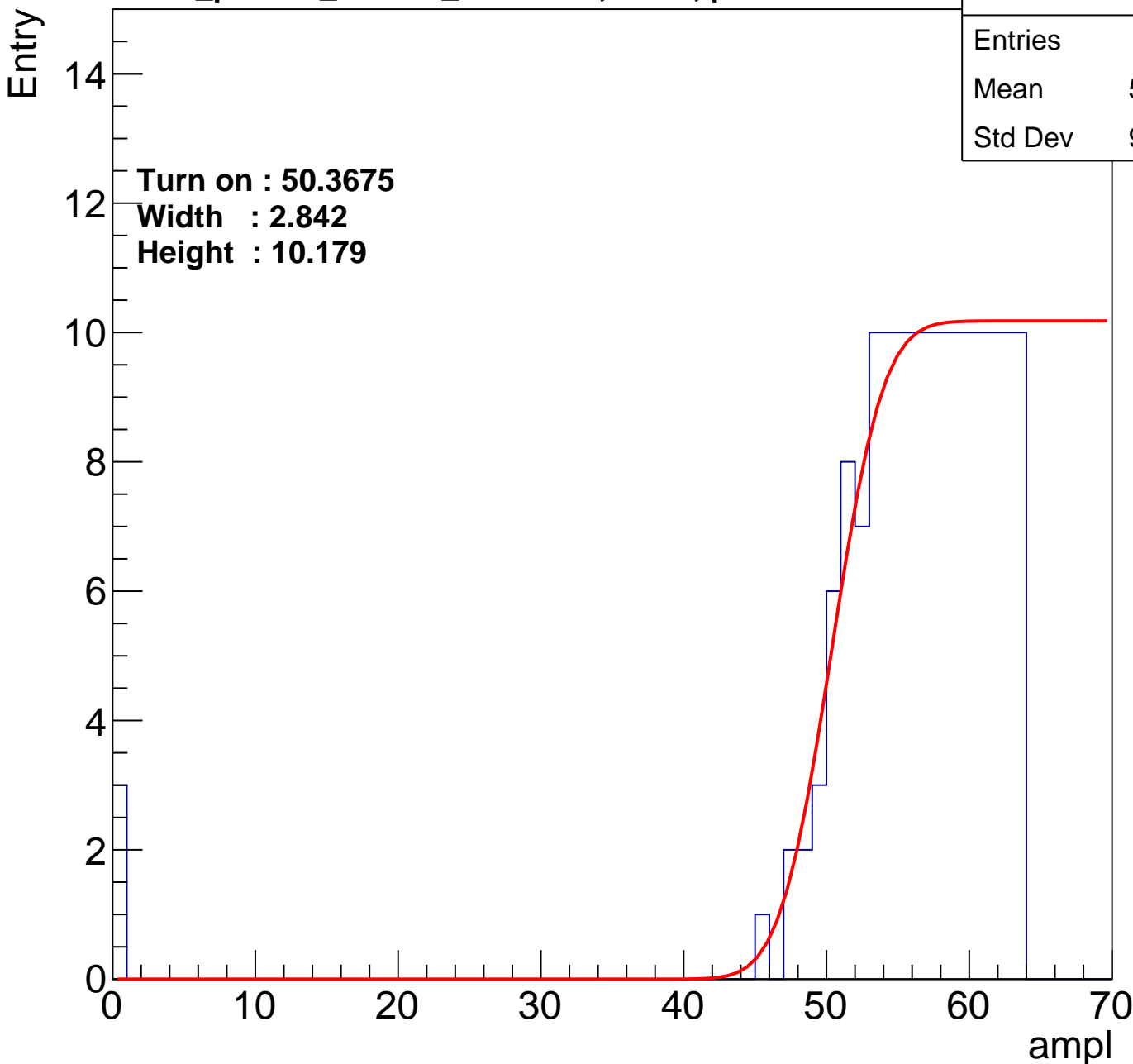
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	142
Mean	55.17
Std Dev	9.168

**Turn on : 50.3675**

**Width : 2.842**

**Height : 10.179**



# B0L103S, U23-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.55
Std Dev	11.05

Turn on : 52.9147

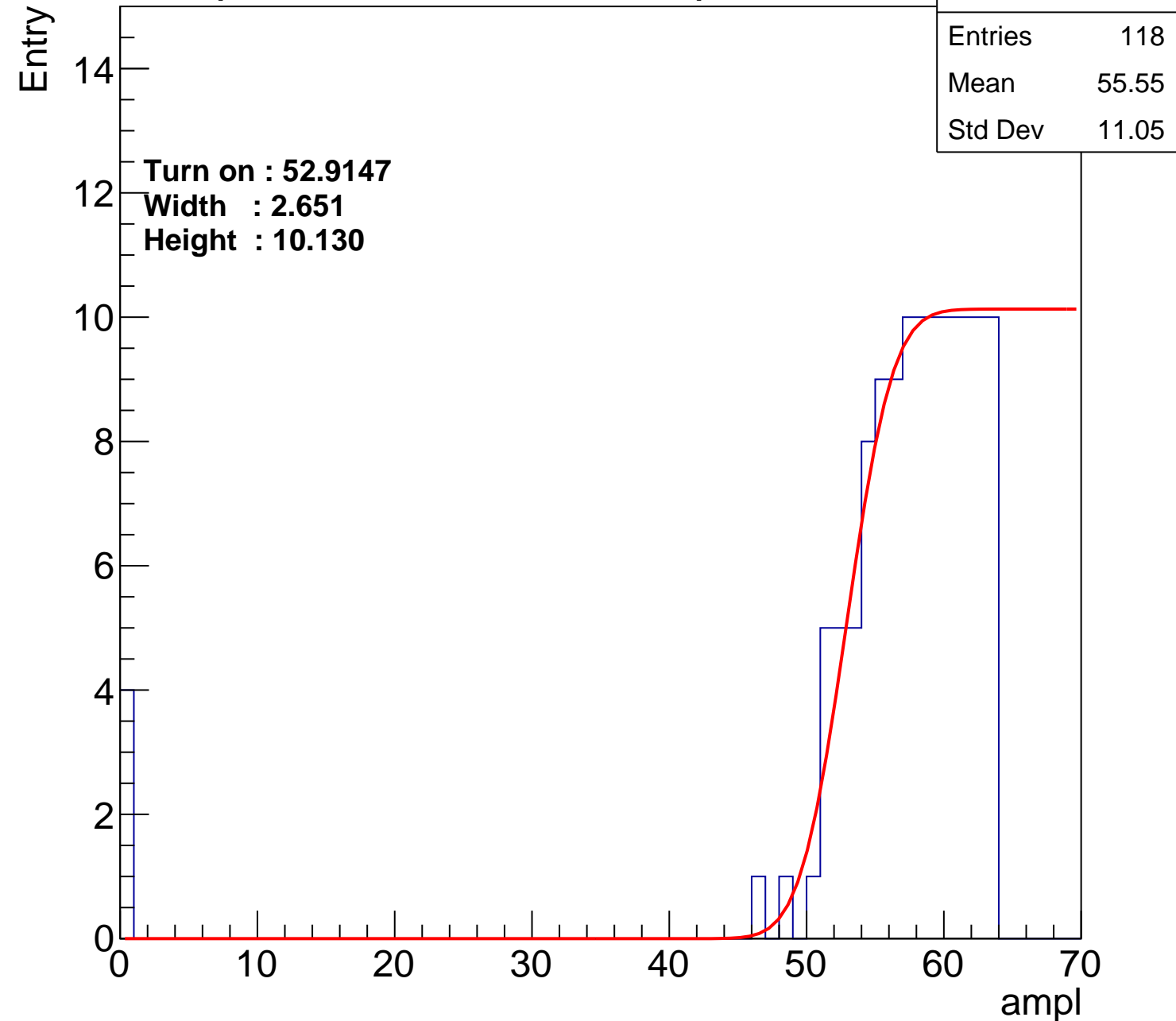
Width : 2.651

Height : 10.130

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	55.27
Std Dev	12.49

Turn on : 54.1443

Width : 2.704

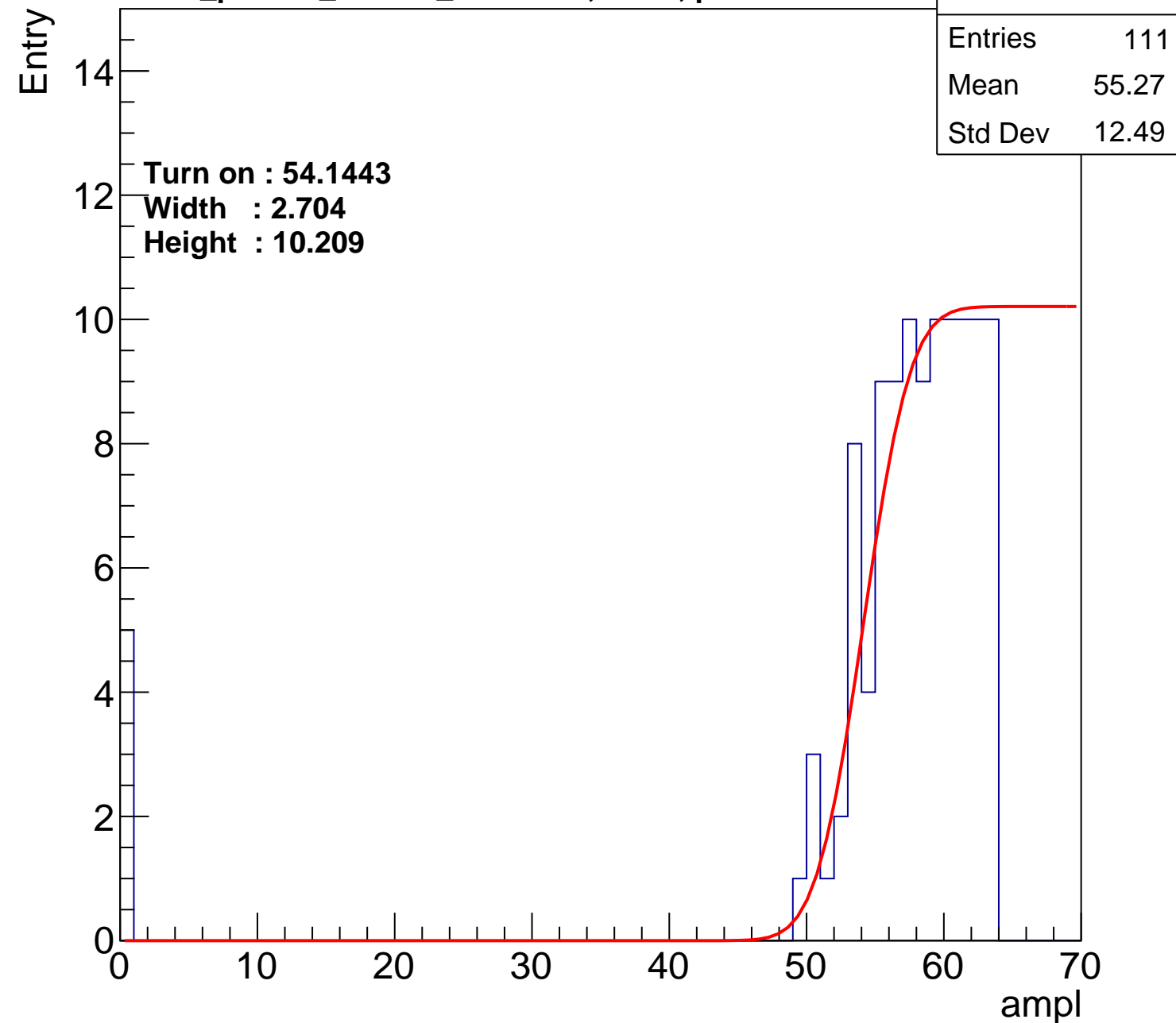
Height : 10.209

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70





**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	127
Mean	56.13
Std Dev	8.102

Mean	56.13
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Std Dev	8.102
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**Width : 2.596**

**Height : 9.982**



# B0L103S, U23-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.65
Std Dev	11.46

Turn on : 51.2189

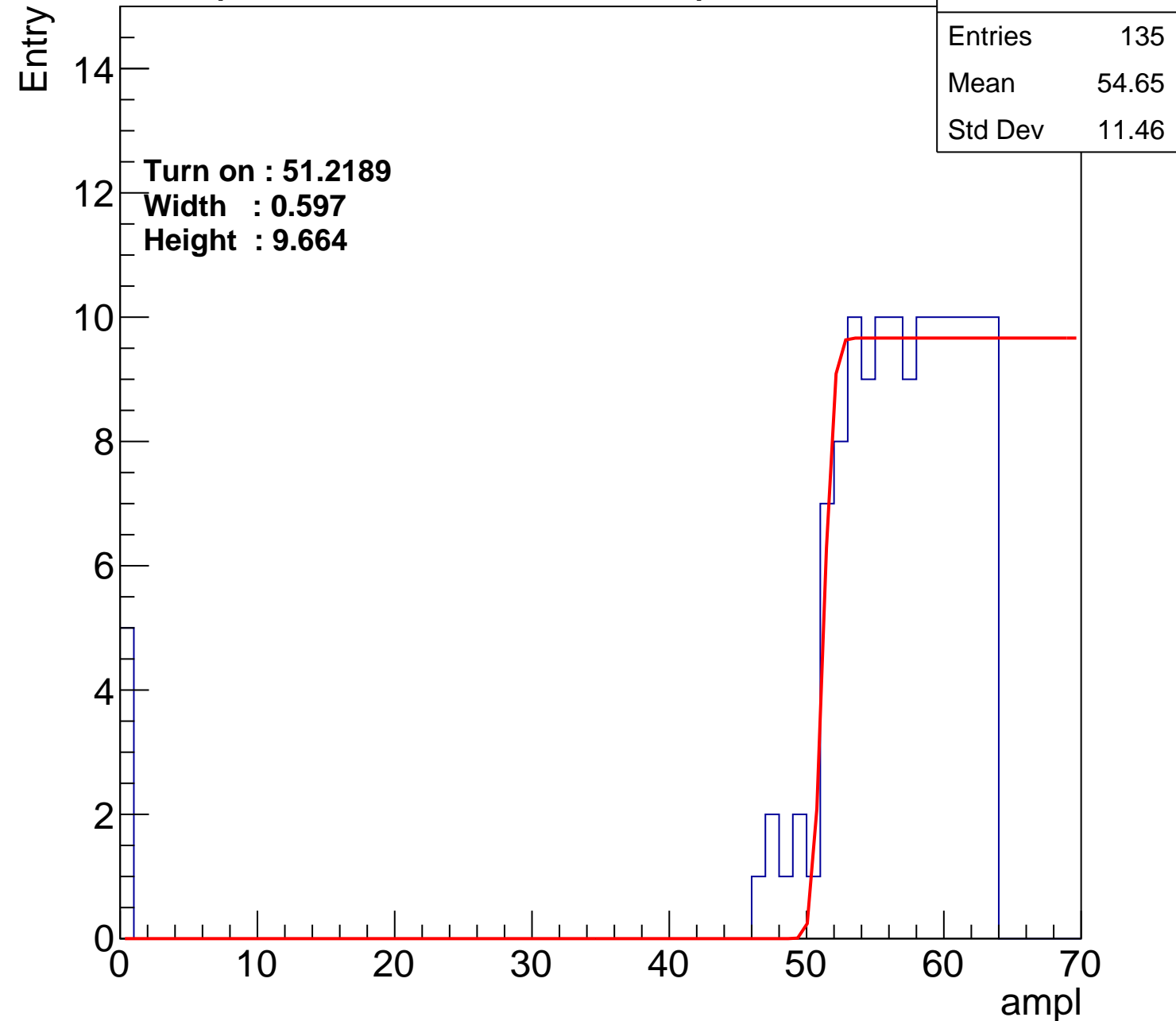
Width : 0.597

Height : 9.664

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.96
Std Dev	8.005

Turn on : 50.9611

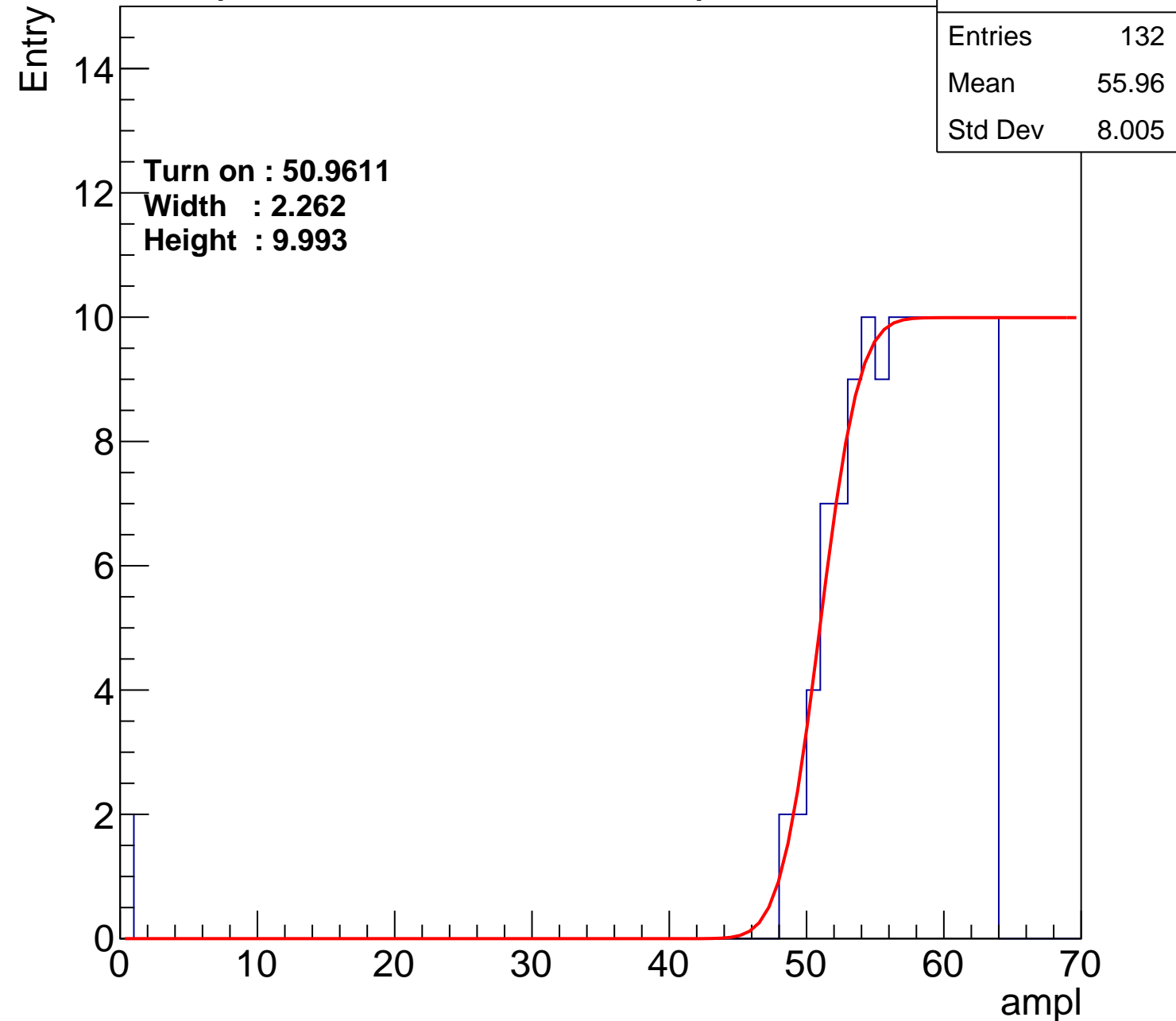
Width : 2.262

Height : 9.993

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.6
Std Dev	9.464

Turn on : 51.4709

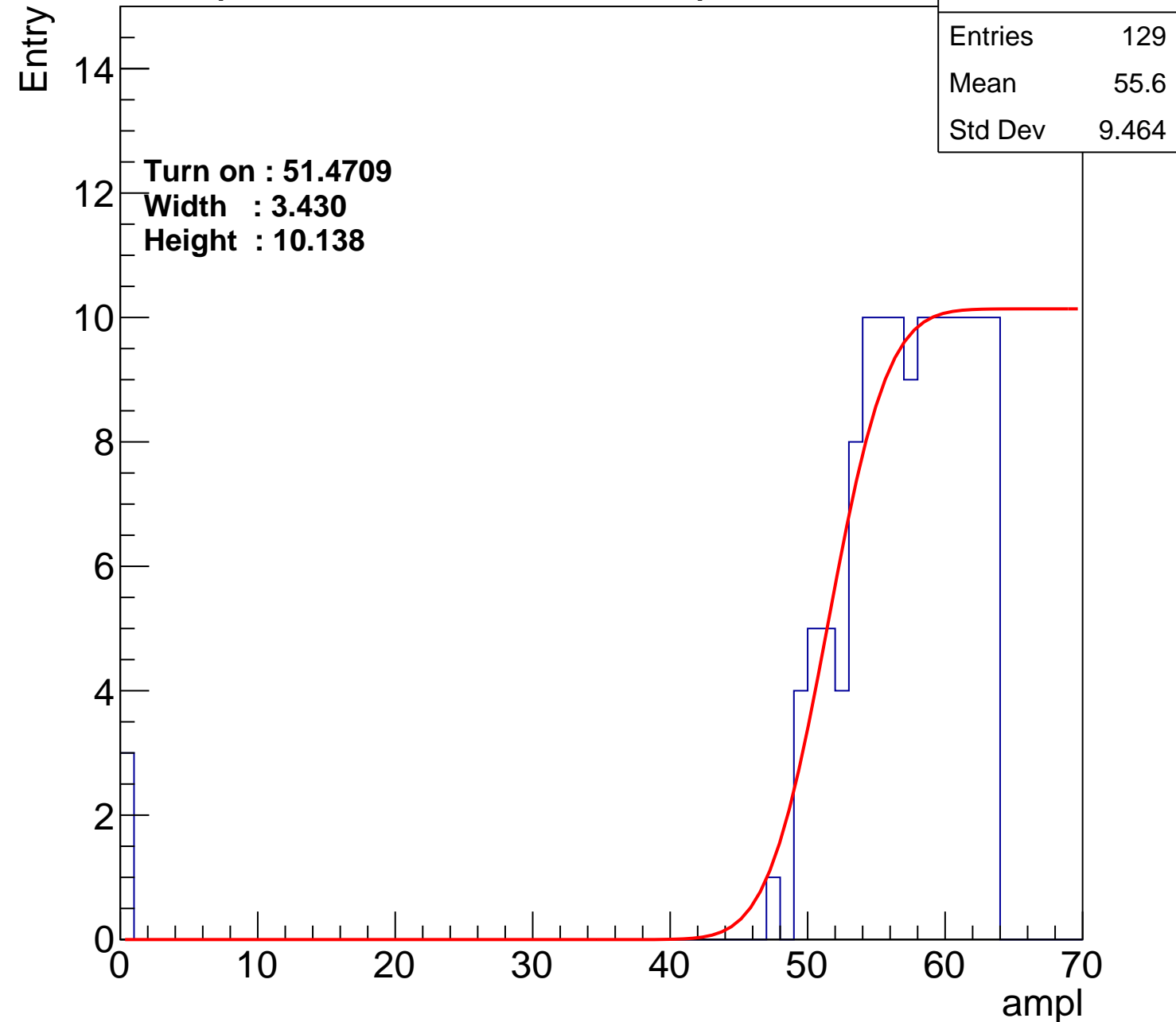
Width : 3.430

Height : 10.138

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	57.09
Std Dev	6.503

**Turn on : 52.6279**

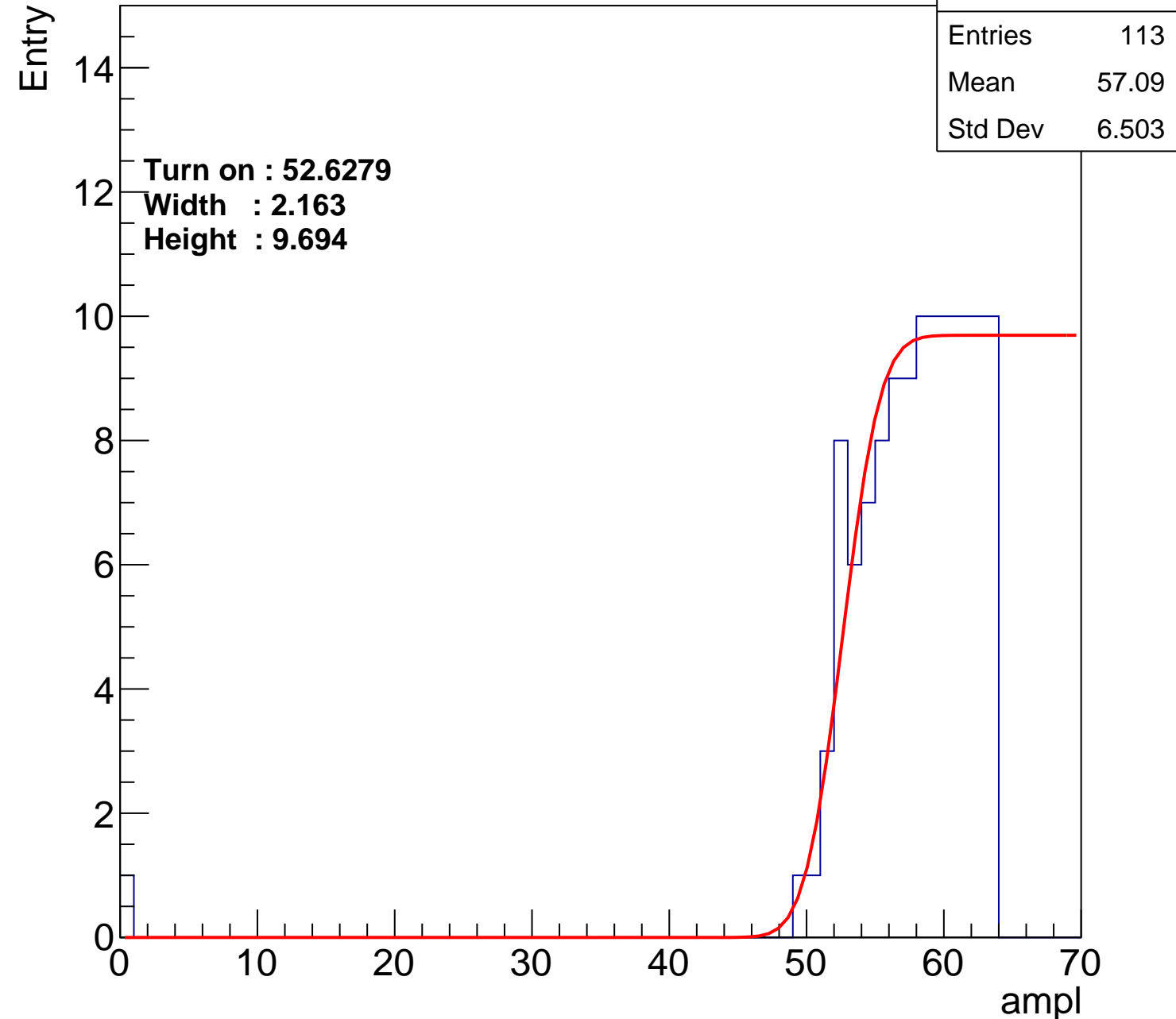
**Width : 2.163**

**Height : 9.694**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.88
Std Dev	9.602

Turn on : 52.2904

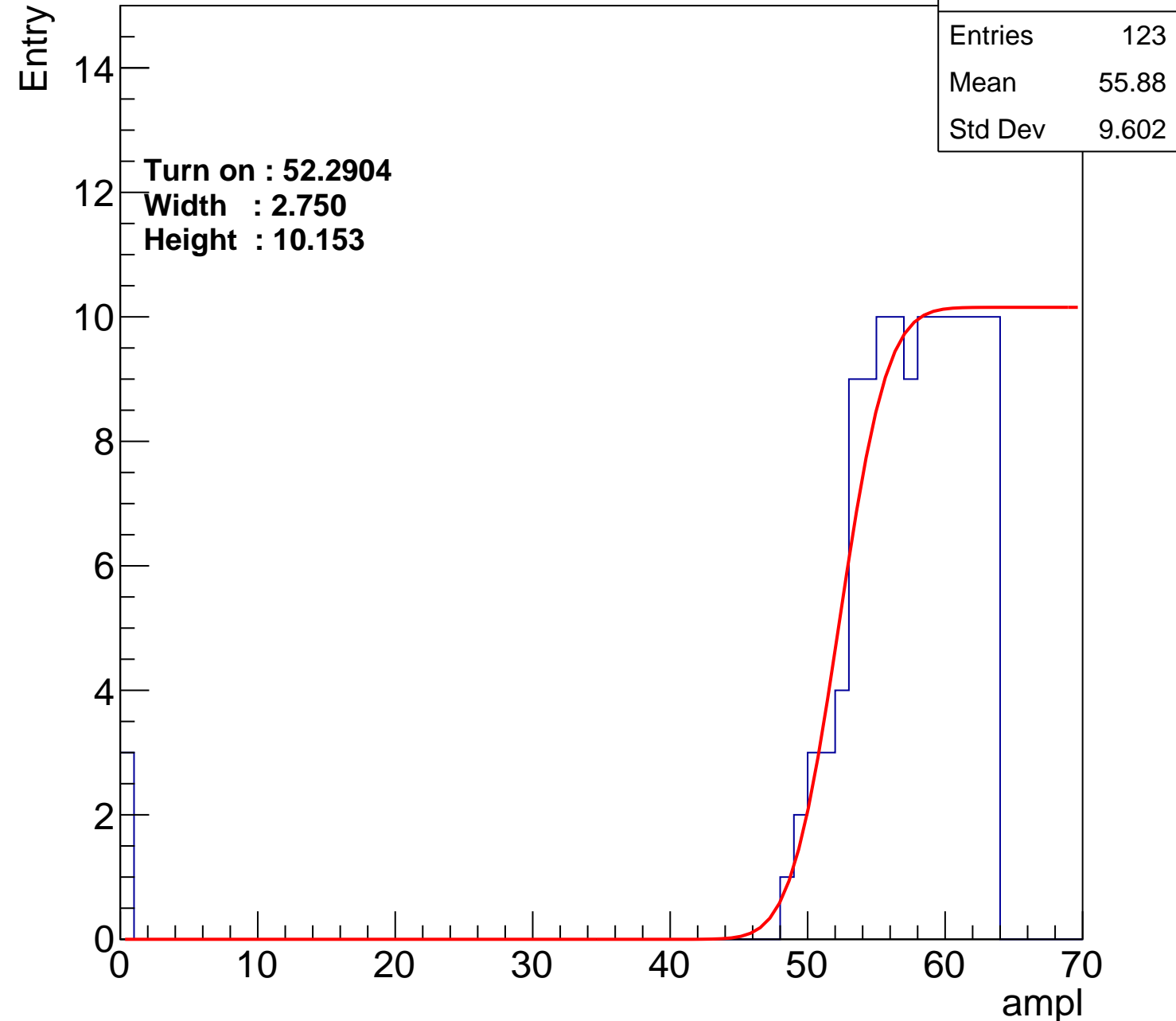
Width : 2.750

Height : 10.153

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.32
Std Dev	12.37

Turn on : 51.2407

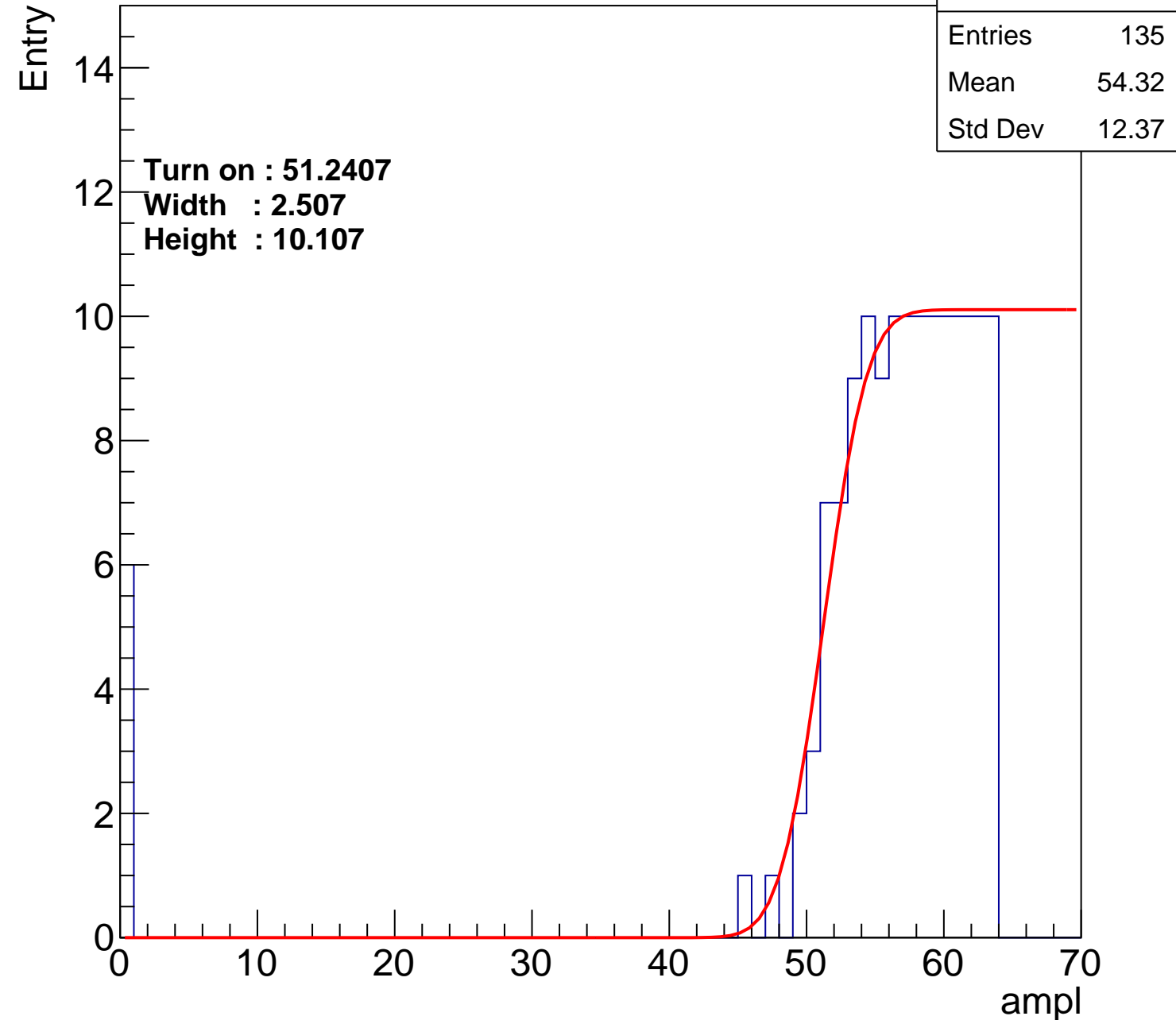
Width : 2.507

Height : 10.107

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

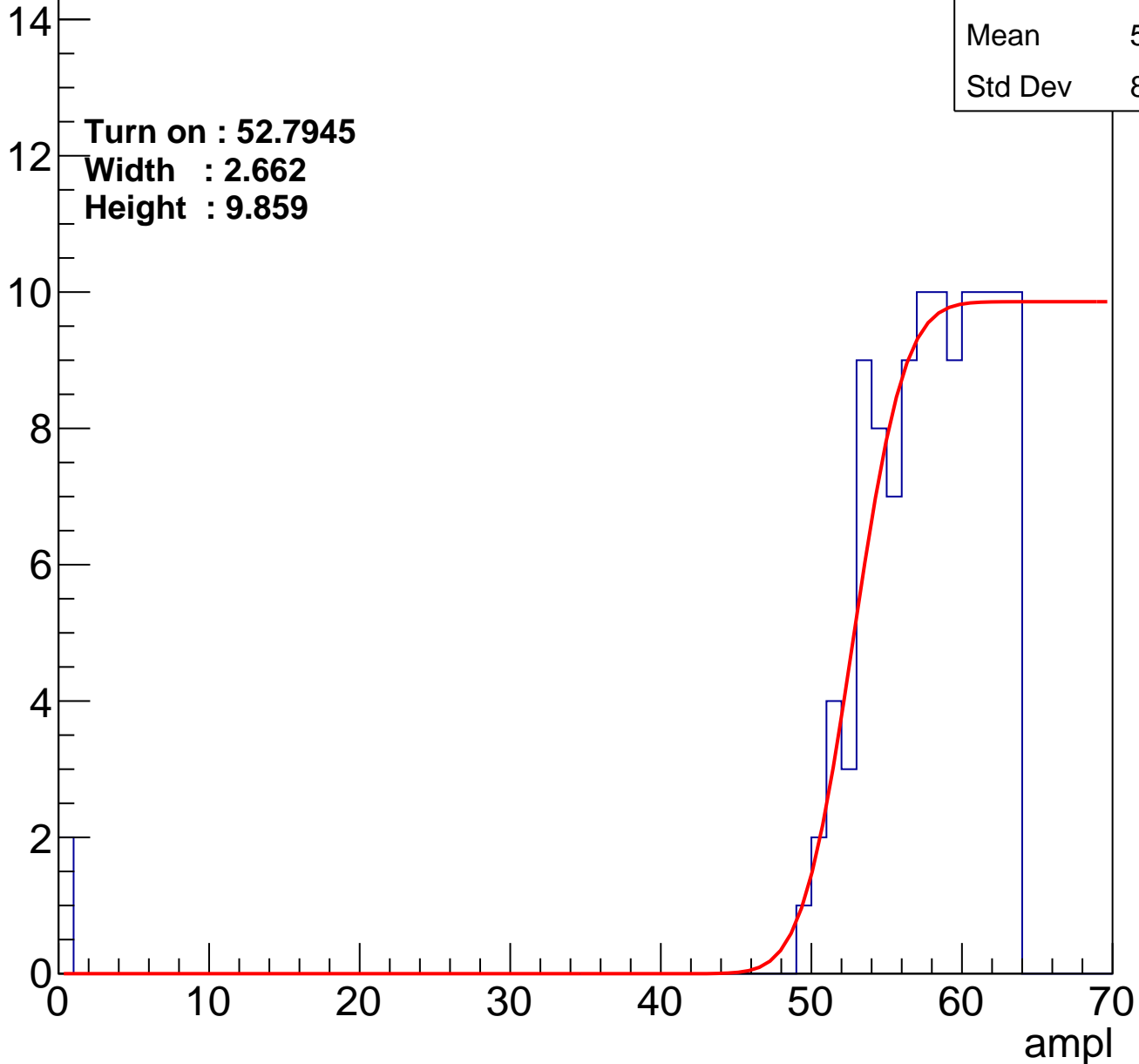
Entries	114
Mean	56.56
Std Dev	8.385

Turn on : 52.7945

Width : 2.662

Height : 9.859

Entry





# B0L103S, U23-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	54.46
Std Dev	12.58

**Turn on : 51.9420**

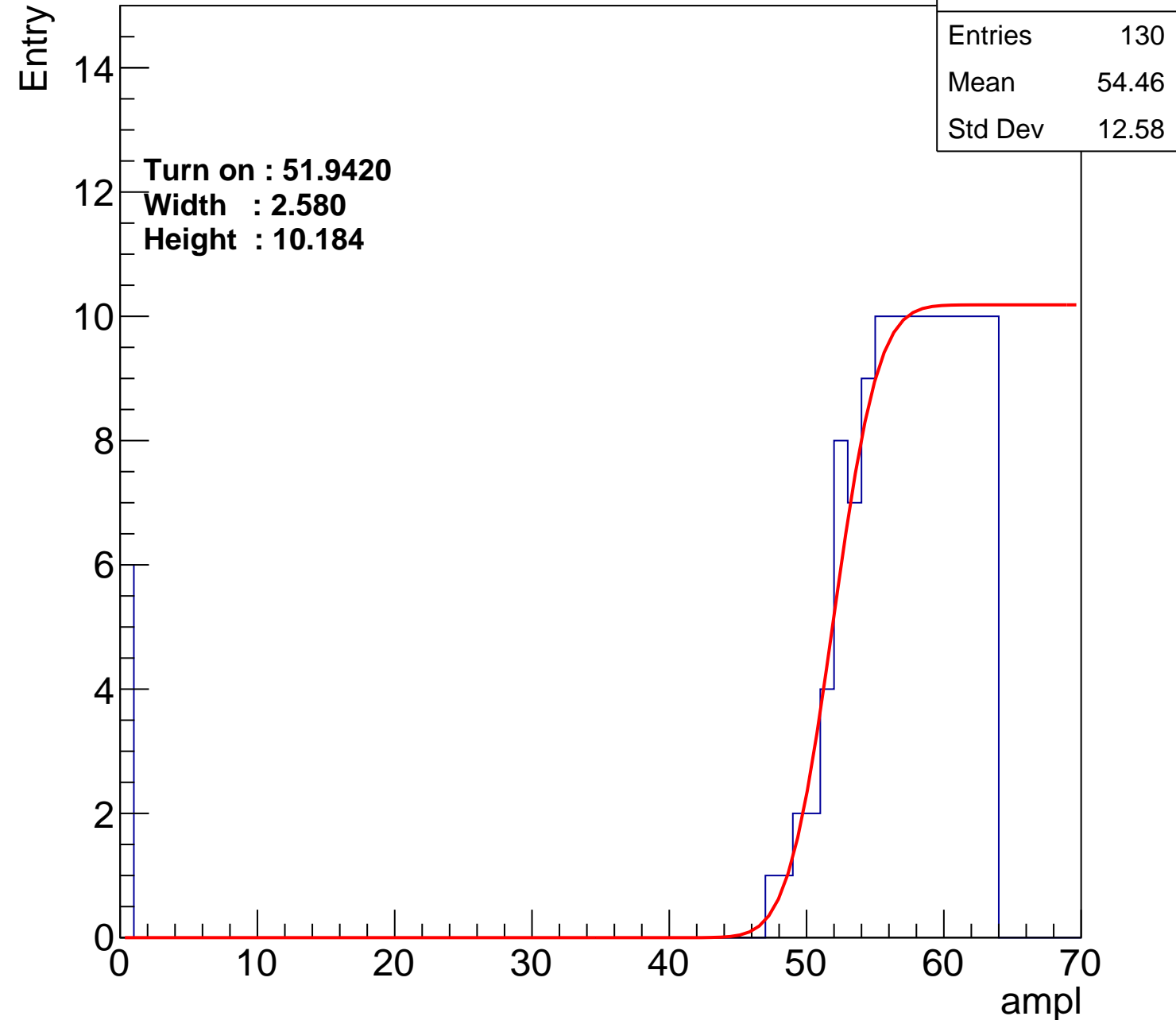
**Width : 2.580**

**Height : 10.184**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.96
Std Dev	8.05

Turn on : 51.1406

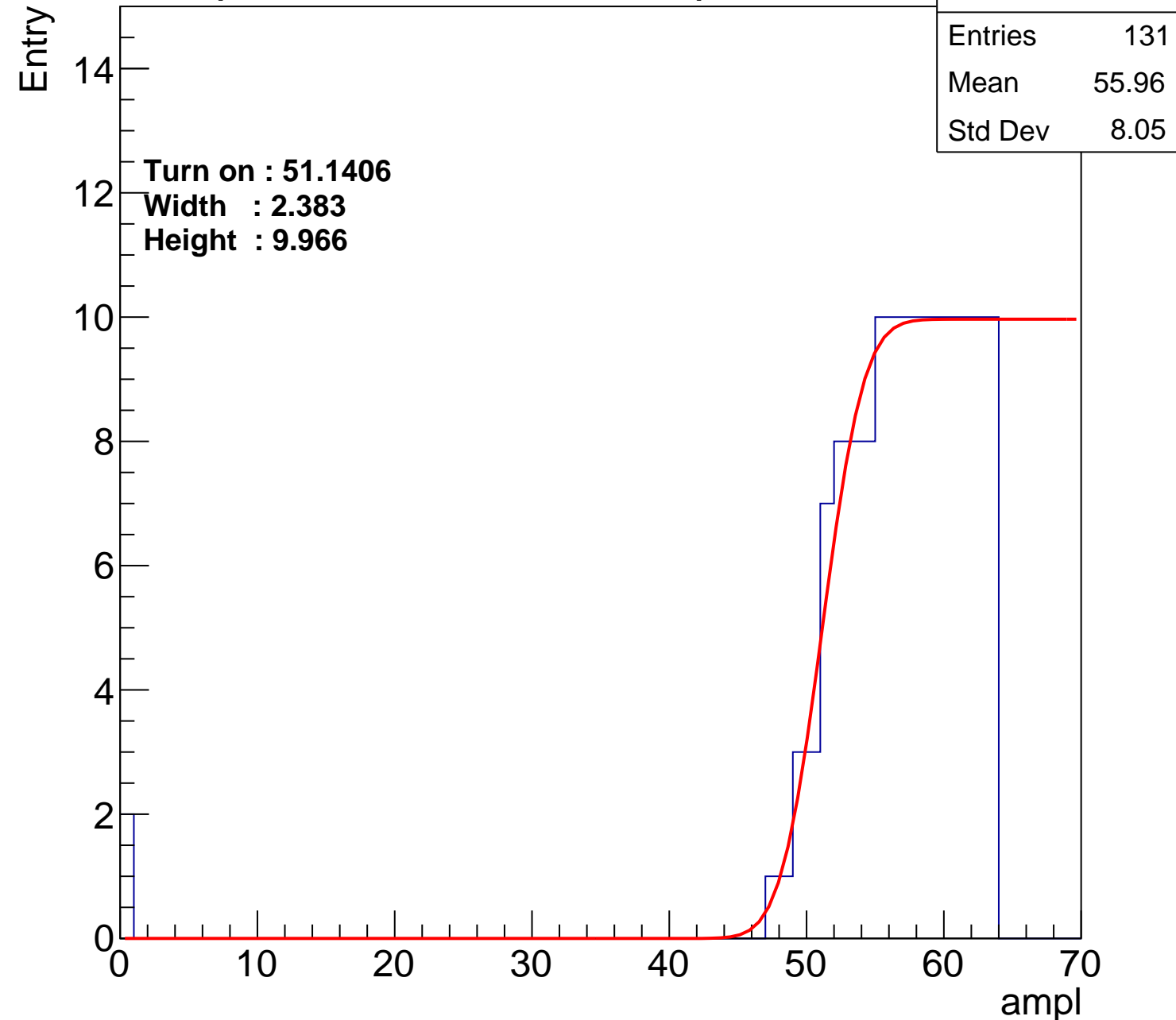
Width : 2.383

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	56.58
Std Dev	6.446

Turn on : 51.7602

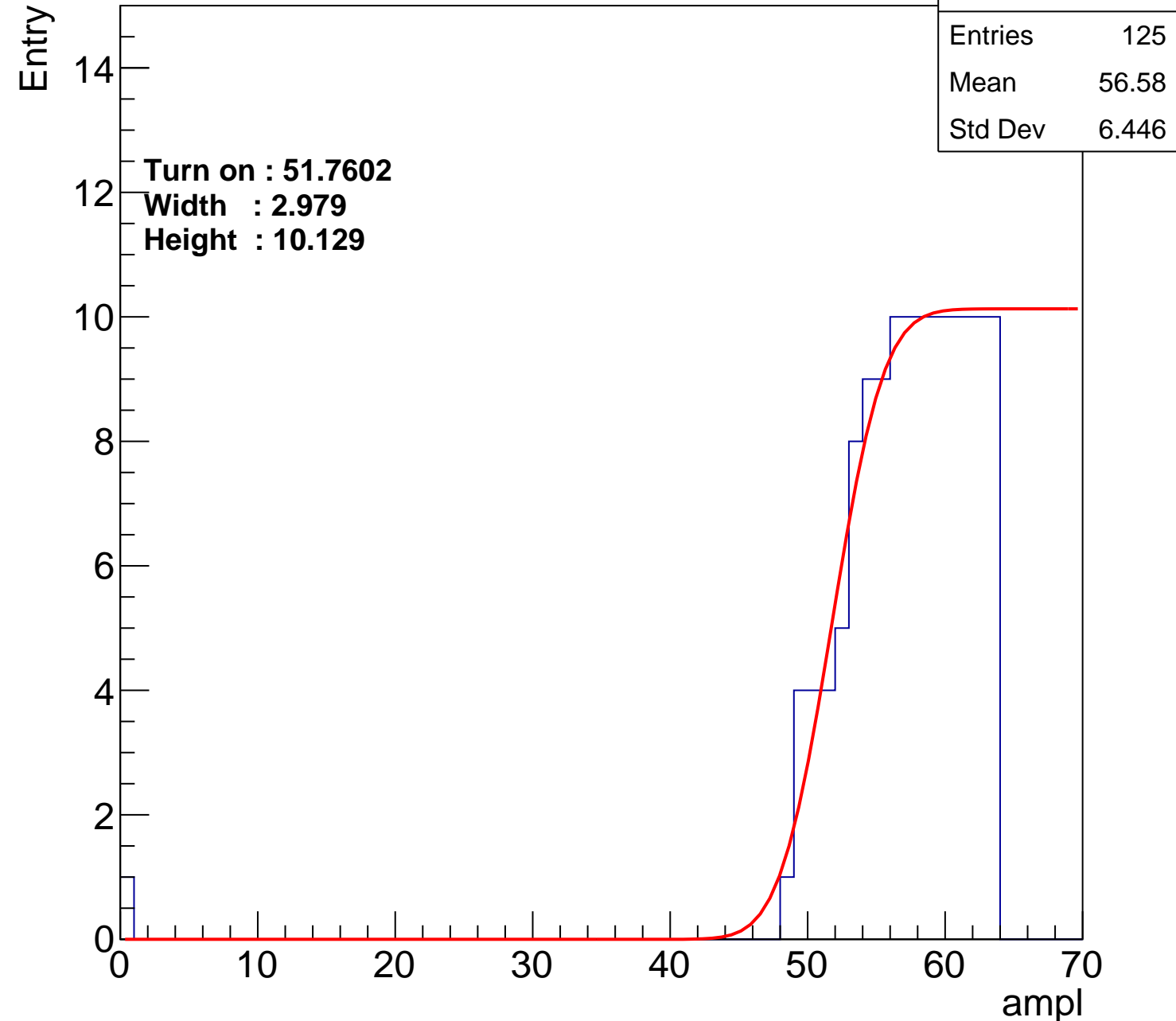
Width : 2.979

Height : 10.129

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

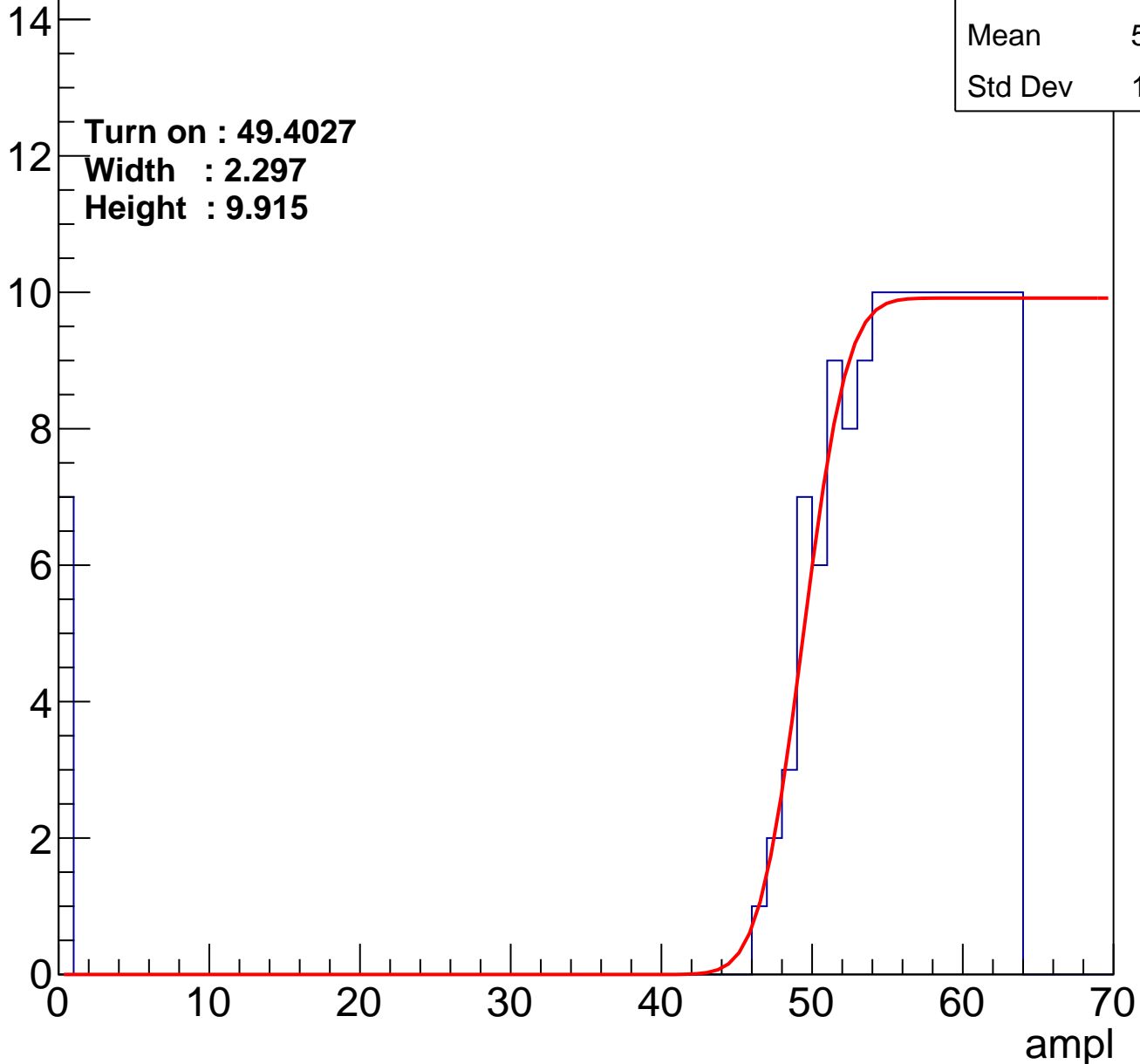
Entries	152
Mean	53.48
Std Dev	12.54

Turn on : 49.4027

Width : 2.297

Height : 9.915

Entry



# B0L103S, U23-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	155
Mean	54.32
Std Dev	9.971

Turn on : 49.0902

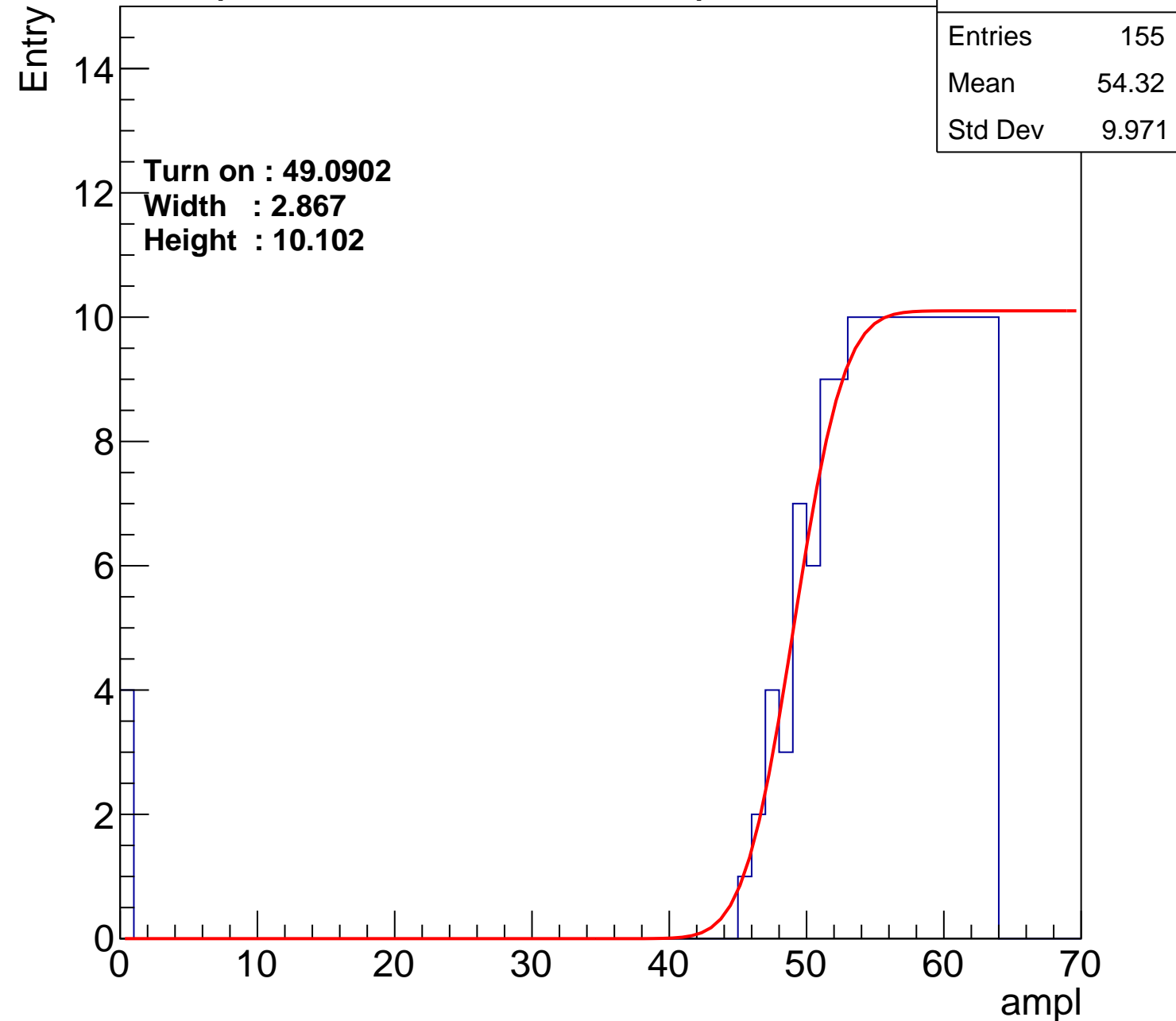
Width : 2.867

Height : 10.102

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.23
Std Dev	9.37

Turn on : 51.4921

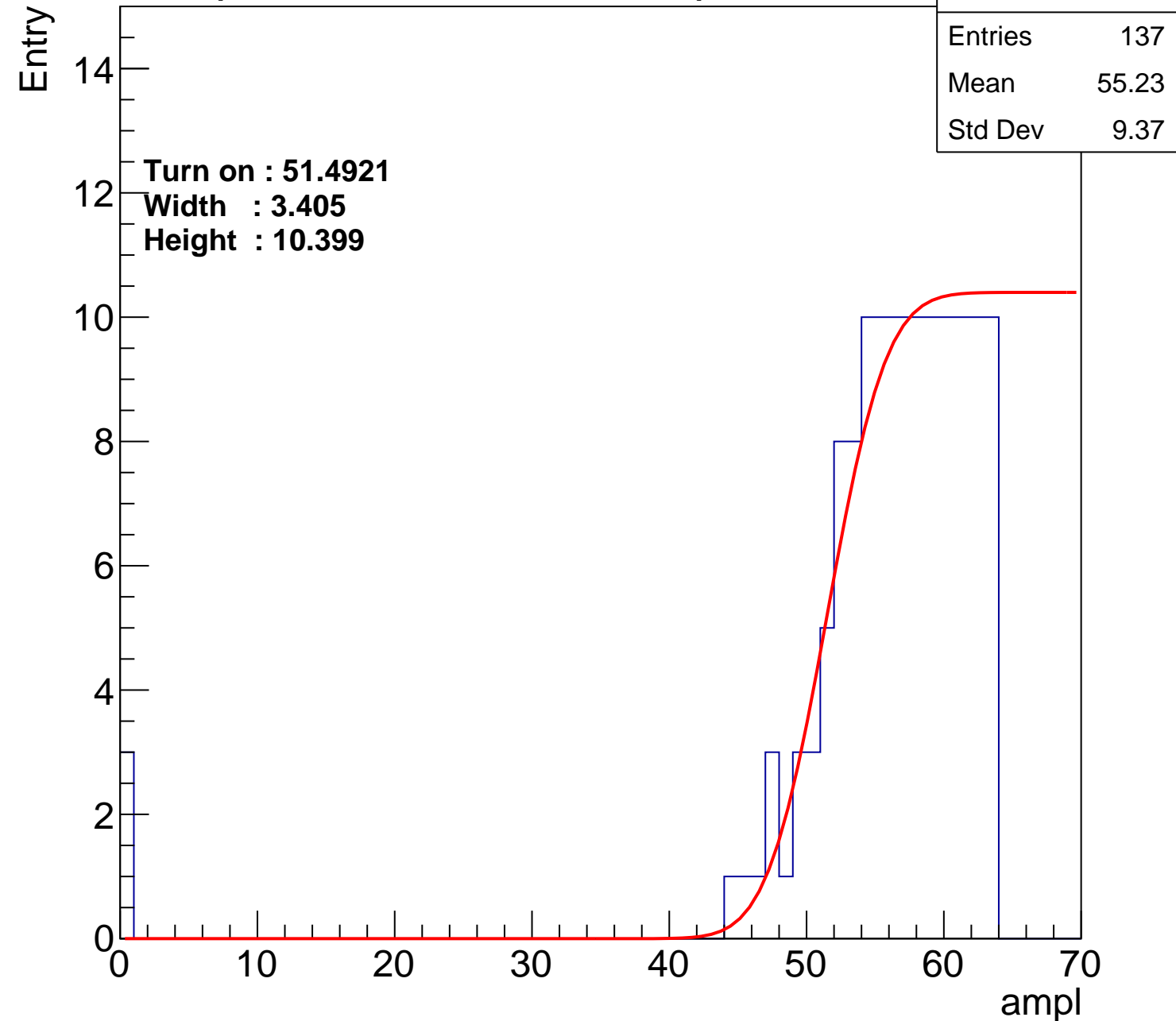
Width : 3.405

Height : 10.399

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.2
Std Dev	10.56

Turn on : 51.4313

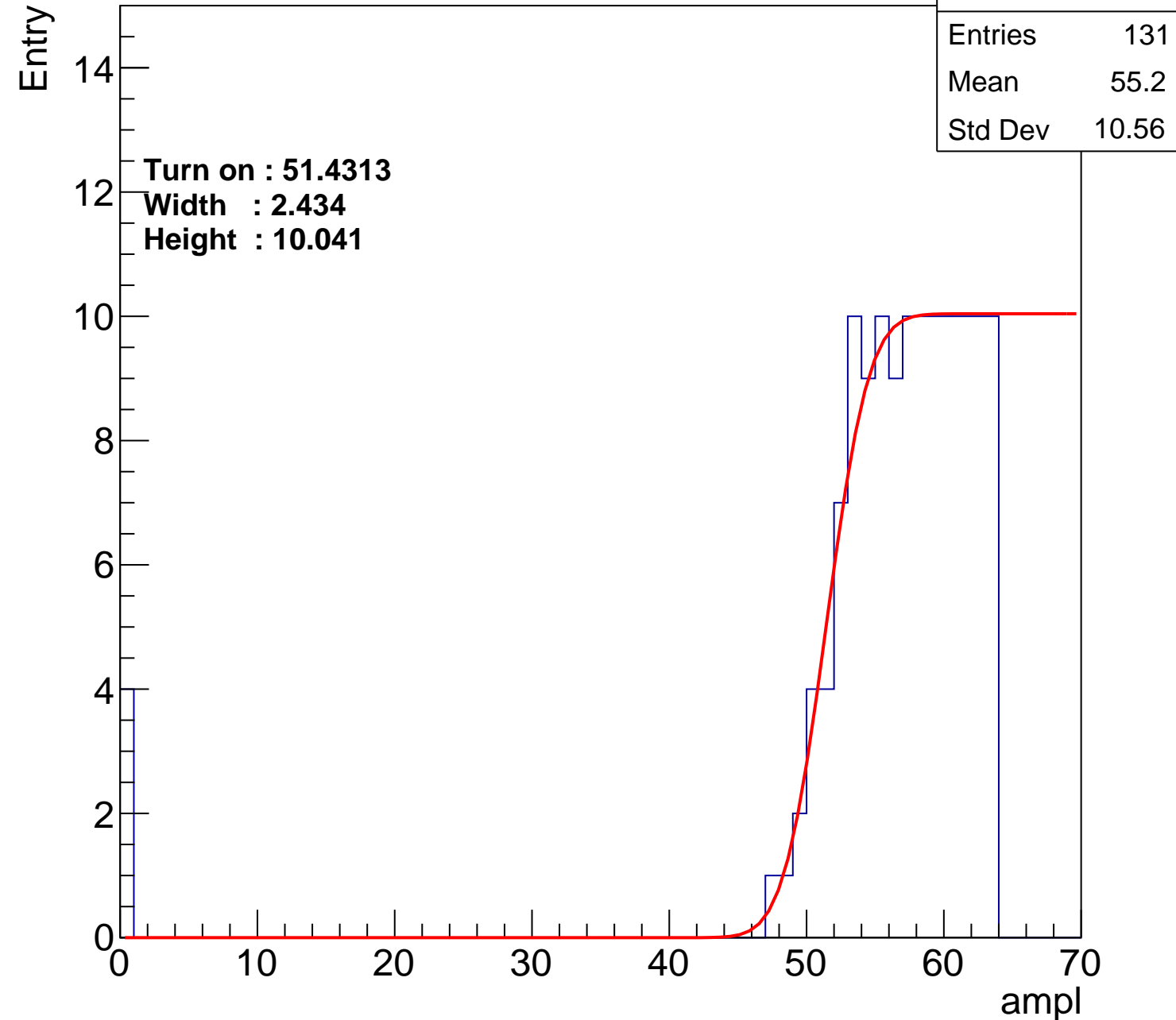
Width : 2.434

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.7
Std Dev	9.428

Turn on : 51.6914

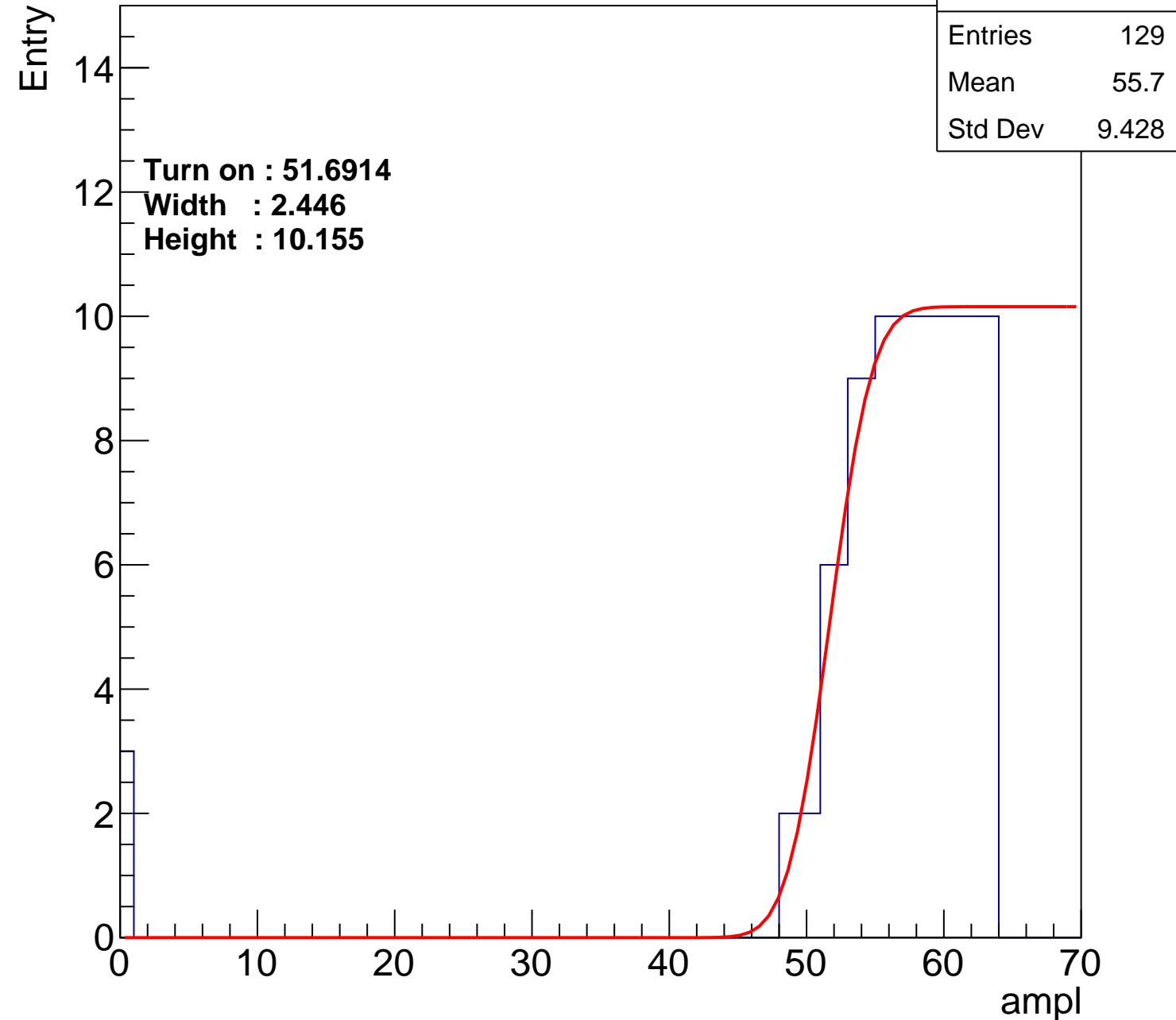
Width : 2.446

Height : 10.155

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

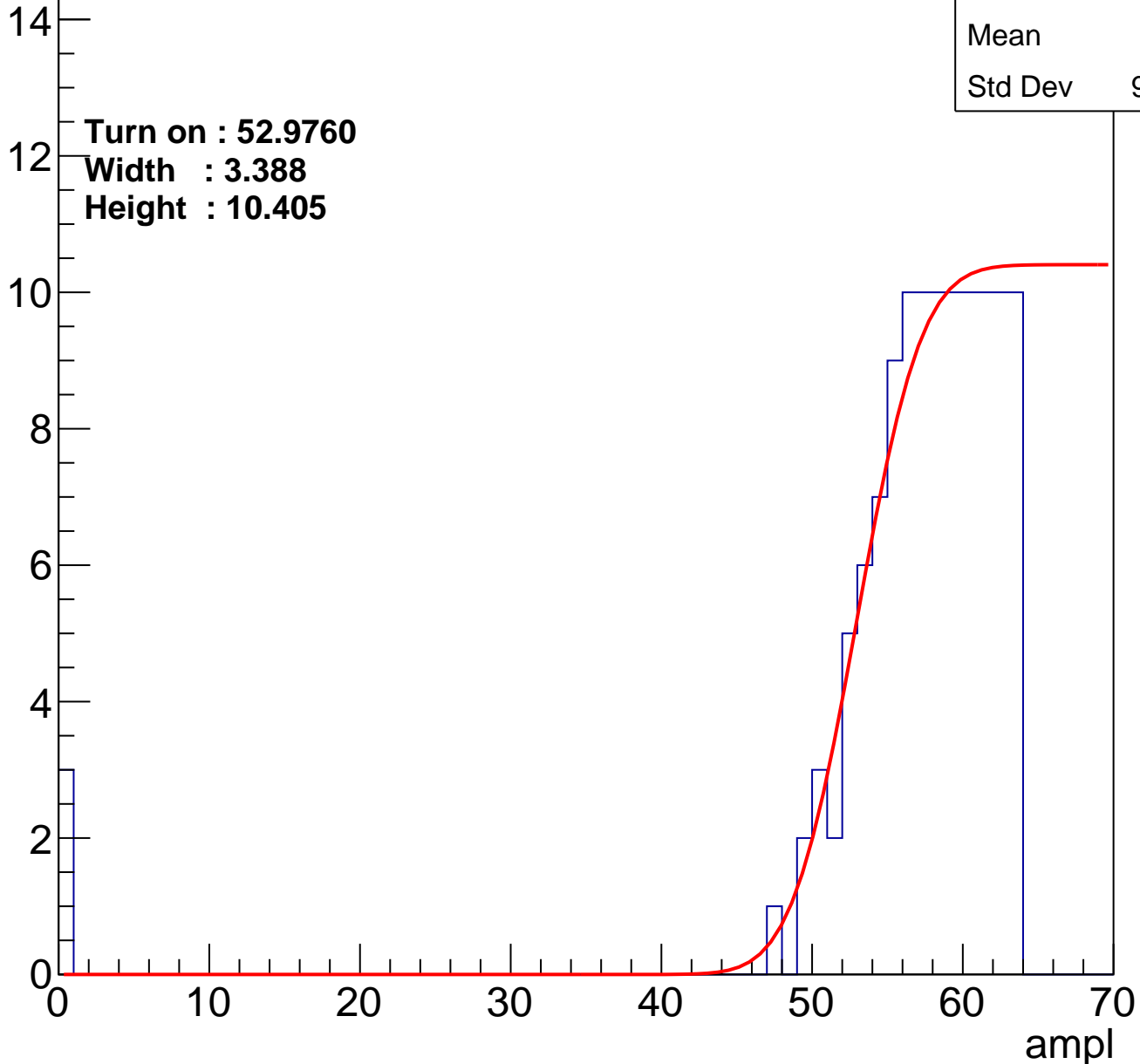
Entries	118
Mean	56
Std Dev	9.793

Turn on : 52.9760

Width : 3.388

Height : 10.405

Entry



# B0L103S, U23-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	54.72
Std Dev	11.62

Turn on : 51.6499

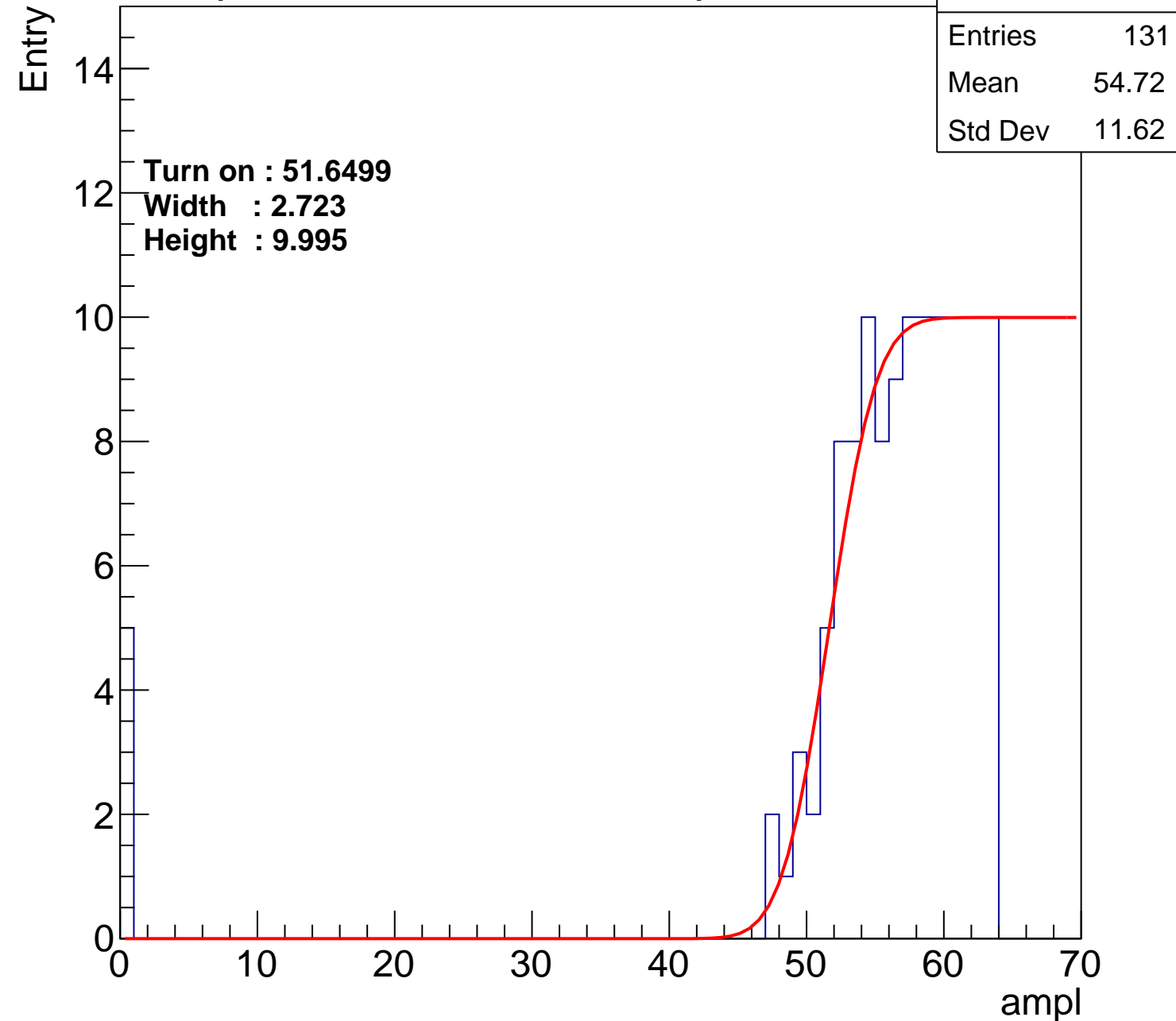
Width : 2.723

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch81

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	111
Mean	55.39
Std Dev	12.48

Turn on : 53.8571

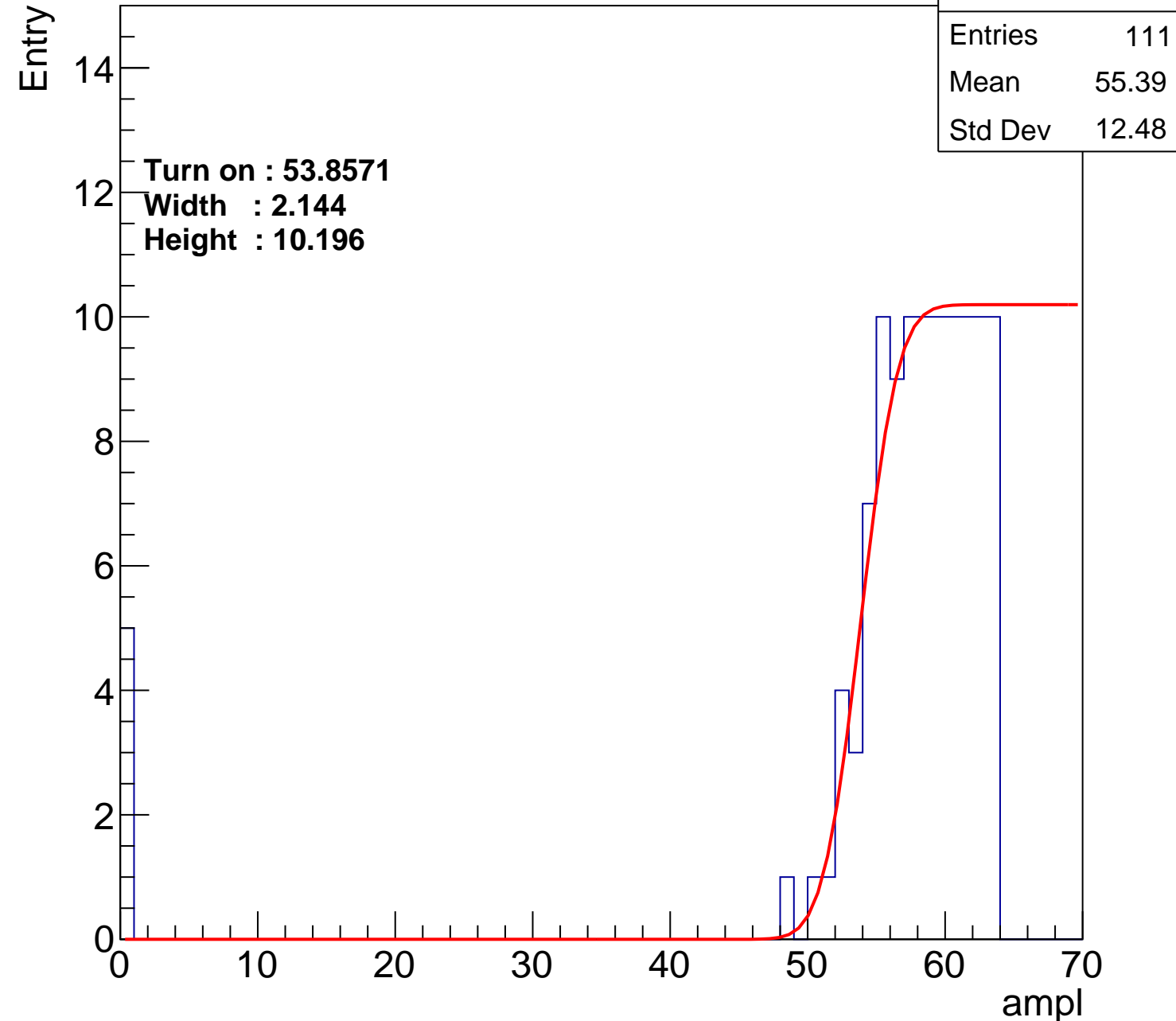
Width : 2.144

Height : 10.196

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

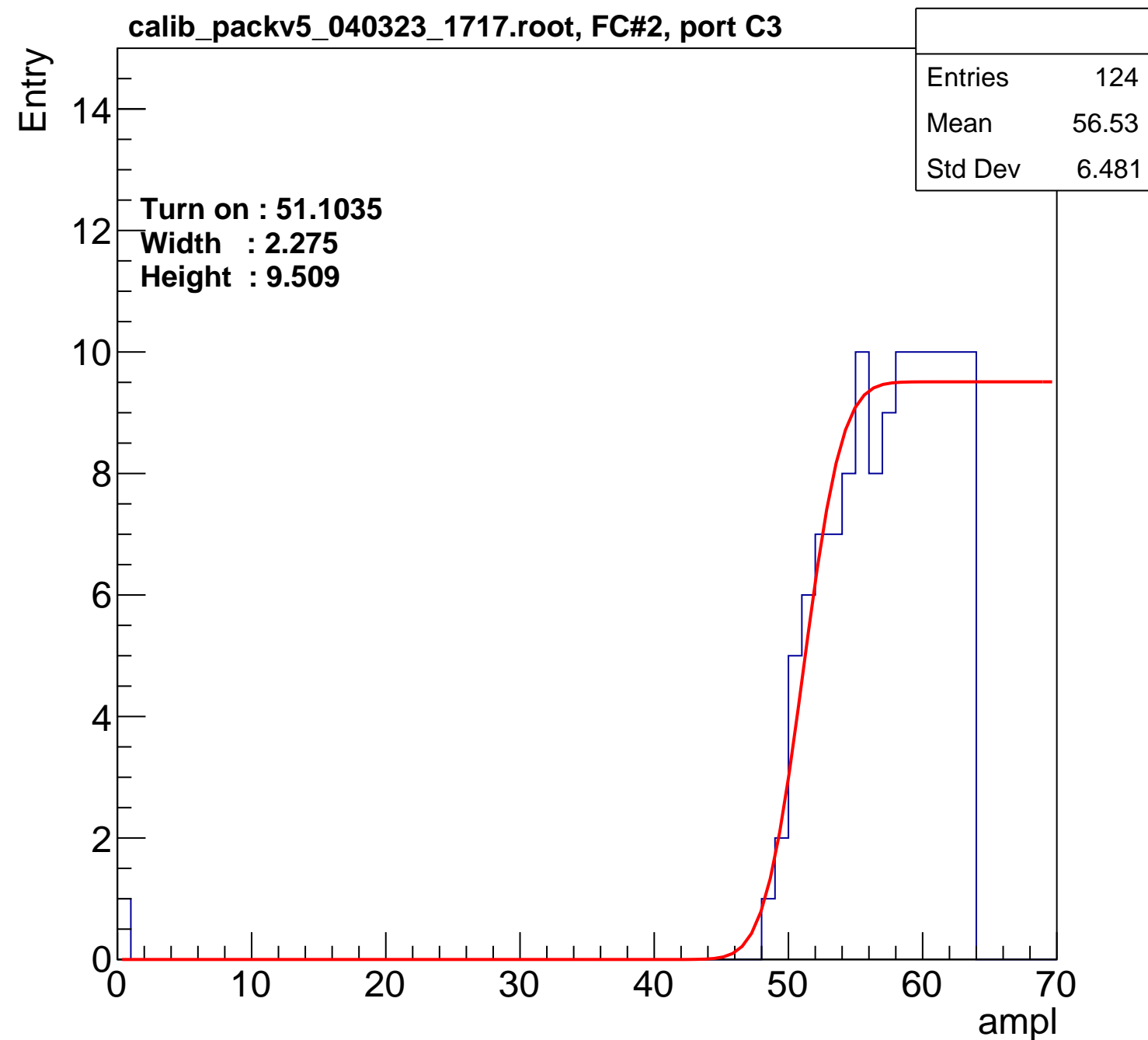
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 51.1035  
Width : 2.275  
Height : 9.509

Entries	124
Mean	56.53
Std Dev	6.481

ampl



# B0L103S, U23-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	113
Mean	56.97
Std Dev	6.619

Turn on : 53.0871

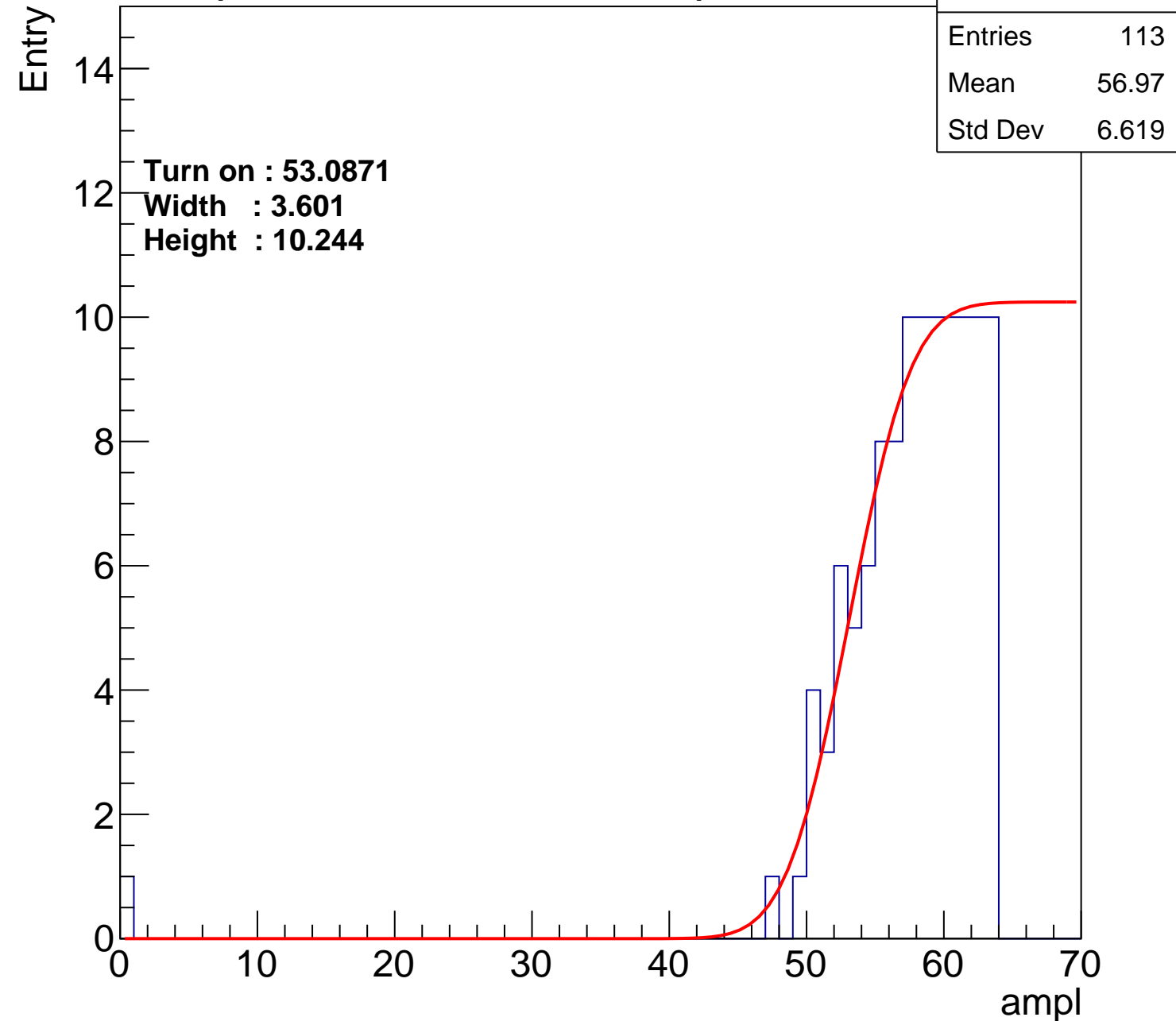
Width : 3.601

Height : 10.244

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.25
Std Dev	10.63

Turn on : 51.7187

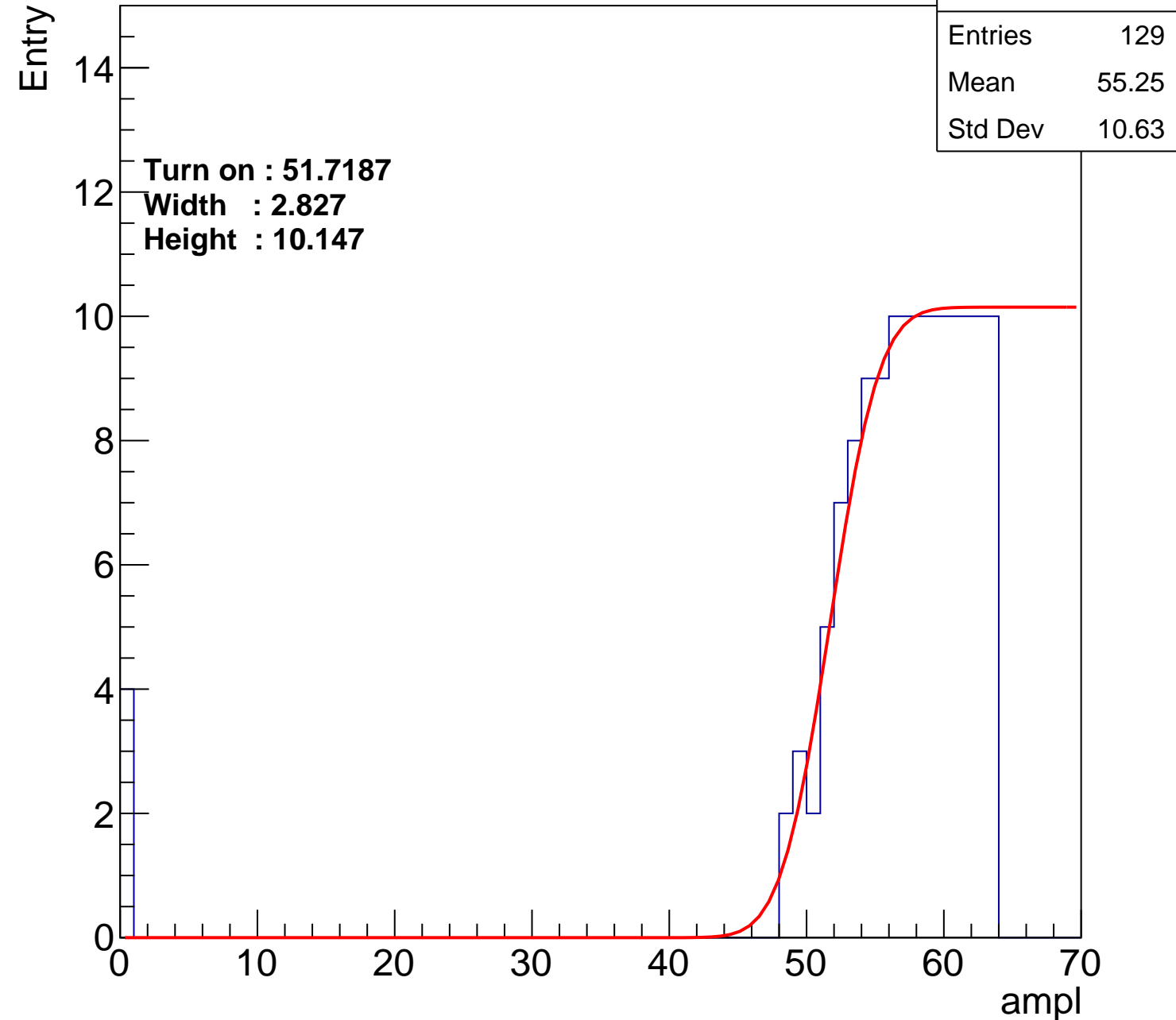
Width : 2.827

Height : 10.147

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.05
Std Dev	10.53

Turn on : 51.5043

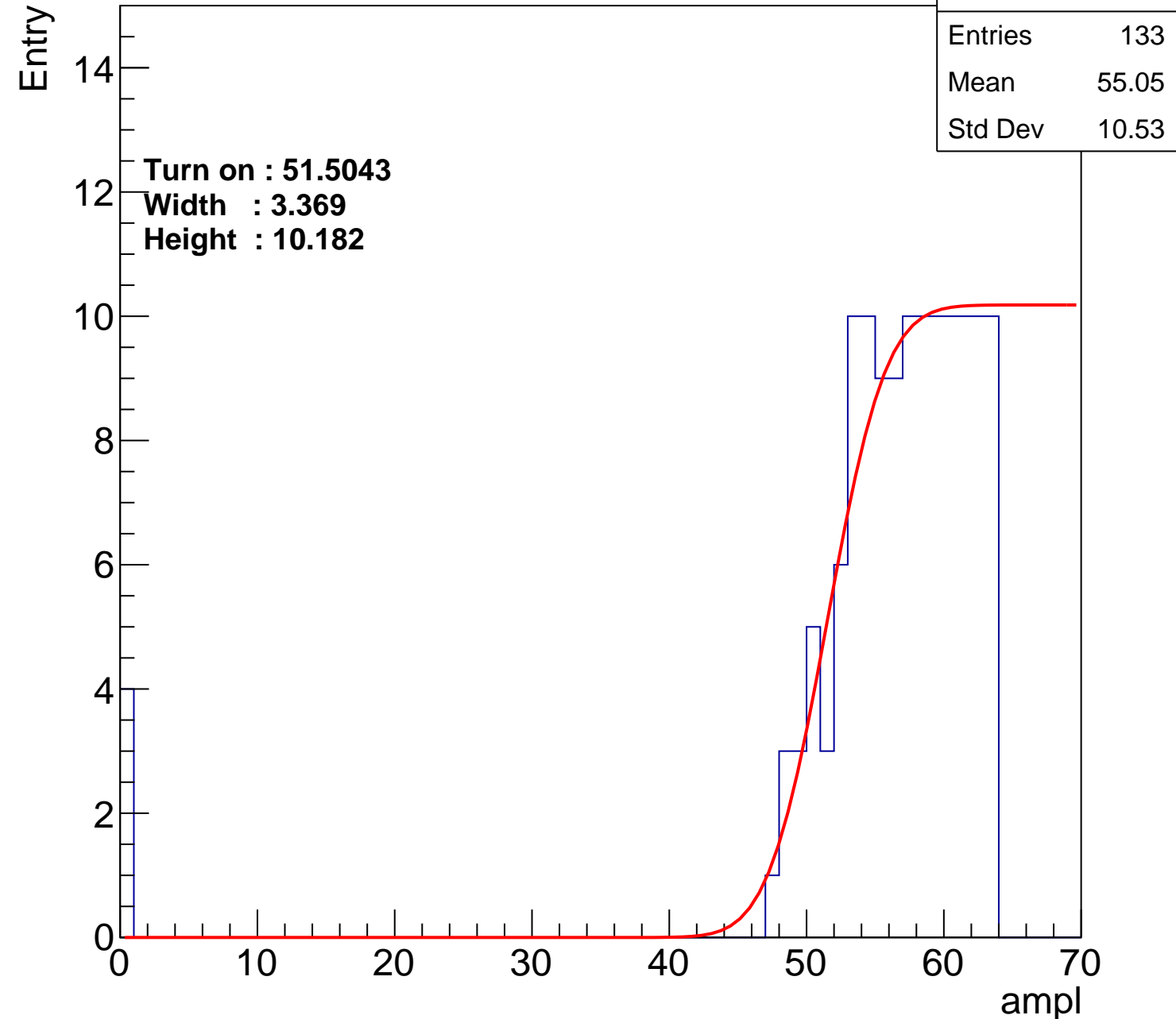
Width : 3.369

Height : 10.182

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.82
Std Dev	10.52

Turn on : 51.1890

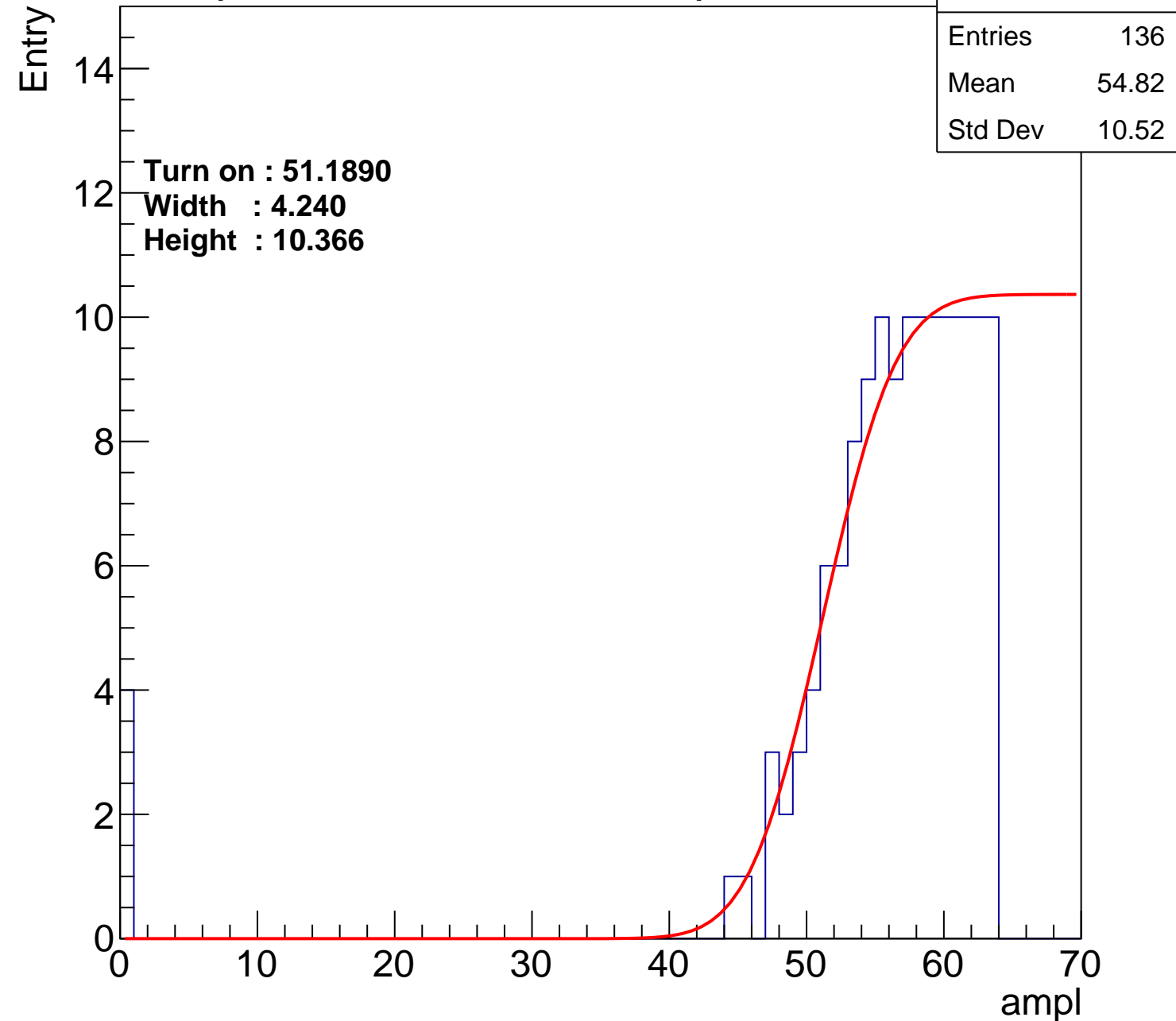
Width : 4.240

Height : 10.366

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

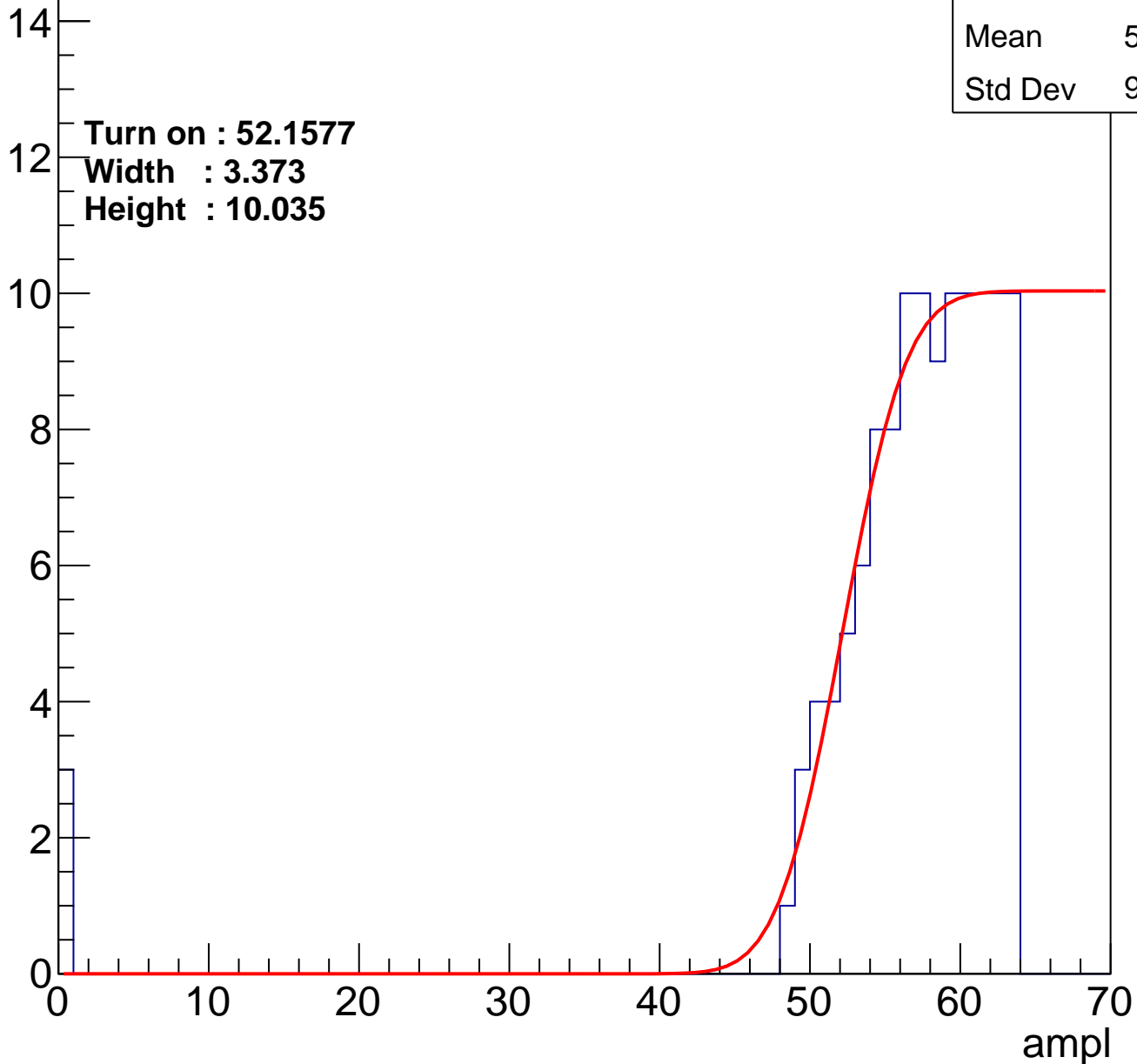
Entries	121
Mean	55.79
Std Dev	9.718

Turn on : 52.1577

Width : 3.373

Height : 10.035

Entry



# B0L103S, U23-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	55.7
Std Dev	8.032

Turn on : 51.0872

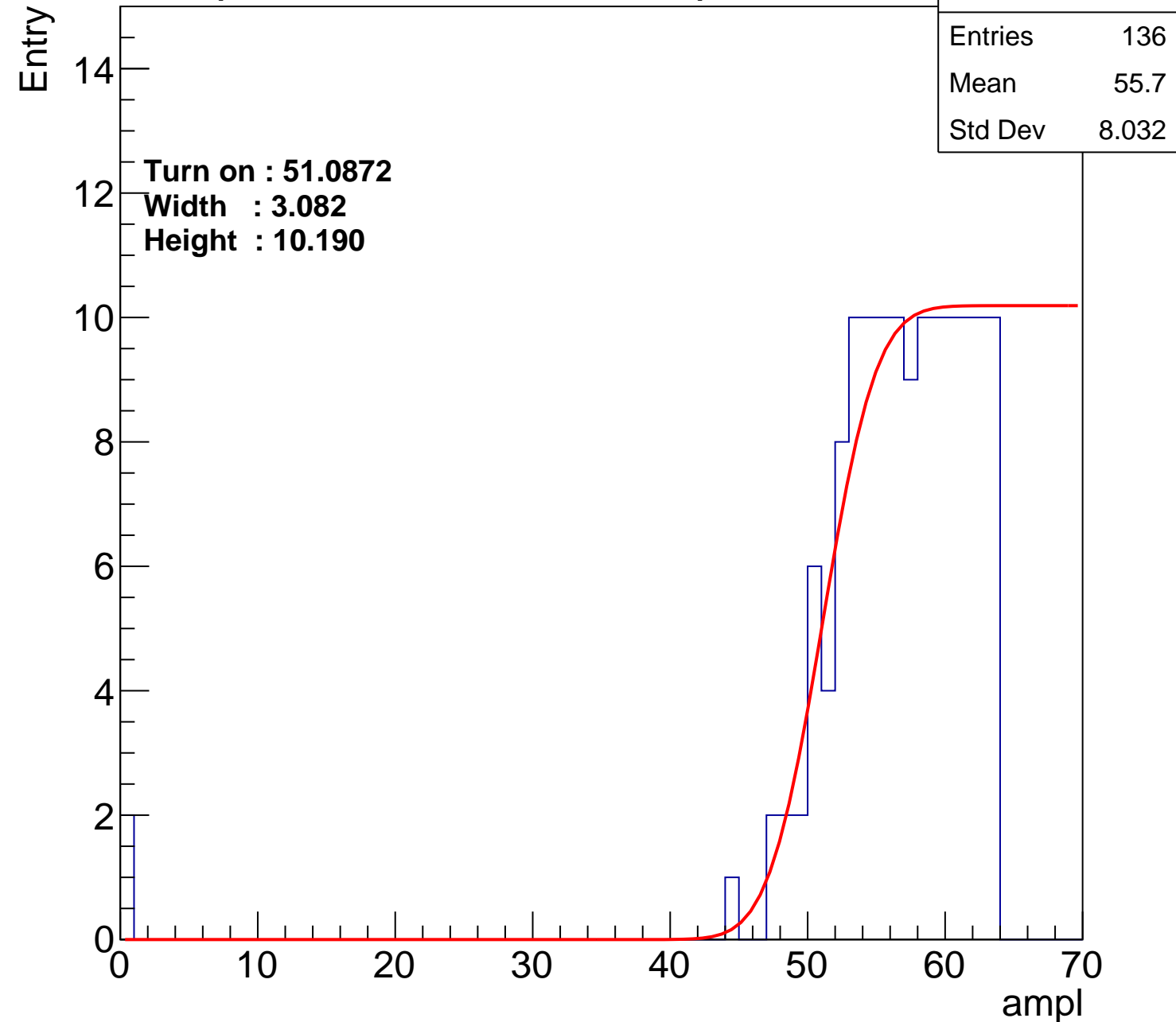
Width : 3.082

Height : 10.190

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.77
Std Dev	9.693

Turn on : 52.3846

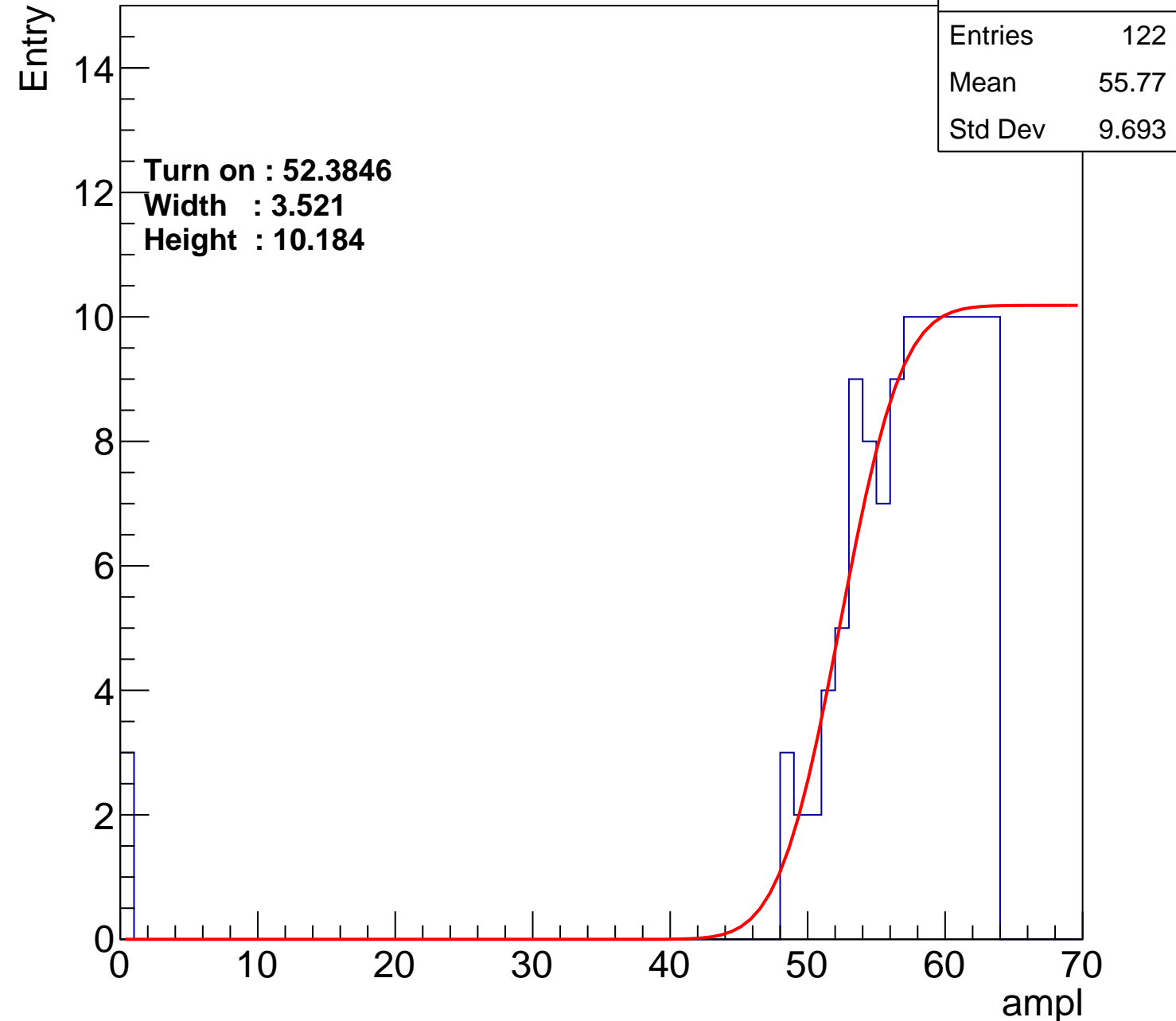
Width : 3.521

Height : 10.184

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	108
Mean	56.2
Std Dev	10.19

Turn on : 53.4763

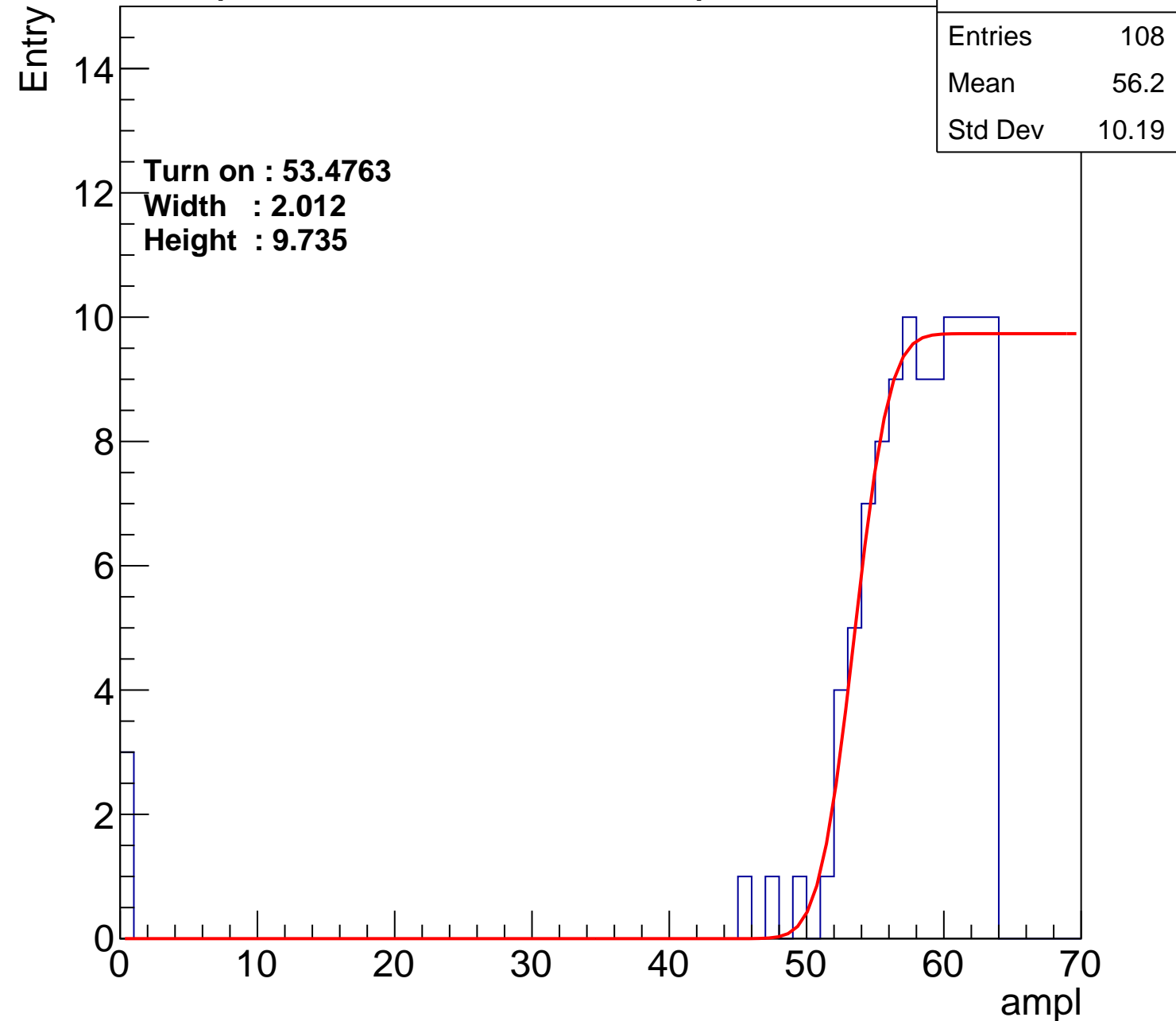
Width : 2.012

Height : 9.735

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	54.97
Std Dev	12.19

Turn on : 52.9745

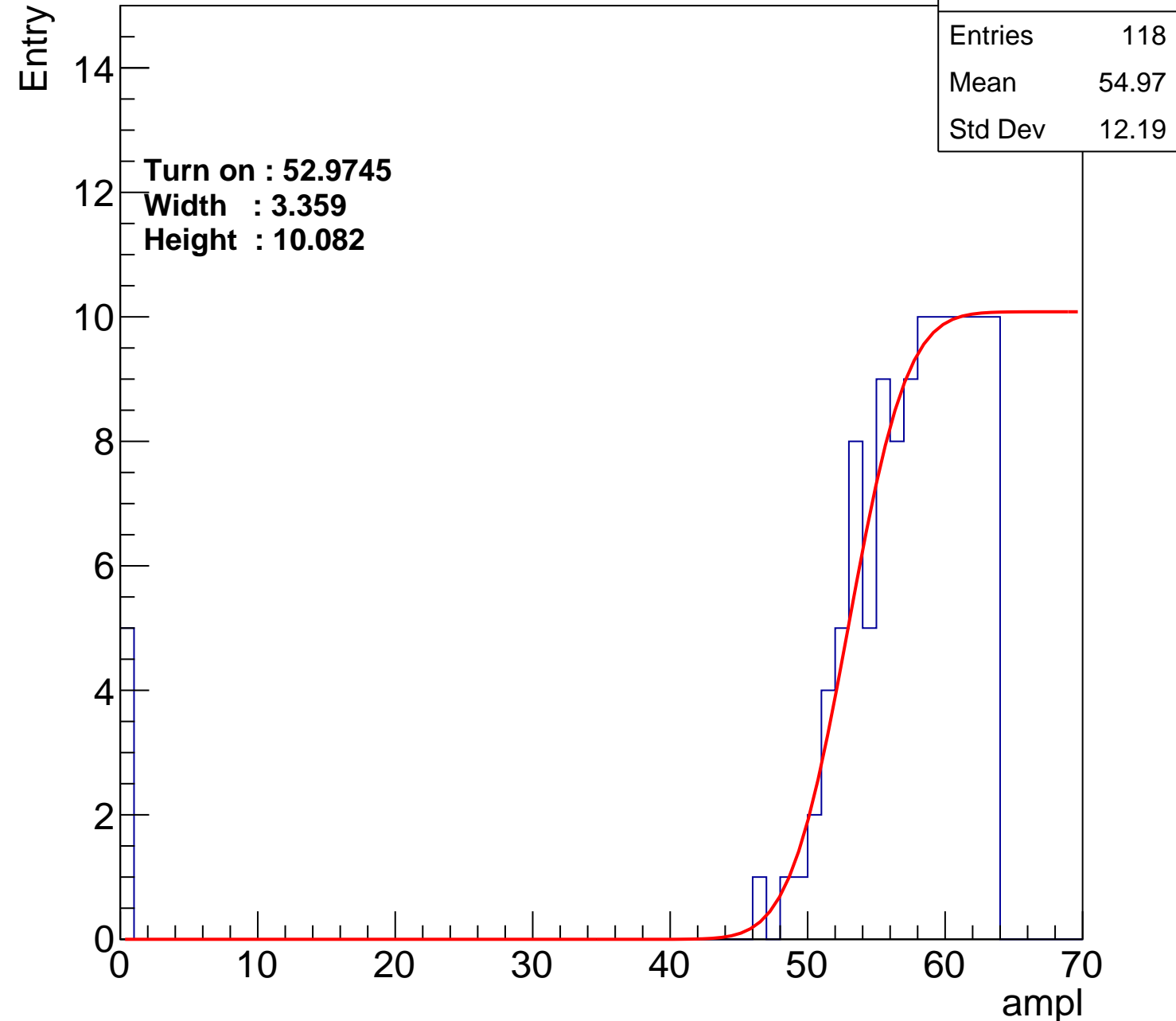
Width : 3.359

Height : 10.082

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.79
Std Dev	10.52

Turn on : 51.4923

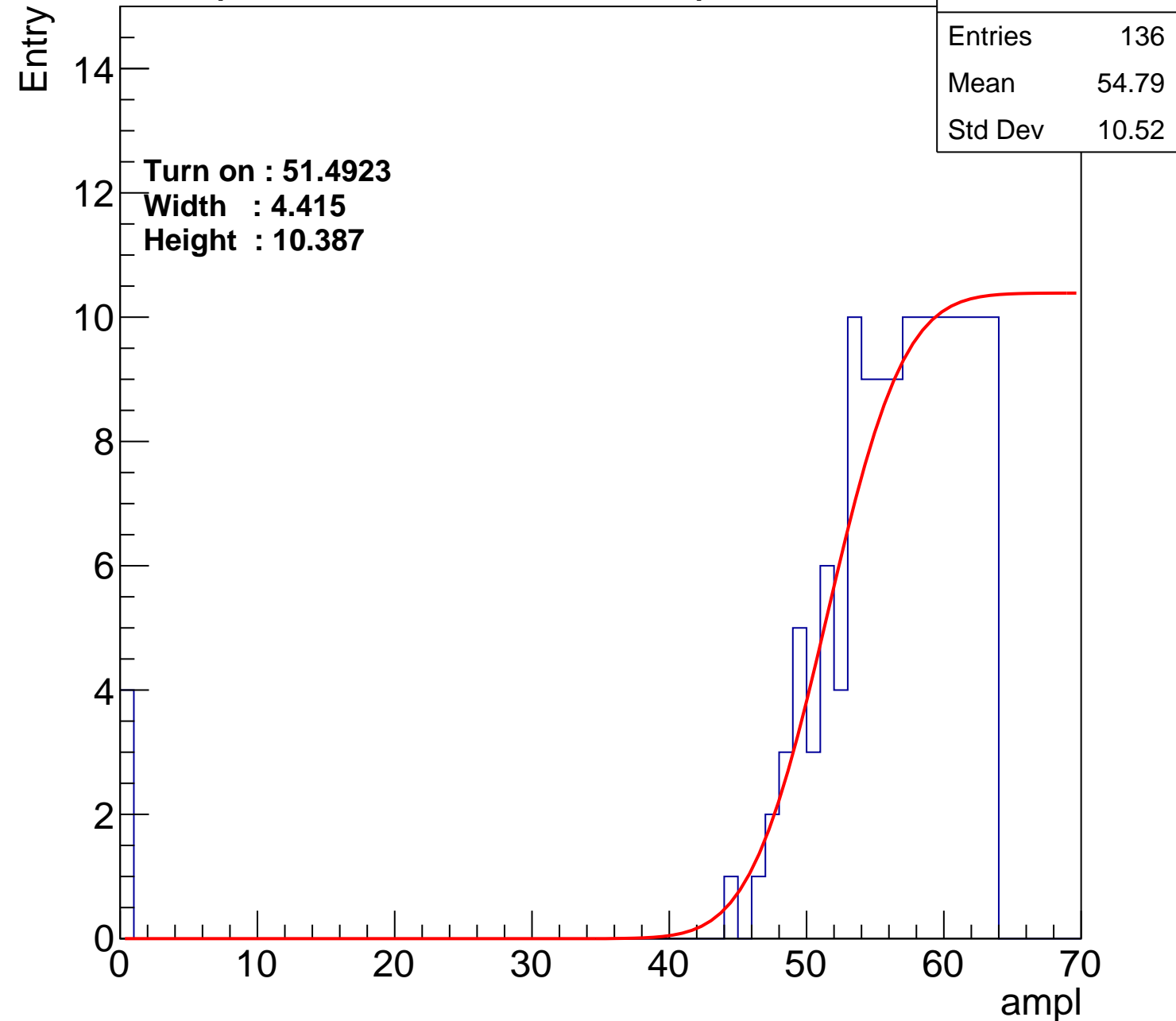
Width : 4.415

Height : 10.387

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	118
Mean	55.97
Std Dev	9.786

Turn on : 52.5515

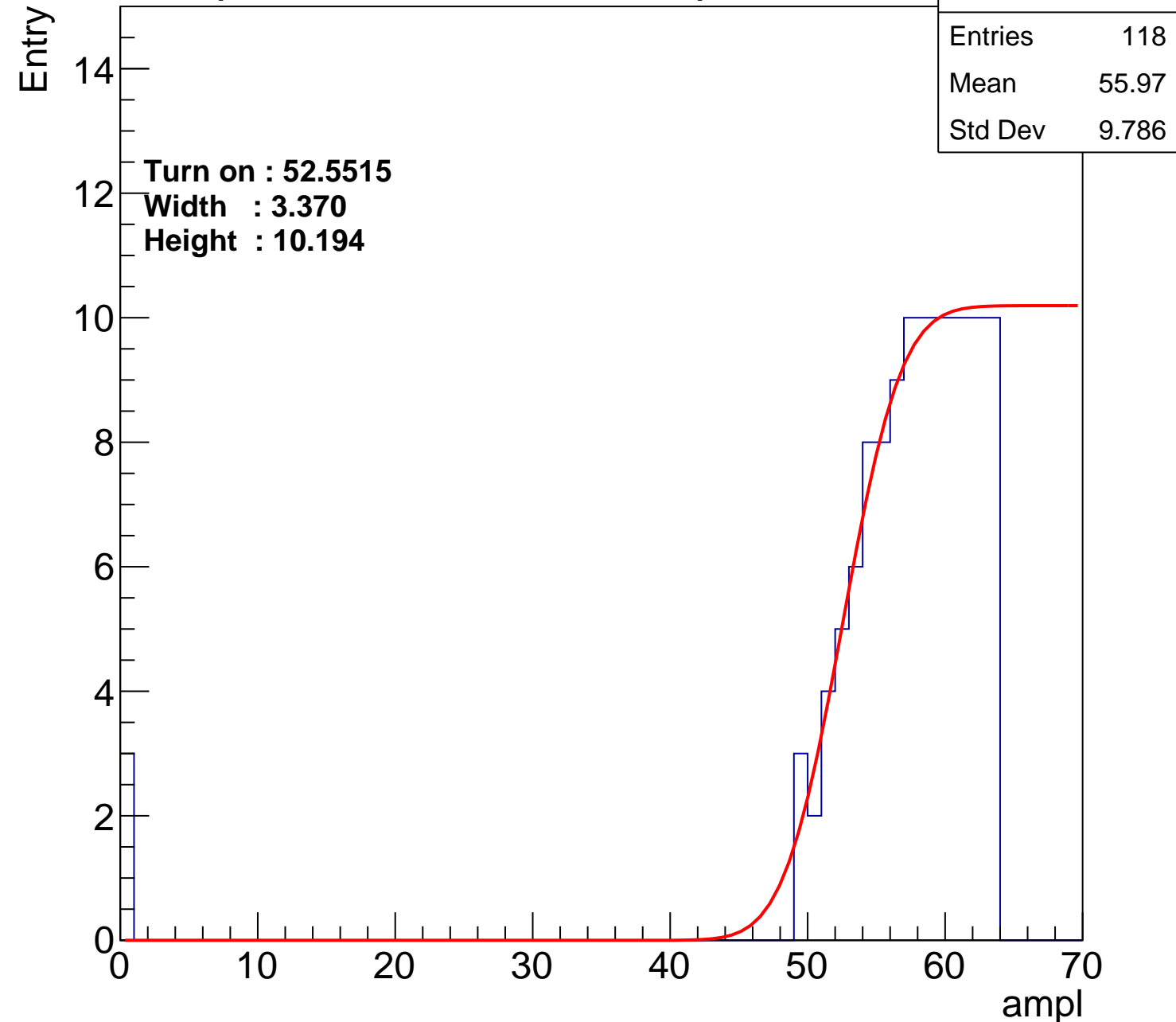
Width : 3.370

Height : 10.194

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	55.83
Std Dev	8.052

Turn on : 50.6725

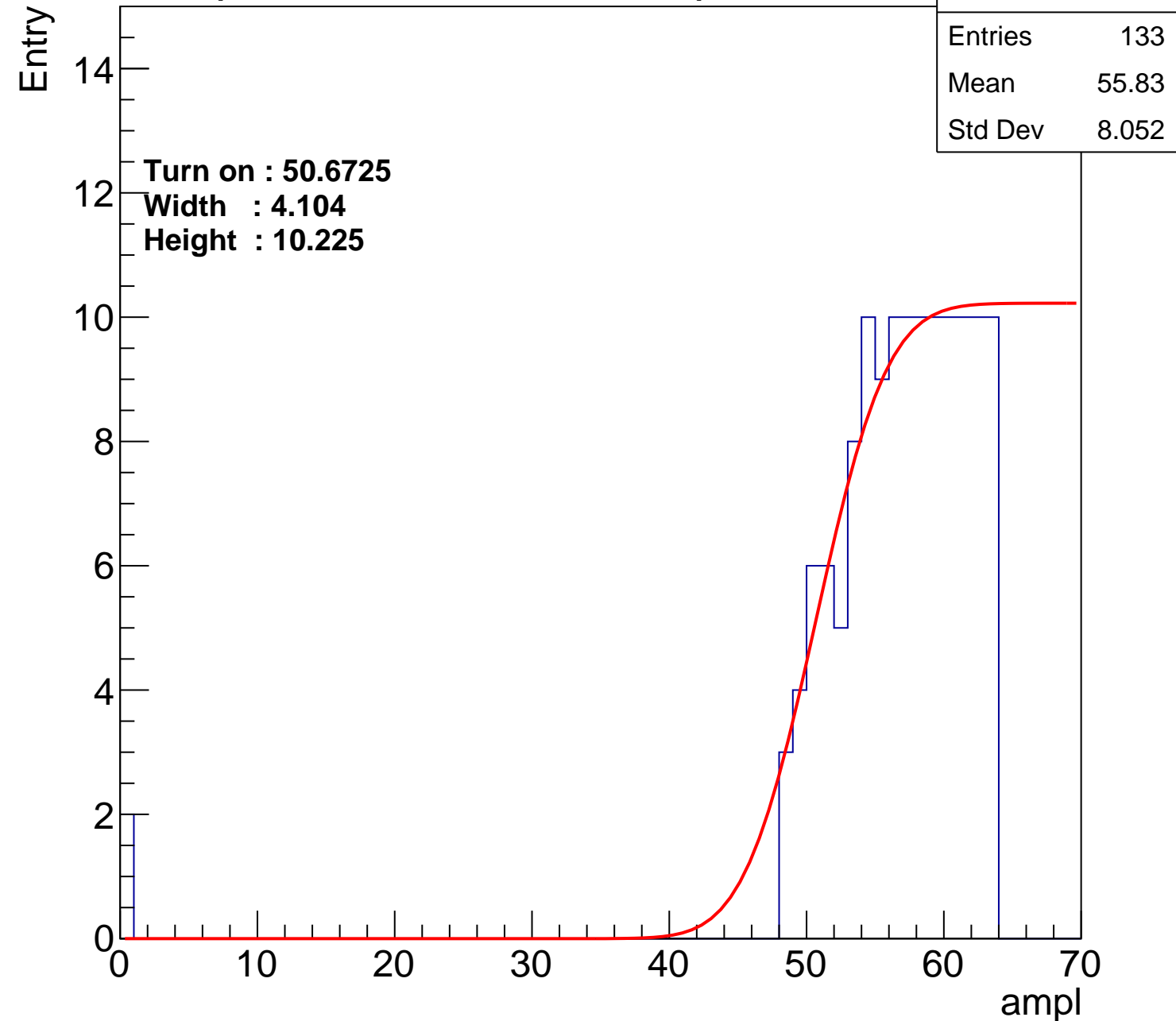
Width : 4.104

Height : 10.225

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	55.66
Std Dev	9.515

Turn on : 51.6390

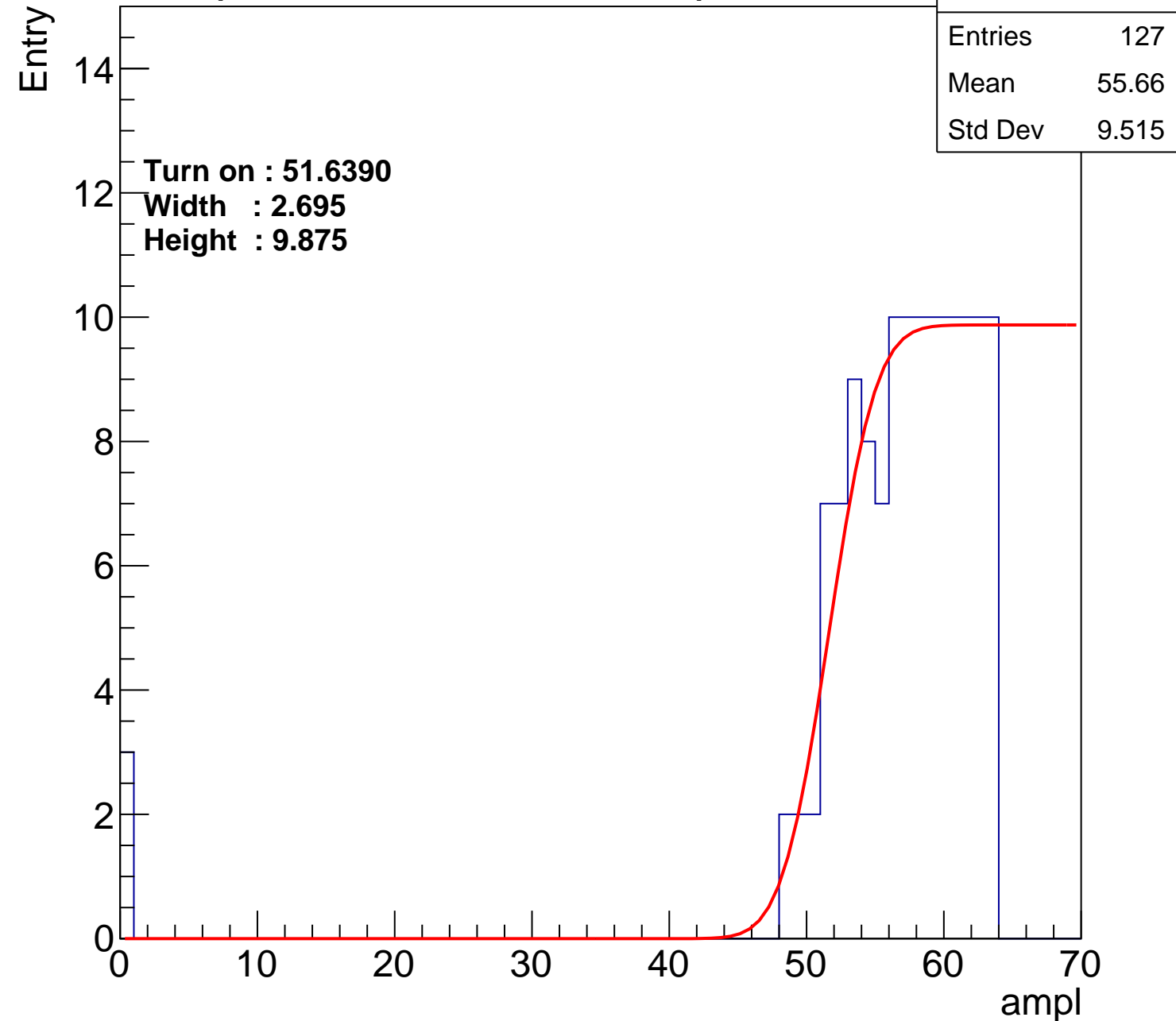
Width : 2.695

Height : 9.875

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	54.57
Std Dev	12.85

Turn on : 52.2817

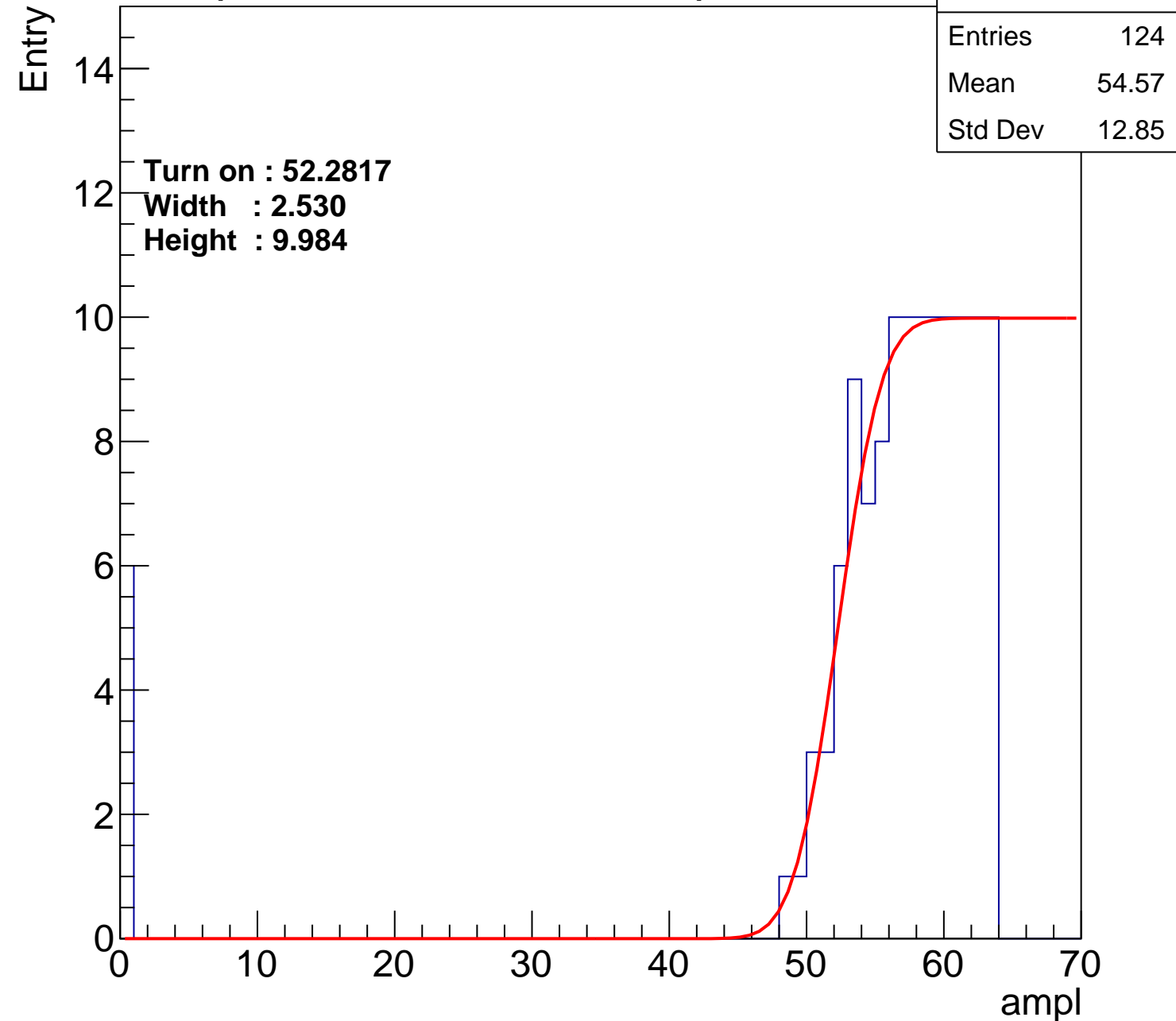
Width : 2.530

Height : 9.984

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	55.34
Std Dev	7.989

Turn on : 48.9290

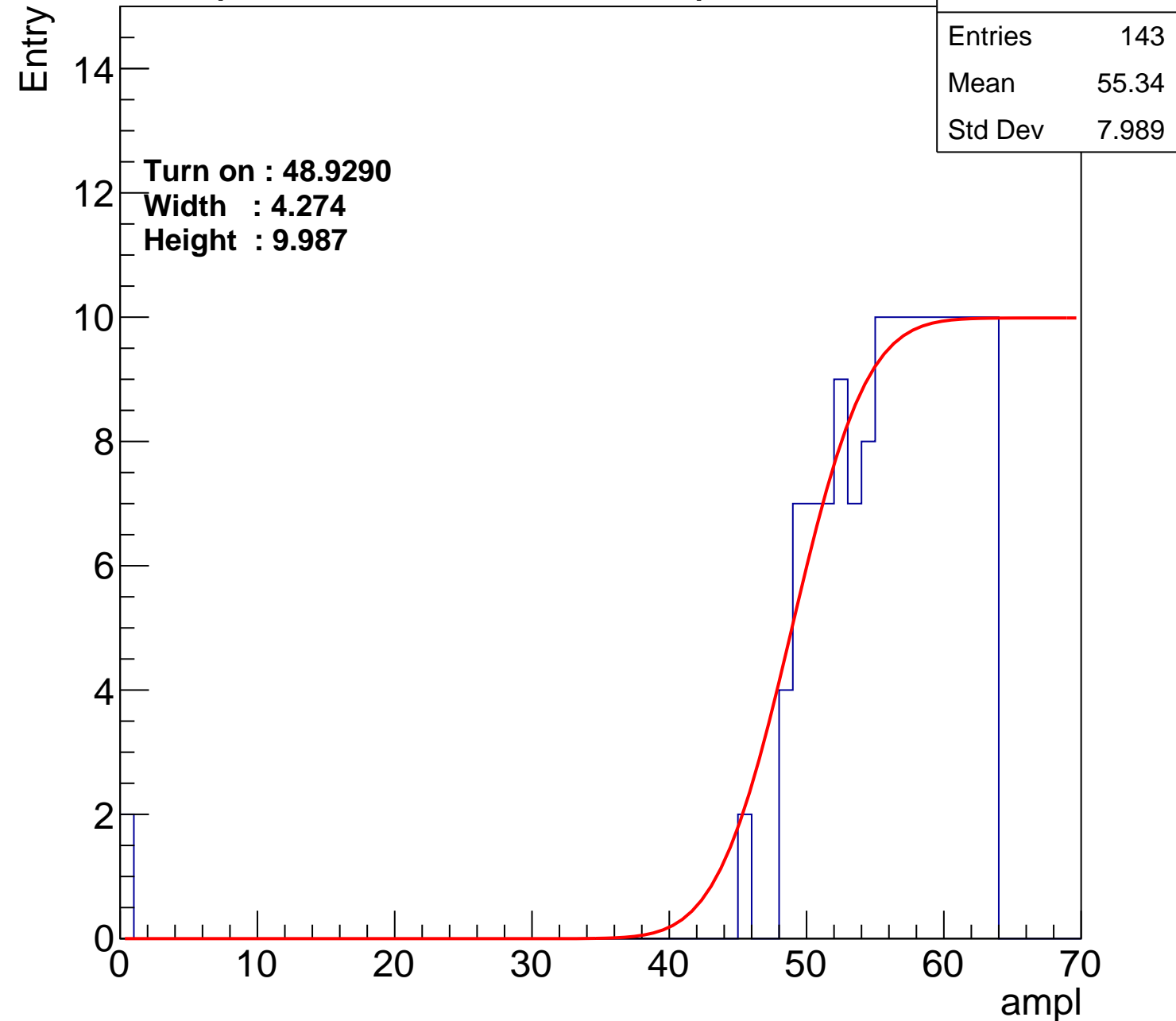
Width : 4.274

Height : 9.987

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	142
Mean	55.07
Std Dev	9.213

Turn on : 50.3325

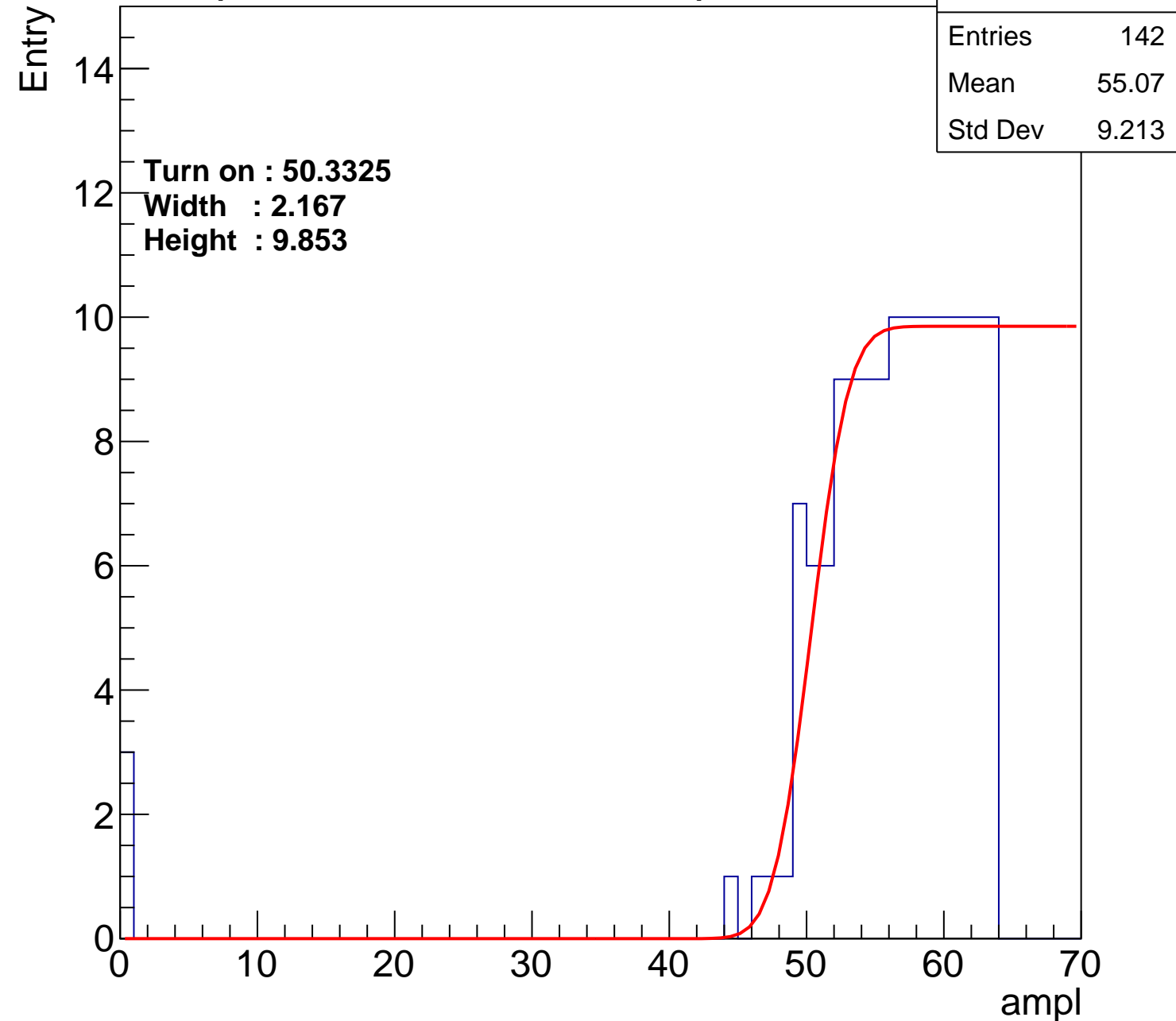
Width : 2.167

Height : 9.853

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	120
Mean	55.93
Std Dev	9.747

Turn on : 53.1006

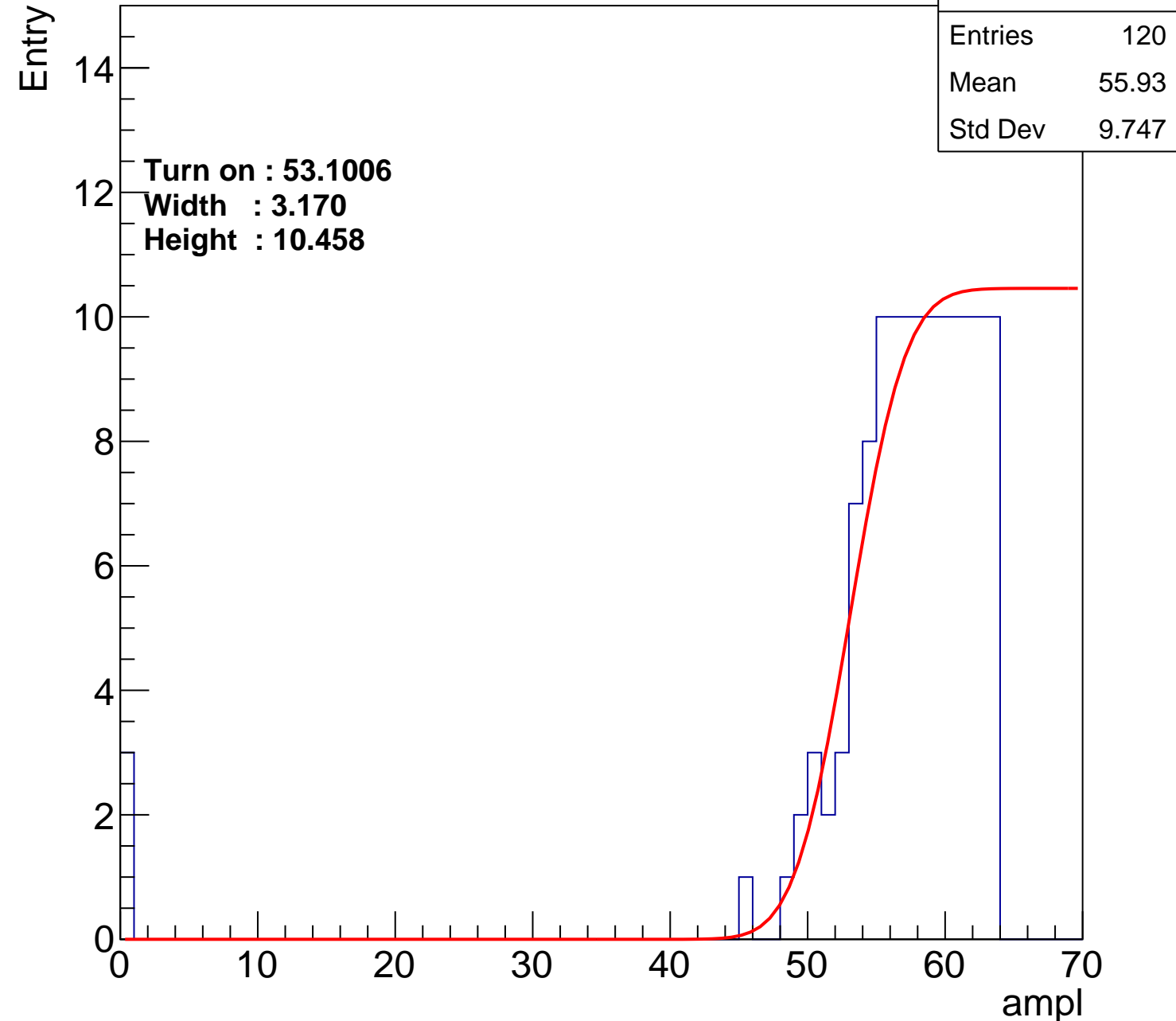
Width : 3.170

Height : 10.458

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

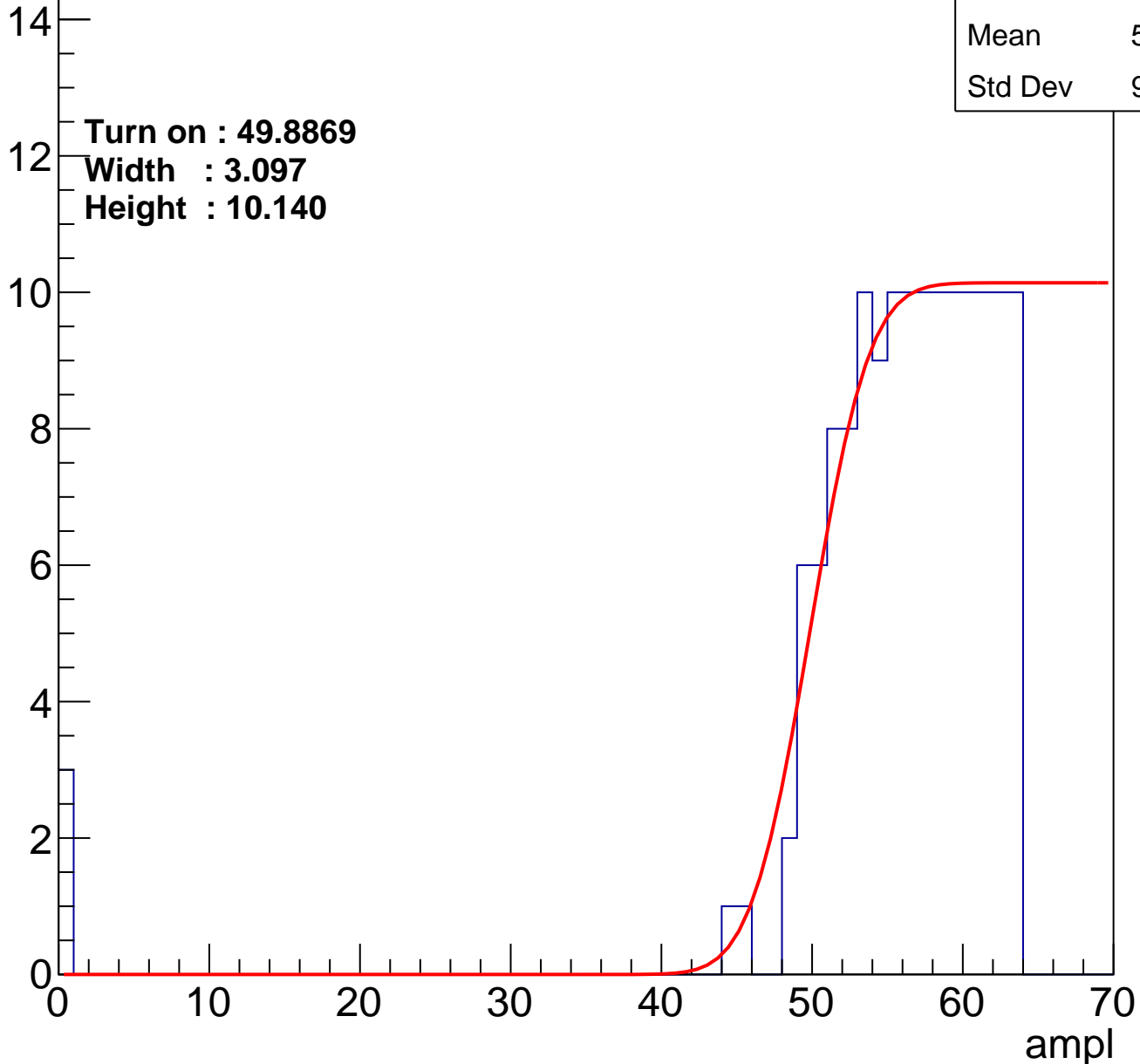
Entries	144
Mean	55.06
Std Dev	9.147

Turn on : 49.8869

Width : 3.097

Height : 10.140

Entry



# B0L103S, U23-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

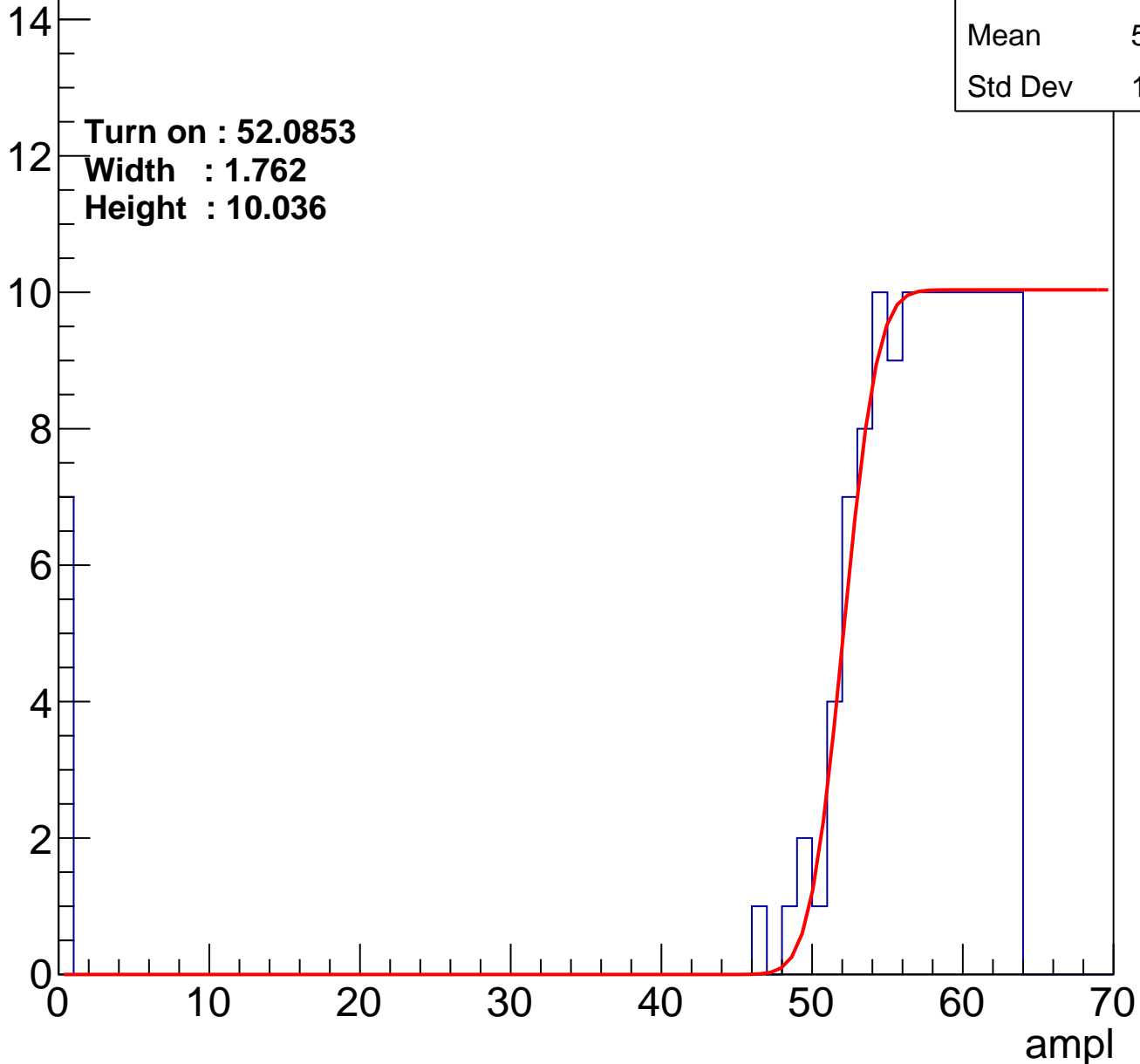
Entries	130
Mean	54.07
Std Dev	13.44

Turn on : 52.0853

Width : 1.762

Height : 10.036

Entry



# B0L103S, U23-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

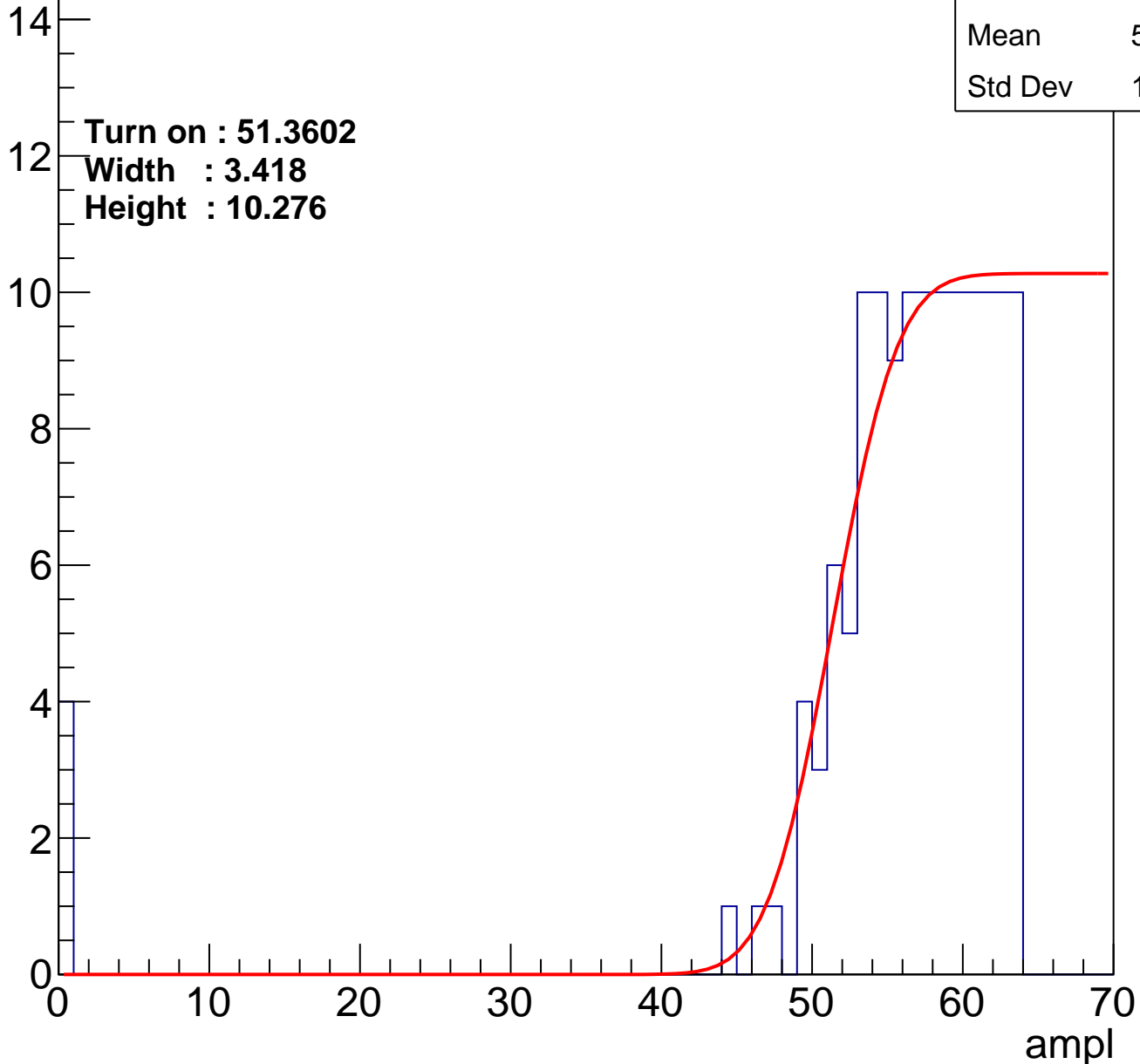
Entries	134
Mean	55.03
Std Dev	10.52

Turn on : 51.3602

Width : 3.418

Height : 10.276

Entry





# B0L103S, U23-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.26
Std Dev	10.62

Turn on : 51.5437

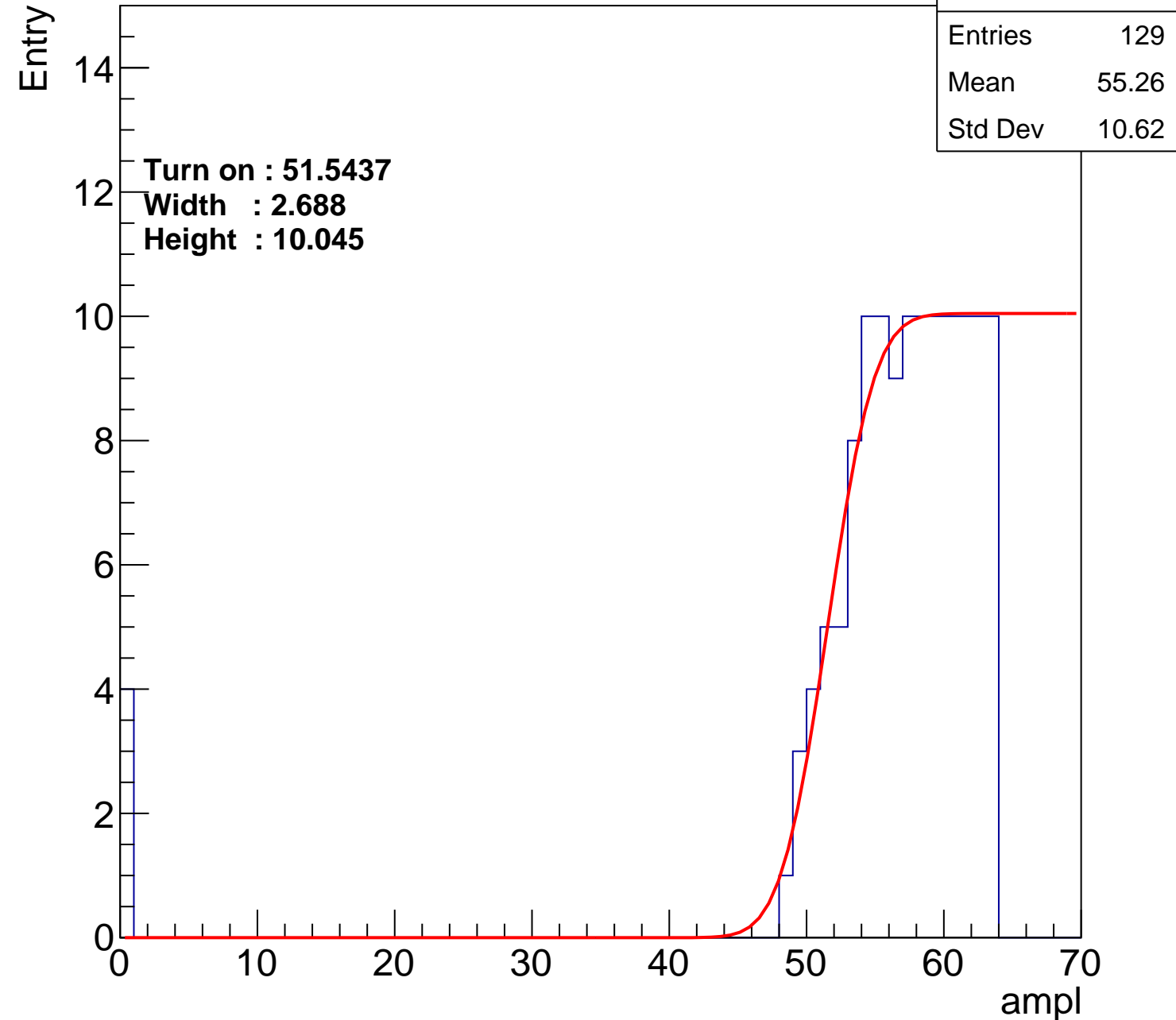
Width : 2.688

Height : 10.045

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.35
Std Dev	10.84

Turn on : 52.5486

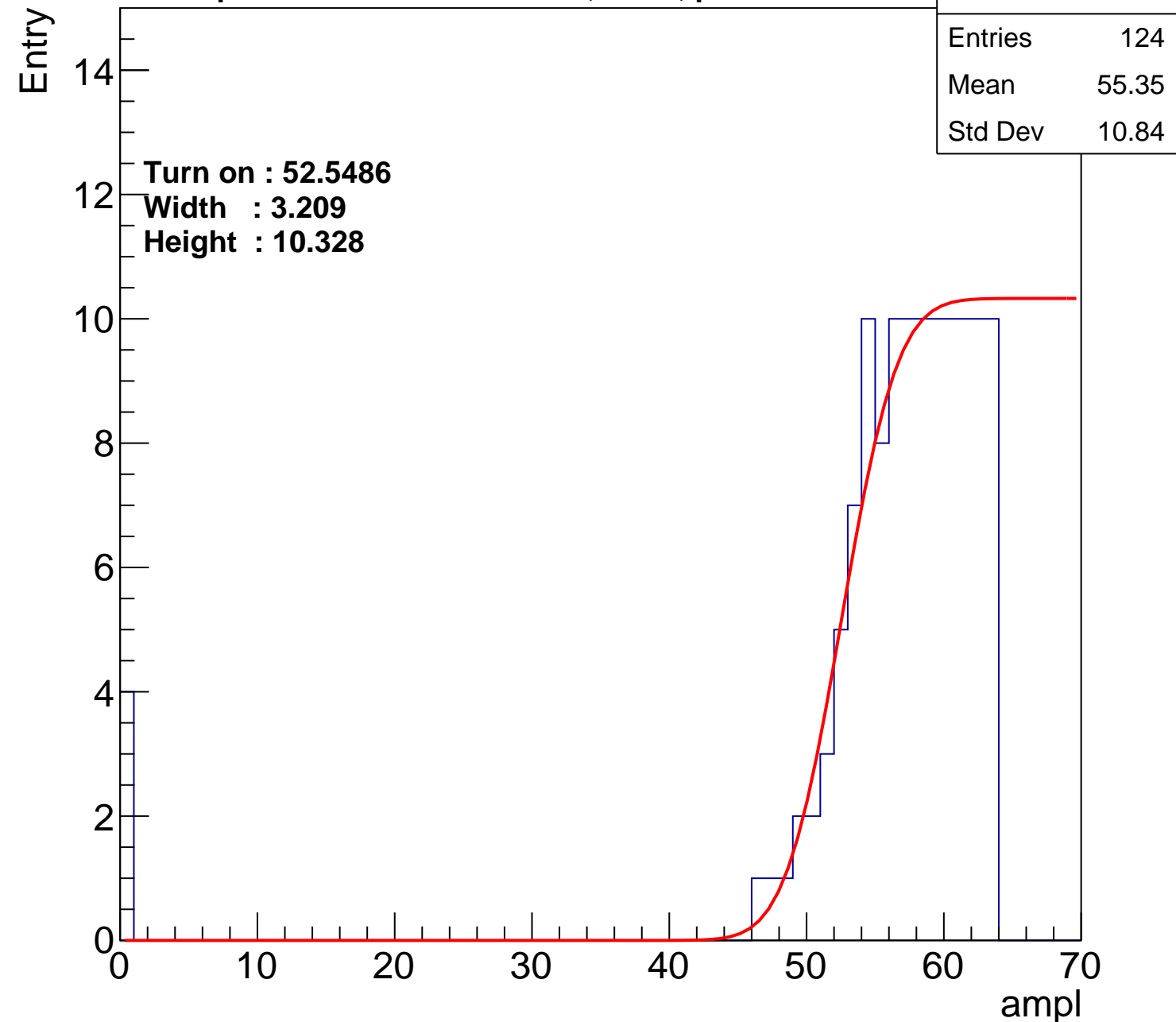
Width : 3.209

Height : 10.328

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	116
Mean	55.99
Std Dev	9.878

**Turn on : 53.2043**

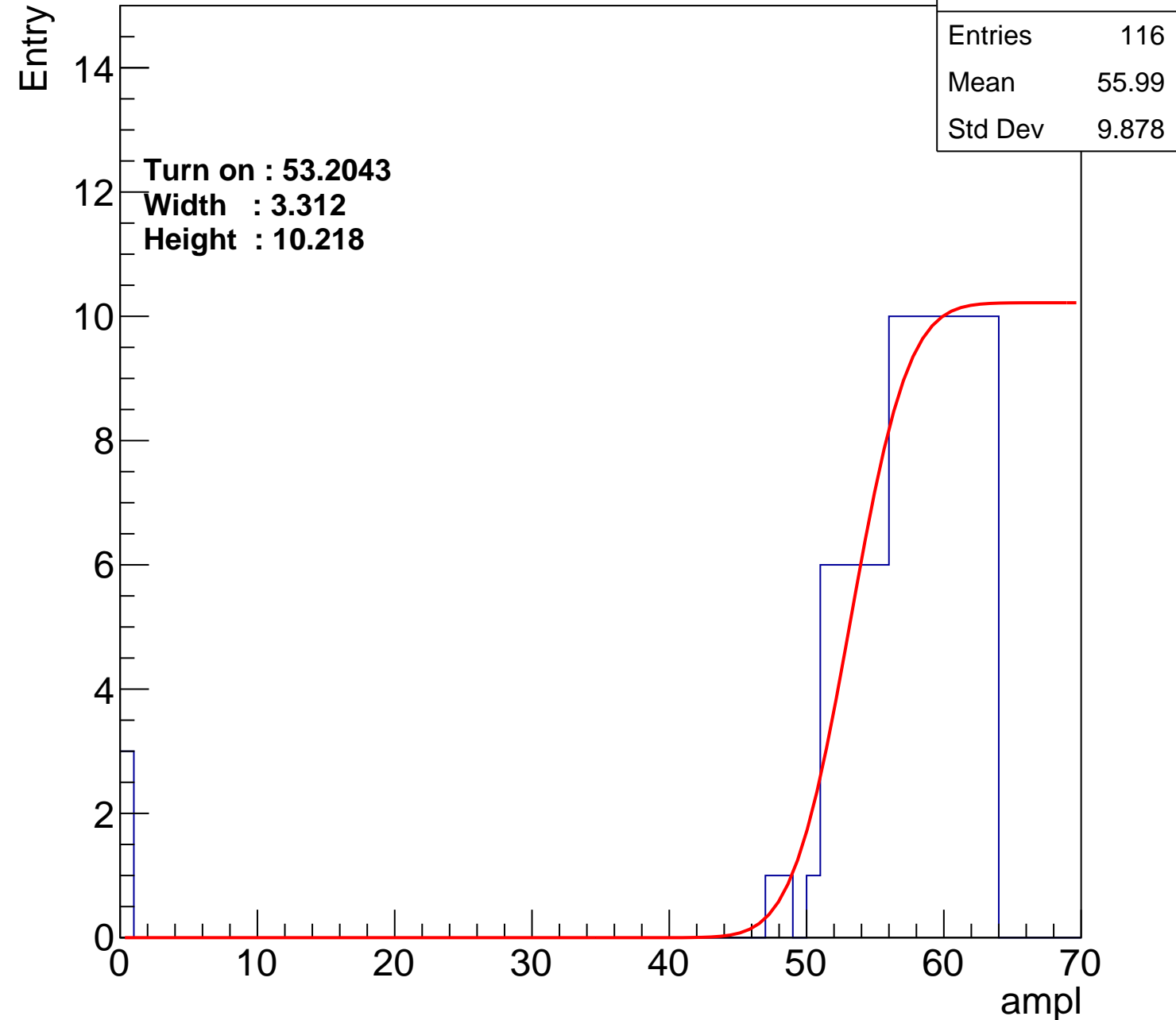
**Width : 3.312**

**Height : 10.218**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.11
Std Dev	11.98

Turn on : 52.1722

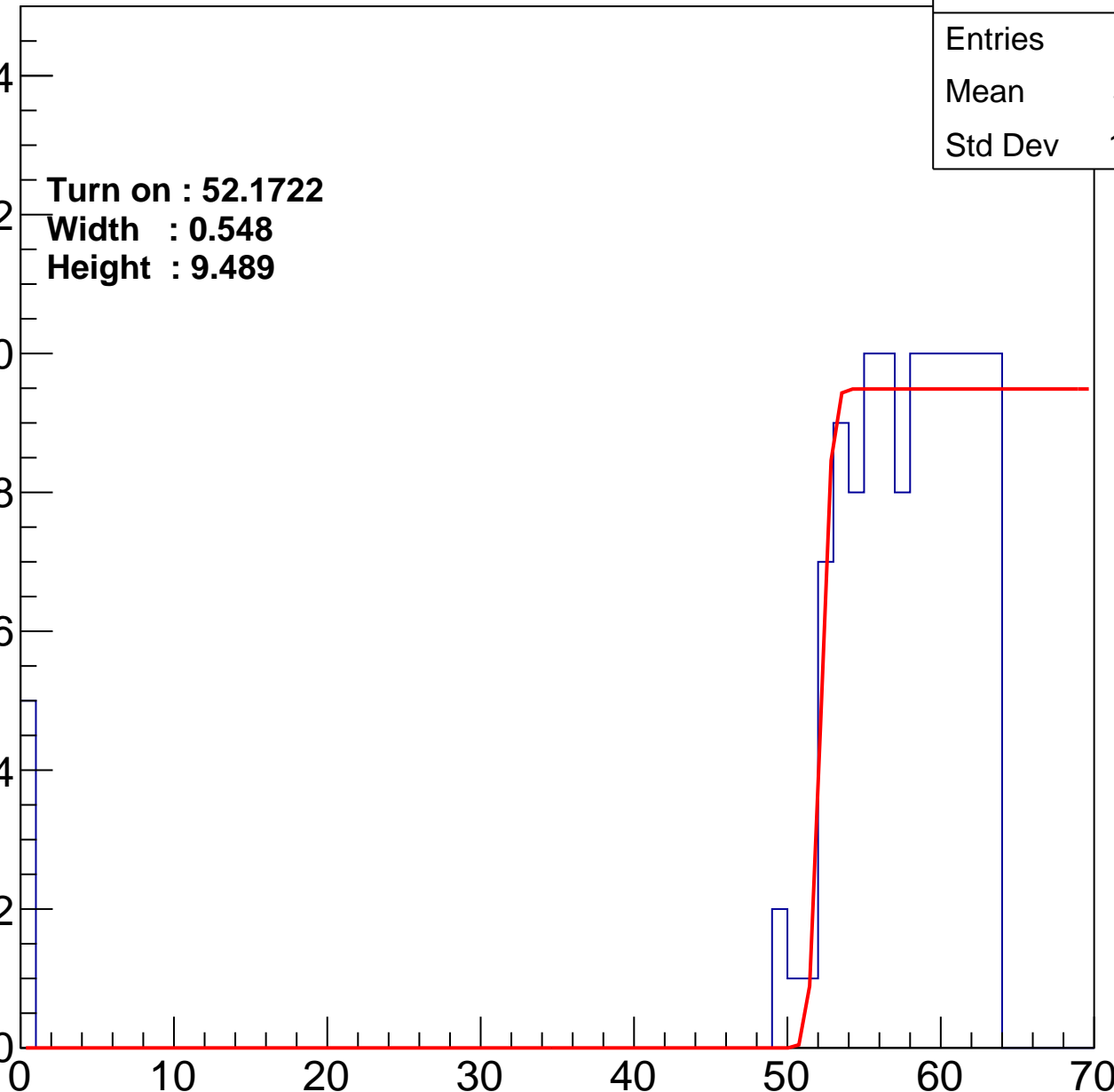
Width : 0.548

Height : 9.489

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

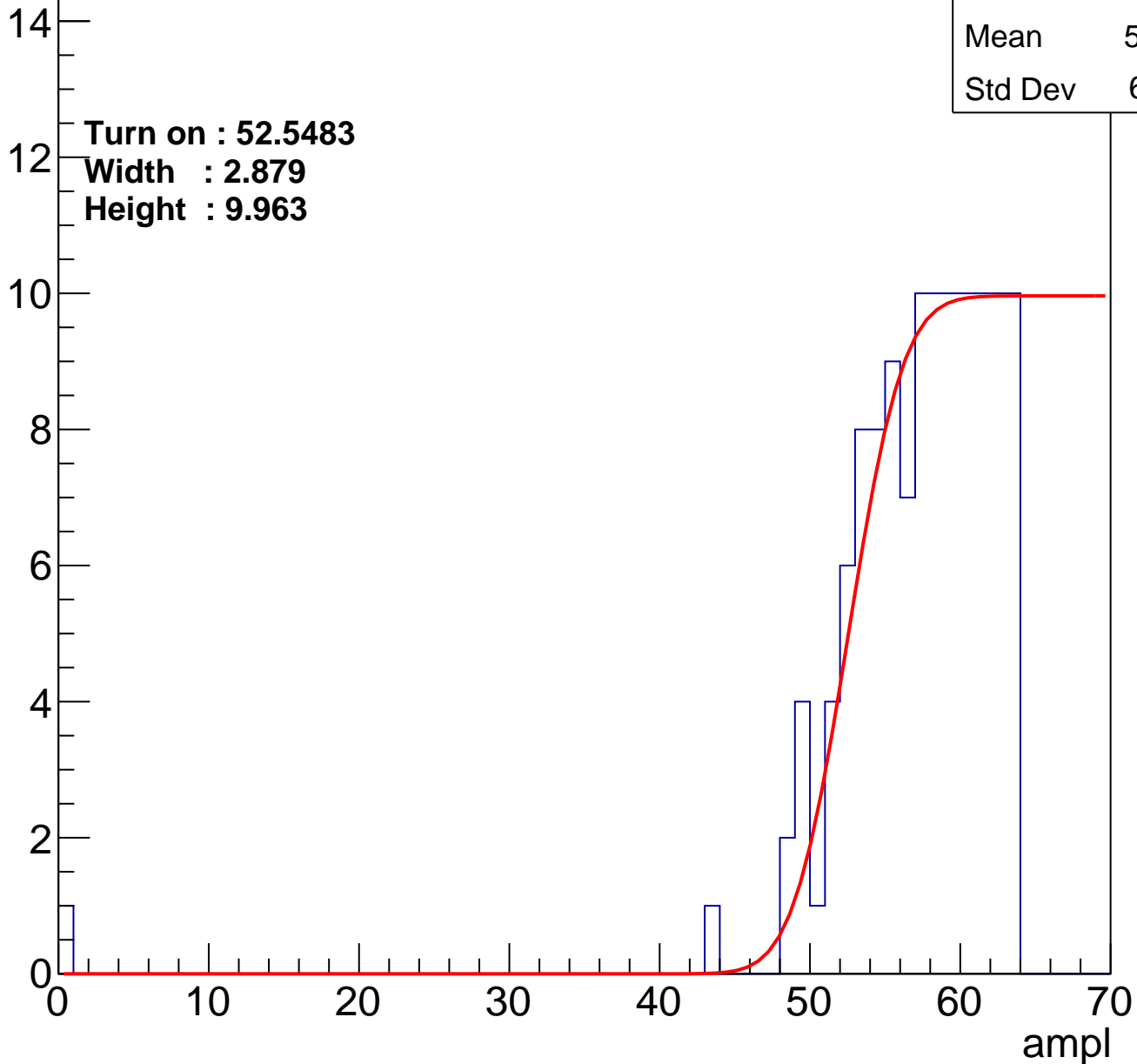
Entries	121
Mean	56.56
Std Dev	6.641

Turn on : 52.5483

Width : 2.879

Height : 9.963

Entry



# B0L103S, U23-ch108

calib\_packv5\_040323\_1717.root, FC#2, port C3

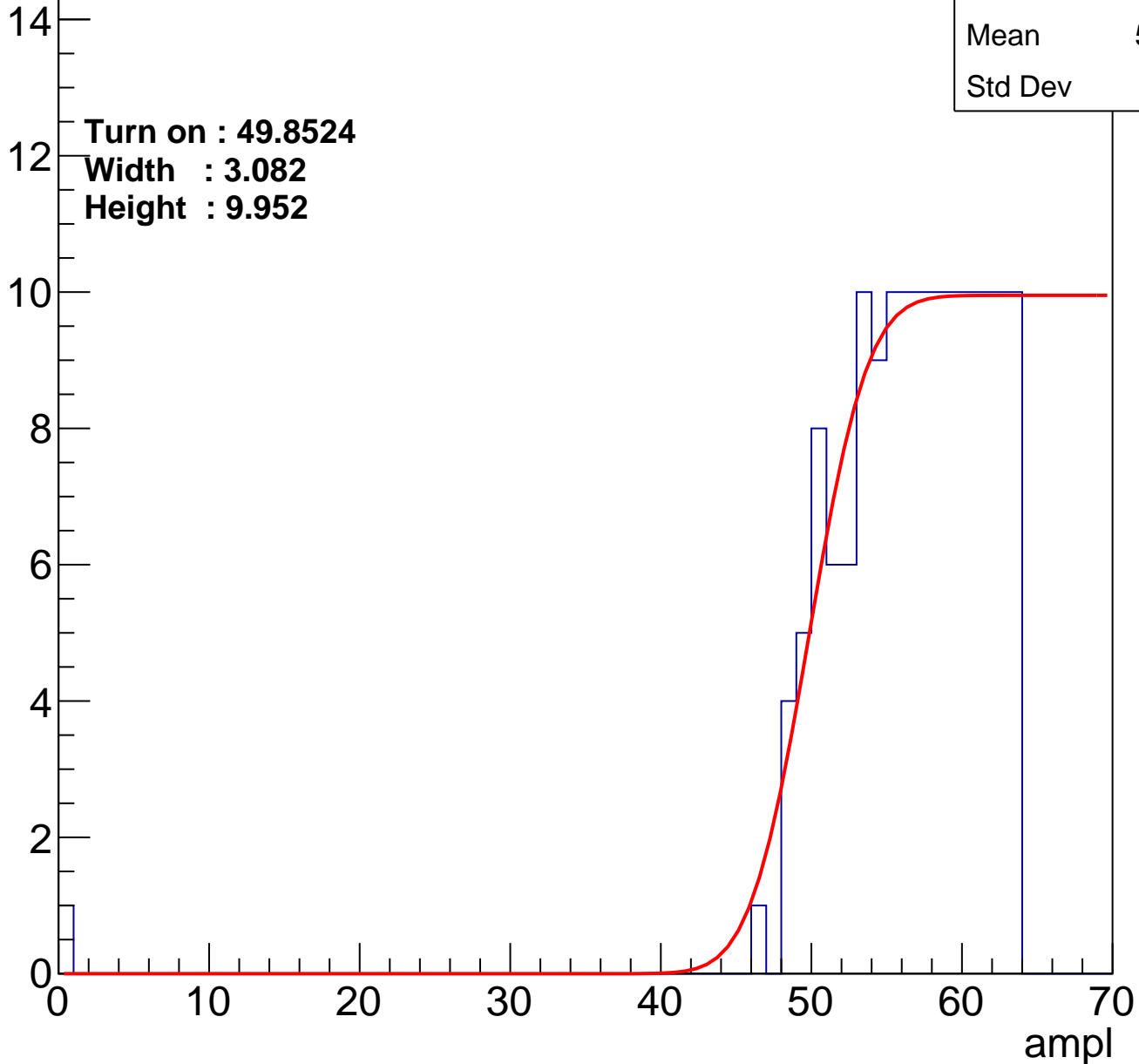
Entries	140
Mean	55.91
Std Dev	6.44

Turn on : 49.8524

Width : 3.082

Height : 9.952

Entry



# B0L103S, U23-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.06
Std Dev	8.275

Turn on : 52.7160

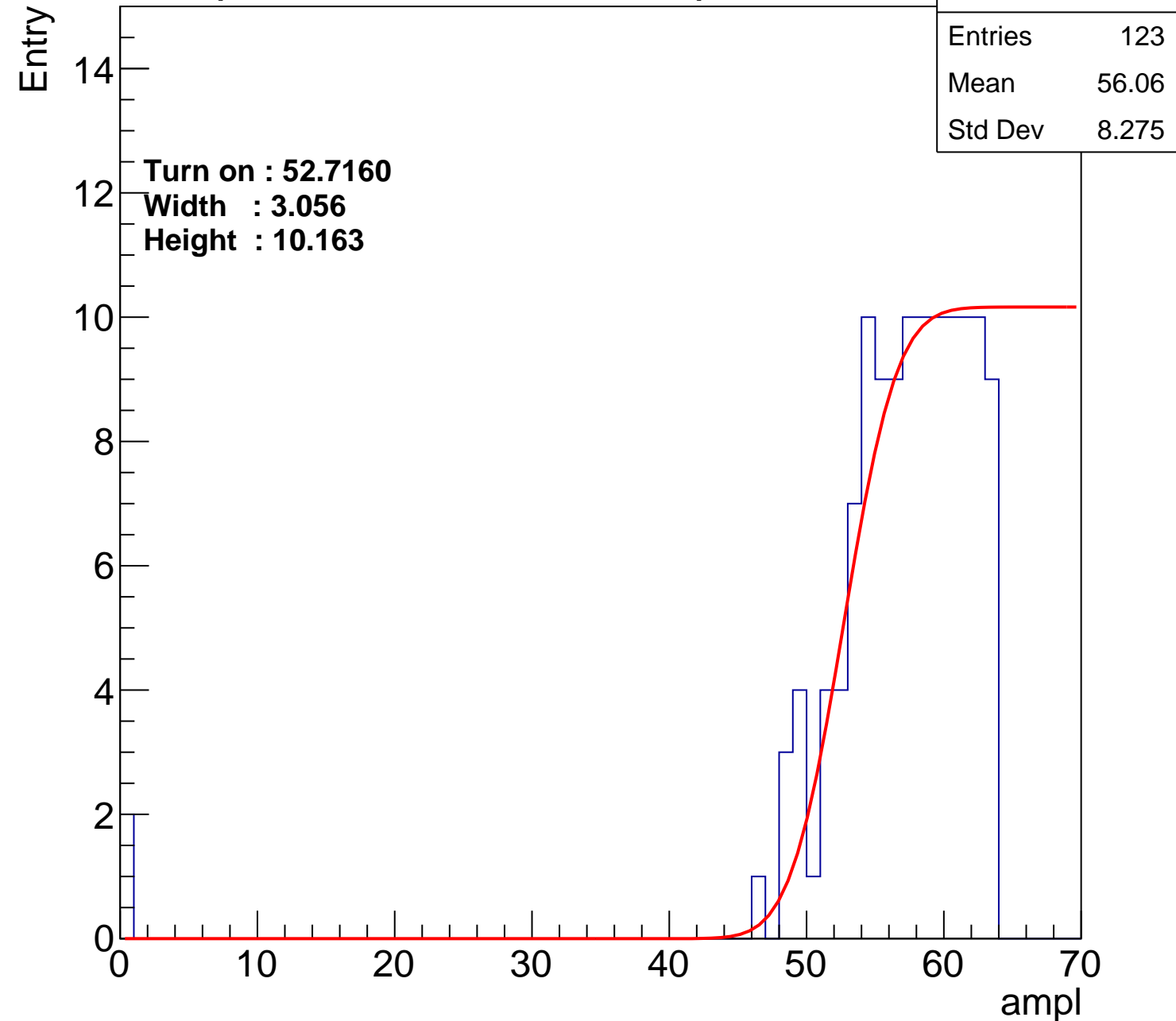
Width : 3.056

Height : 10.163

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	54.64
Std Dev	10.3

Turn on : 50.1531

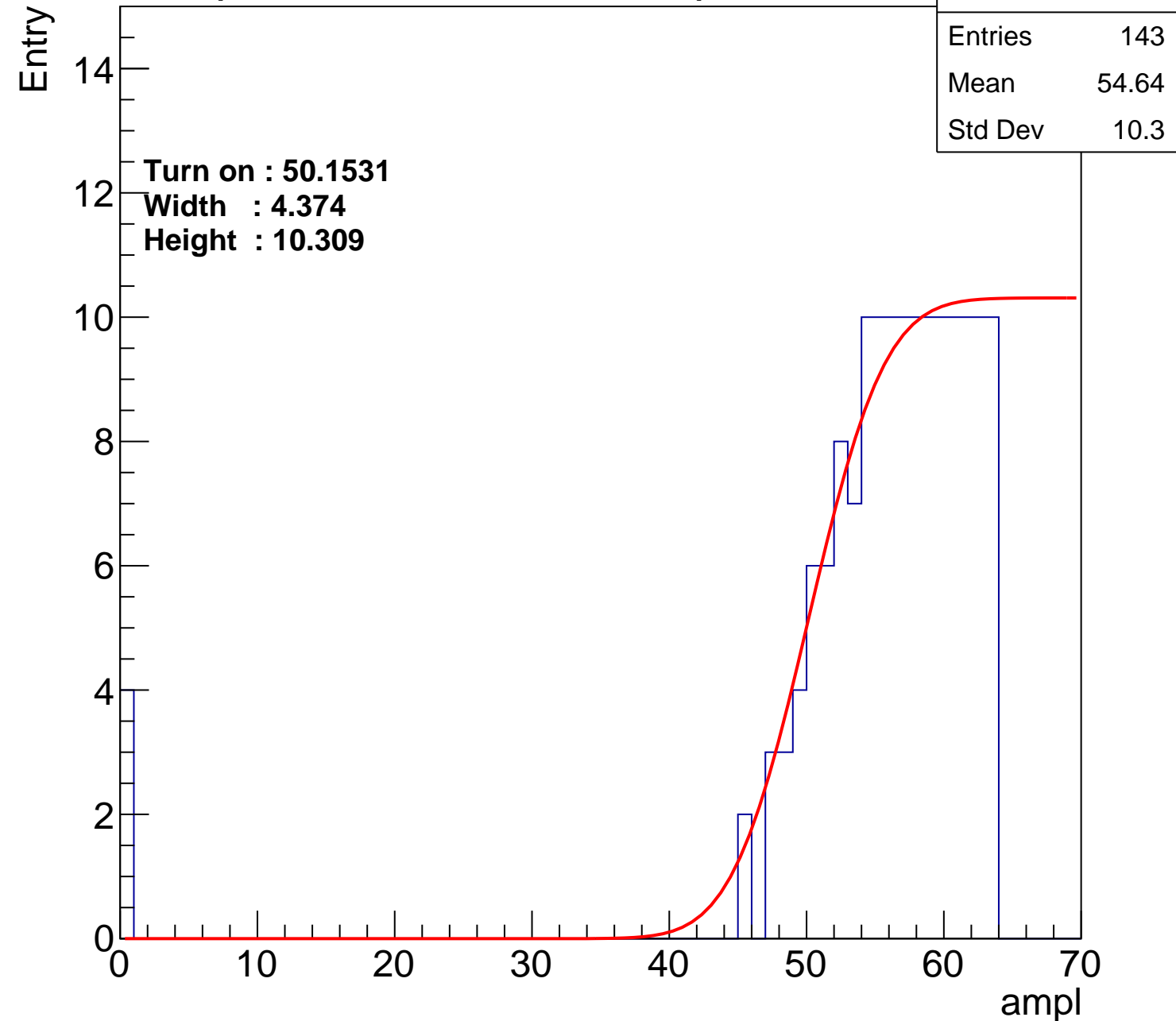
Width : 4.374

Height : 10.309

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch111

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.65
Std Dev	11.46

Turn on : 51.0151

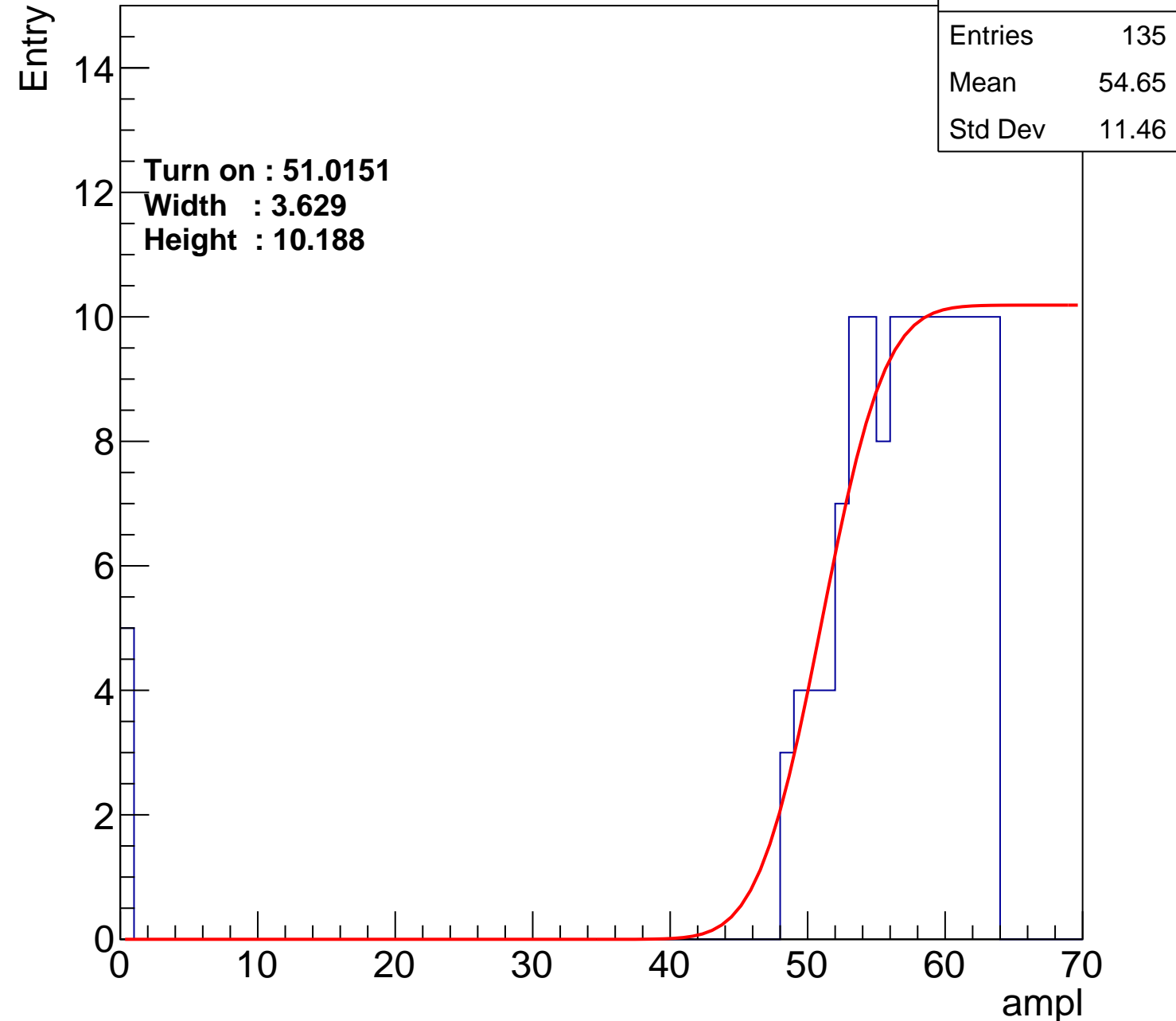
Width : 3.629

Height : 10.188

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	159
Mean	54.46
Std Dev	8.936

Turn on : 48.7321

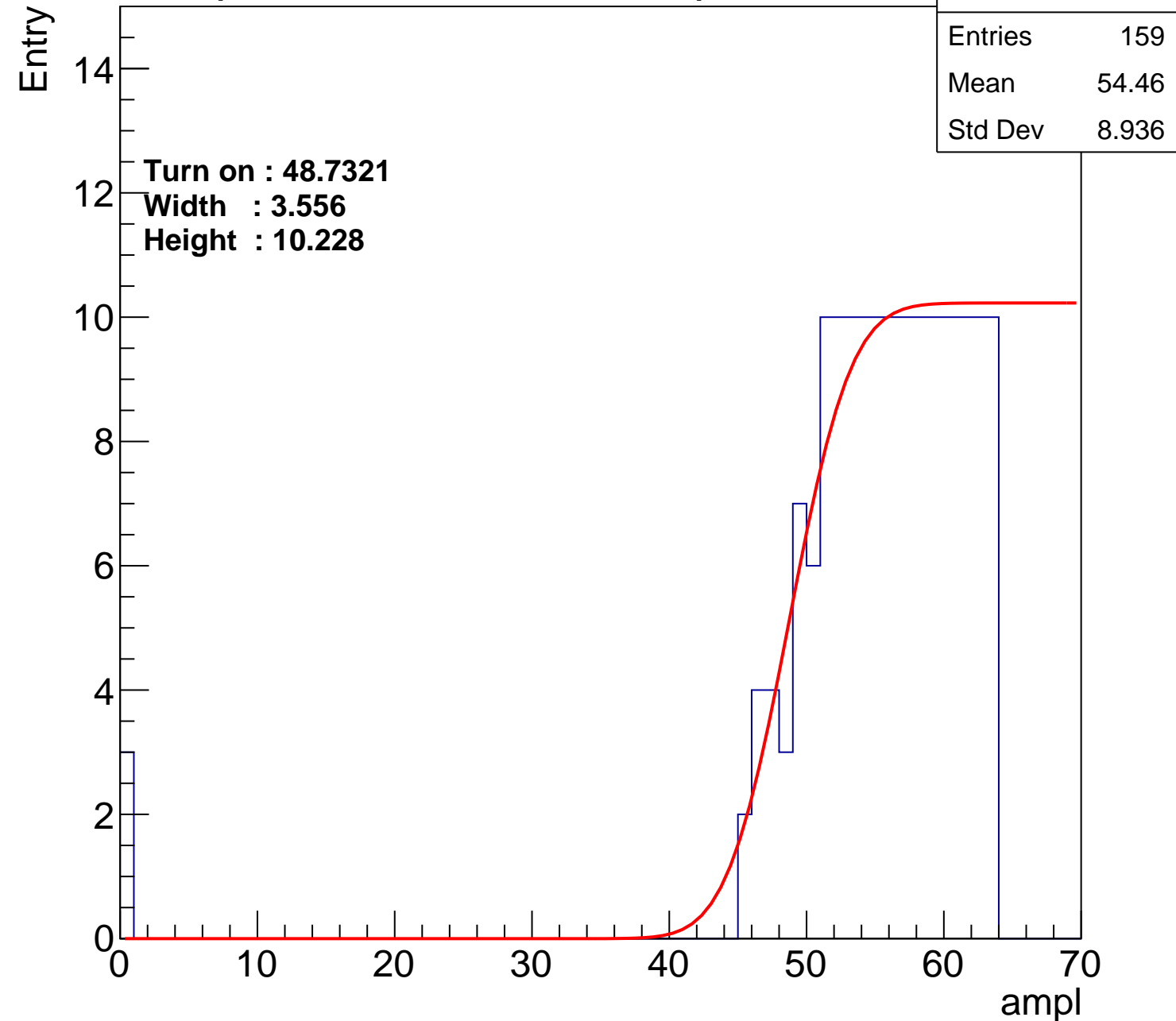
Width : 3.556

Height : 10.228

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

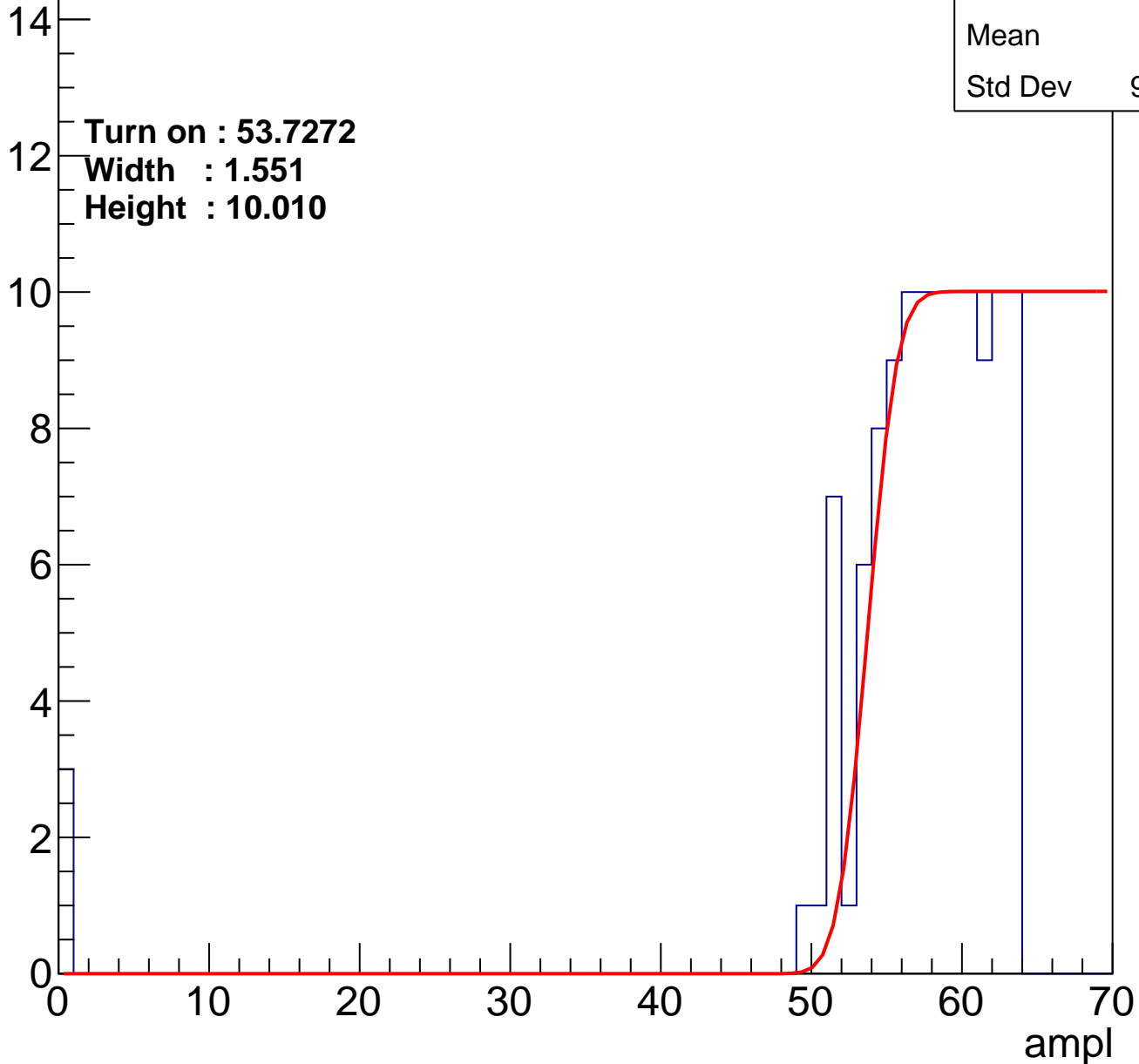
Entries	115
Mean	56.1
Std Dev	9.848

Turn on : 53.7272

Width : 1.551

Height : 10.010

Entry



# B0L103S, U23-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.32
Std Dev	10.83

Turn on : 52.5066

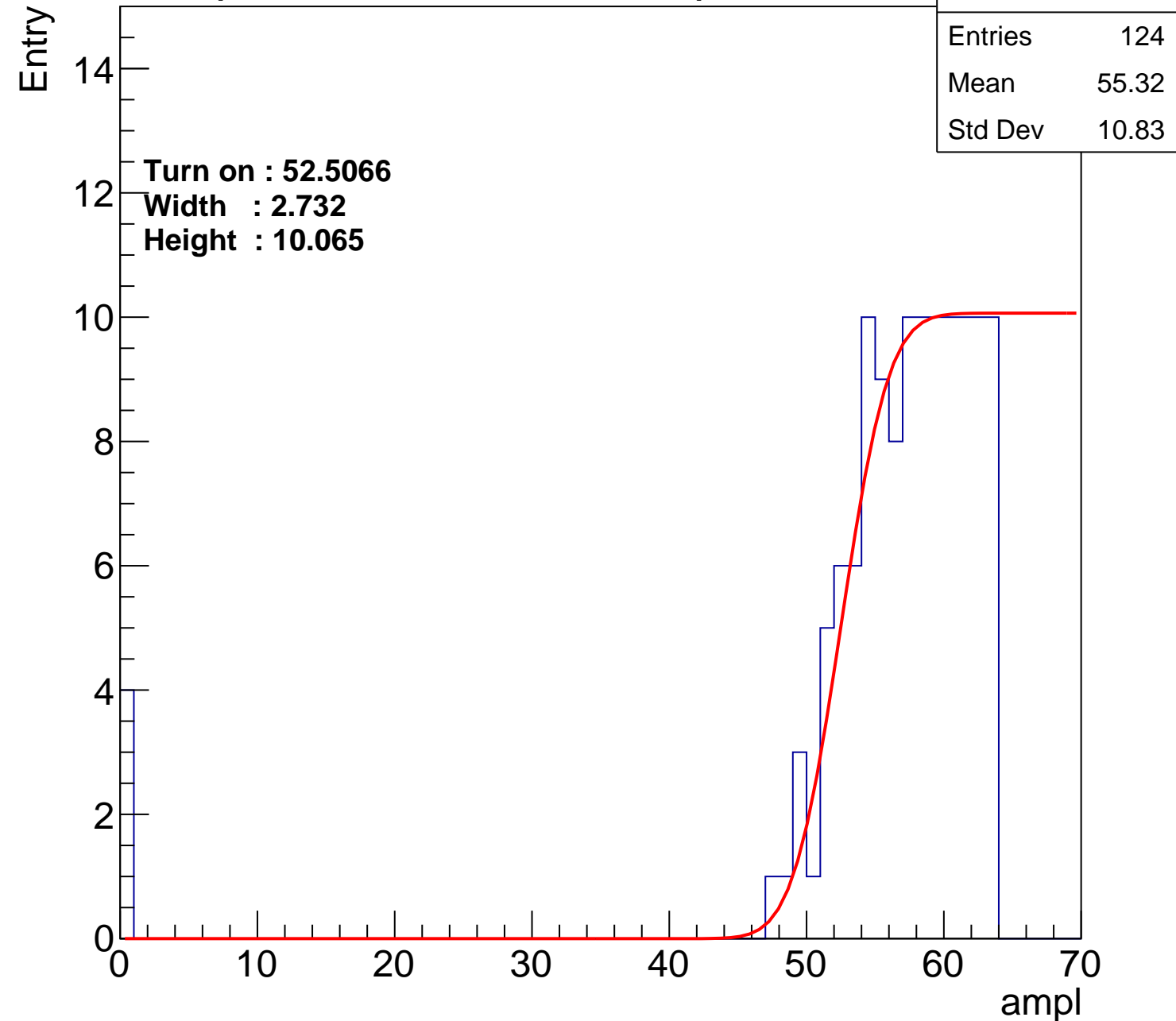
Width : 2.732

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.37
Std Dev	10.84

Turn on : 51.9514

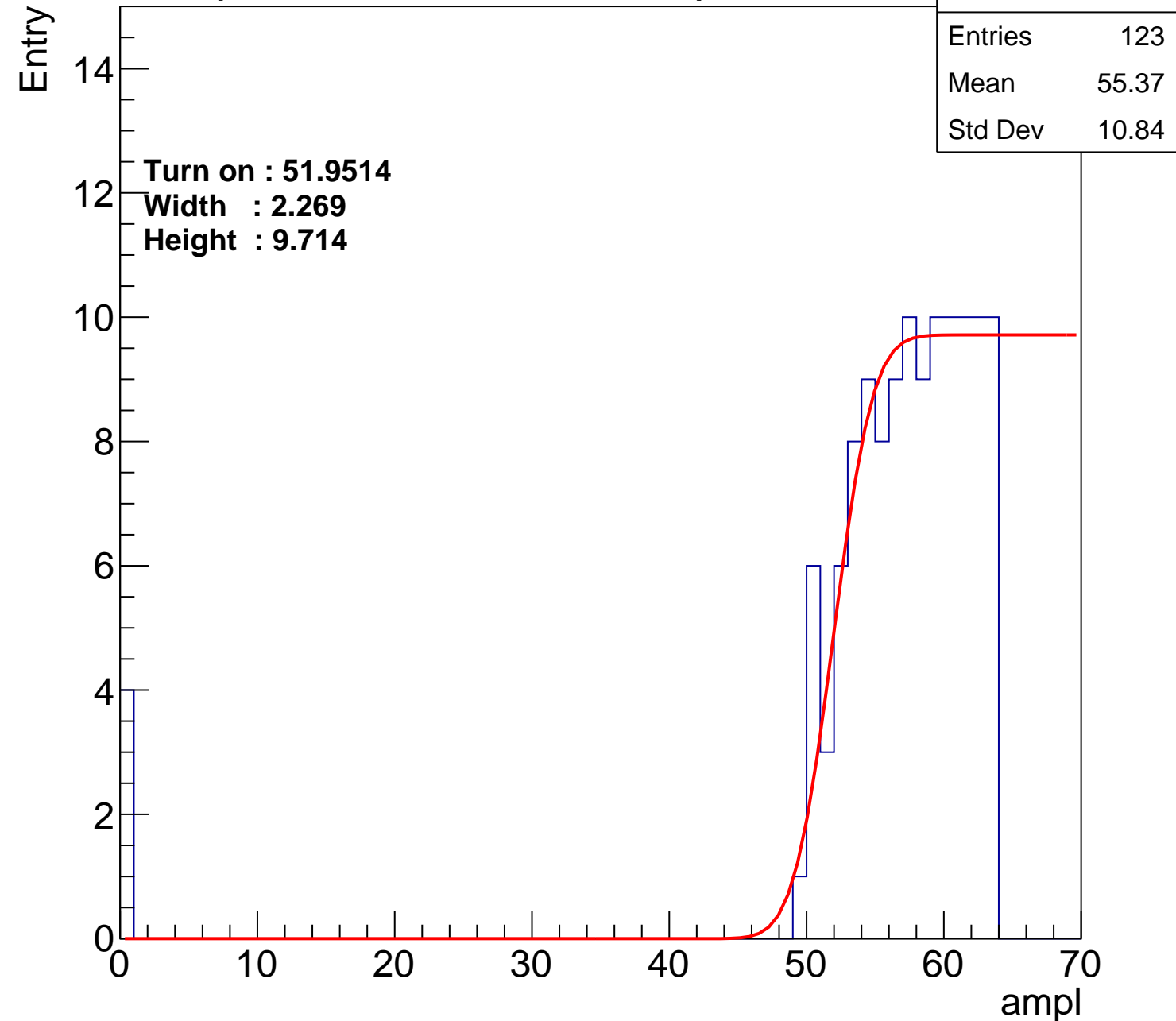
Width : 2.269

Height : 9.714

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	53.28
Std Dev	15.4

Turn on : 52.7301

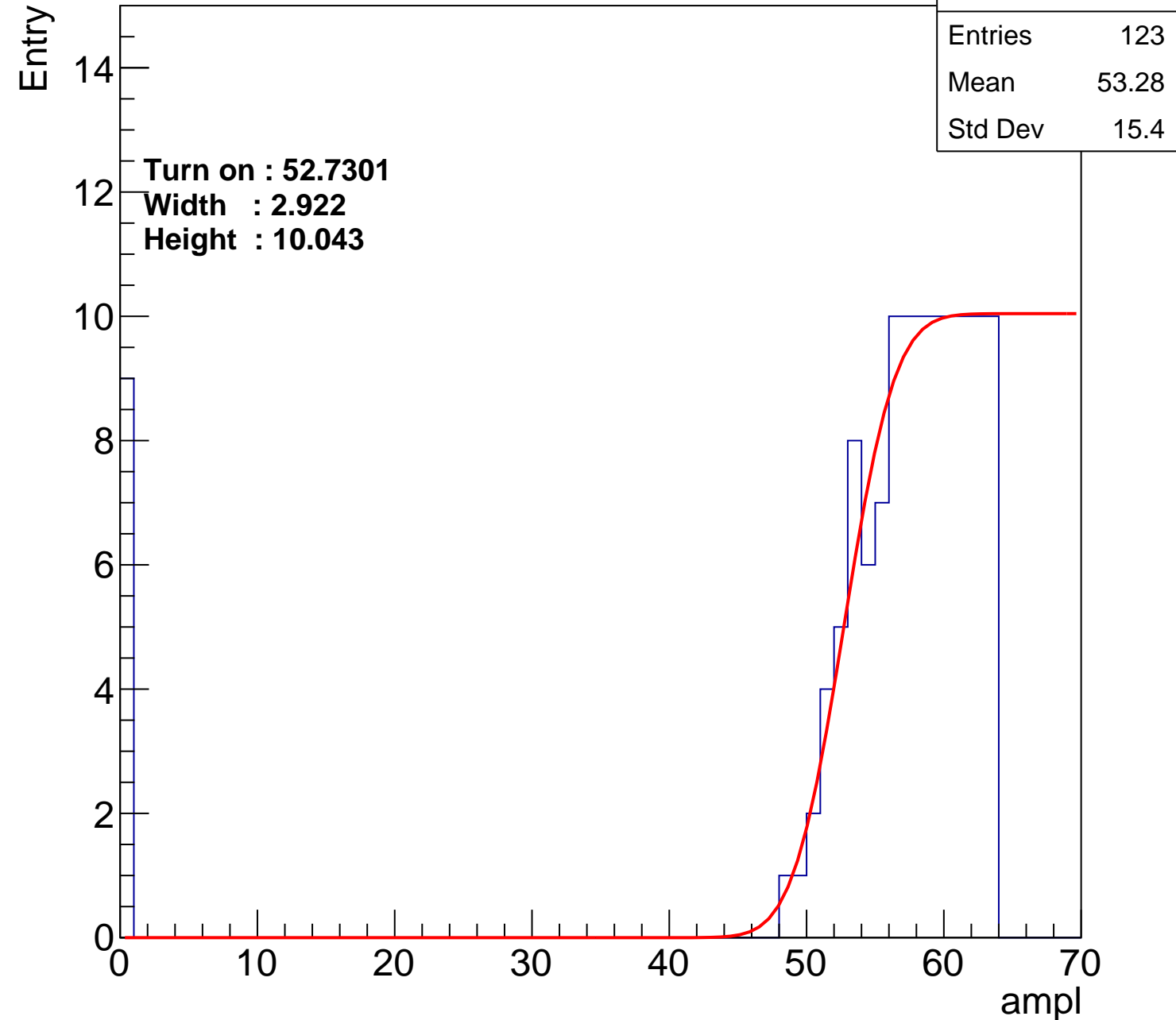
Width : 2.922

Height : 10.043

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch117

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.43
Std Dev	9.453

Turn on : 51.4149

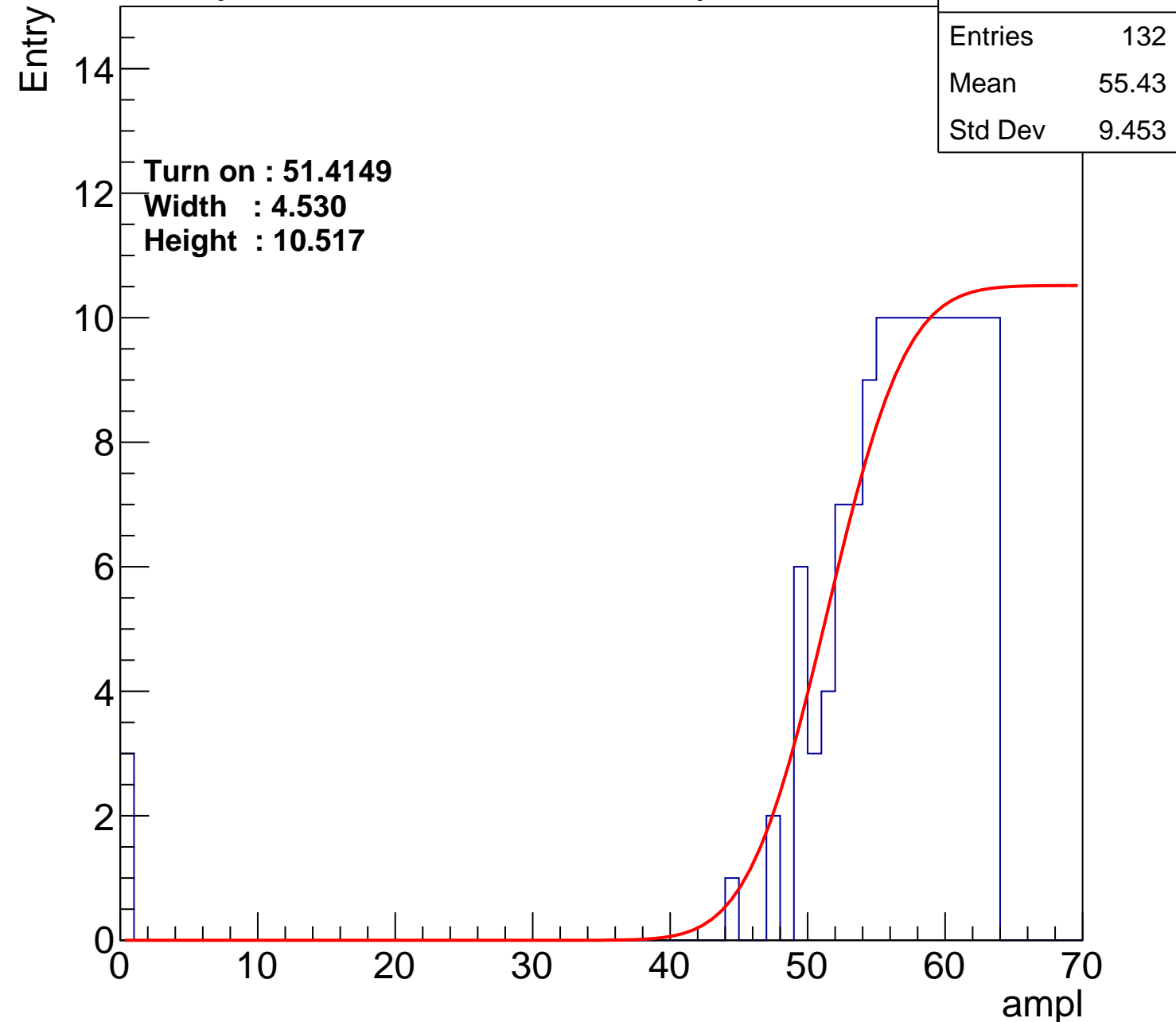
Width : 4.530

Height : 10.517

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

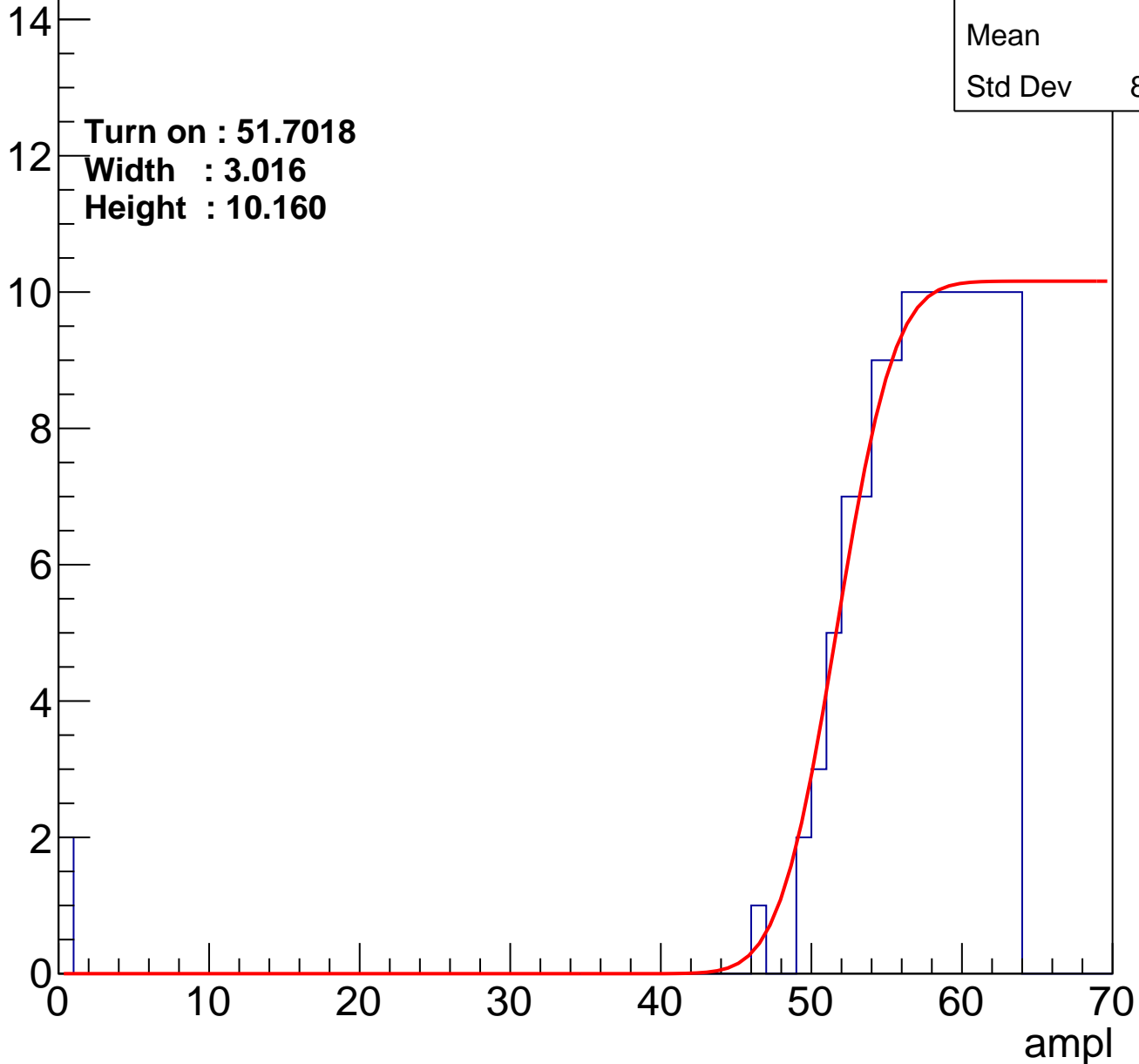
Entries	125
Mean	56.2
Std Dev	8.157

Turn on : 51.7018

Width : 3.016

Height : 10.160

Entry





# B0L103S, U23-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

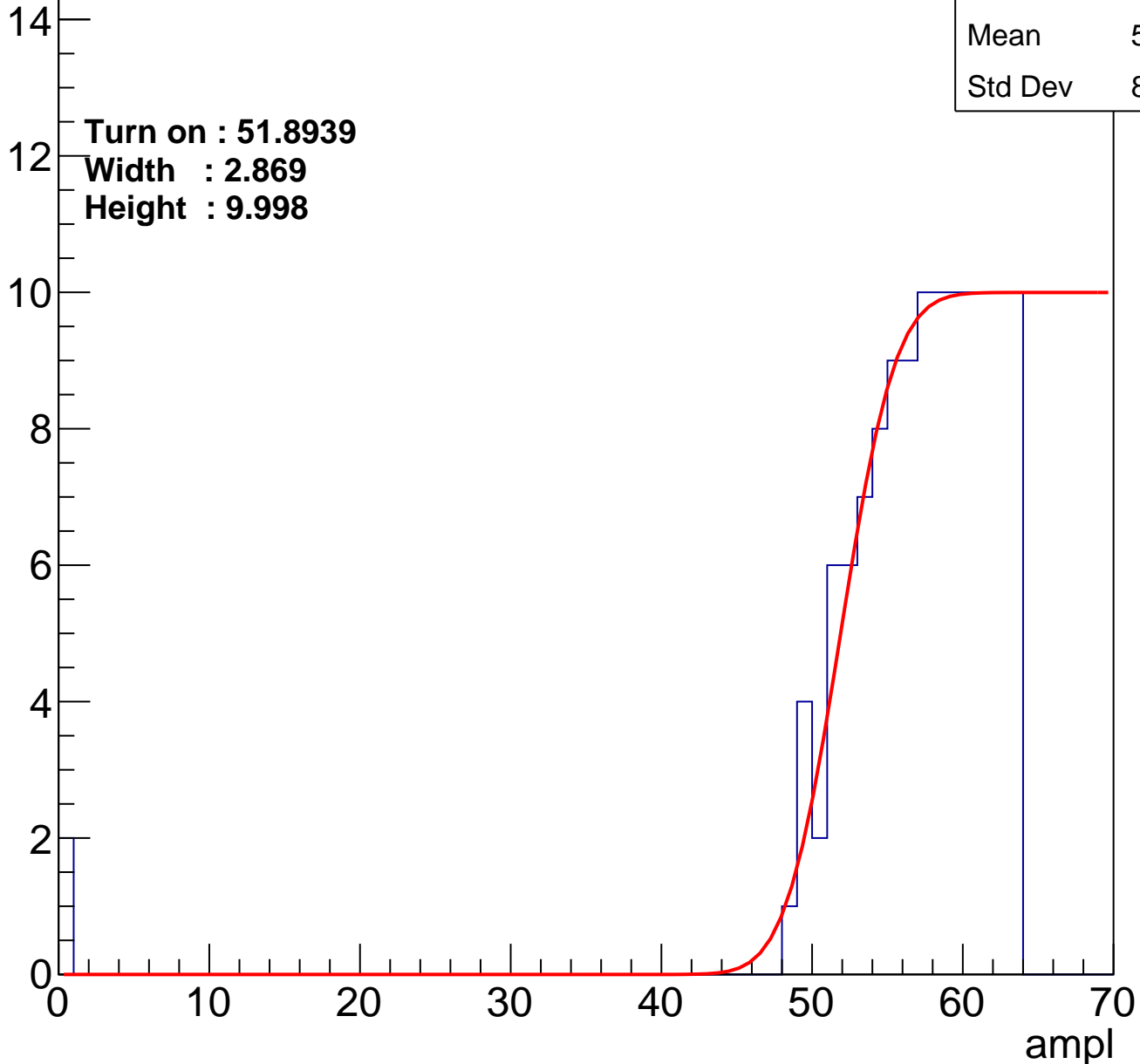
Entries	124
Mean	56.16
Std Dev	8.206

Turn on : 51.8939

Width : 2.869

Height : 9.998

Entry



# B0L103S, U23-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.52
Std Dev	9.47

Turn on : 51.7856

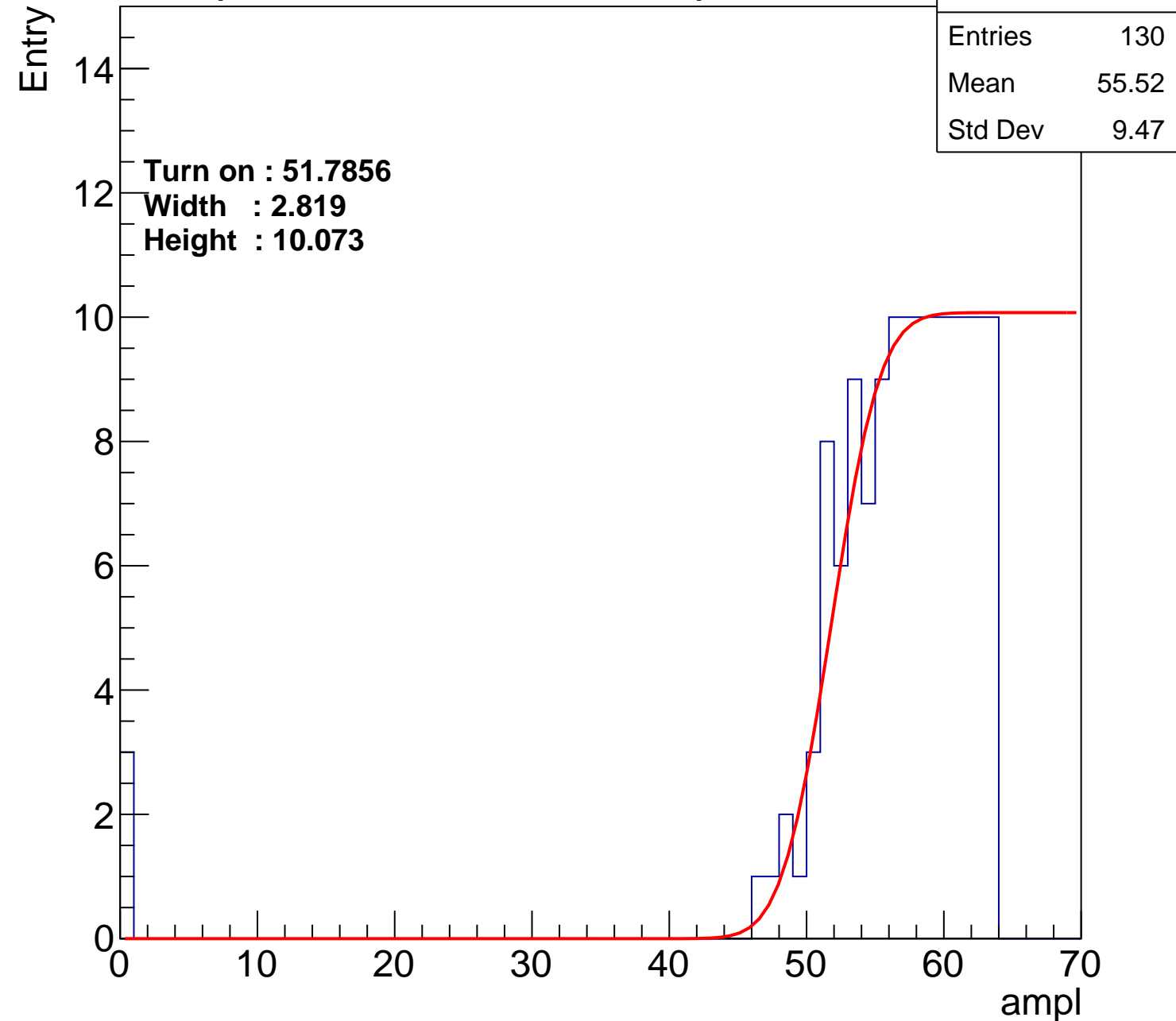
Width : 2.819

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.61
Std Dev	9.478

Turn on : 51.8958

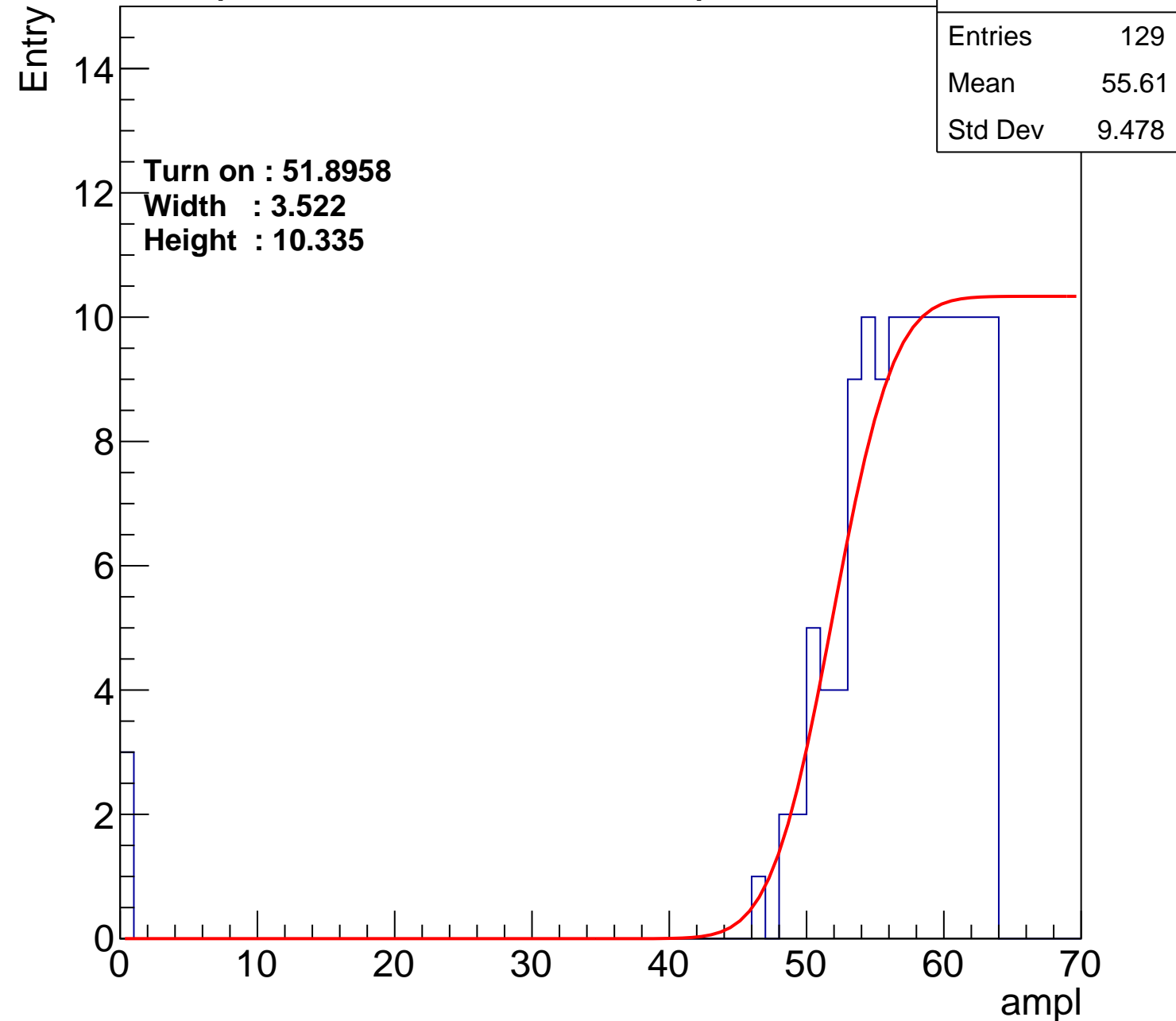
Width : 3.522

Height : 10.335

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	54.62
Std Dev	13.23

Turn on : 53.4572

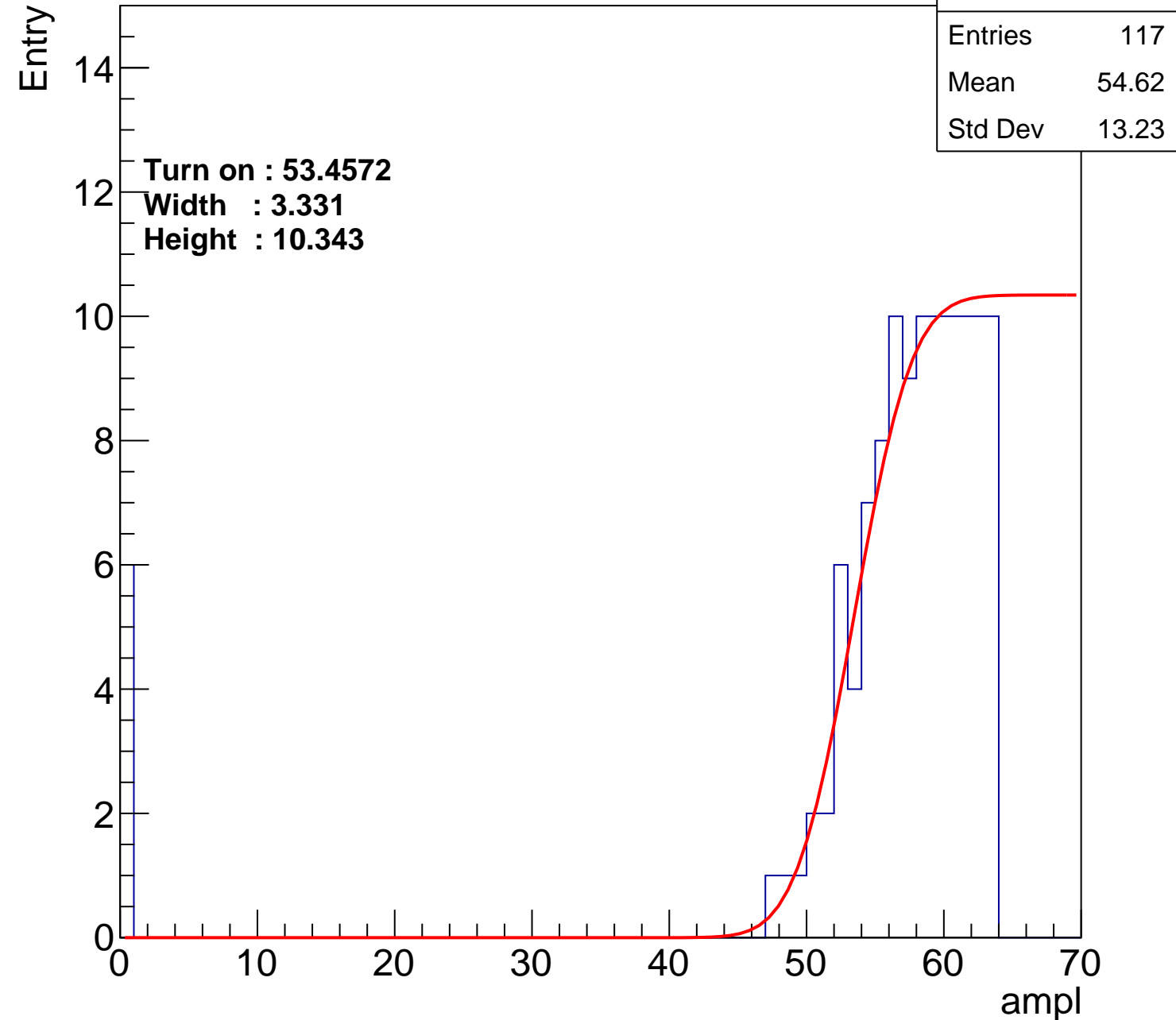
Width : 3.331

Height : 10.343

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch123

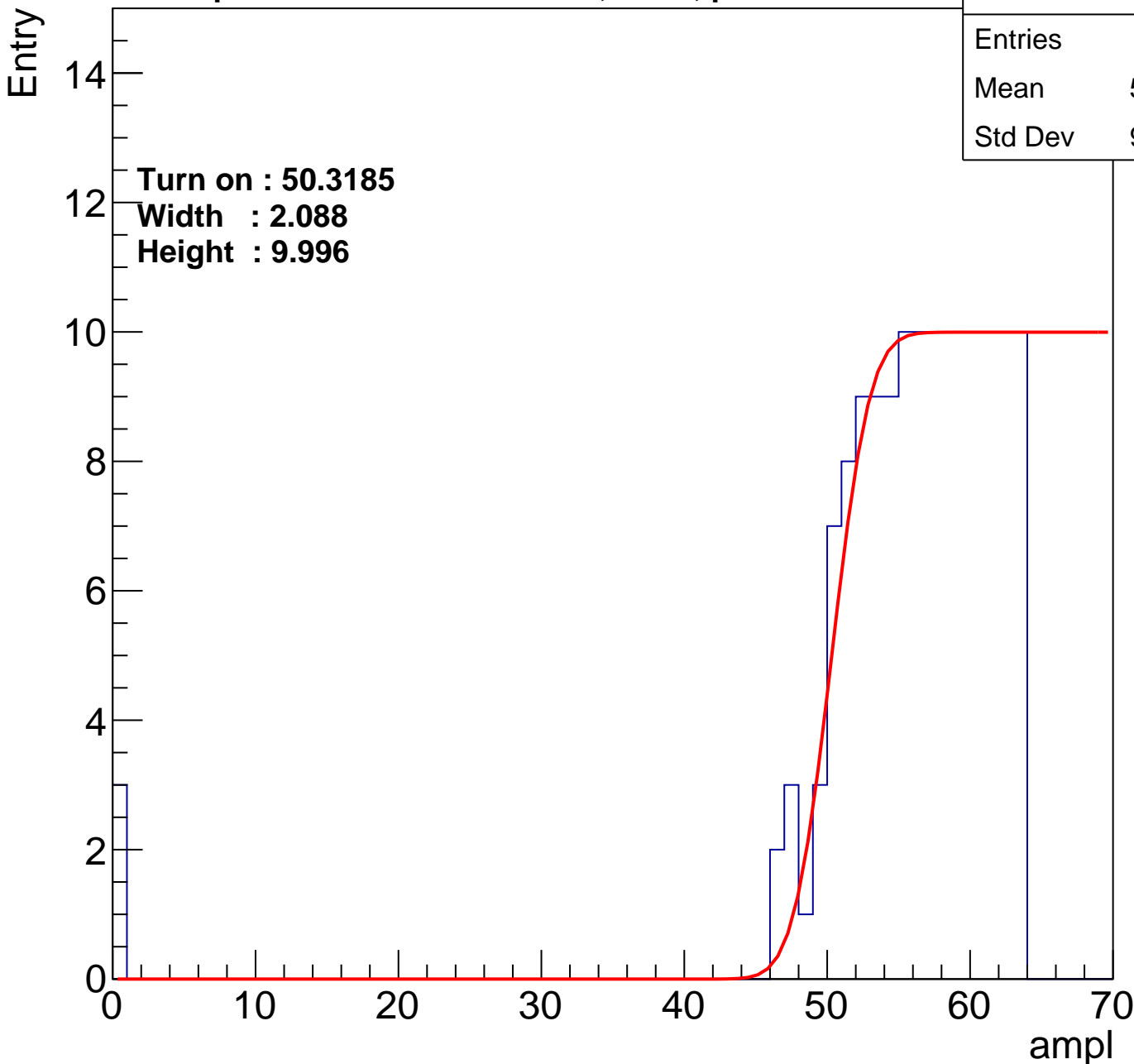
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	144
Mean	55.05
Std Dev	9.149

**Turn on : 50.3185**

**Width : 2.088**

**Height : 9.996**



# B0L103S, U23-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	52.77
Std Dev	14.55

Turn on : 50.2403

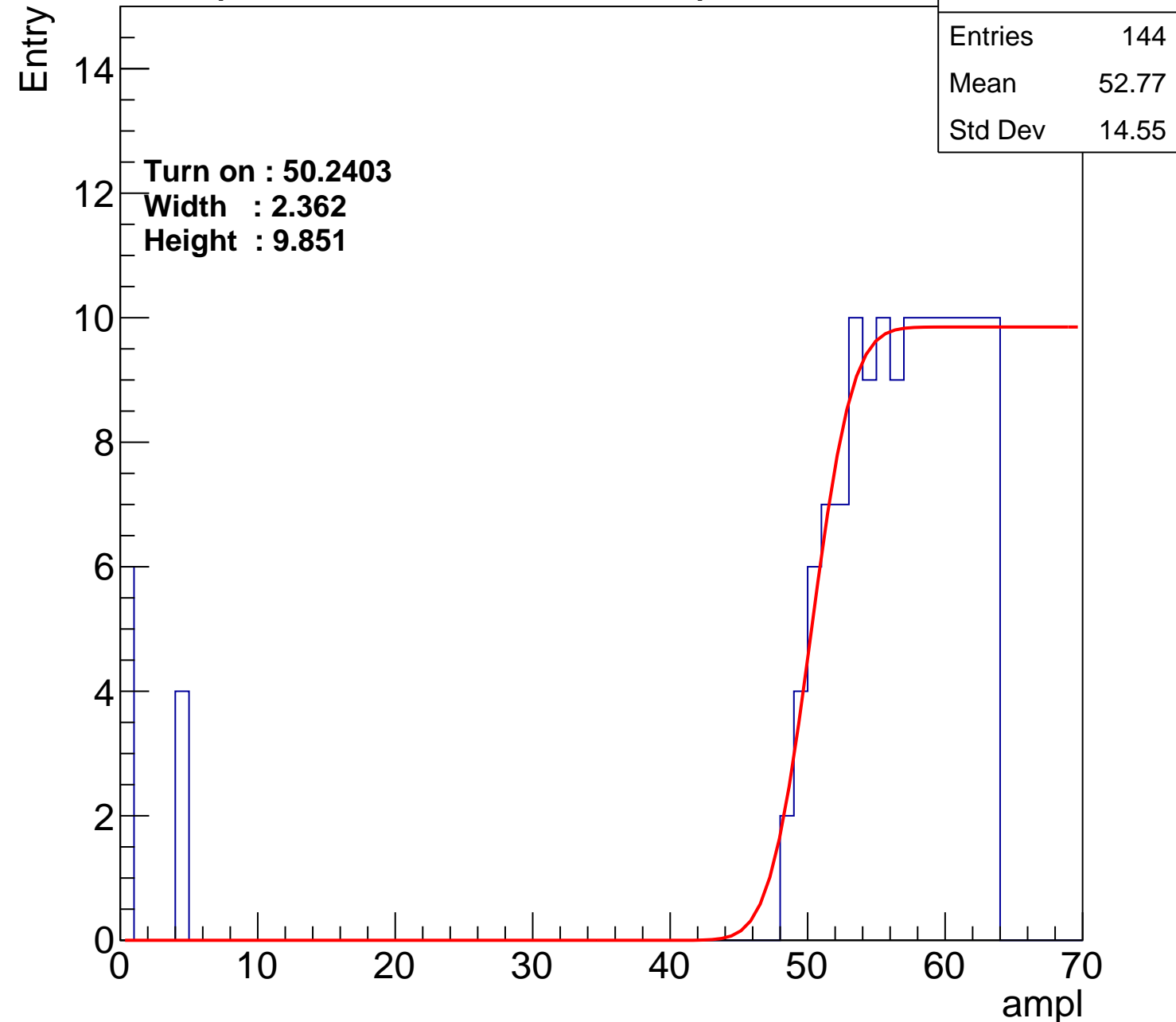
Width : 2.362

Height : 9.851

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch125

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	55.44
Std Dev	12.69

**Turn on : 54.1407**

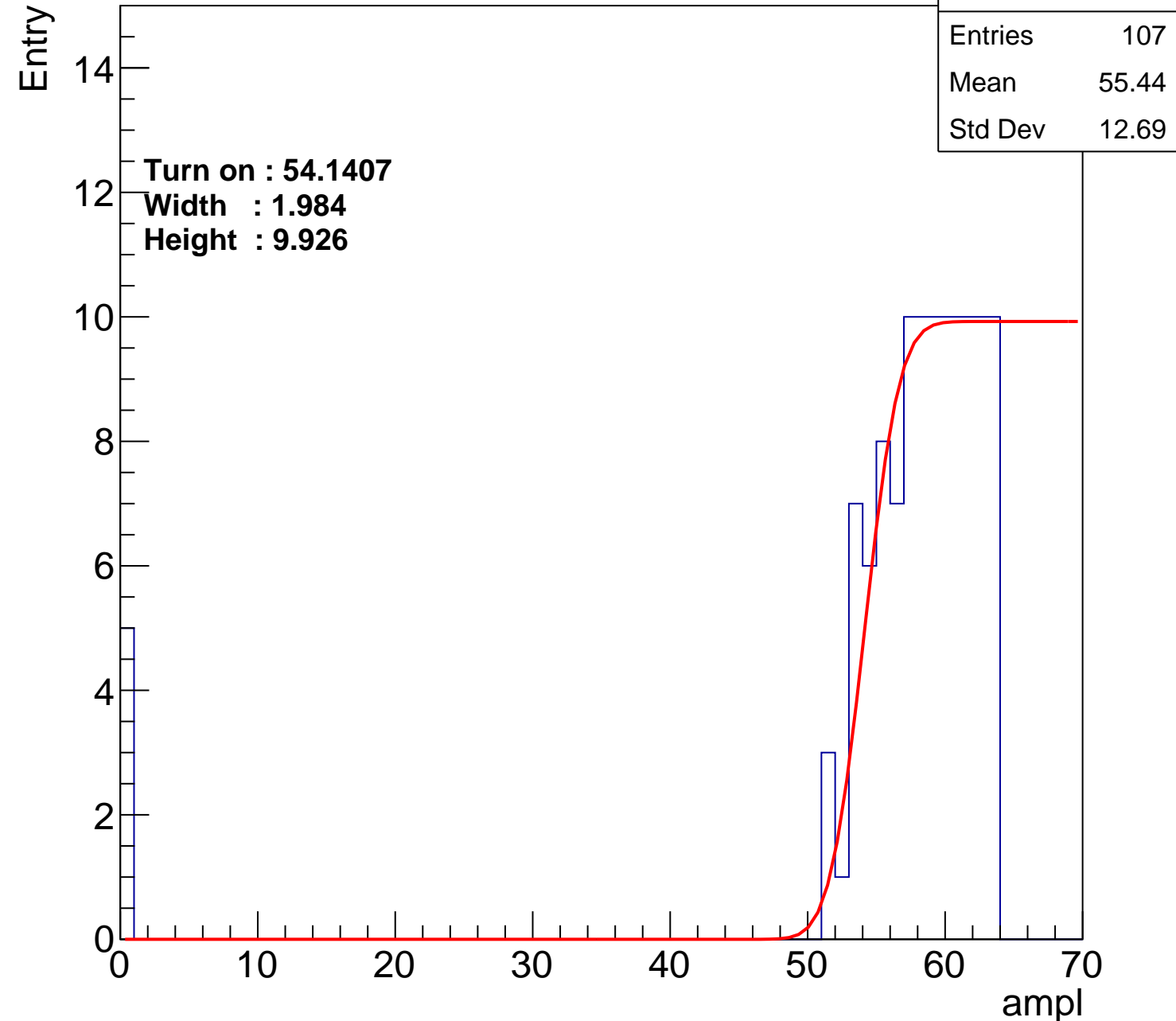
**Width : 1.984**

**Height : 9.926**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U23-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	55.71
Std Dev	7.954

Turn on : 50.6924

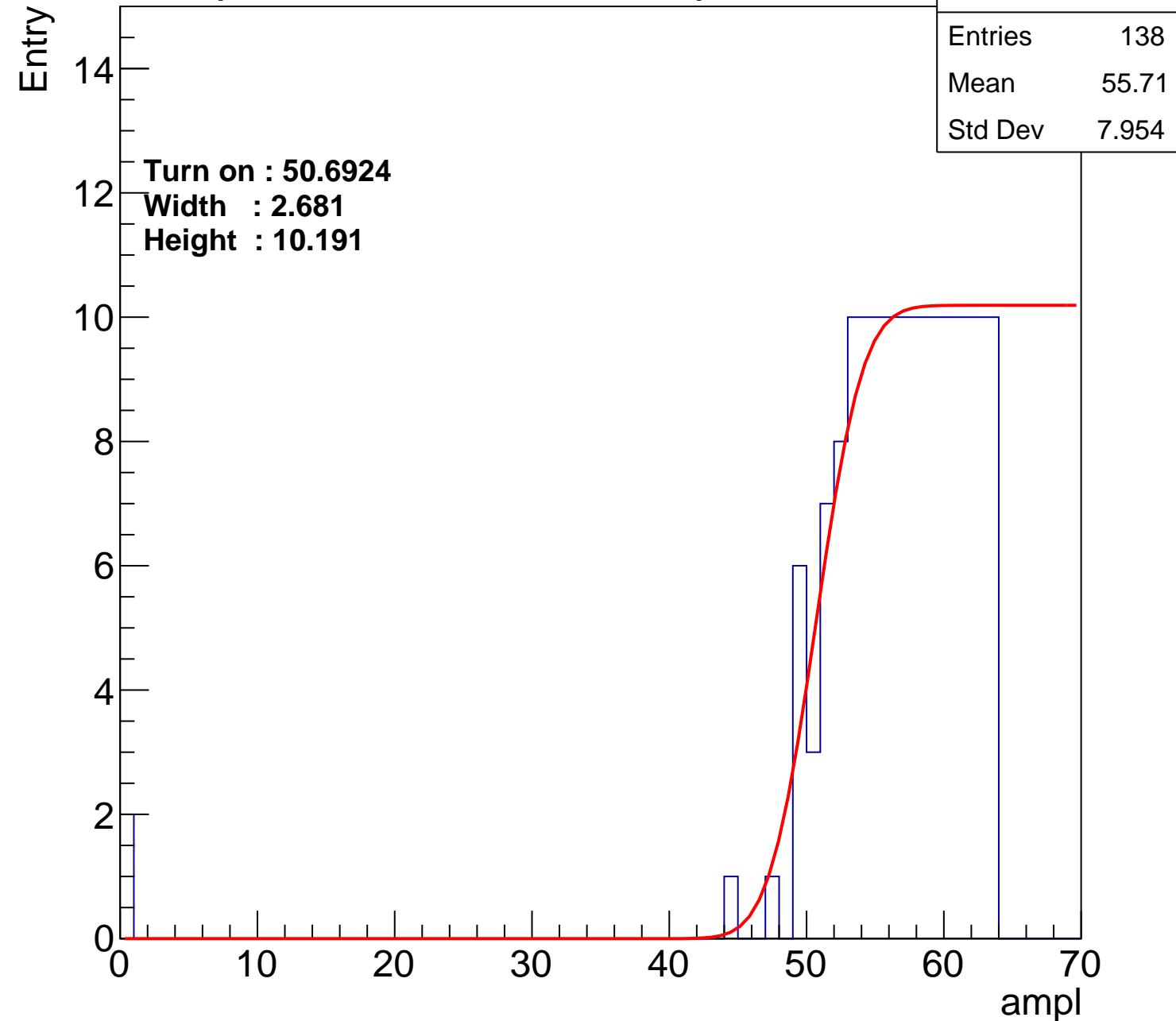
Width : 2.681

Height : 10.191

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U23-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

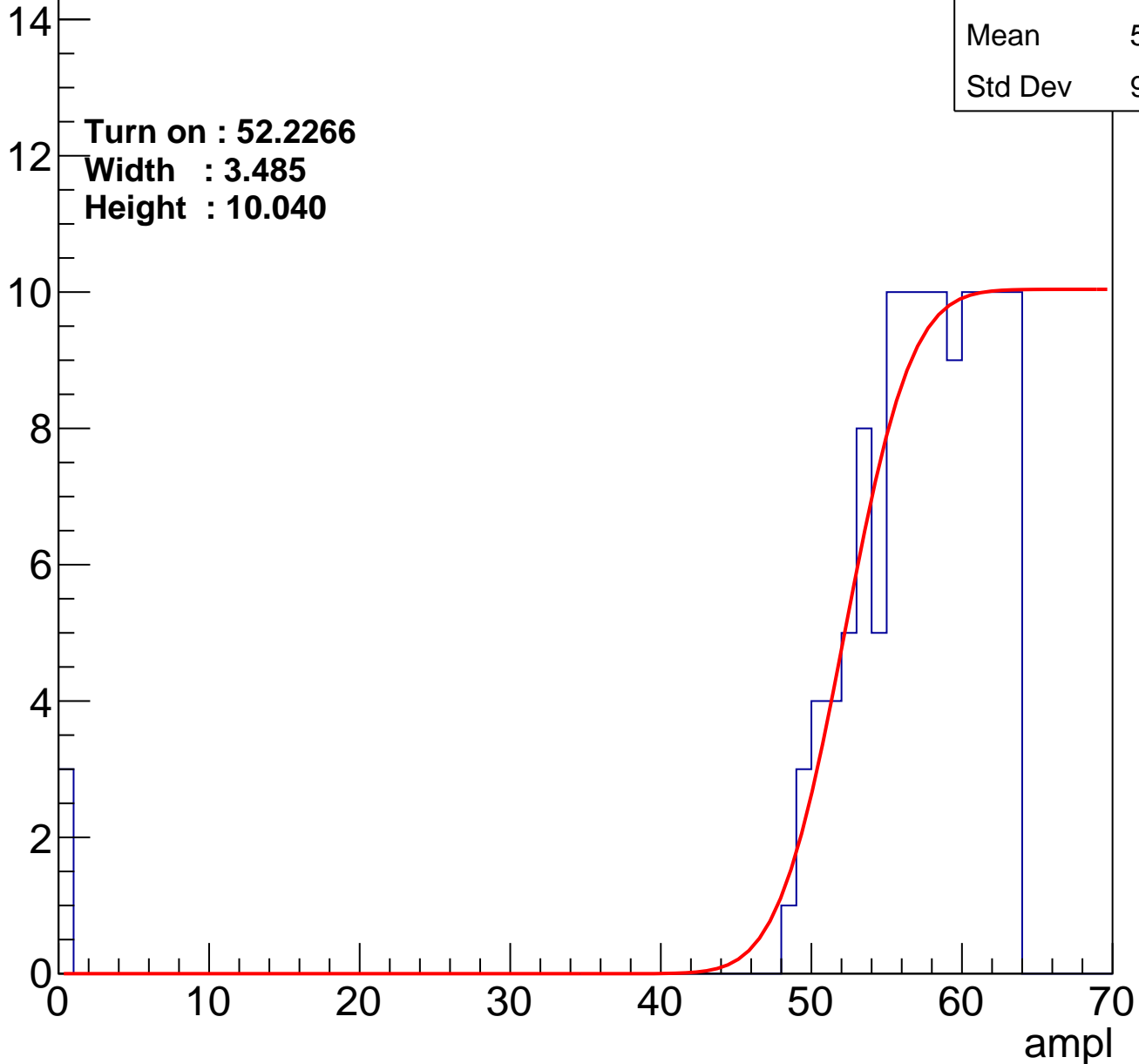
Entries	122
Mean	55.77
Std Dev	9.679

Turn on : 52.2266

Width : 3.485

Height : 10.040

Entry



# B0L103S, U23-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.77
Std Dev	9.679

Turn on : 52.2266

Width : 3.485

Height : 10.040

Entry

