B0L100S, U5-ch0, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
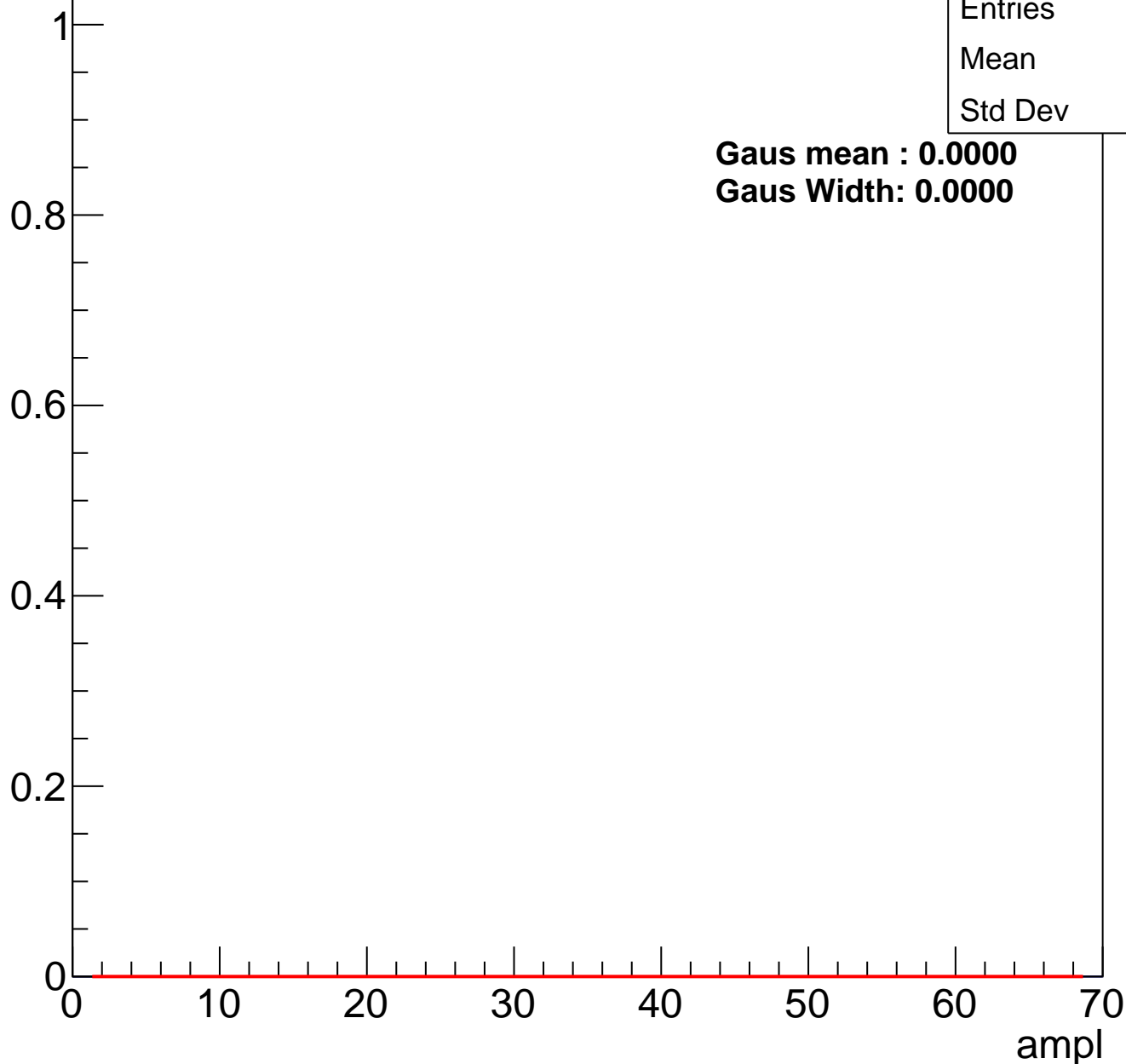
B0L100S, U5-ch0, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch0, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
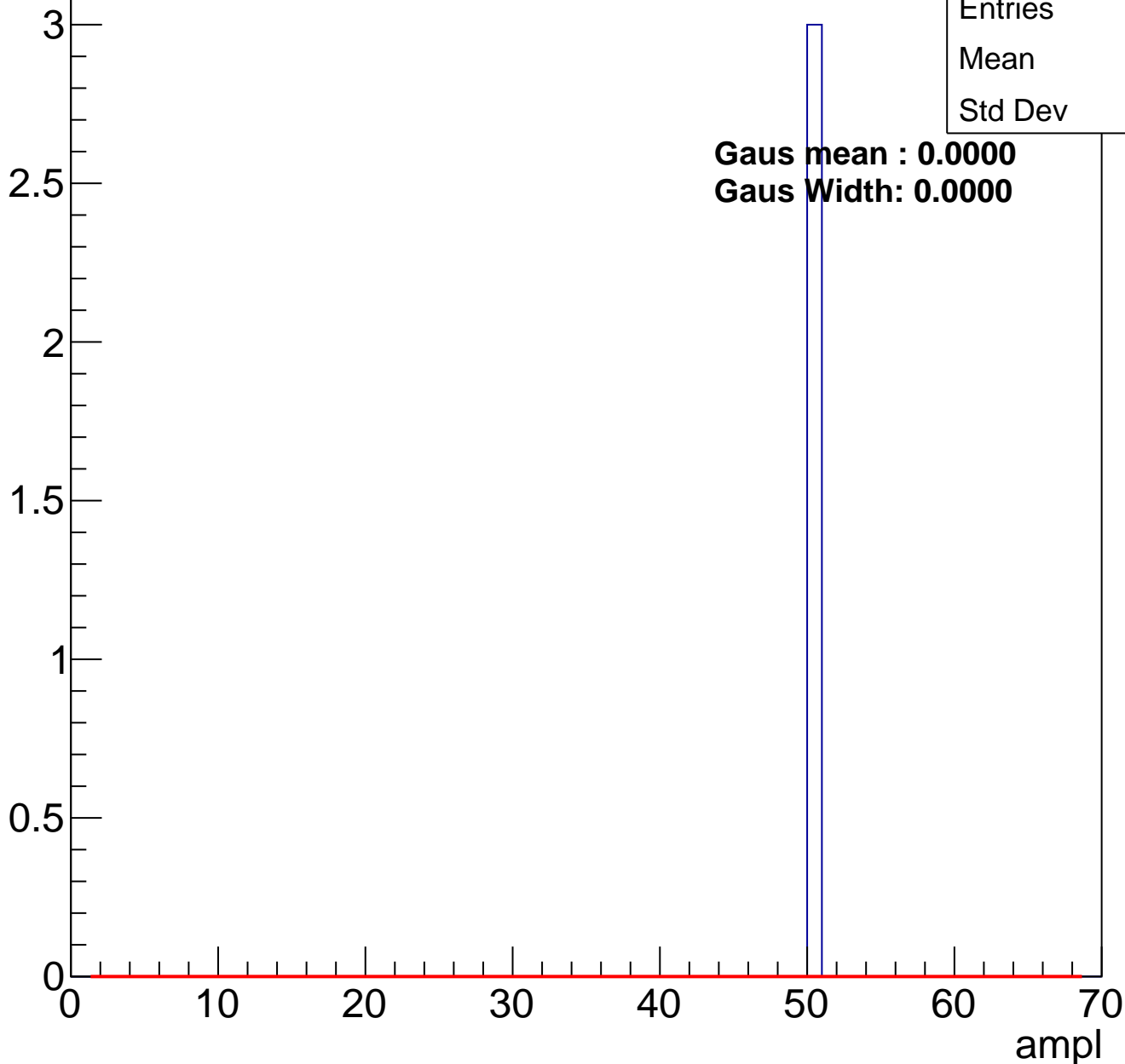
Entry

ampl

B0L100S, U5-ch0, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Entry

ampl

B0L100S, U5-ch0, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch0, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl
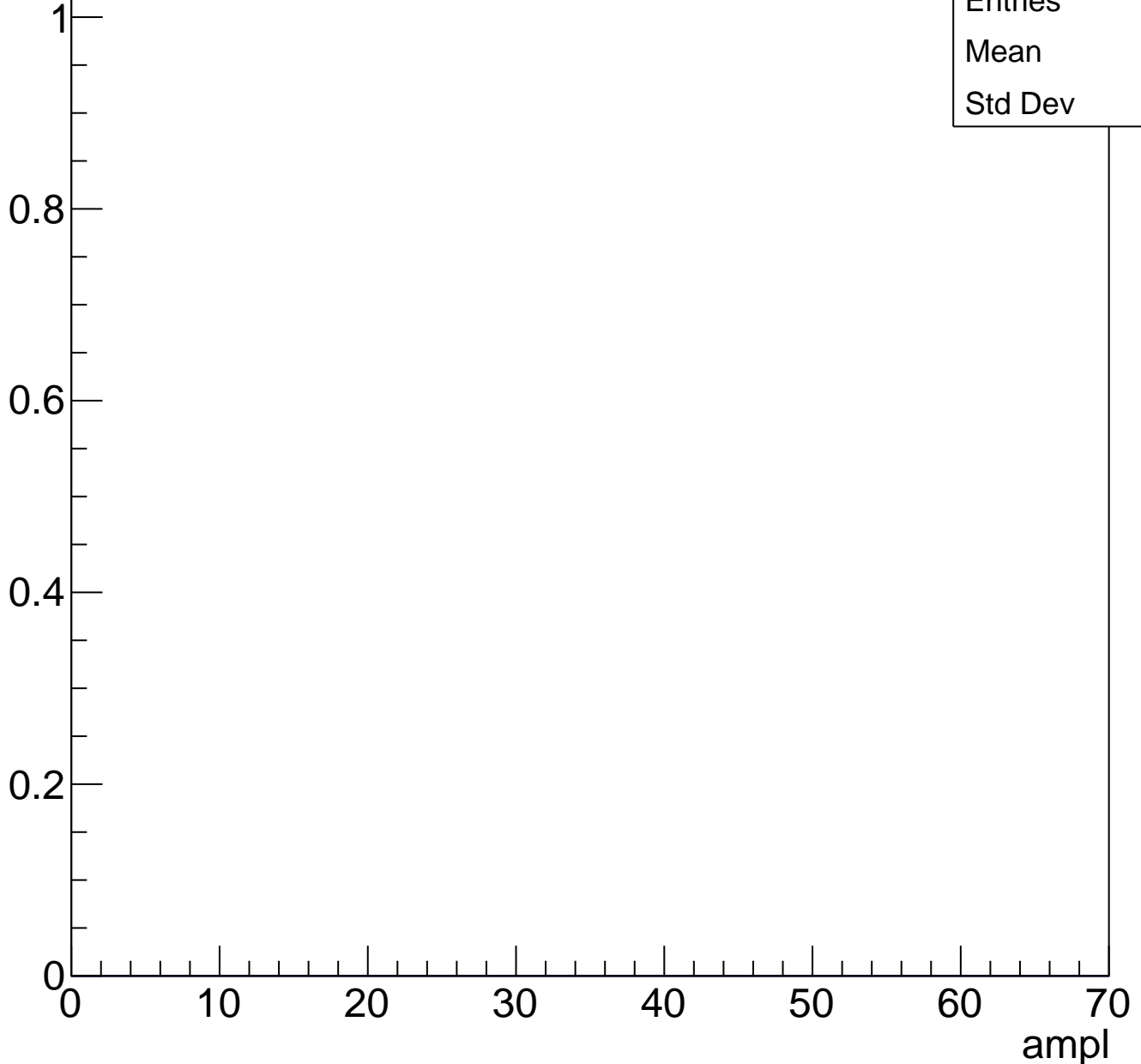
B0L100S, U5-ch0, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch0, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch1, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch1, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch1, adc2

calib_packv5_042523_0143.root, FC#6, port A1
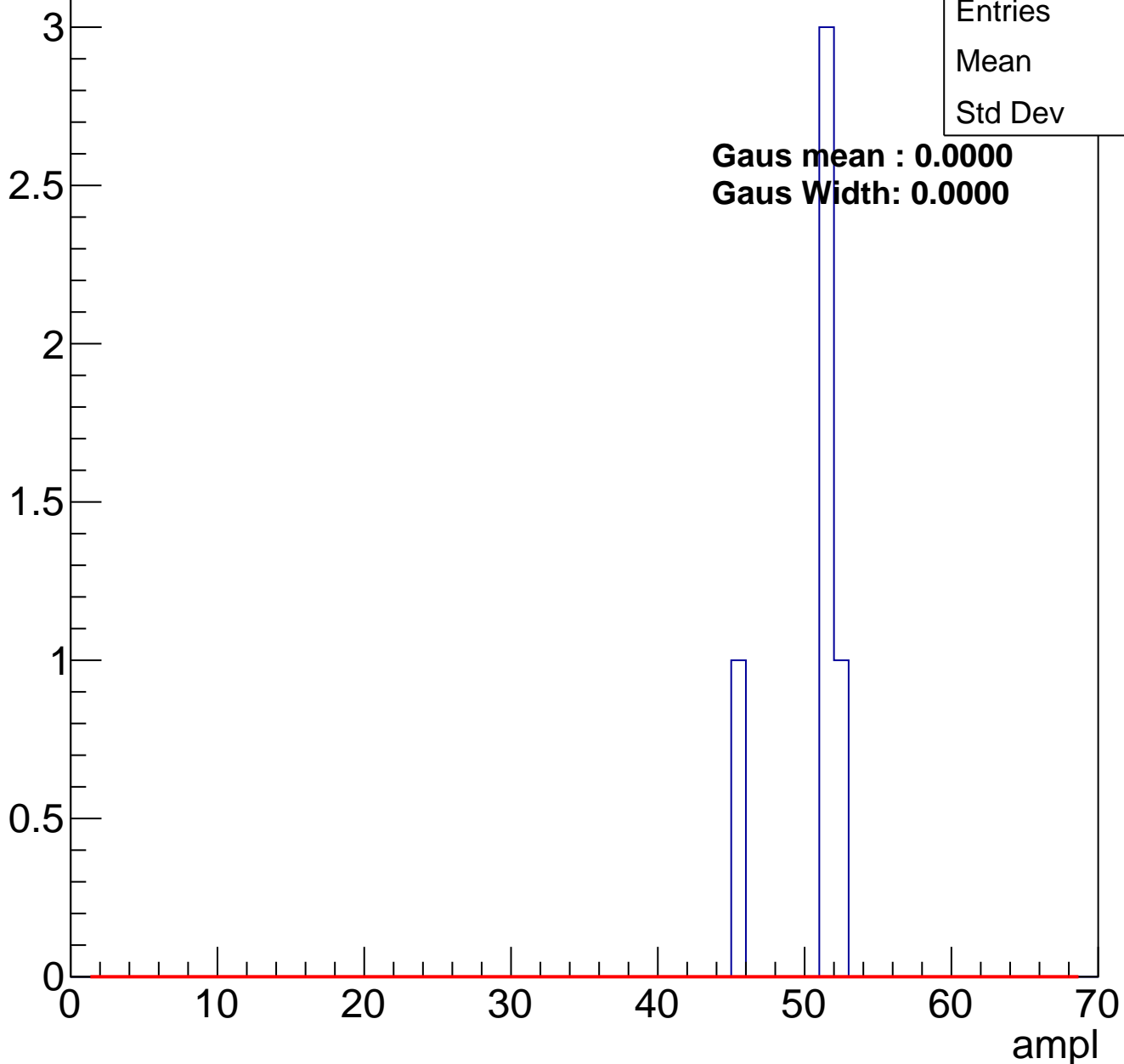
Entries	5
Mean	50
Std Dev	2.53

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch1, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	51.33
Std Dev	0.4714

Entry

ampl
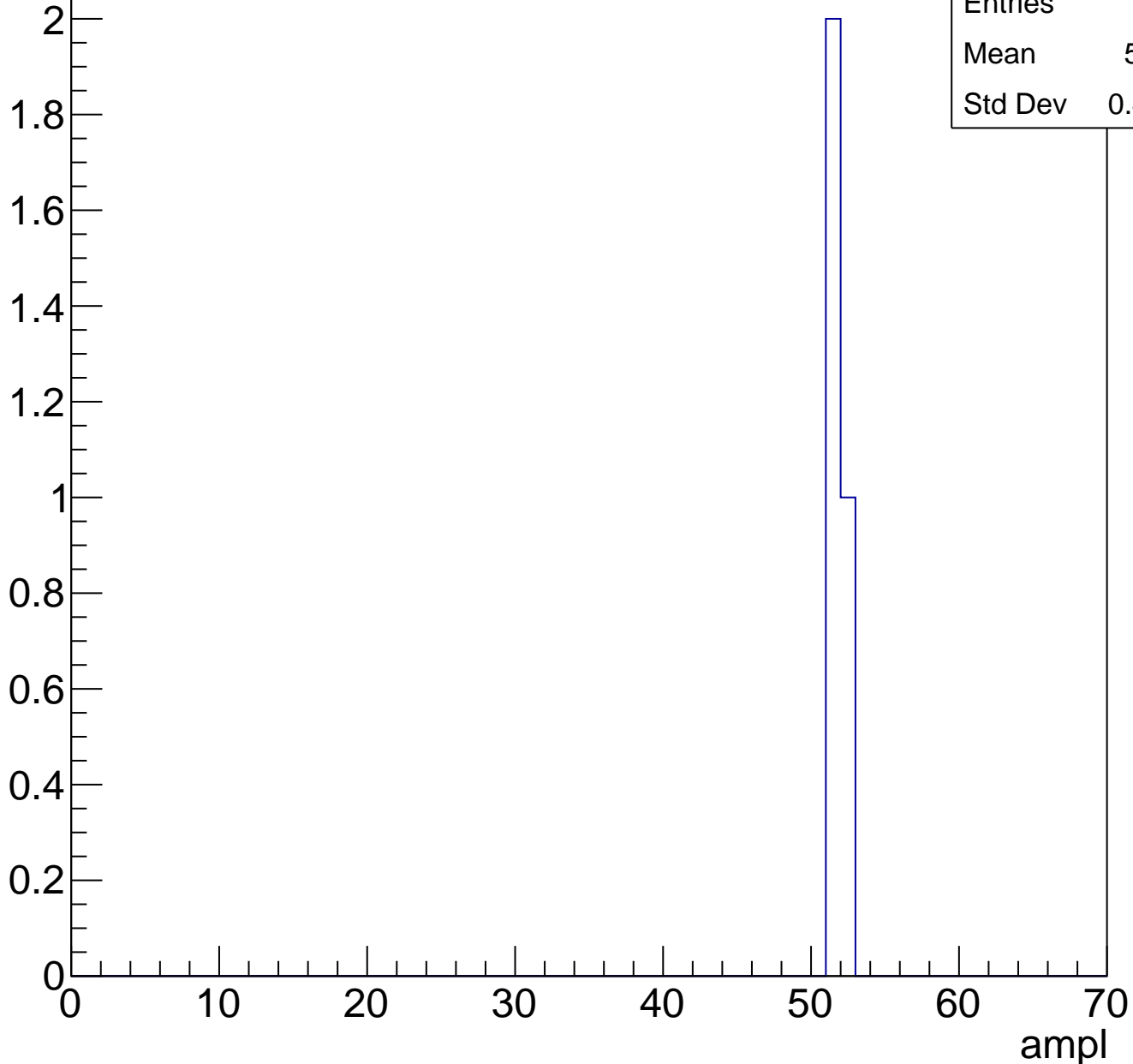
B0L100S, U5-ch1, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch1, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch1, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch1, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch2, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	13
Mean	33.62
Std Dev	0.8356

Gaus mean : 34.1356
Gaus Width: 0.9004

B0L100S, U5-ch2, adc1

calib_packv5_042523_0143.root, FC#6, port A1
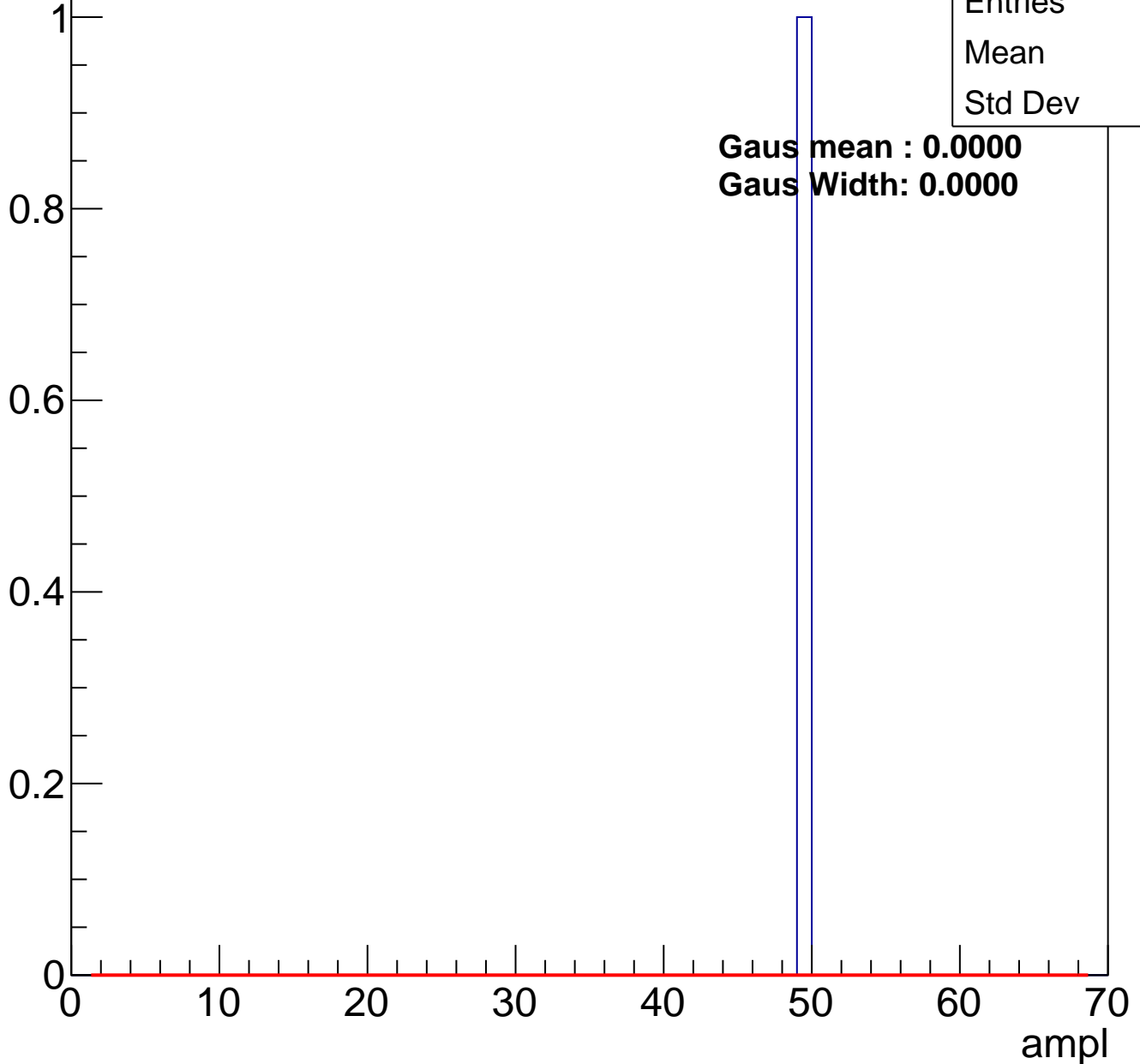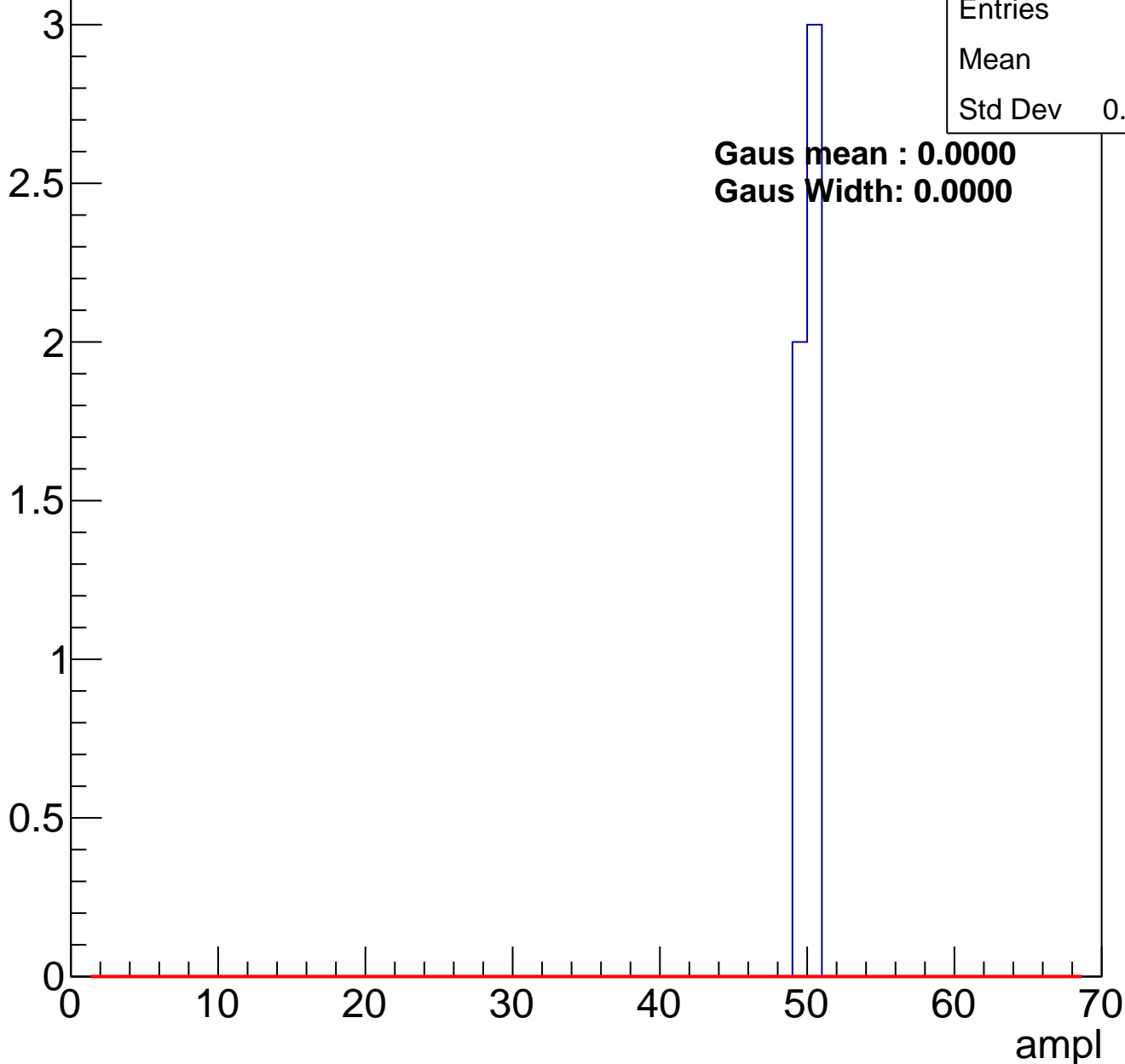
Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch2, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	53.2
Std Dev	3.059

ampl
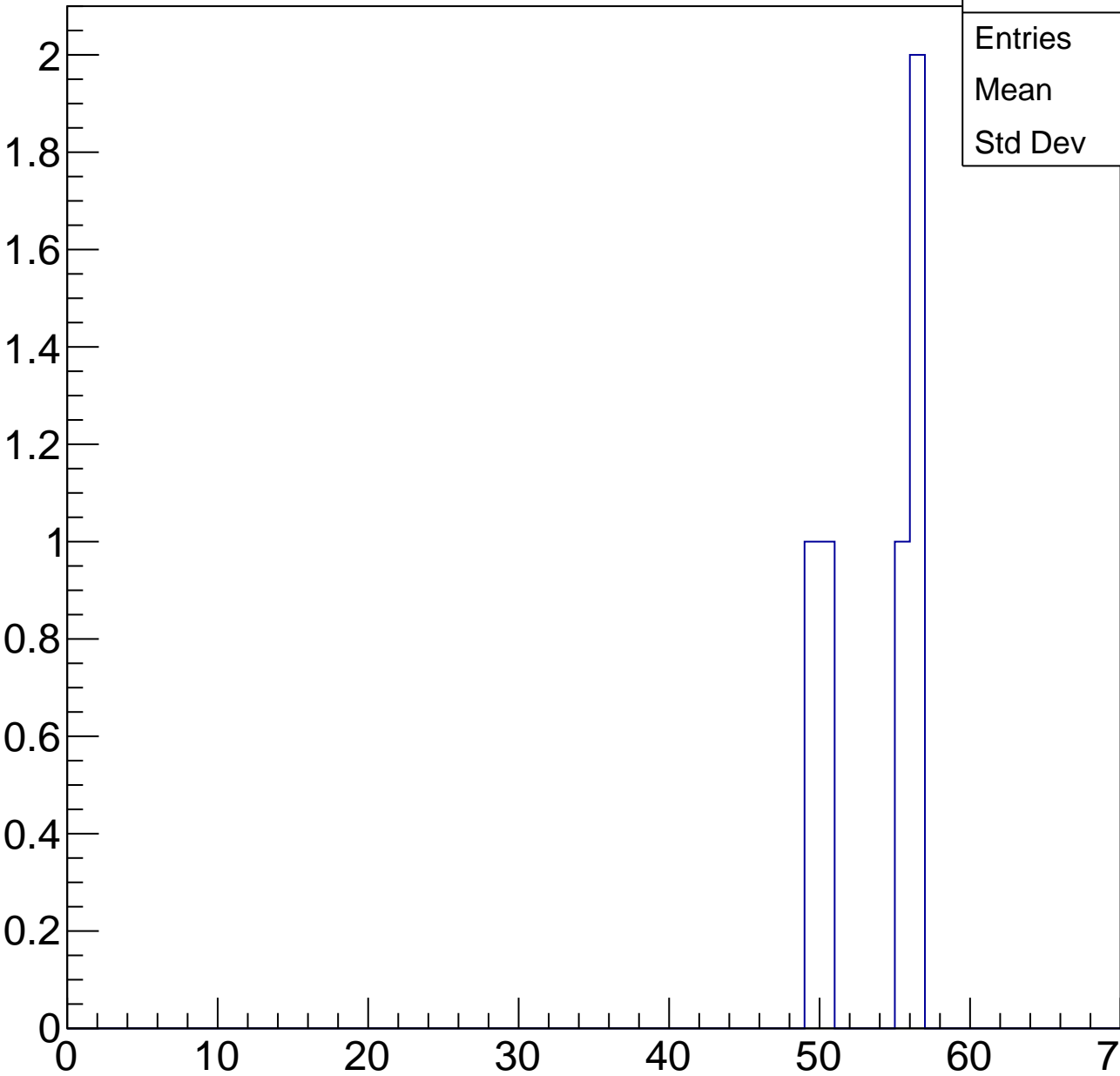
Entry

B0L100S, U5-ch2, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch2, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch2, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch2, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch3, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch3, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch3, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch3, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl
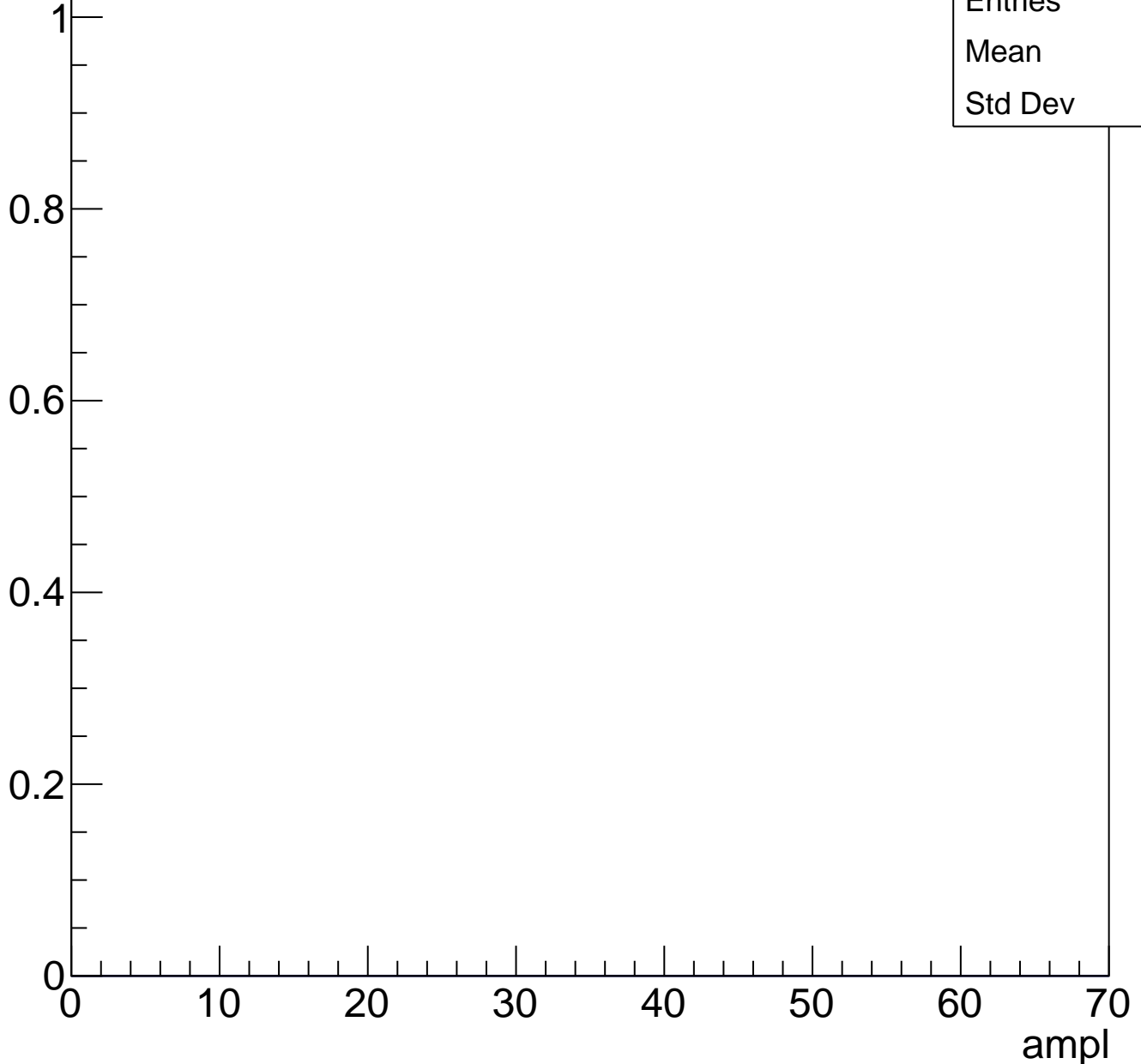
B0L100S, U5-ch3, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch3, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch3, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch3, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch4, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch4, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 43.5581
Gaus Width: 11.1146

Entries	11
Mean	45.45
Std Dev	2.426
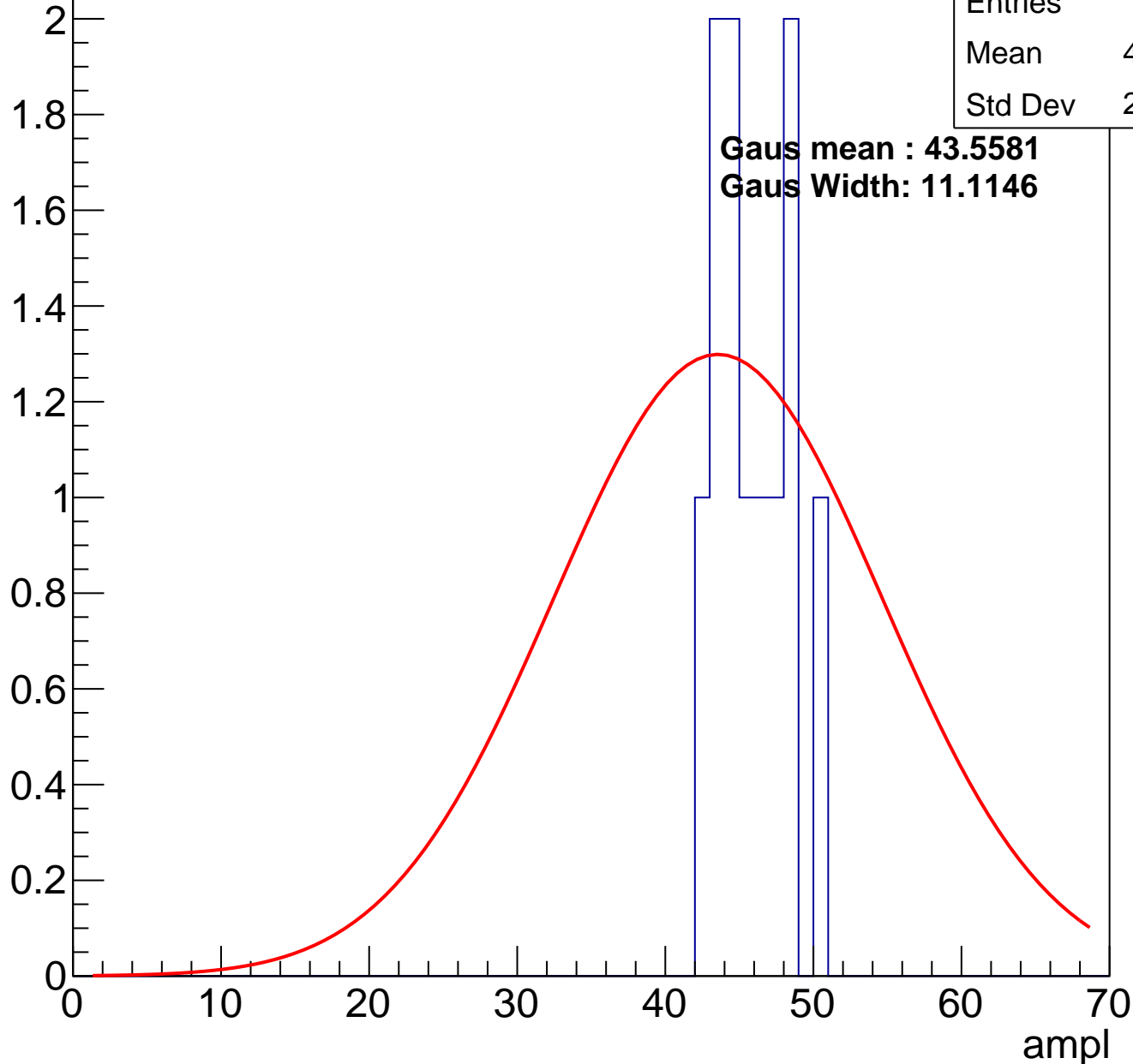
Entry

ampl

B0L100S, U5-ch4, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	21
Mean	47.43
Std Dev	2.106

Gaus mean : 47.3409
Gaus Width: 2.7440
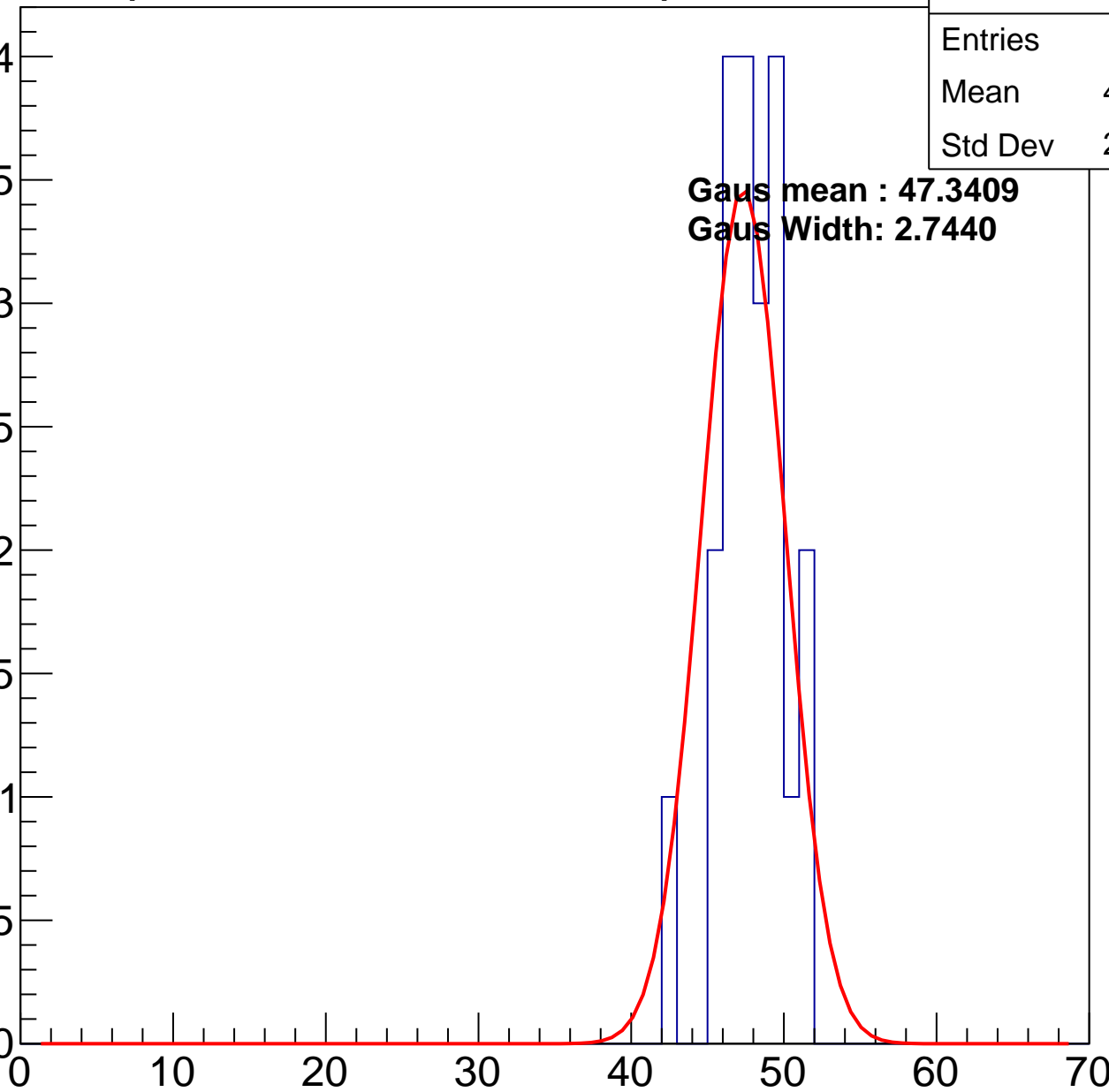
B0L100S, U5-ch4, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	50.6
Std Dev	0.4899

Entry

ampl
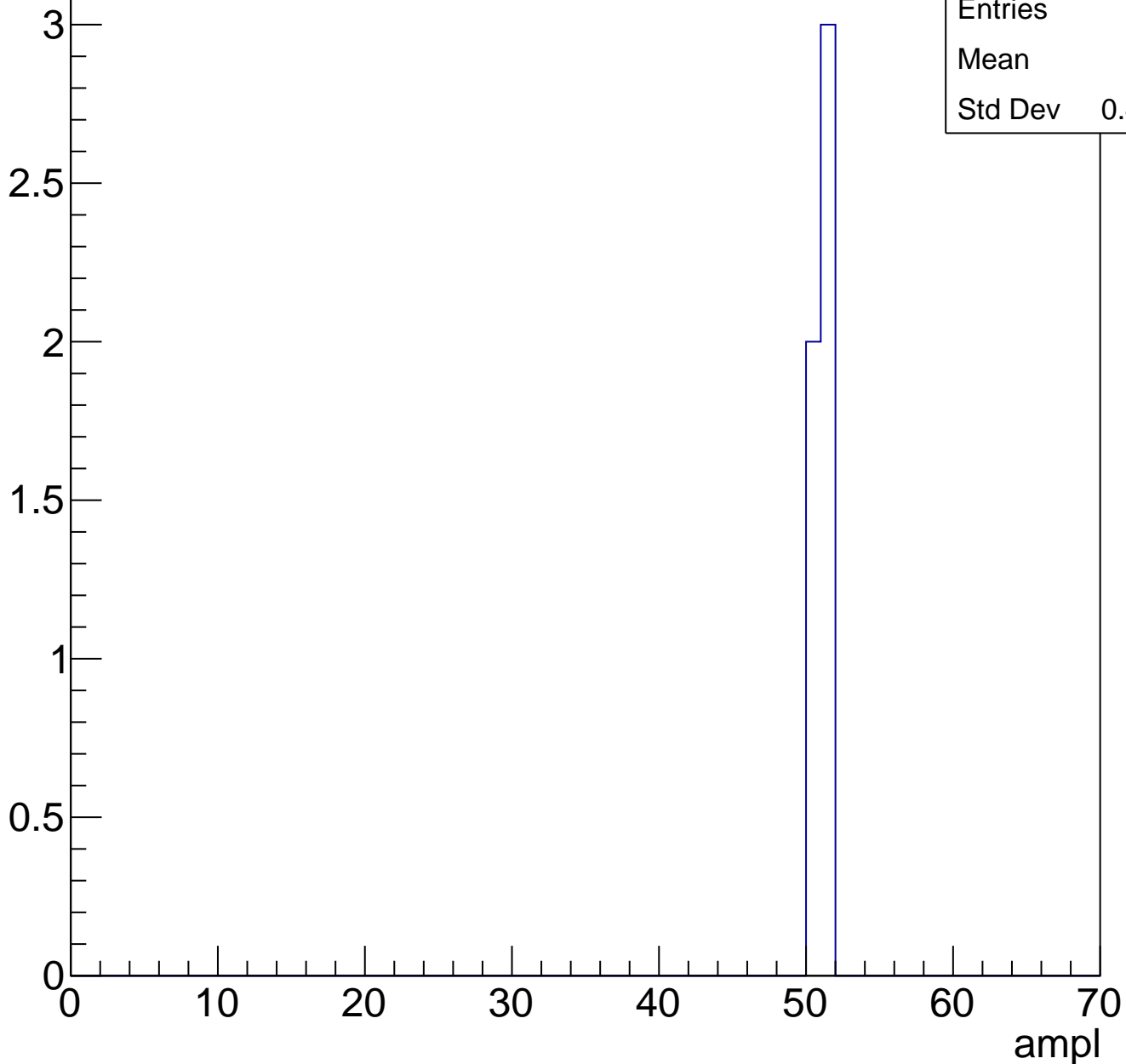
B0L100S, U5-ch4, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch4, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch4, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch4, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch5, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch5, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch5, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch5, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	52
Std Dev	0

Entry

ampl

B0L100S, U5-ch5, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch5, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch5, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch5, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch6, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
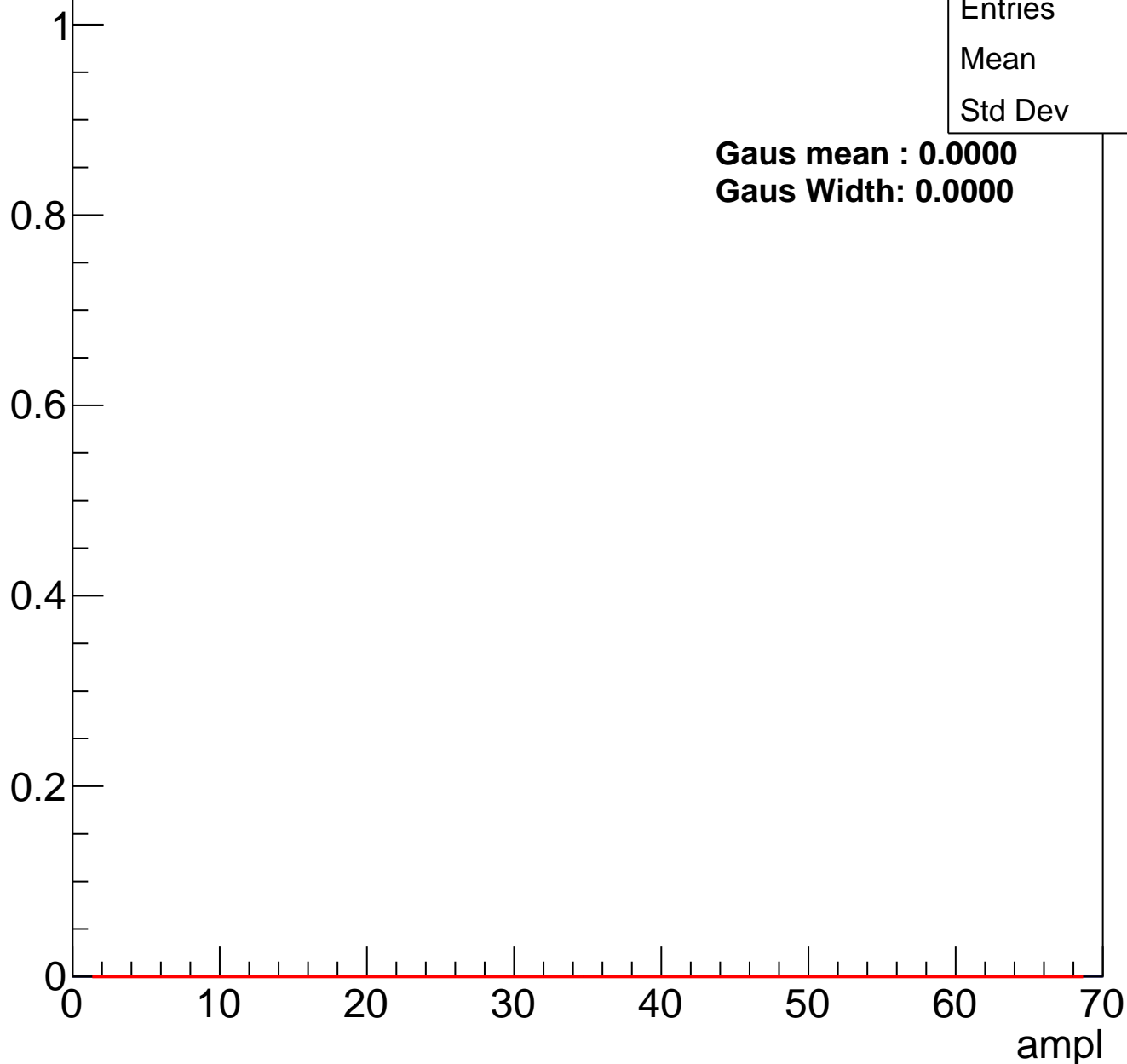
B0L100S, U5-ch6, adc1

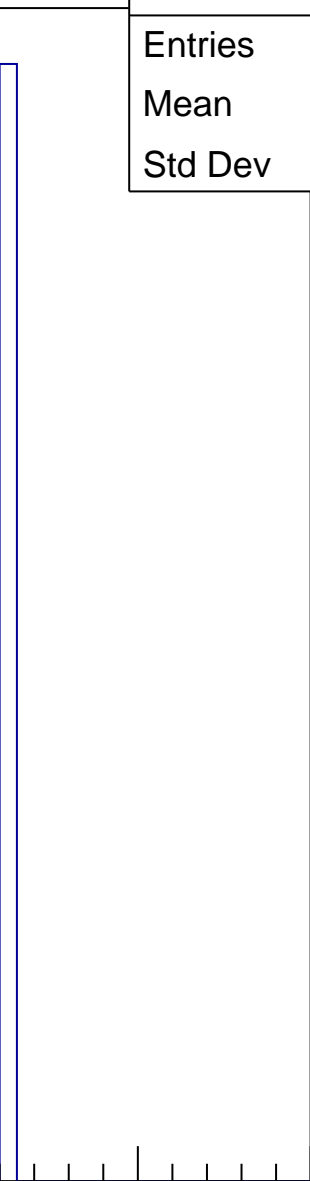**calib_packv5_042523_0143.root, FC#6, port A1**

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch6, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch6, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch6, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch6, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch6, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl
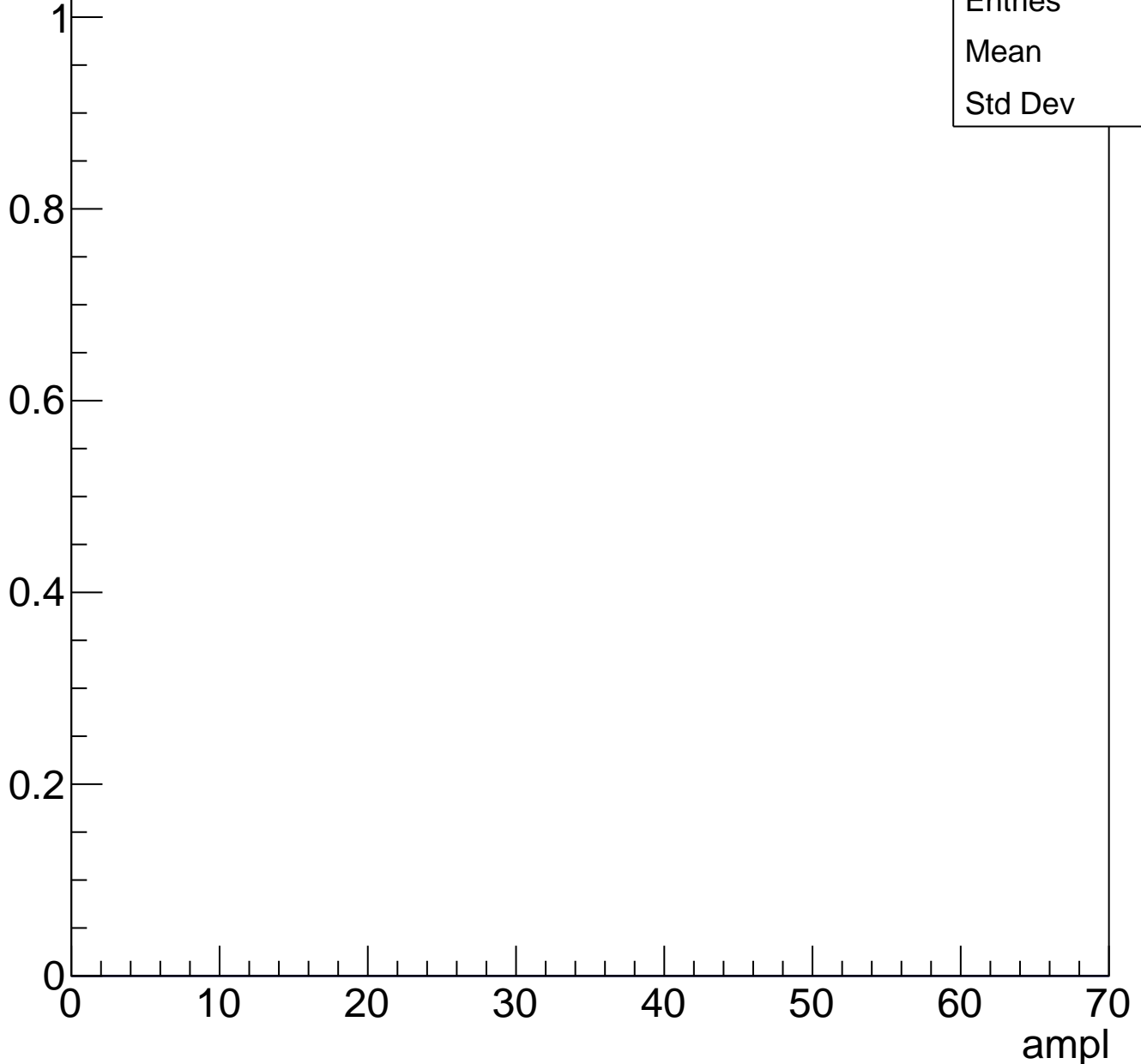
B0L100S, U5-ch6, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch7, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch7, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch7, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch7, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch7, adc4

calib_packv5_042523_0143.root, FC#6, port A1
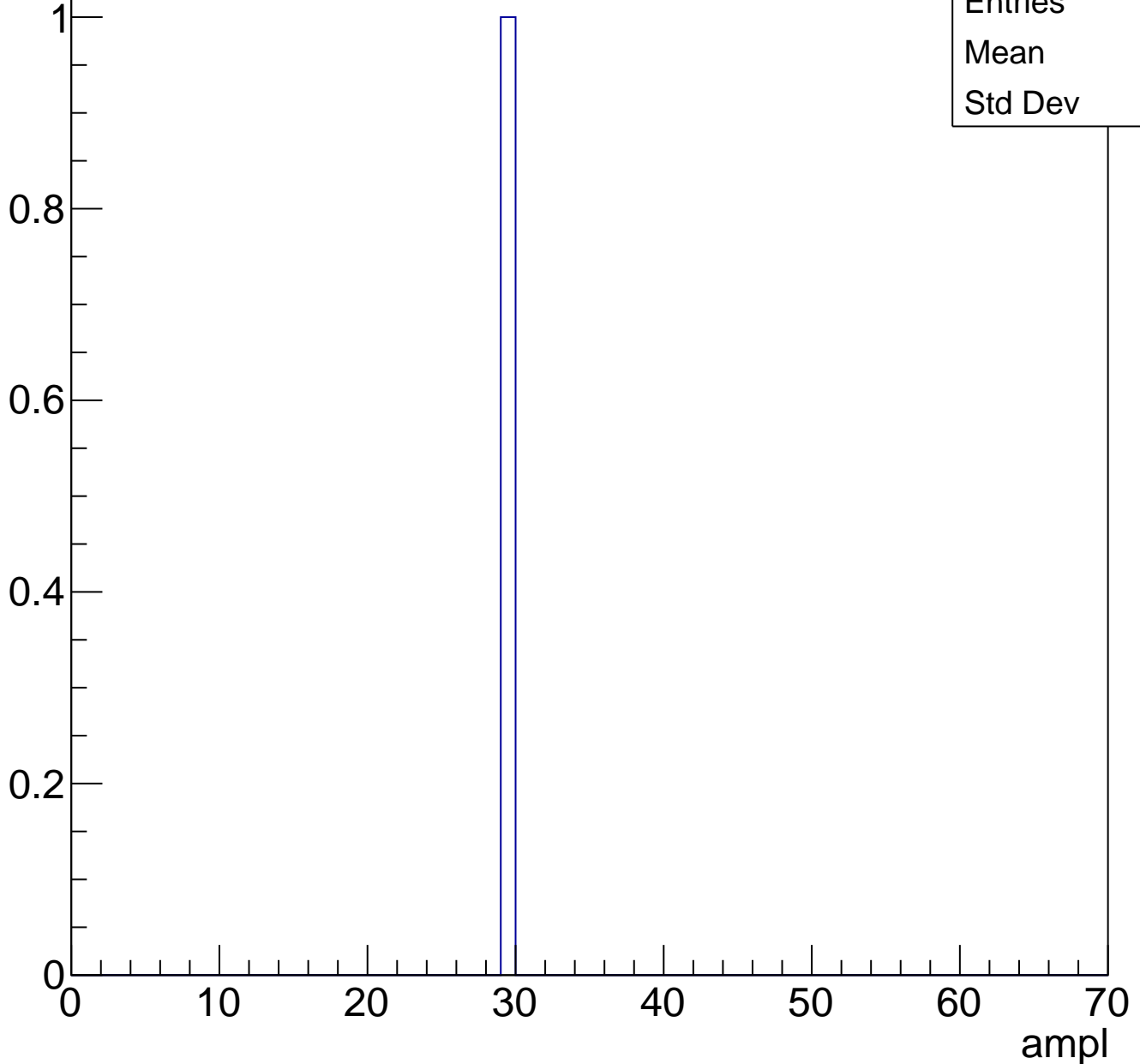
Entries	1
Mean	29
Std Dev	0

B0L100S, U5-ch7, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch7, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch7, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch8, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch8, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch8, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch8, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch8, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch8, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch8, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch8, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch9, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch9, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	49
Std Dev	2

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
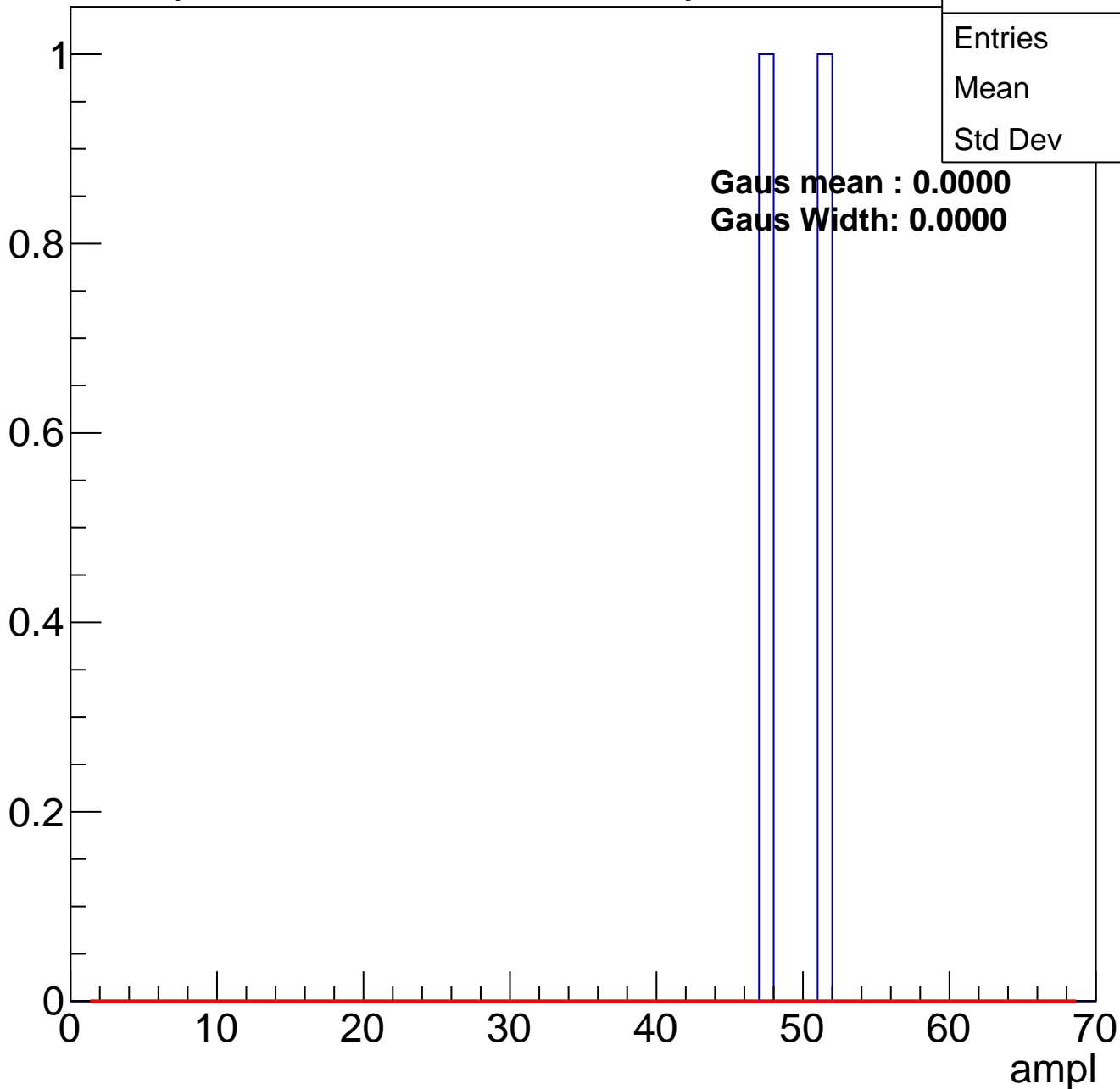
B0L100S, U5-ch9, adc2

calib_packv5_042523_0143.root, FC#6, port A1
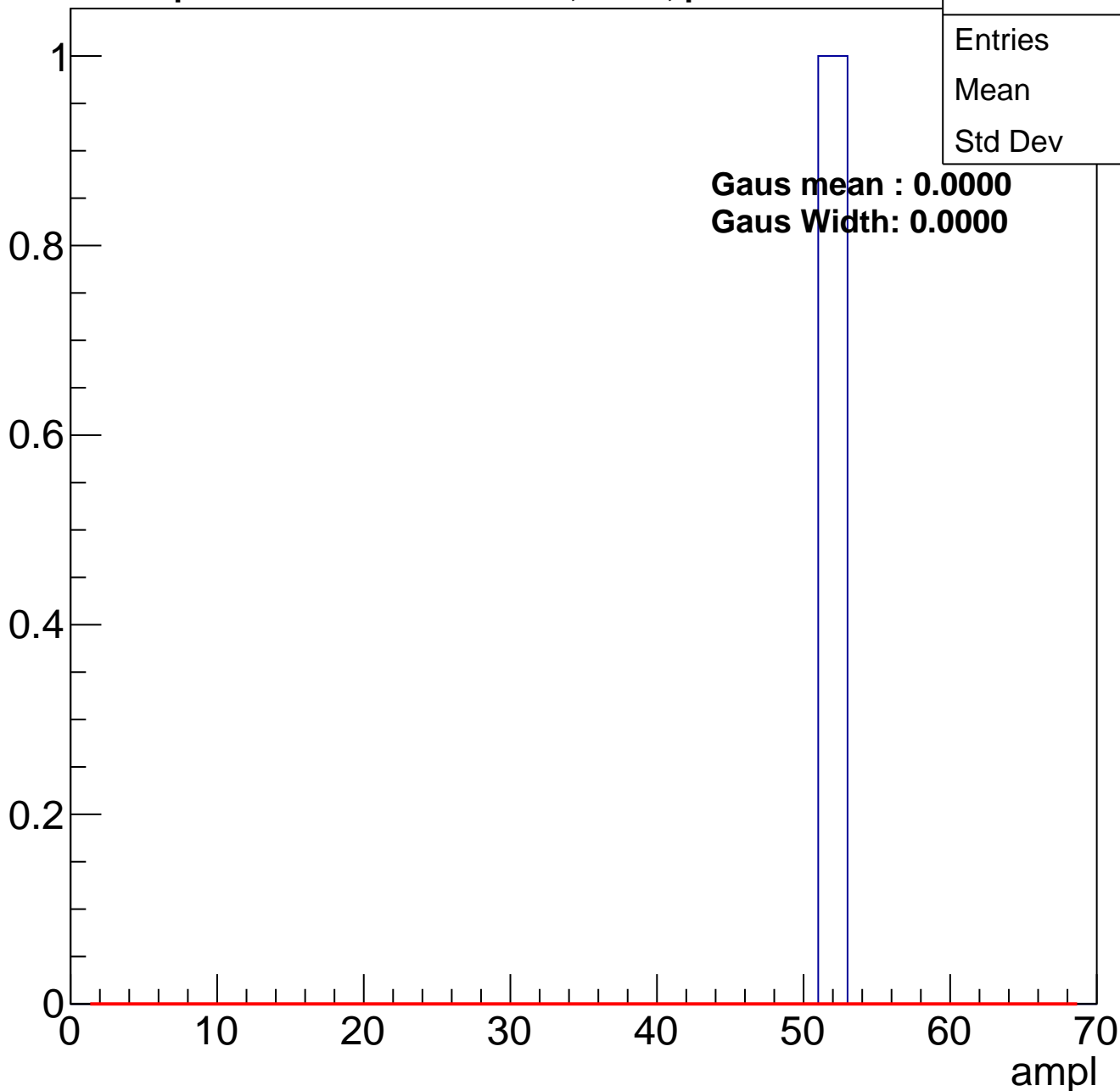
Entries	2
Mean	51.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch9, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	52
Std Dev	0

ampl
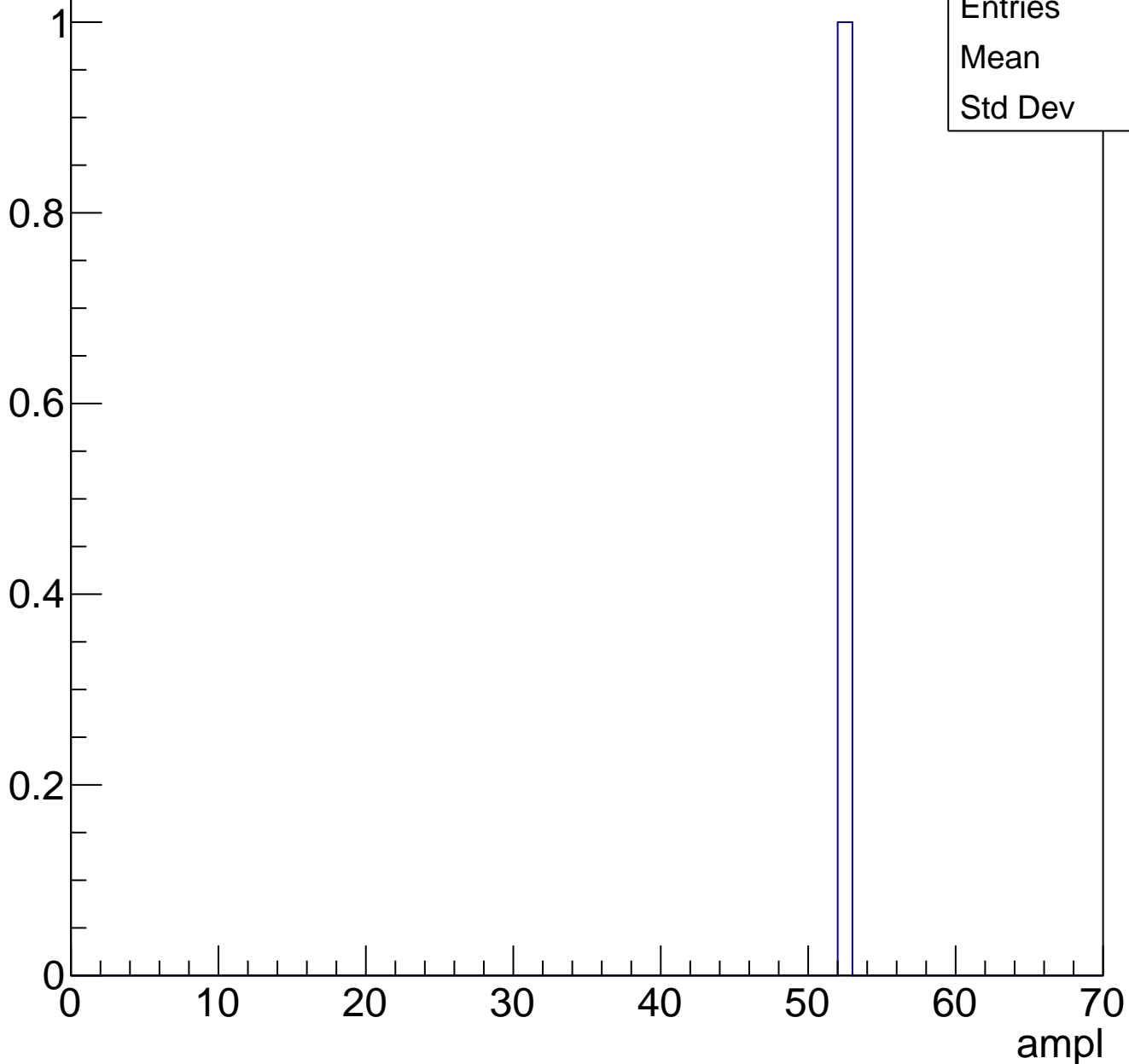
B0L100S, U5-ch9, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch9, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch9, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch9, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch10, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch10, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch10, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	47.5
Std Dev	1.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
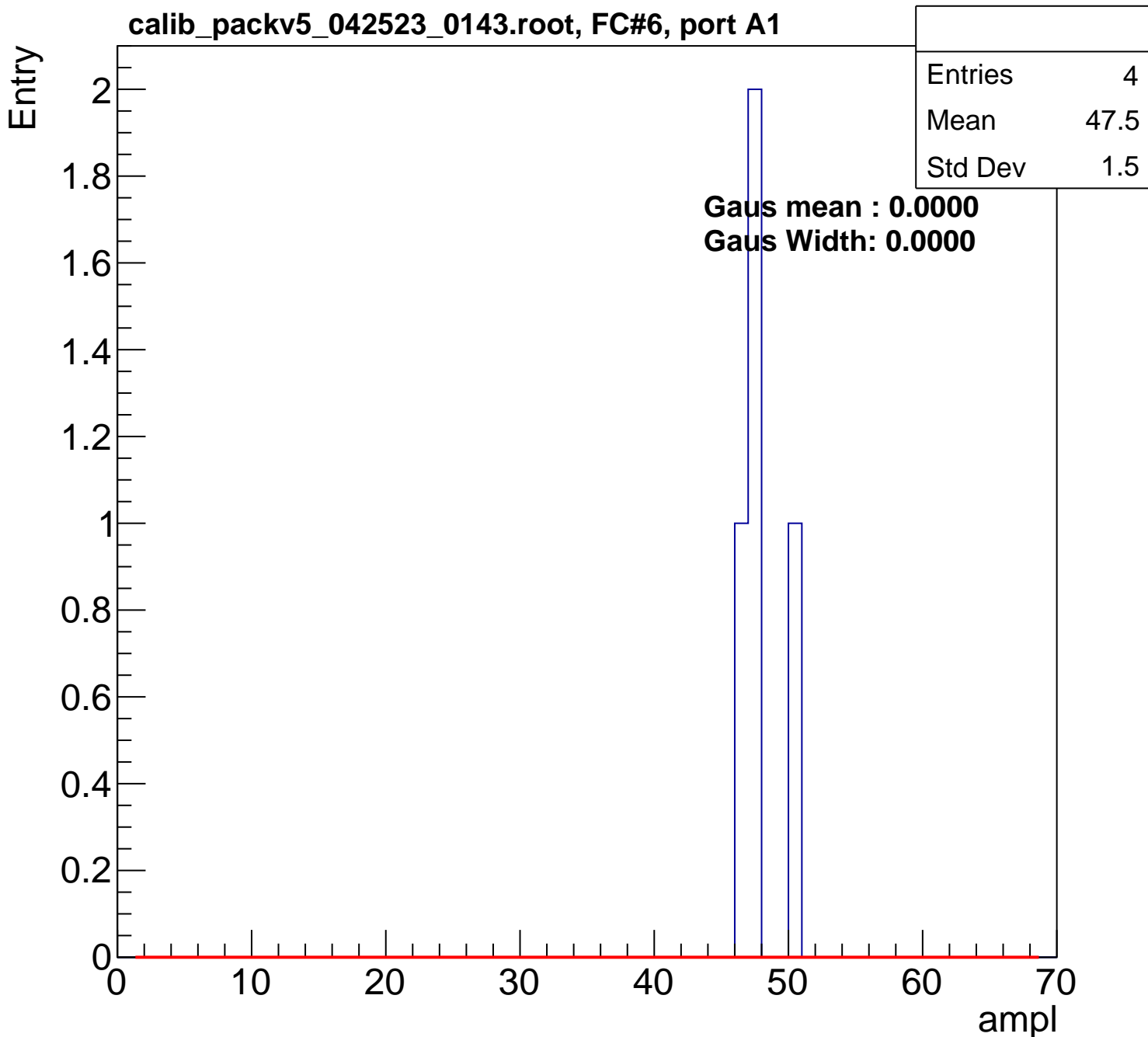
B0L100S, U5-ch10, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	2

Entry

ampl
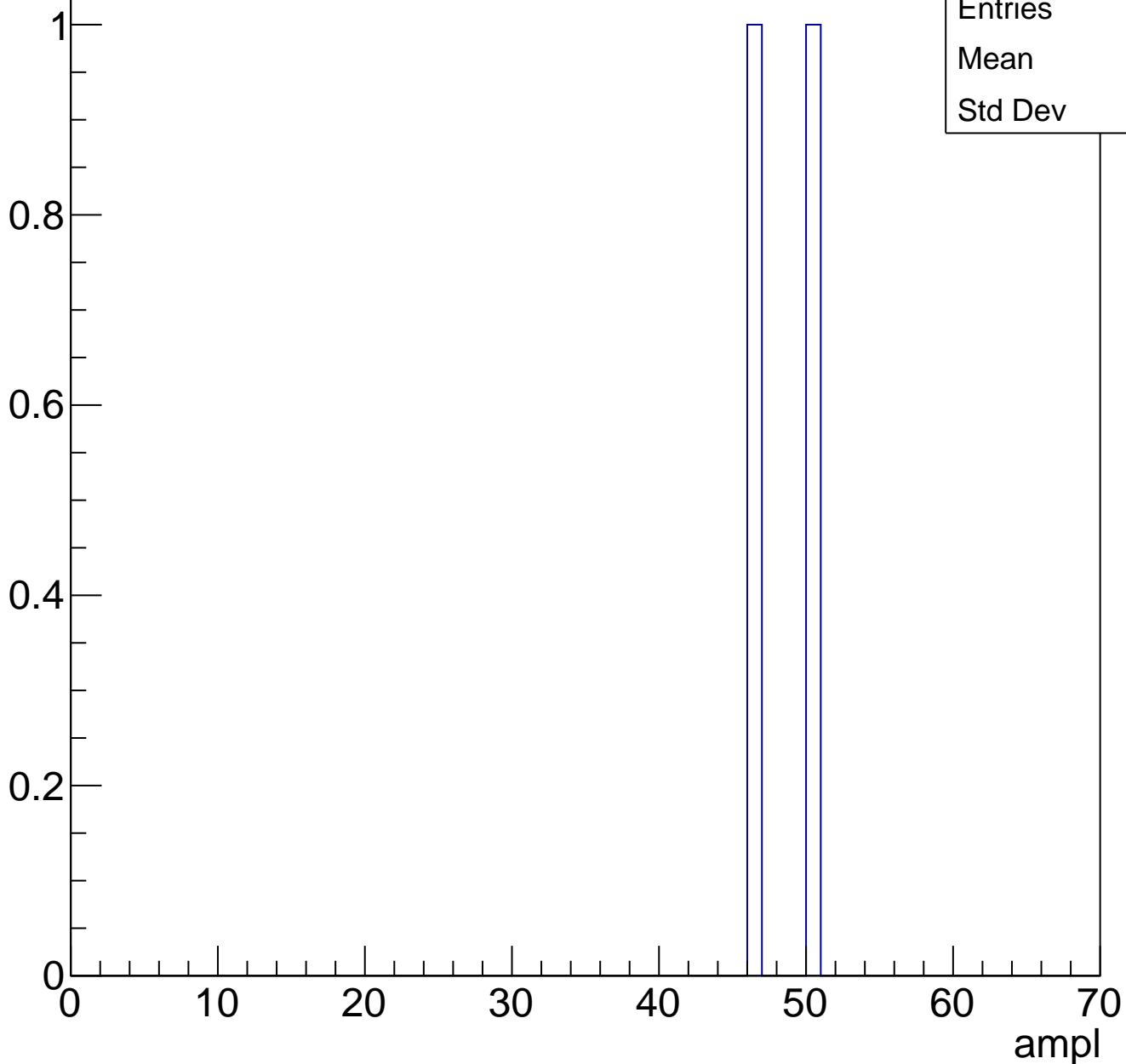
B0L100S, U5-ch10, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch10, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch10, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch10, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch11, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch11, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch11, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch11, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch11, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch11, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch11, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch11, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch12, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch12, adc1

calib_packv5_042523_0143.root, FC#6, port A1

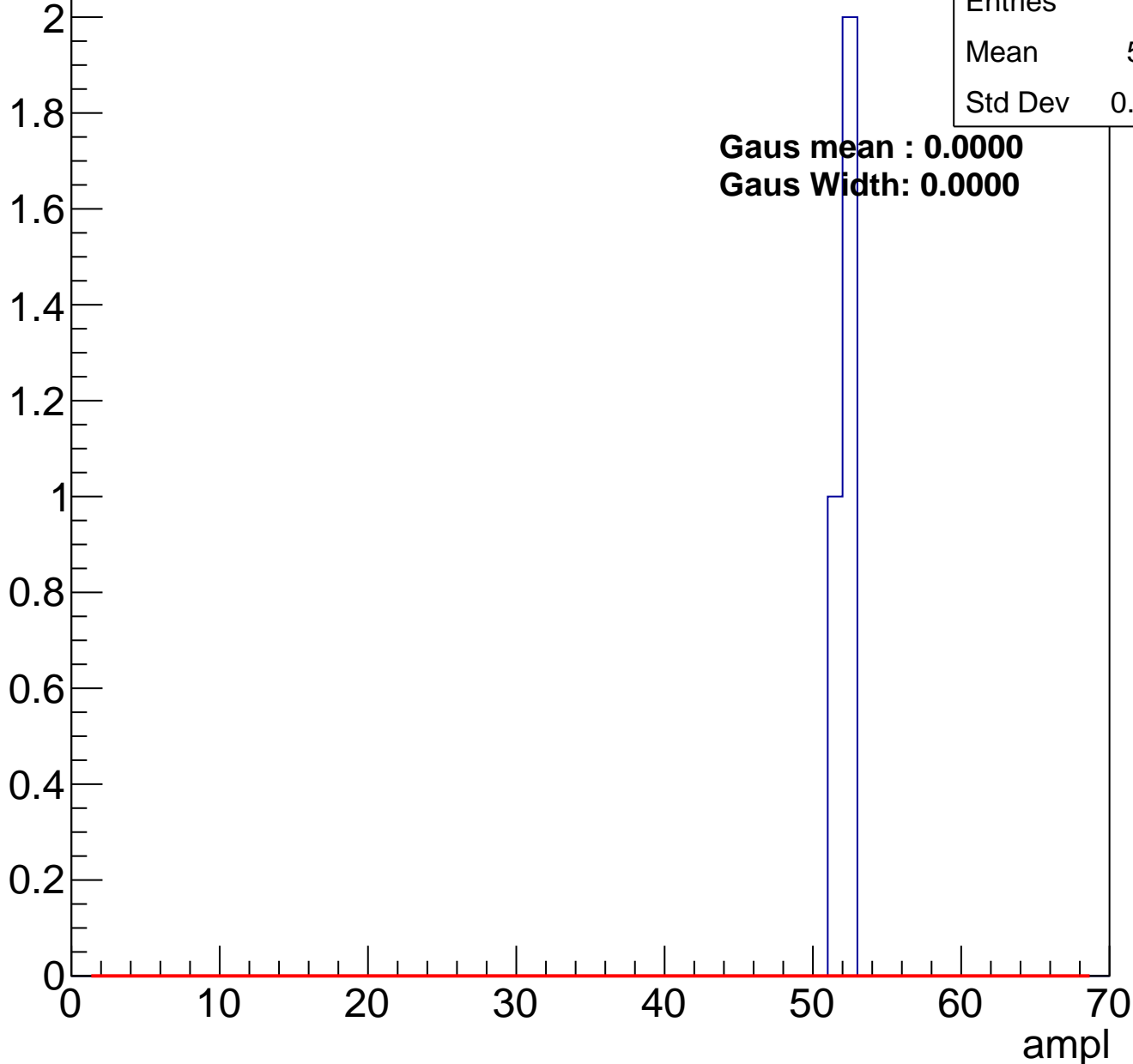|	
Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch12, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	51.67
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch12, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	51.6
Std Dev	0.4899

Entry

ampl
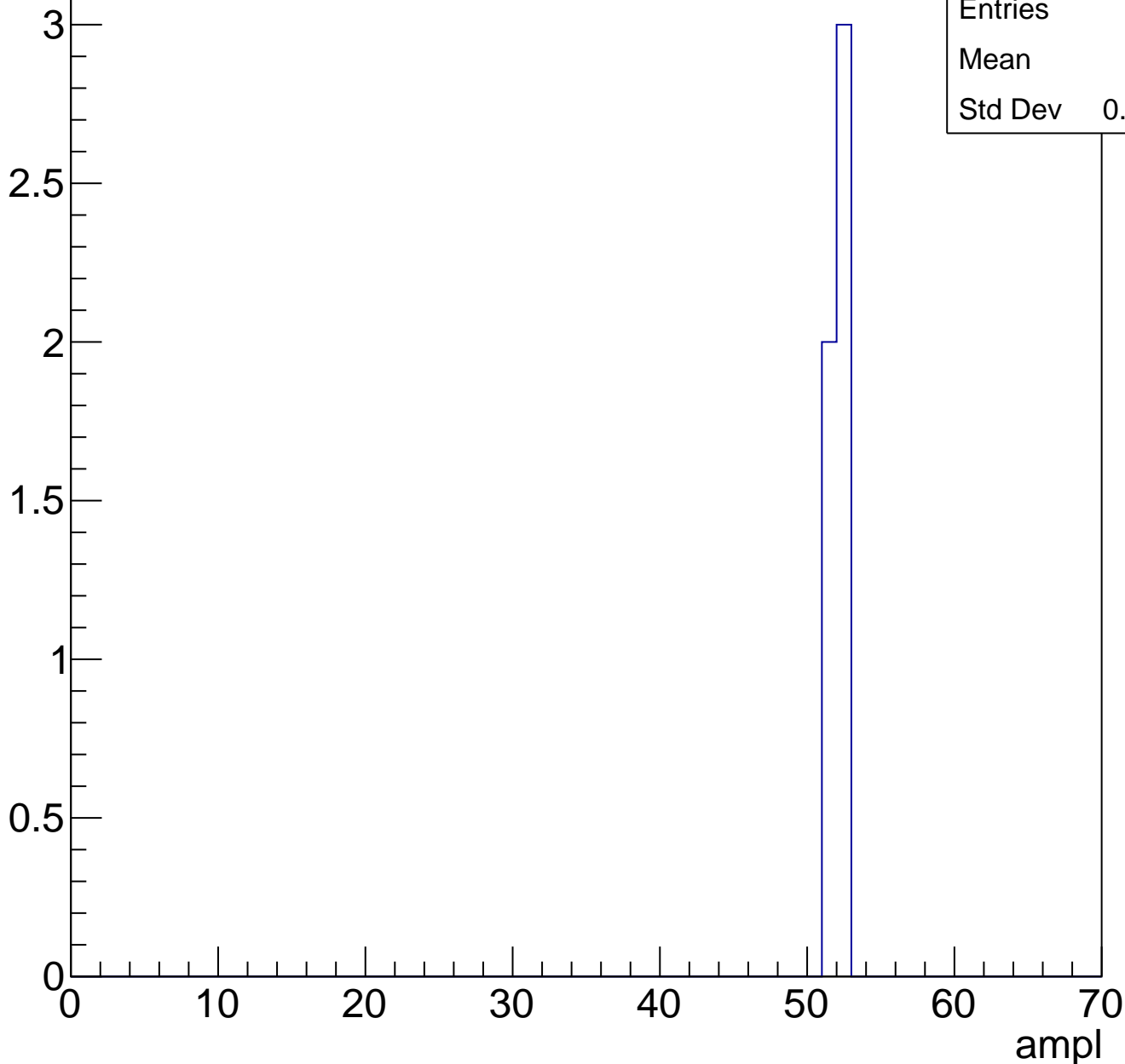
B0L100S, U5-ch12, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch12, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch12, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch12, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch13, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch13, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch13, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch13, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch13, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch13, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch13, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch13, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch14, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch14, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000
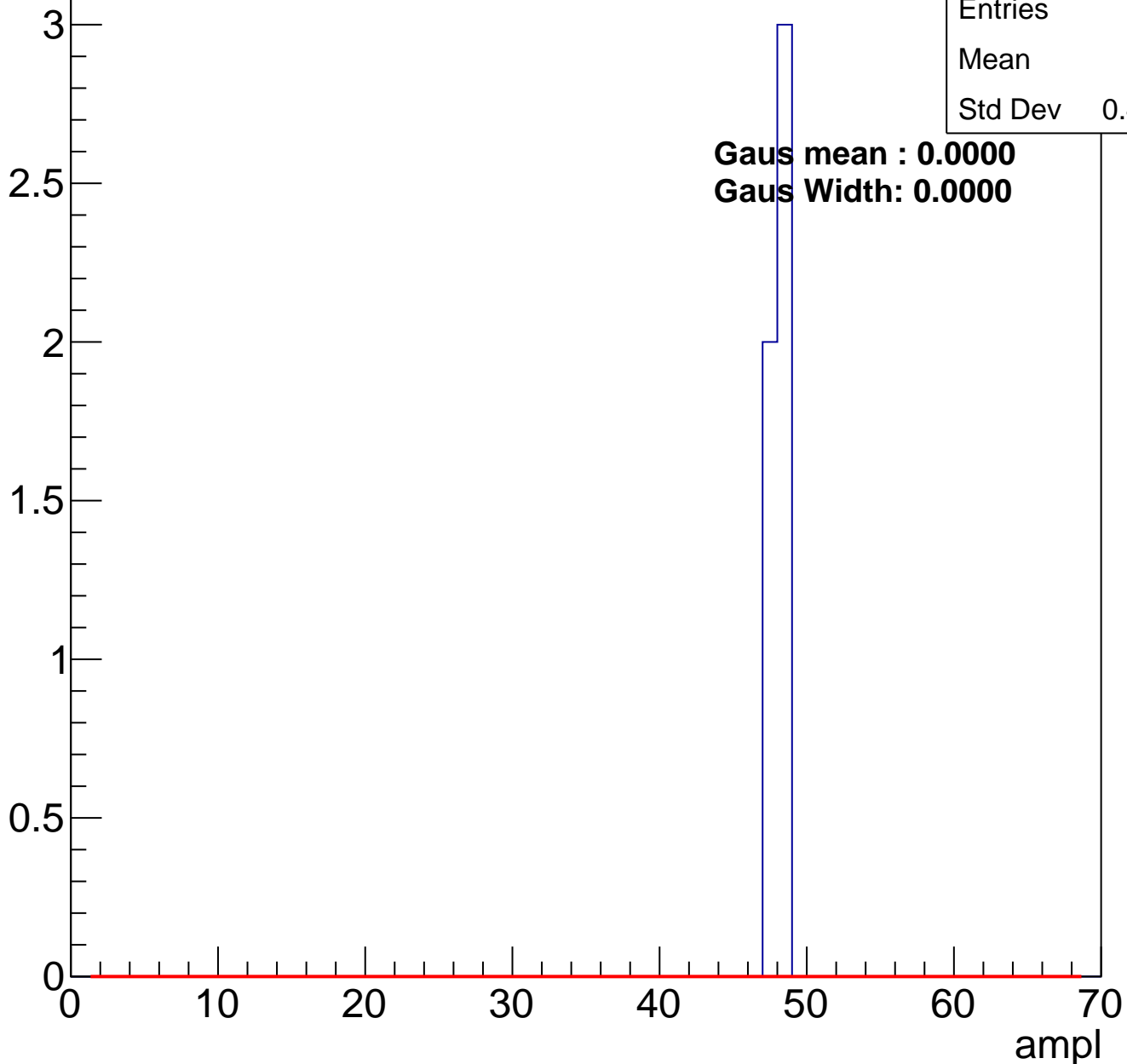
Entries	5
Mean	47.6
Std Dev	0.4899

Entry

ampl

B0L100S, U5-ch14, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	47.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000
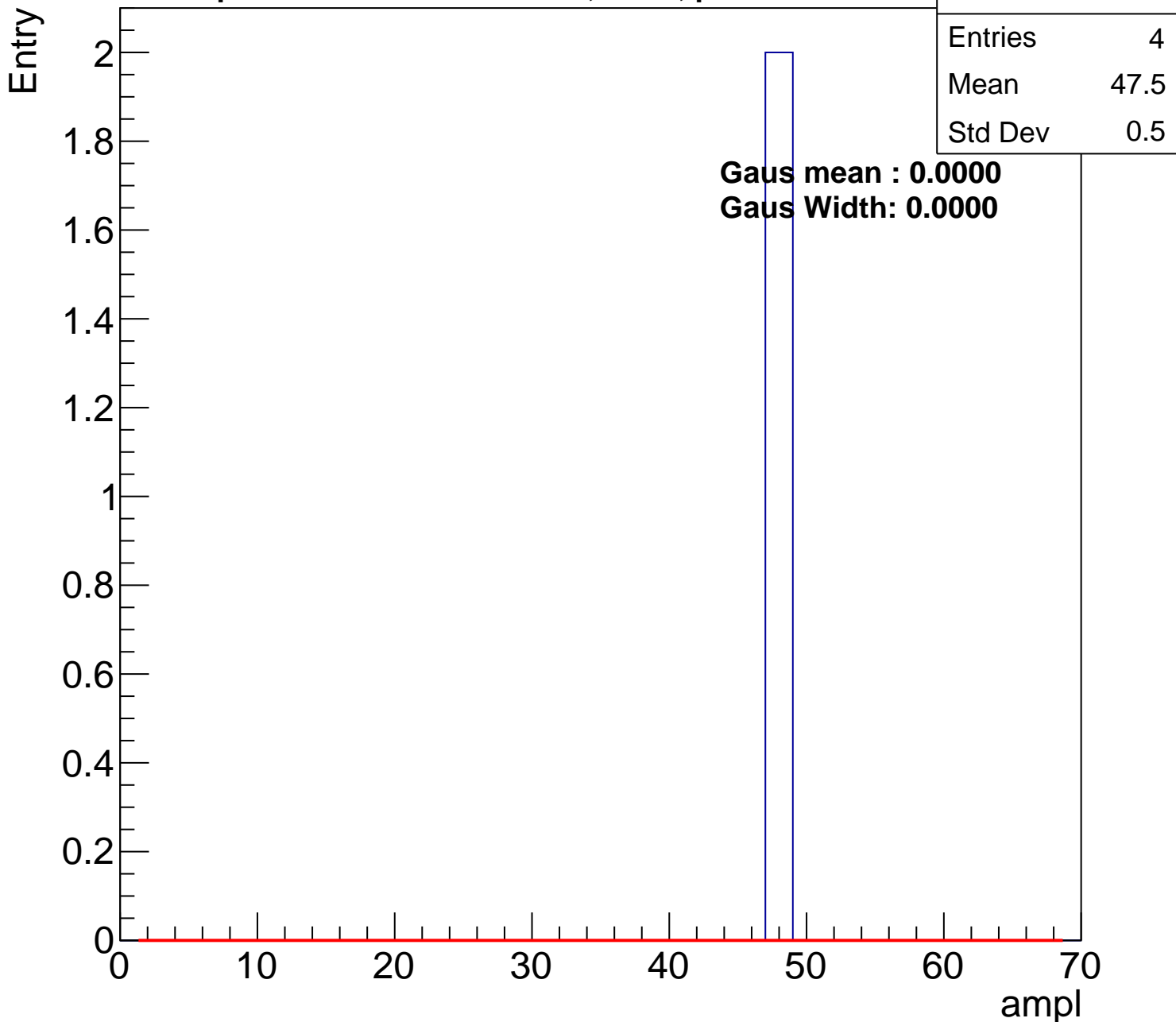
Entry

ampl

B0L100S, U5-ch14, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	56
Std Dev	0

Entry

ampl

B0L100S, U5-ch14, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch14, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch14, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch14, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch15, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch15, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	9
Mean	45.22
Std Dev	0.7857
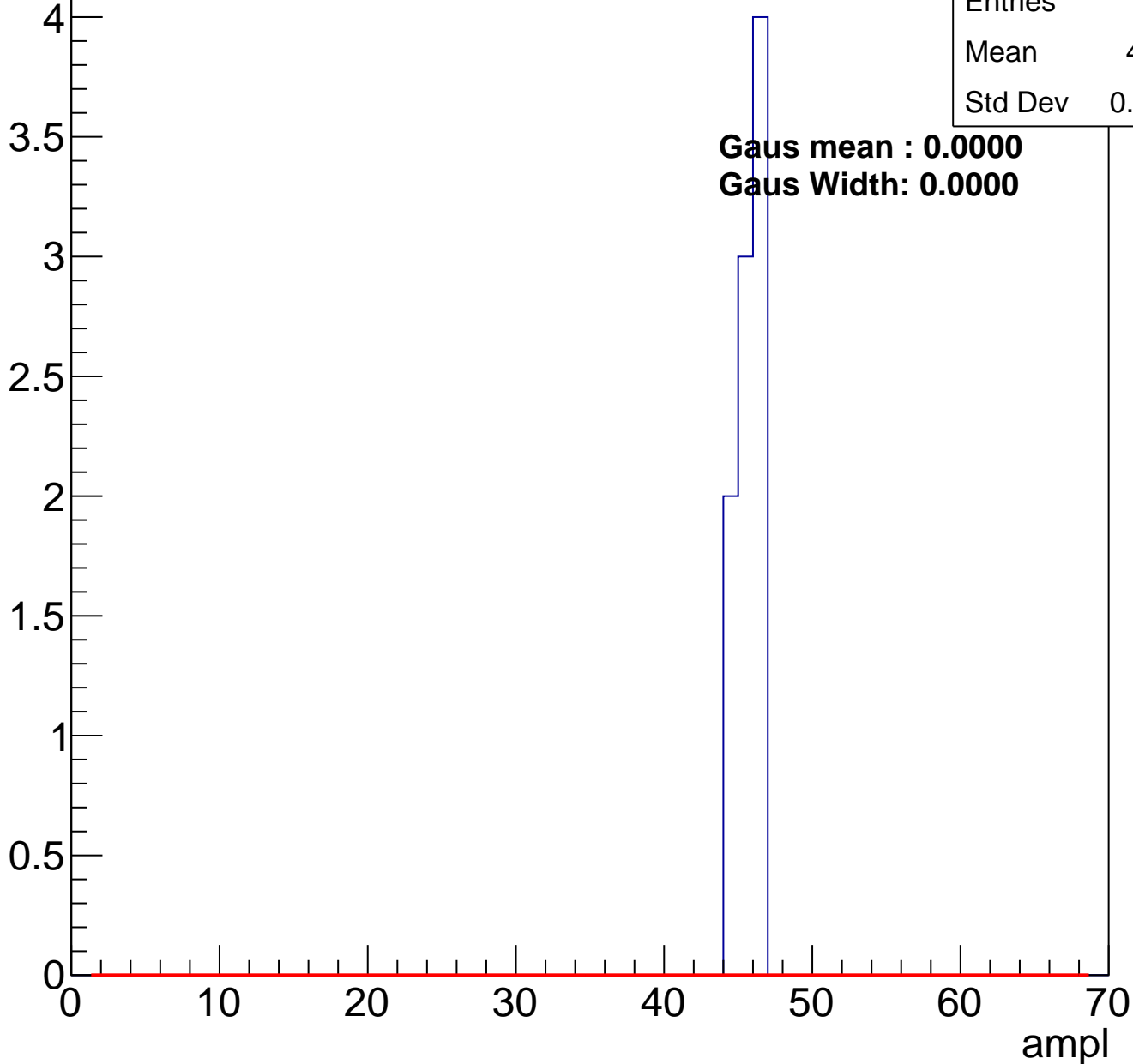
Entry

ampl

B0L100S, U5-ch15, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	47
Std Dev	2.138

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
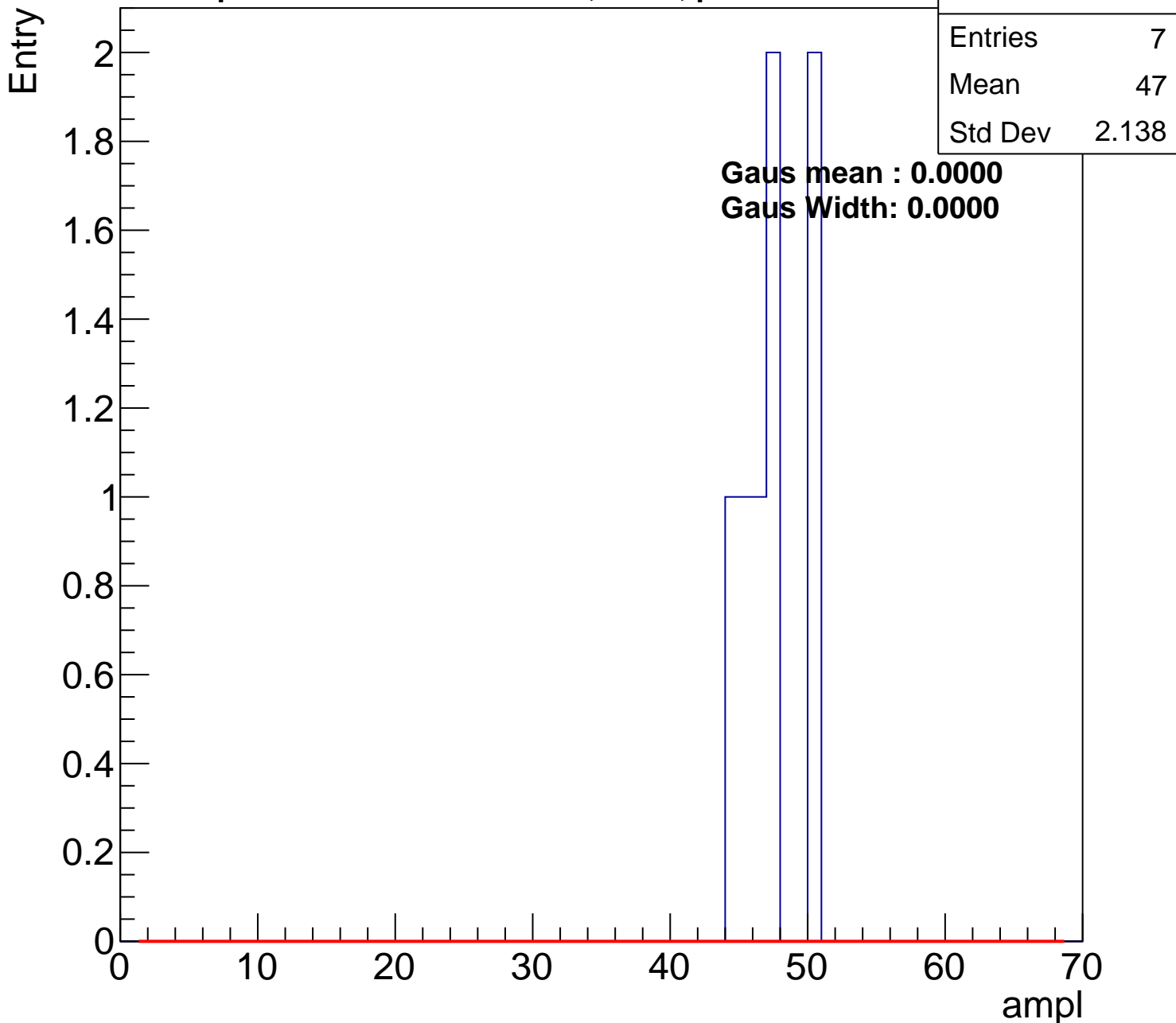
B0L100S, U5-ch15, adc3

calib_packv5_042523_0143.root, FC#6, port A1
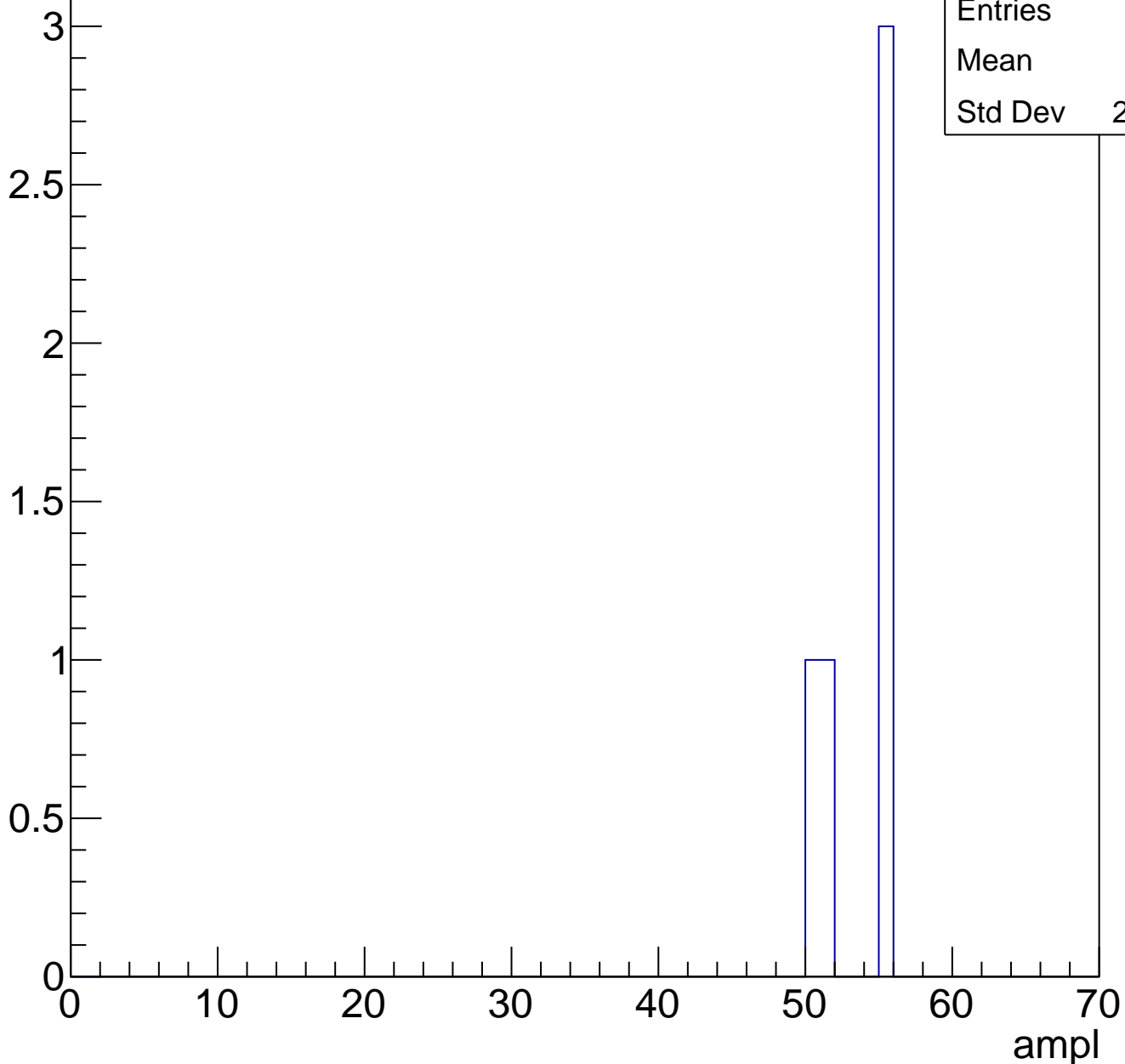
Entries	5
Mean	53.2
Std Dev	2.227

Entry

ampl

B0L100S, U5-ch15, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch15, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch15, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch15, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch16, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch16, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch16, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch16, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch16, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch16, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch16, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch16, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch17, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch17, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	45.69
Std Dev	1.828

Gaus mean : 46.2394
Gaus Width: 2.1765

Entry

ampl
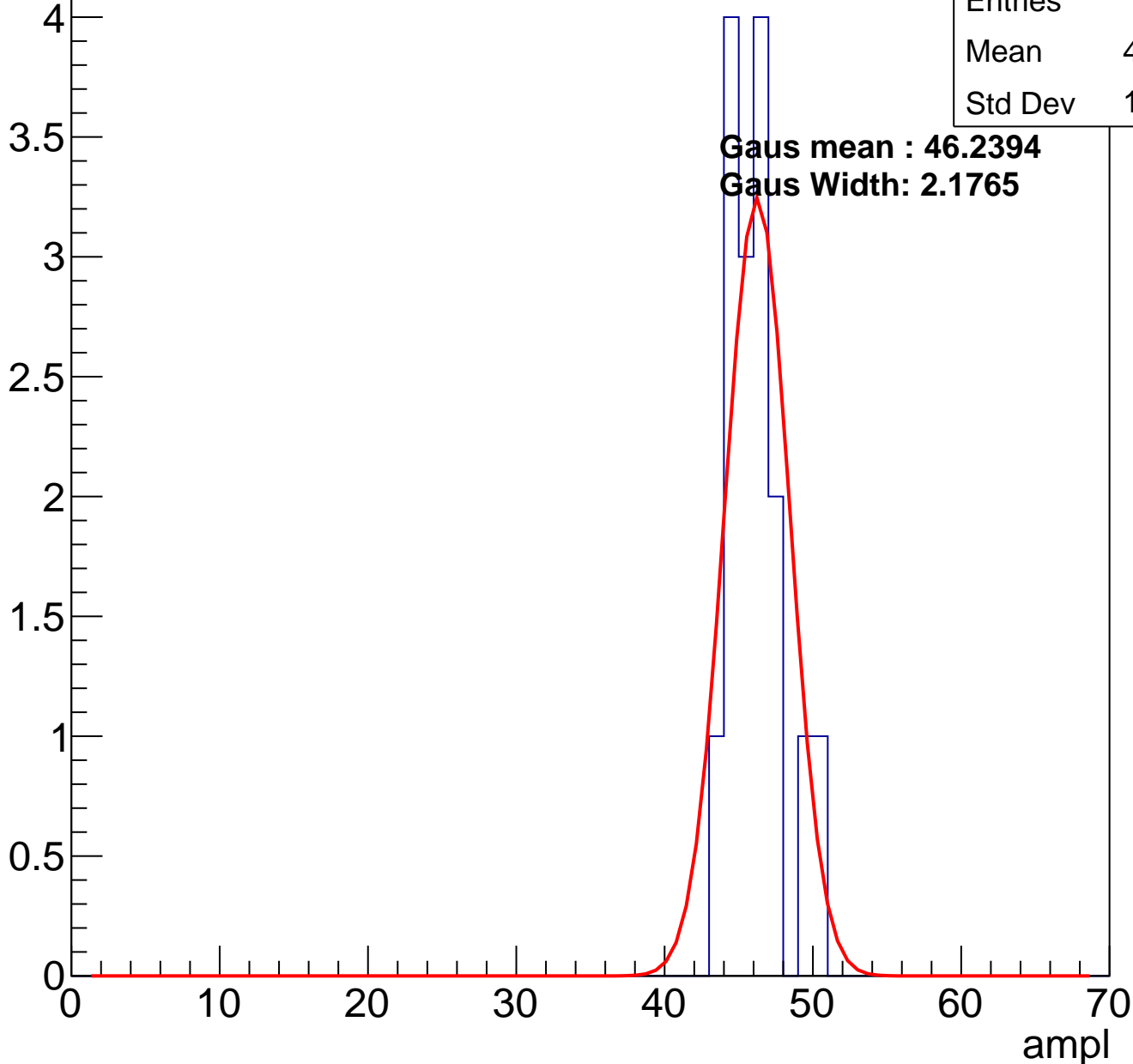
B0L100S, U5-ch17, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	23
Mean	47.61
Std Dev	2.427

Gaus mean : 84.2270
Gaus Width: 24.2689
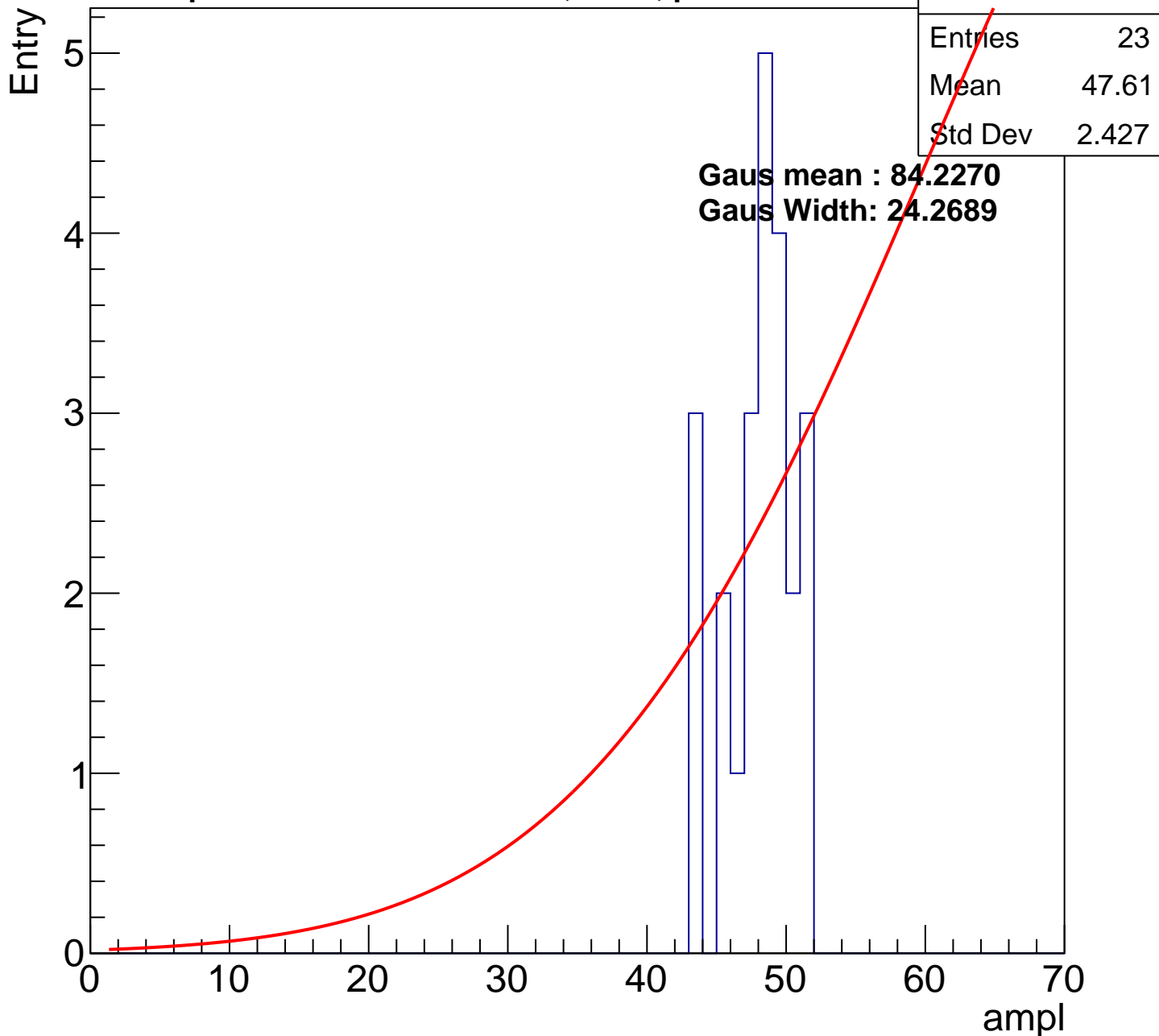
B0L100S, U5-ch17, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	51.14
Std Dev	0.833

Entry

ampl
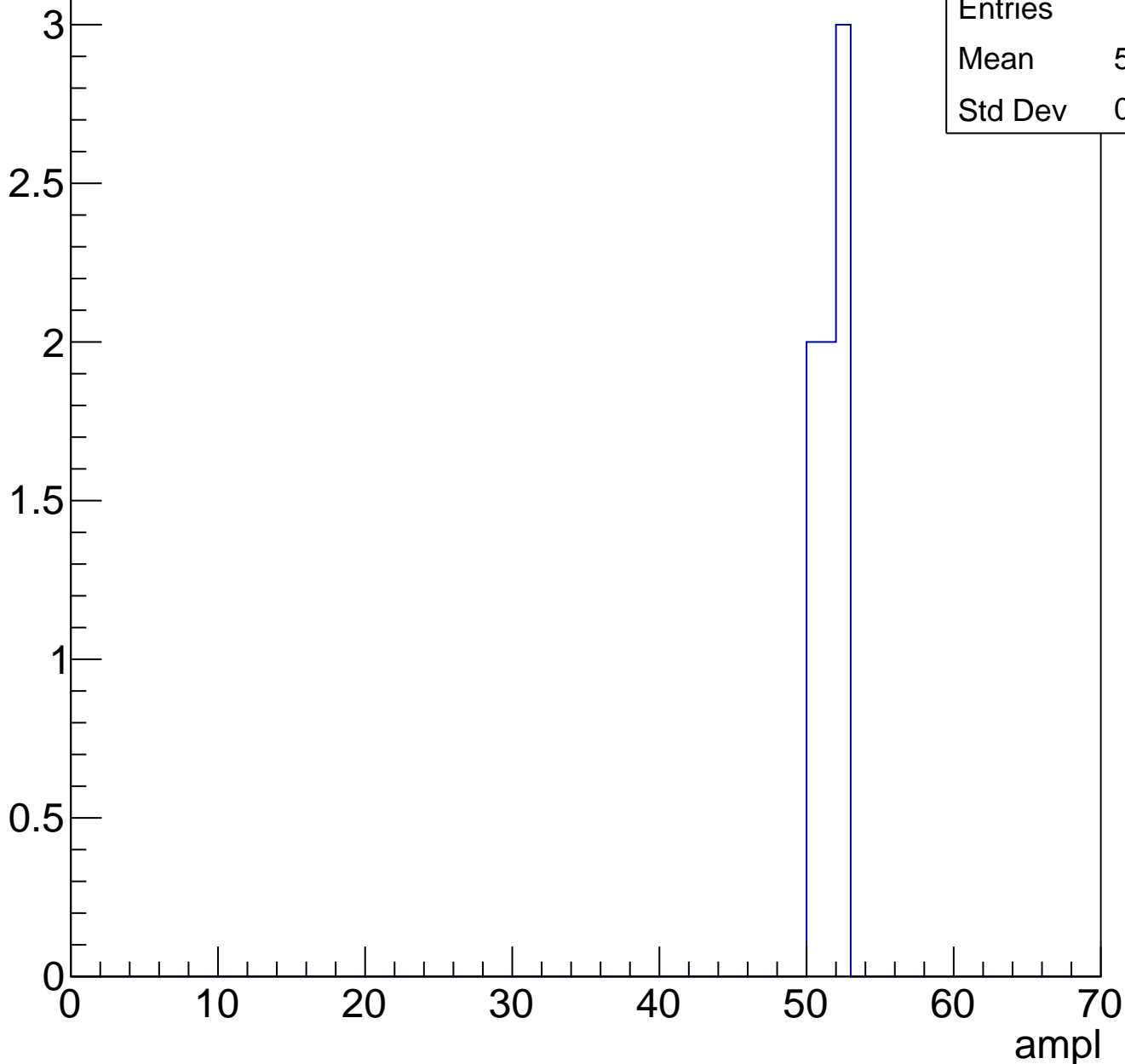
B0L100S, U5-ch17, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch17, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch17, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch17, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch18, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	41
Std Dev	0
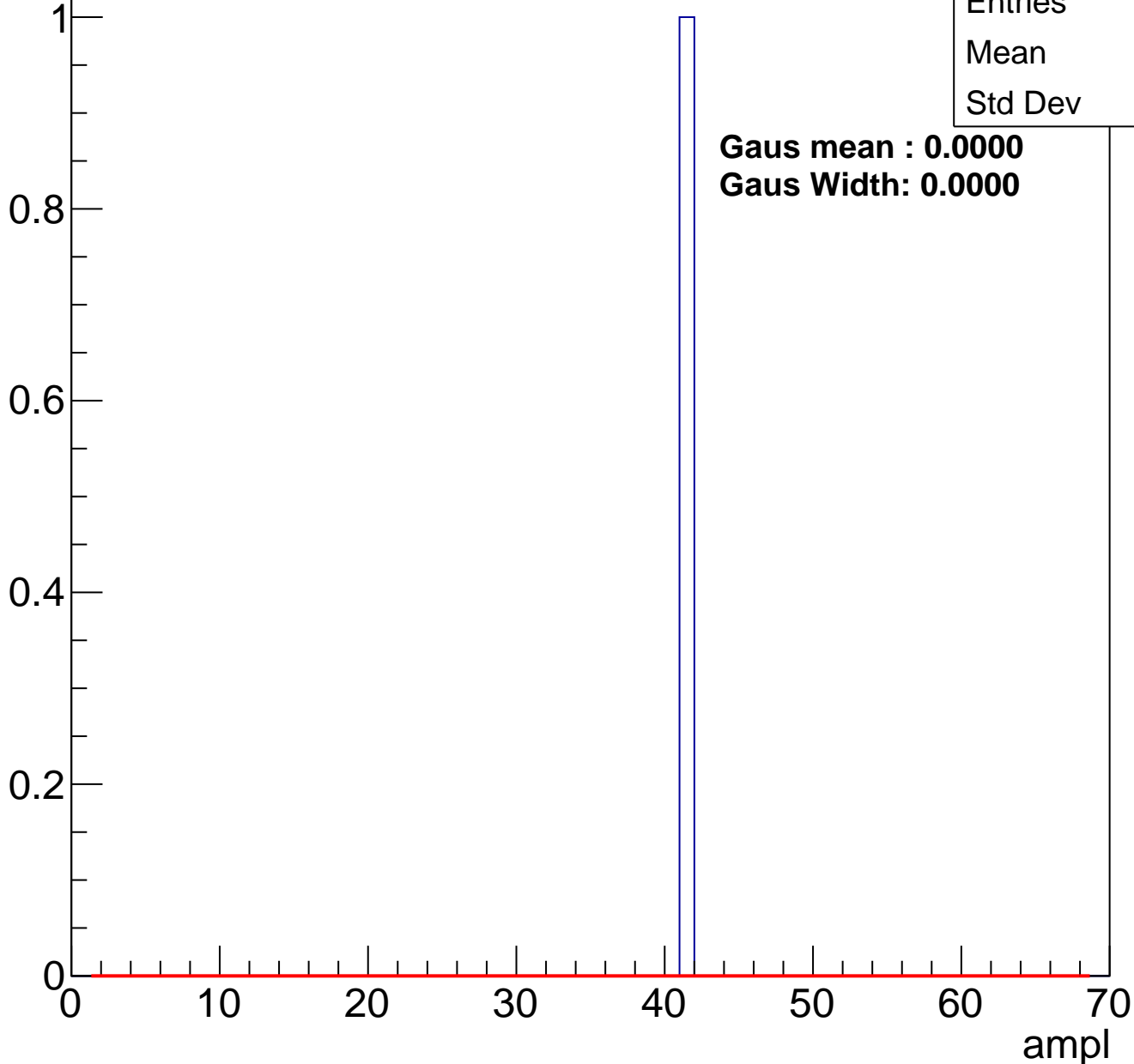
ampl

B0L100S, U5-ch18, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	44.29
Std Dev	3.057

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
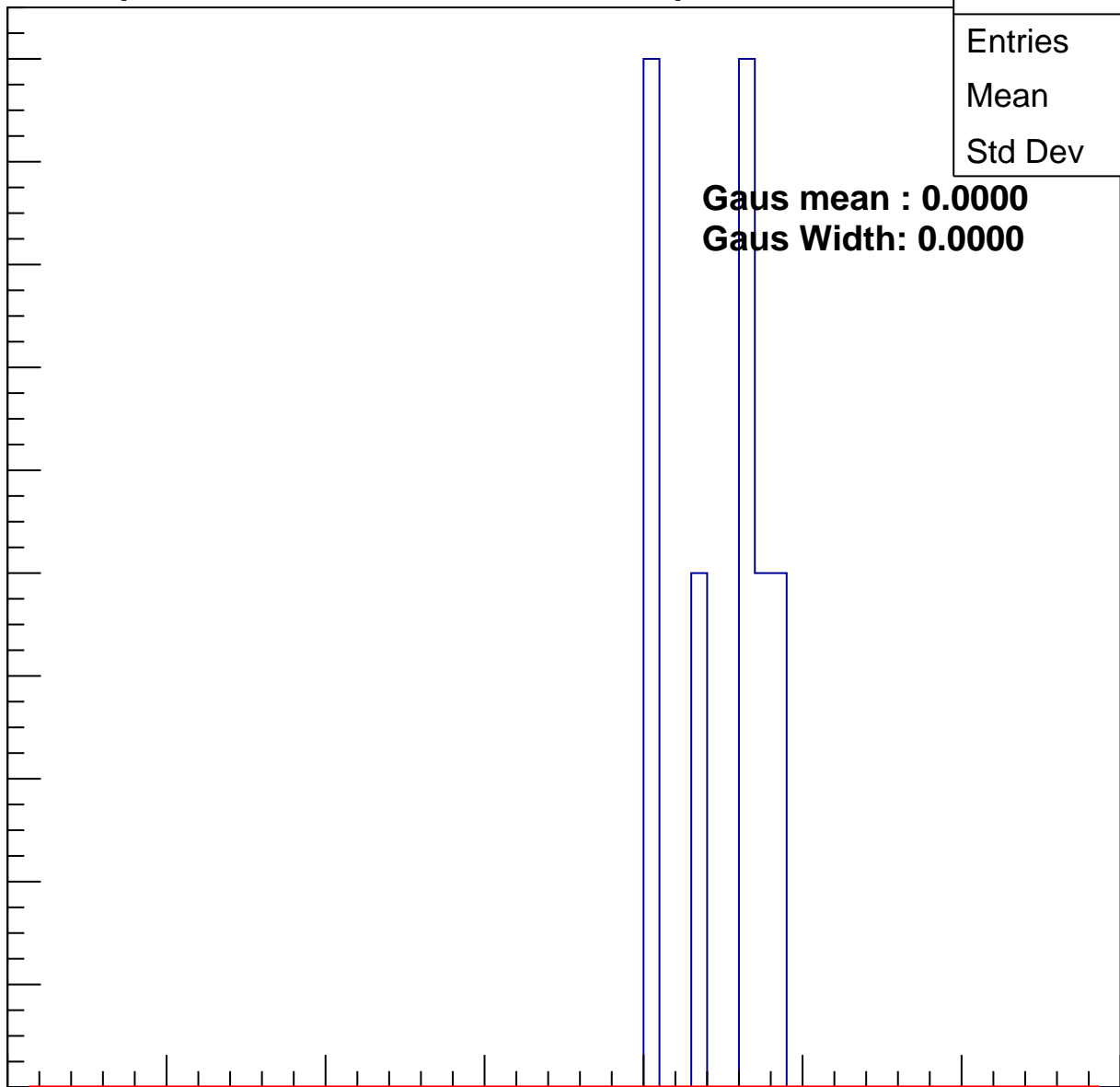
B0L100S, U5-ch18, adc2

calib_packv5_042523_0143.root, FC#6, port A1
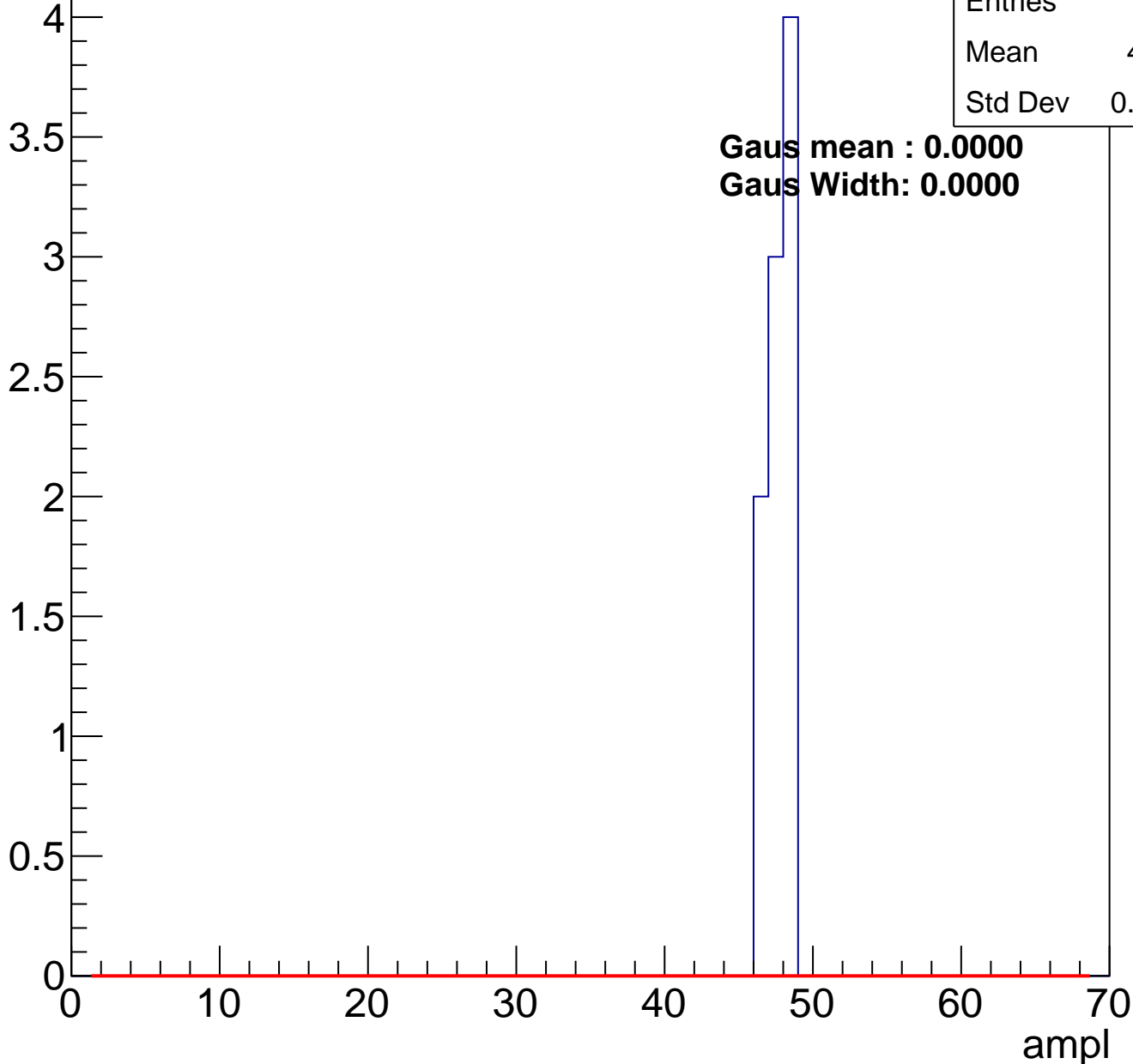
Entries	9
Mean	47.22
Std Dev	0.7857

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch18, adc3

calib_packv5_042523_0143.root, FC#6, port A1

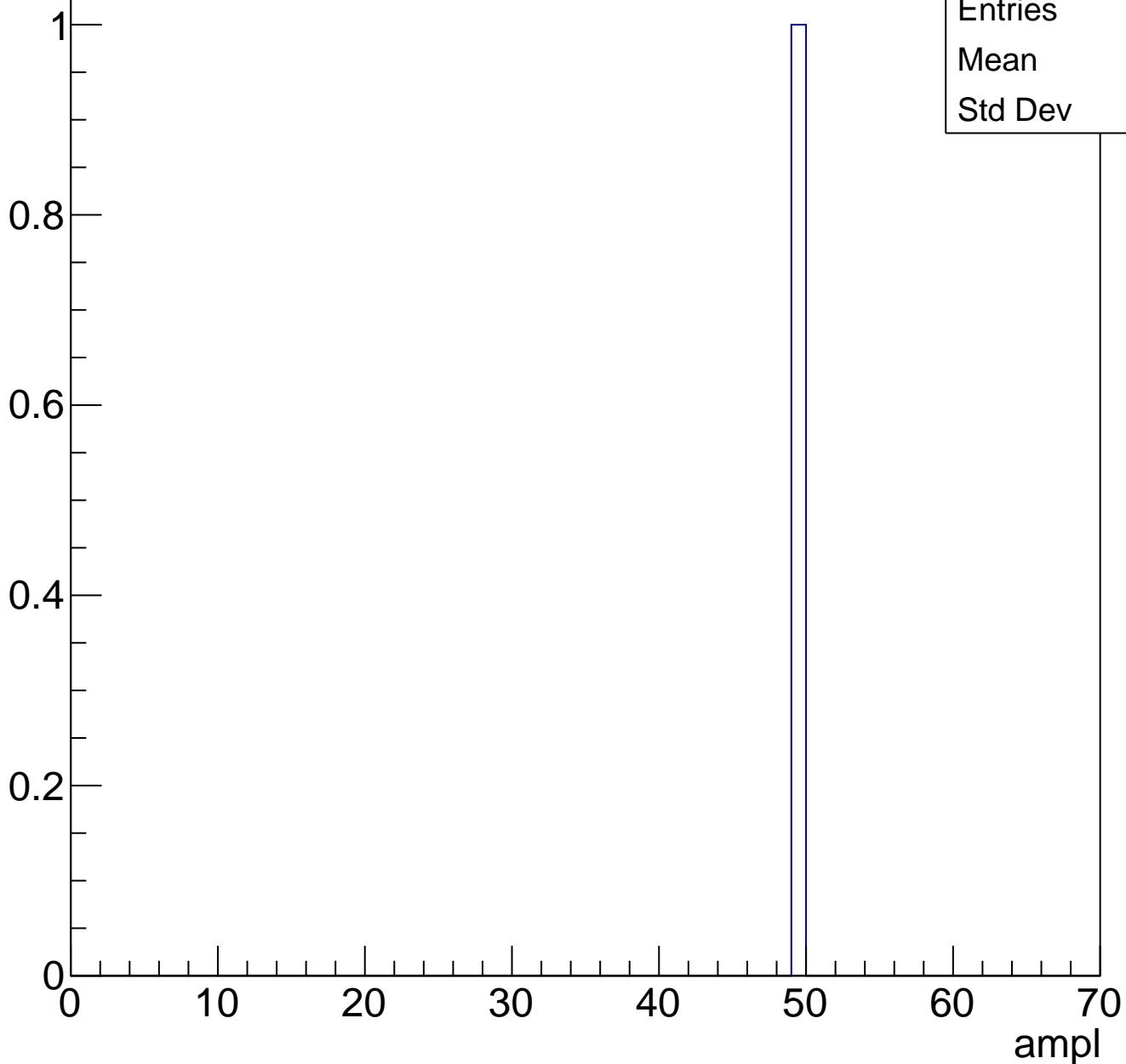|	
Entries	1
Mean	49
Std Dev	0

ampl

B0L100S, U5-ch18, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch18, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch18, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch18, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch19, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch19, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch19, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
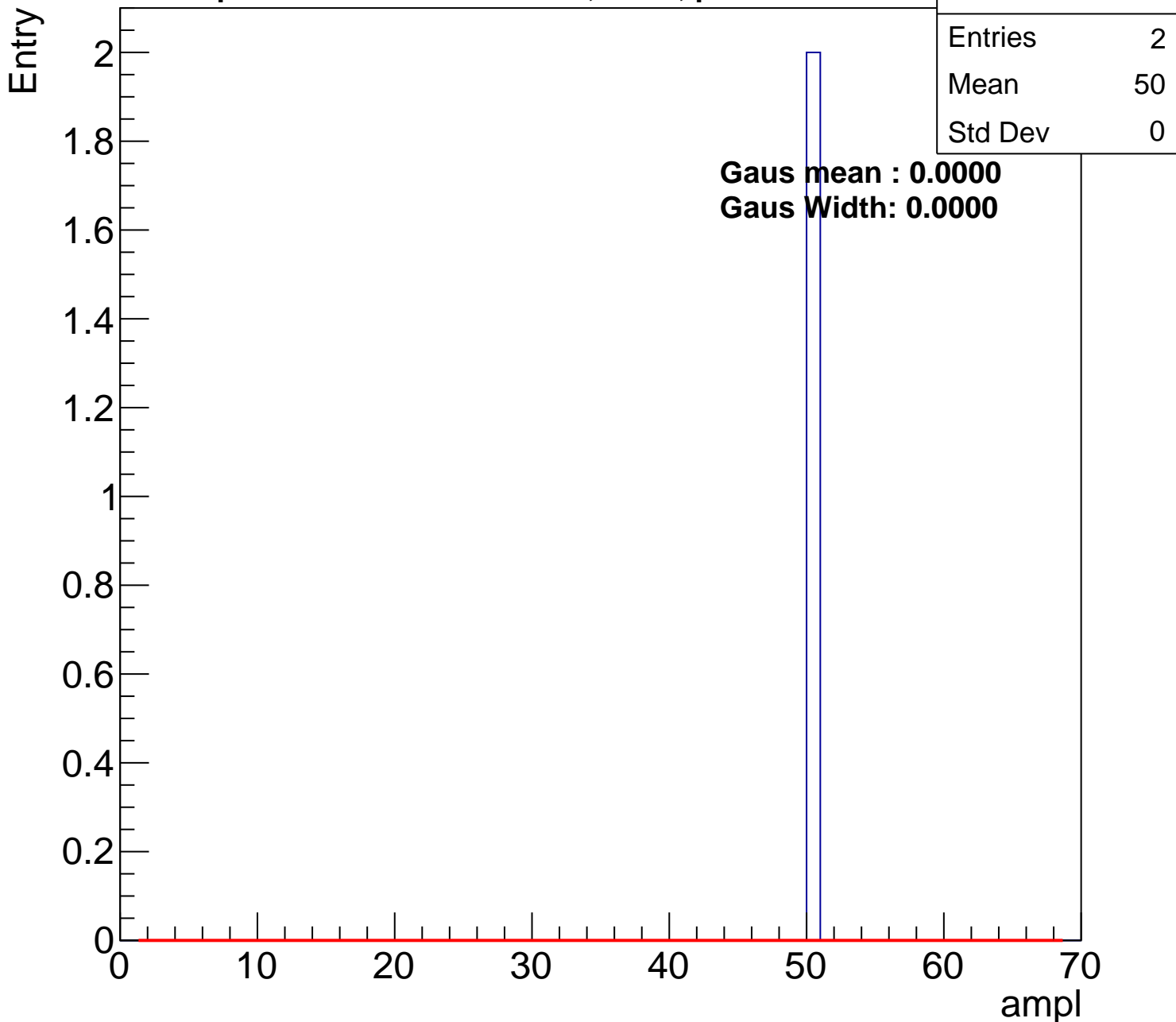
B0L100S, U5-ch19, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch19, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch19, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch19, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch19, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch20, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch20, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	48
Std Dev	1.414

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
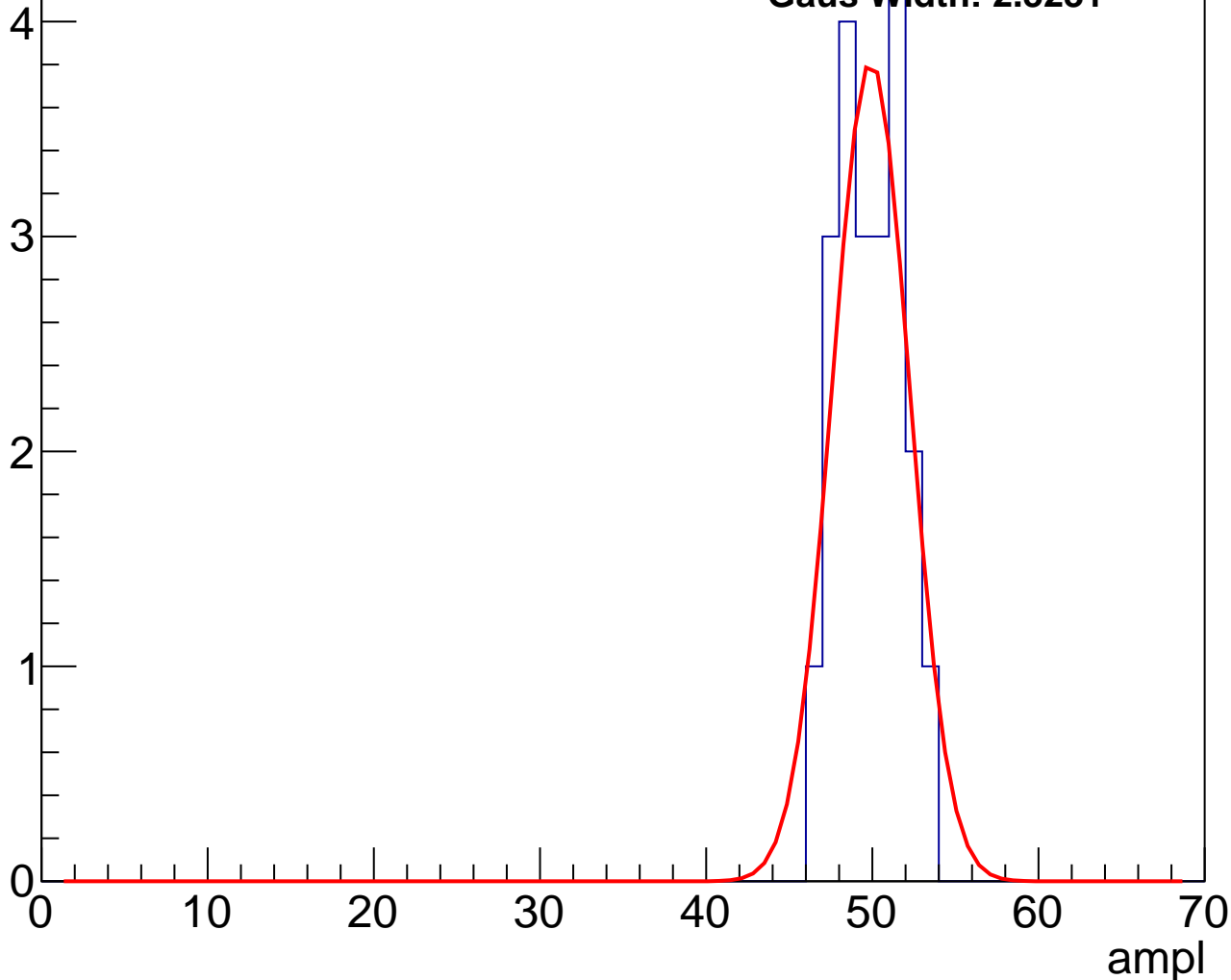
B0L100S, U5-ch20, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50.75
Std Dev	1.92
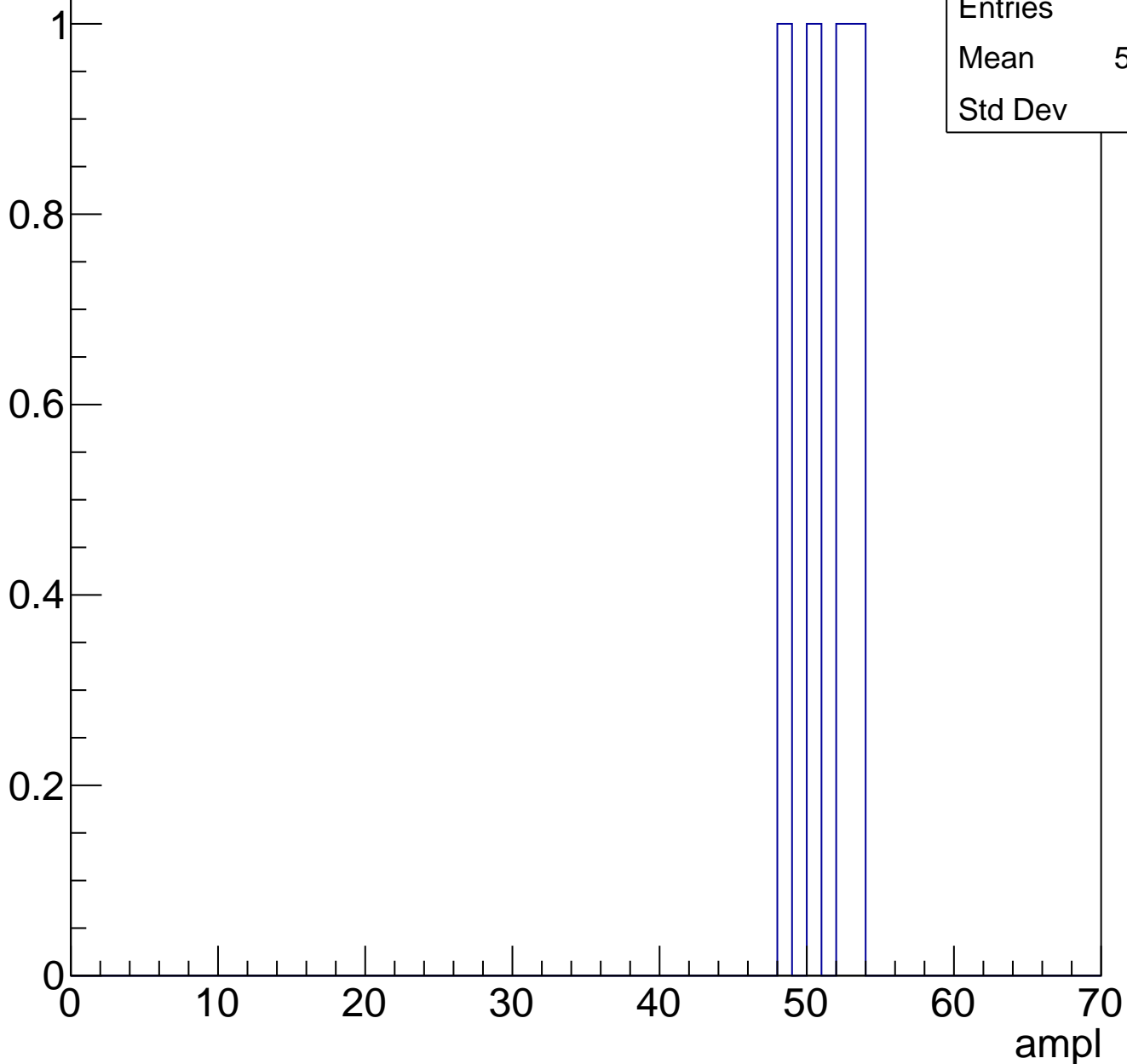
B0L100S, U5-ch20, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch20, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch20, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch20, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch21, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch21, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch21, adc2

calib_packv5_042523_0143.root, FC#6, port A1
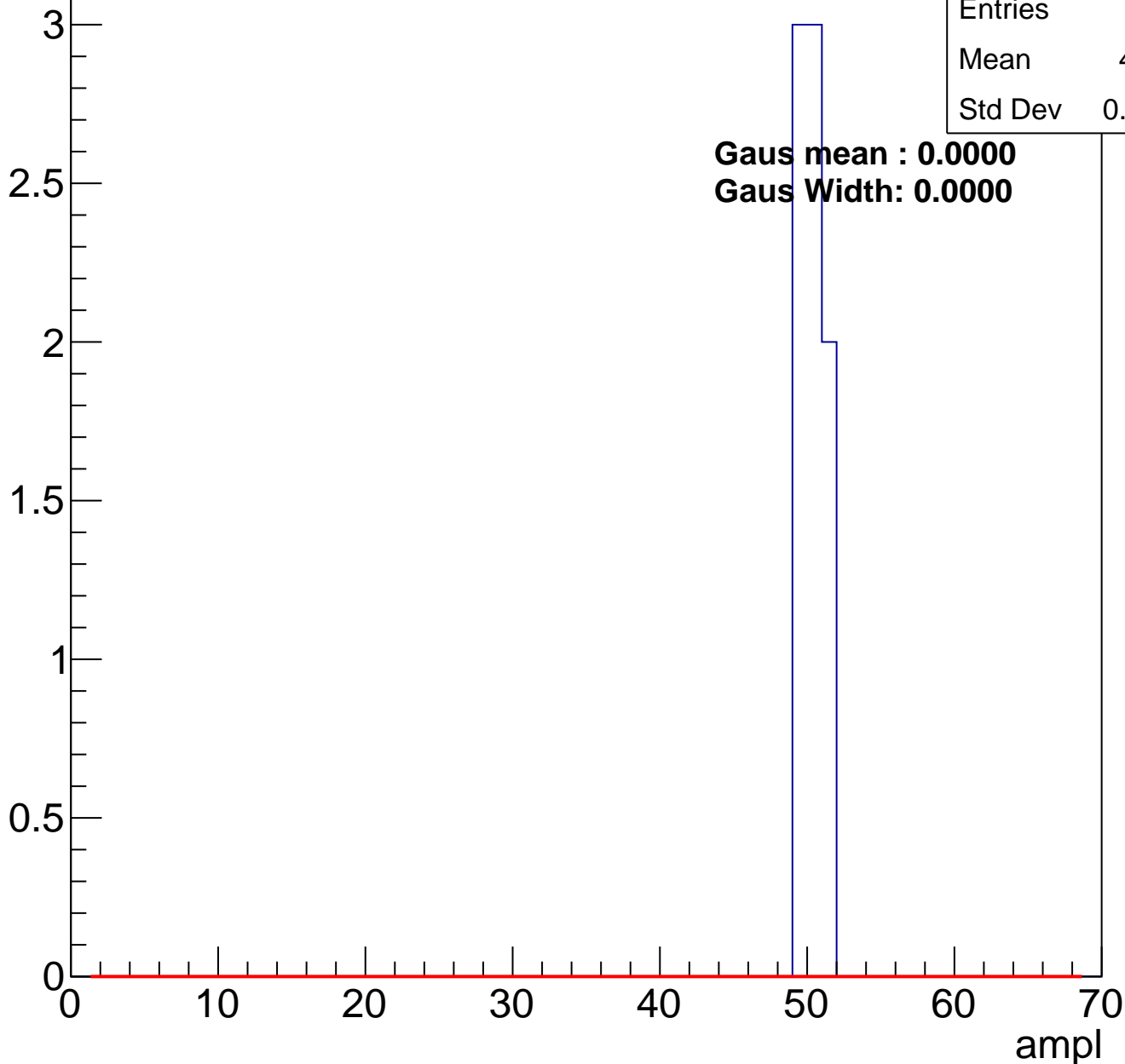
Entries	8
Mean	49.88
Std Dev	0.7806

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch21, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50.33
Std Dev	0.4714

Entry

ampl
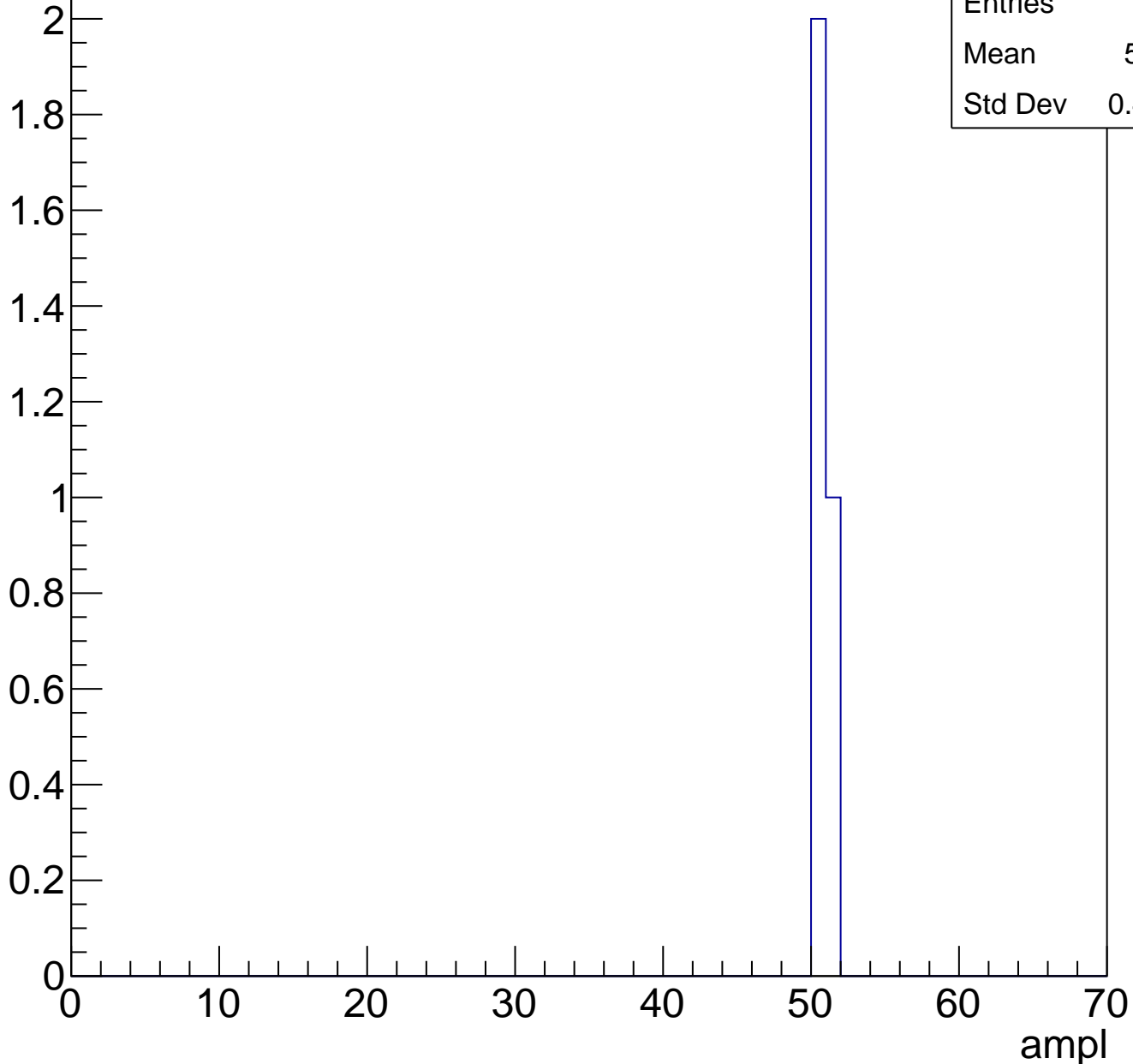
B0L100S, U5-ch21, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch21, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch21, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch21, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch22, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	4
Mean	43
Std Dev	1

ampl

B0L100S, U5-ch22, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	21
Mean	43.67
Std Dev	1.834

Gaus mean : 43.4624
Gaus Width: 3.2899

Entry

ampl
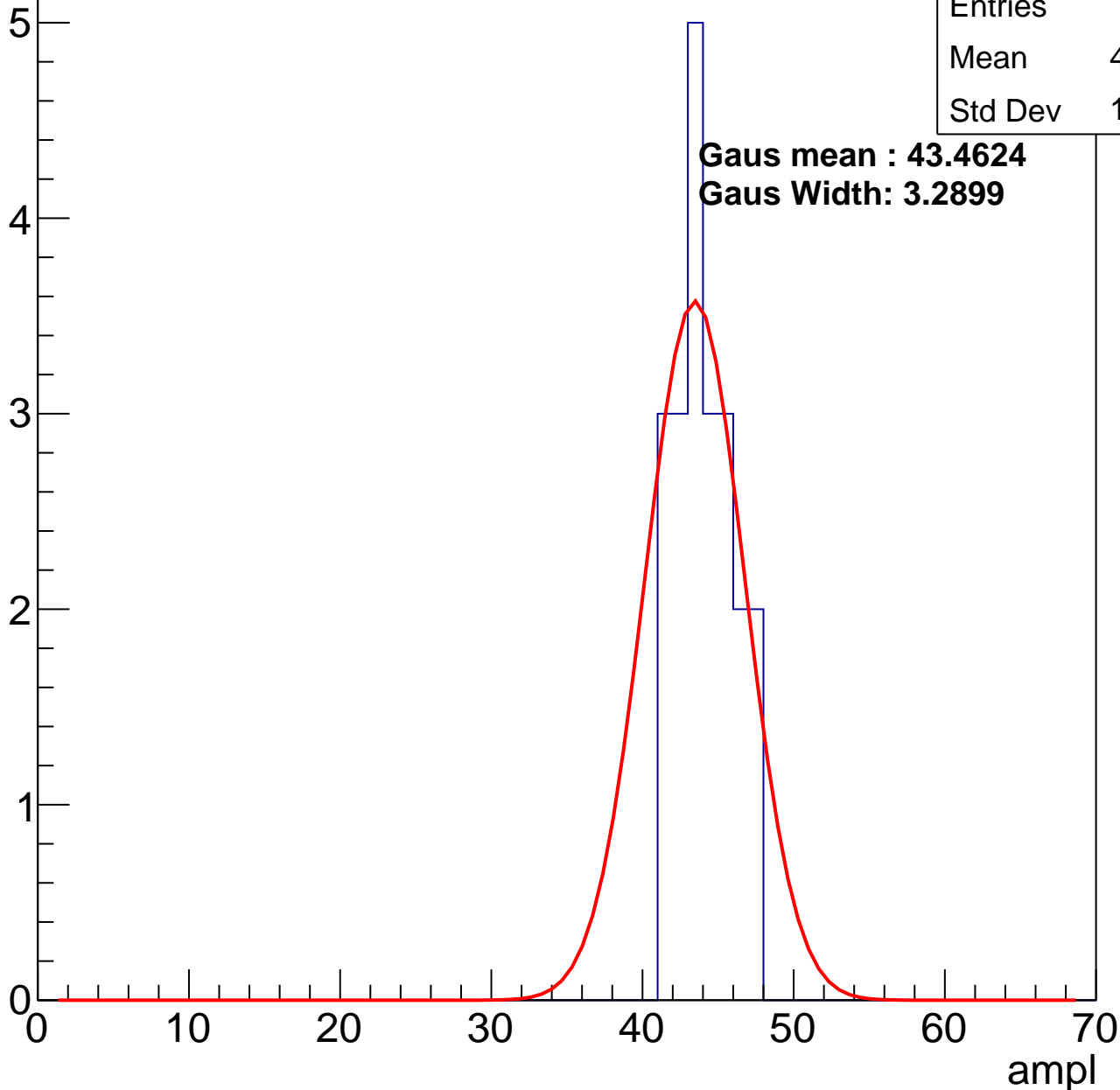
B0L100S, U5-ch22, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	47.14
Std Dev	1.884

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
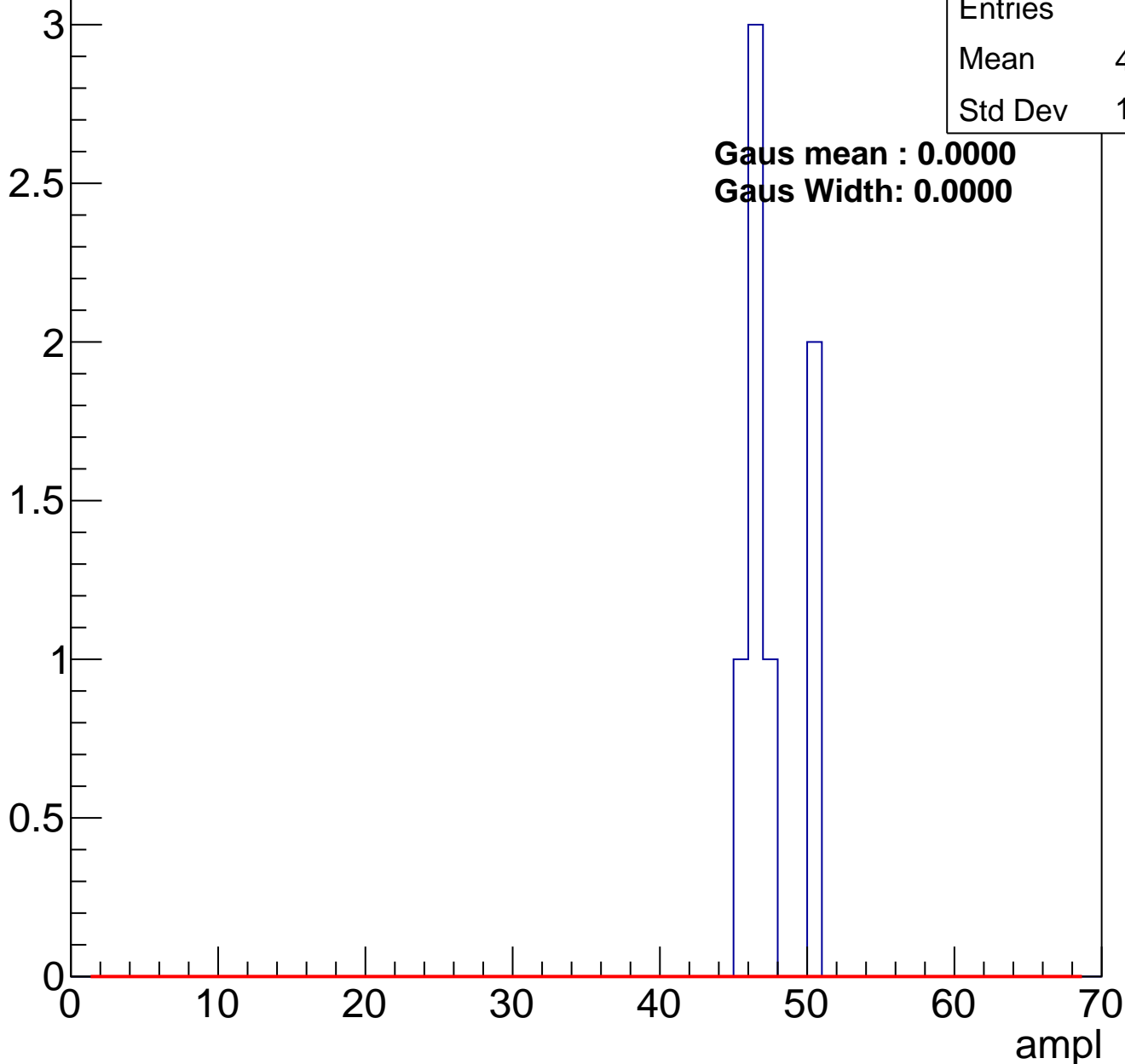
B0L100S, U5-ch22, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	45
Std Dev	0

Entry

ampl
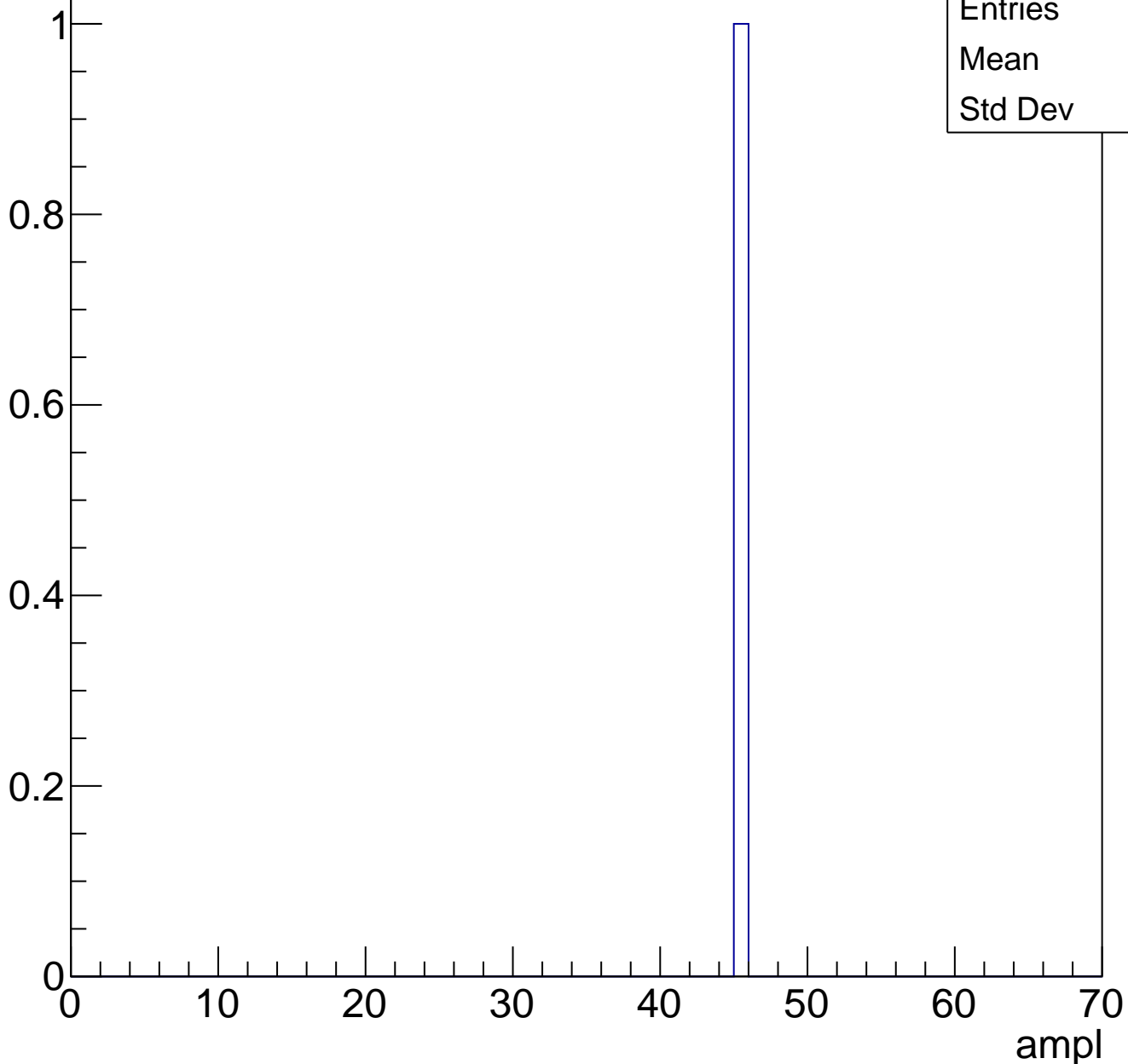
B0L100S, U5-ch22, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch22, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch22, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch22, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch23, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch23, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	45
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
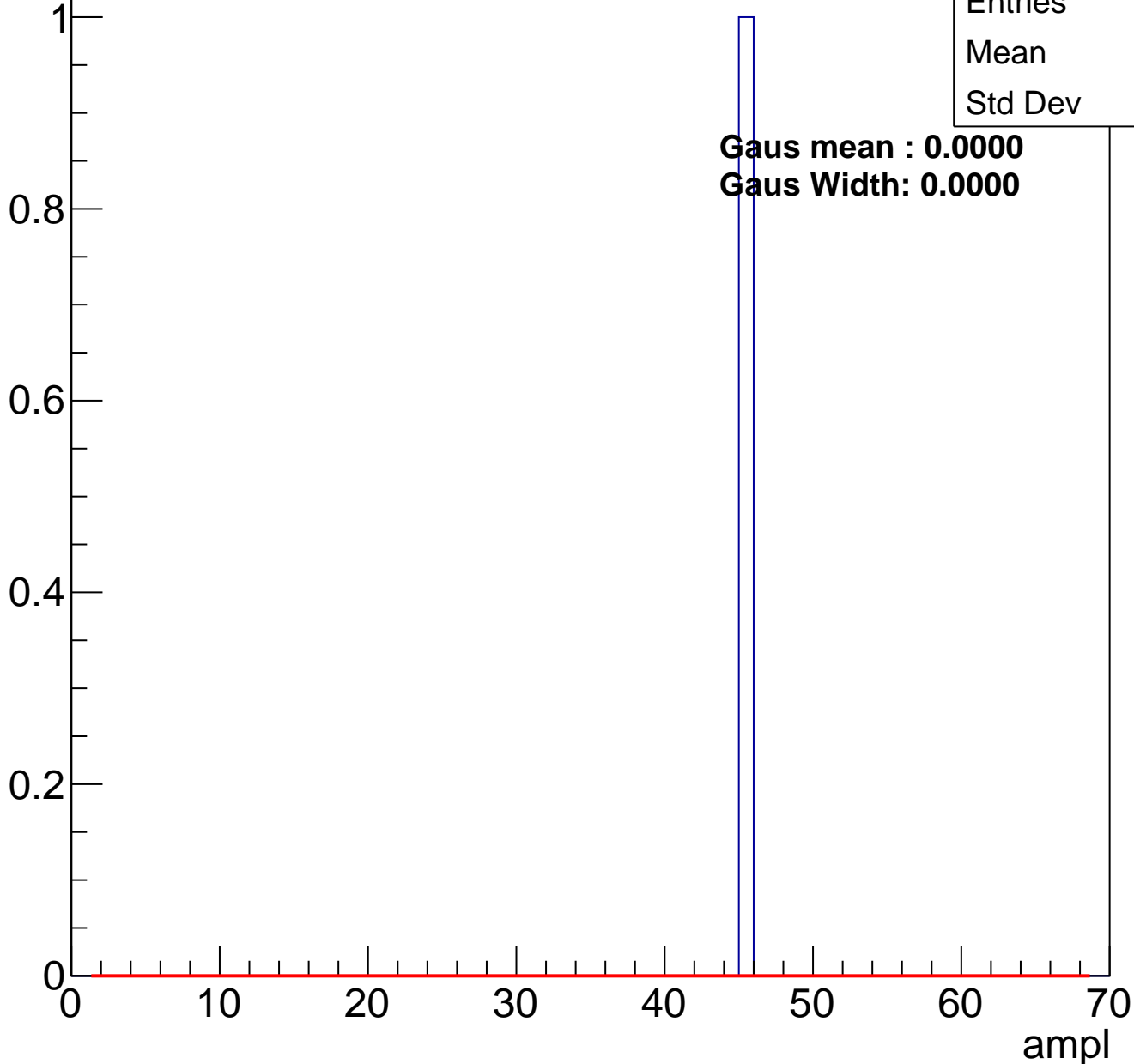
B0L100S, U5-ch23, adc2

calib_packv5_042523_0143.root, FC#6, port A1
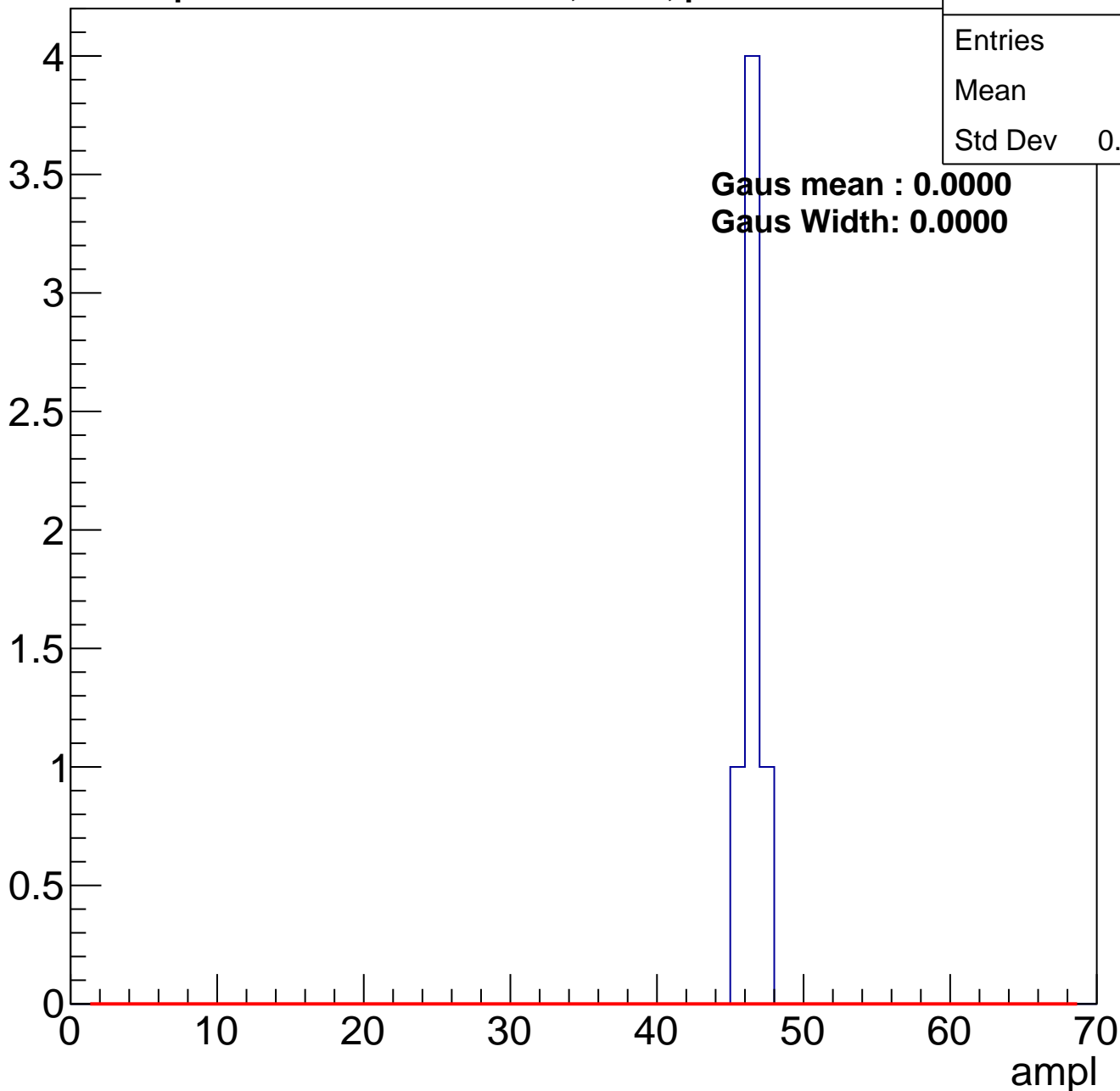
Entries	6
Mean	46
Std Dev	0.5774

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch23, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch23, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch23, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch23, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch23, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch24, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch24, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	44.64
Std Dev	2.057

Gaus mean : 44.4413
Gaus Width: 3.4246
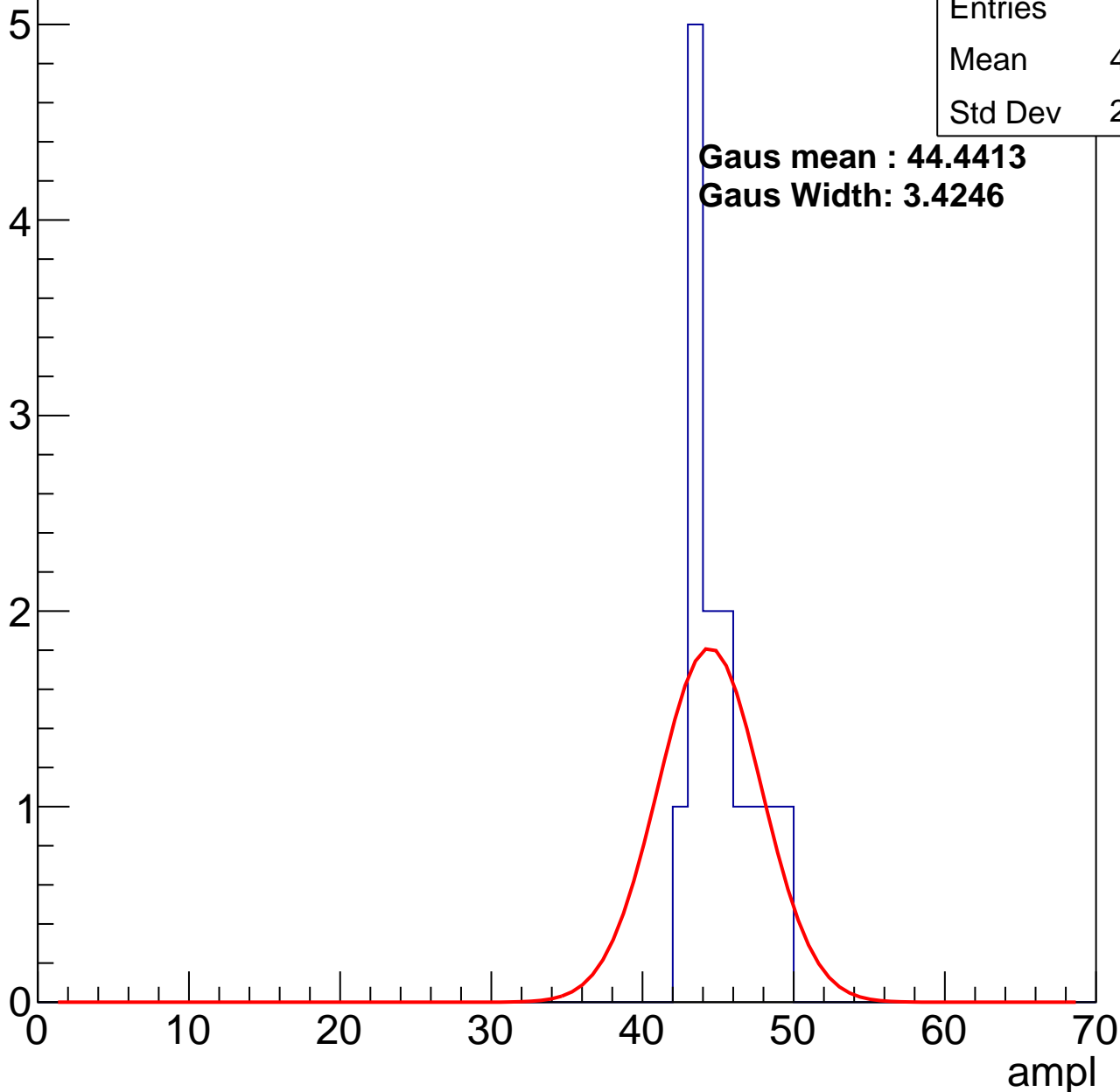
B0L100S, U5-ch24, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	20
Mean	45.65
Std Dev	2.555

Gaus mean : 45.3832
Gaus Width: 4.0973

Entry

ampl
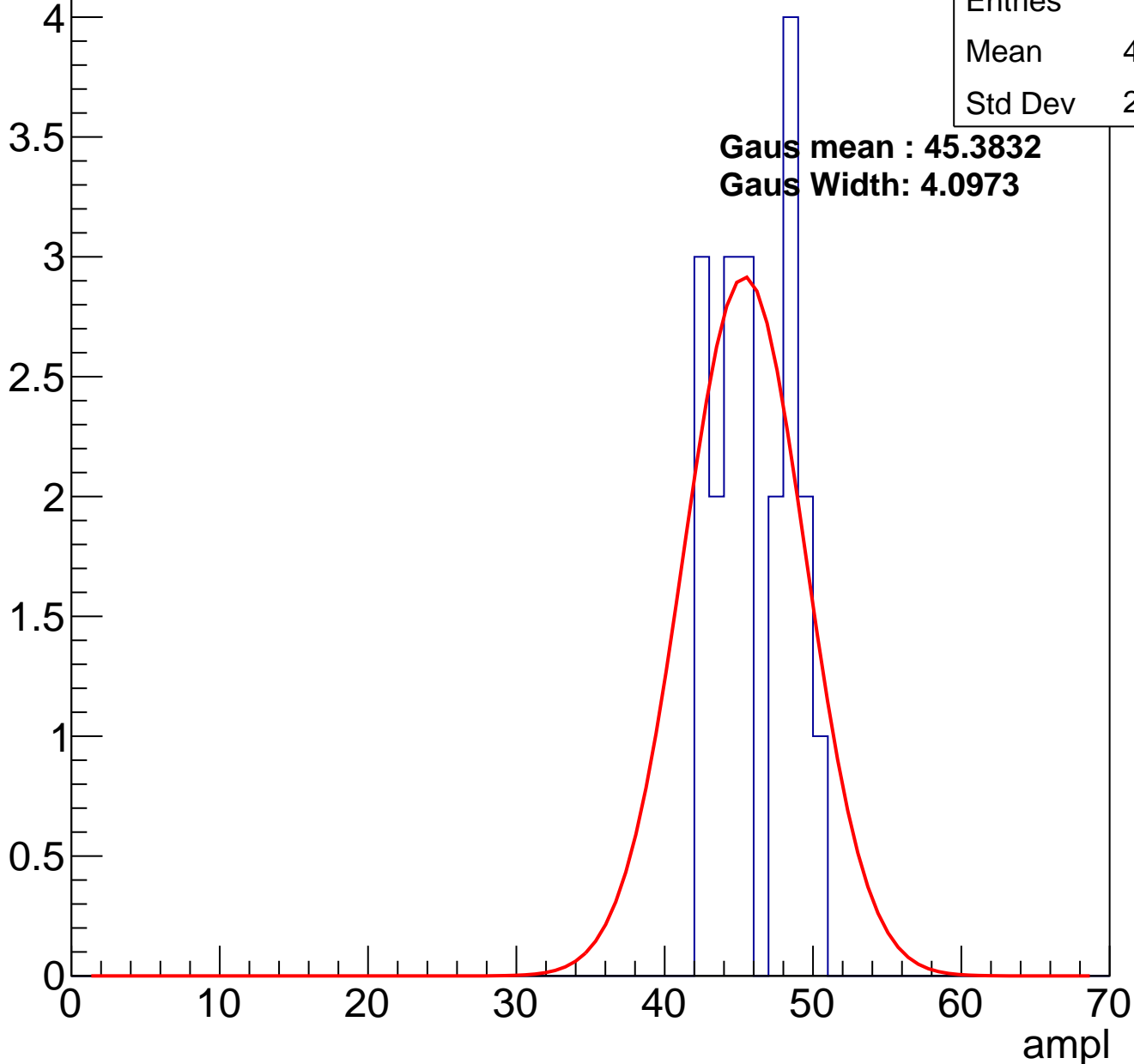
B0L100S, U5-ch24, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	48
Std Dev	1.414
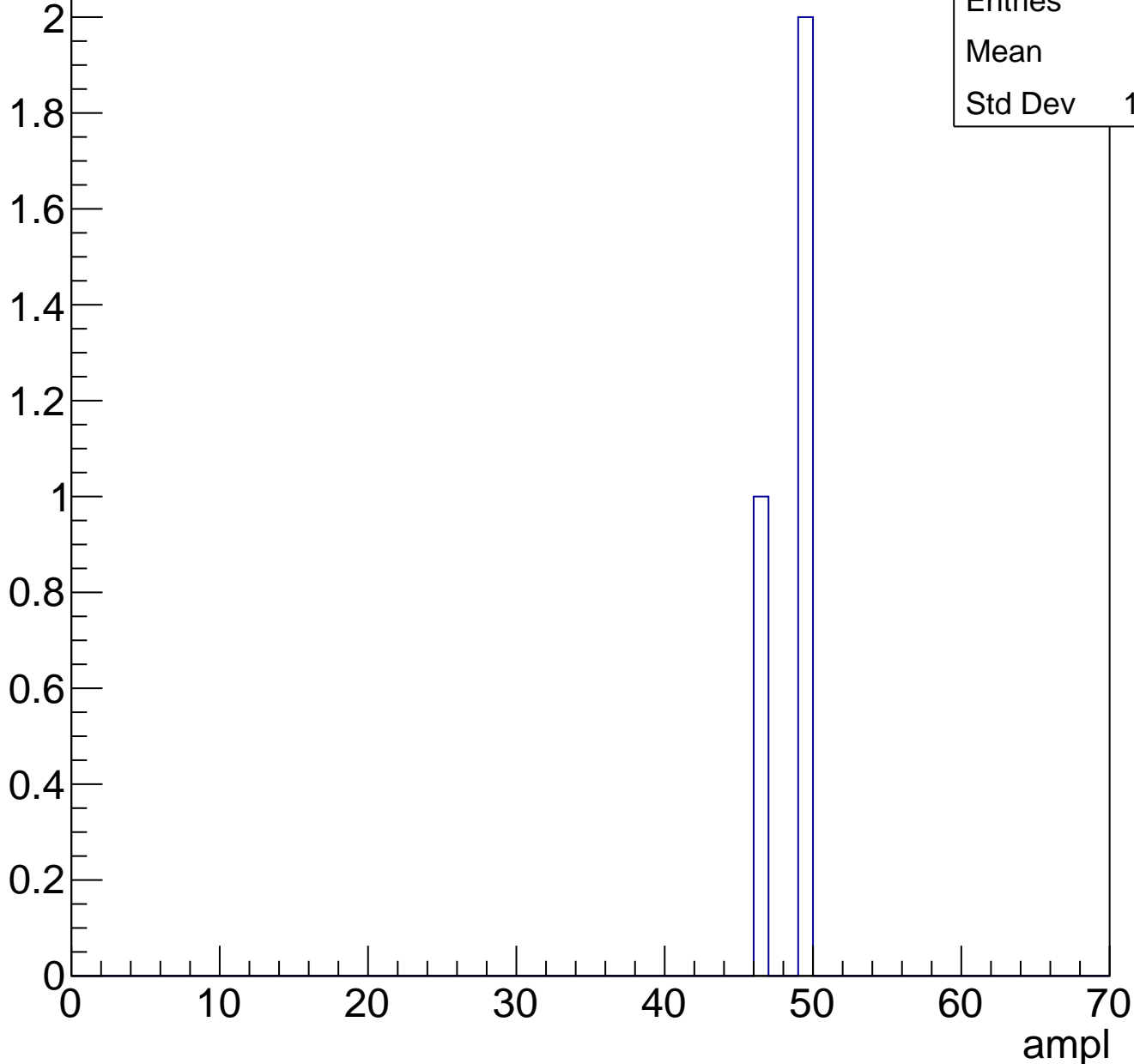
B0L100S, U5-ch24, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch24, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch24, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch24, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch25, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch25, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch25, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch25, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch25, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch25, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch25, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch25, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch26, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch26, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch26, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch26, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch26, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch26, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch26, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch26, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch27, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch27, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	46
Std Dev	0.8165

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
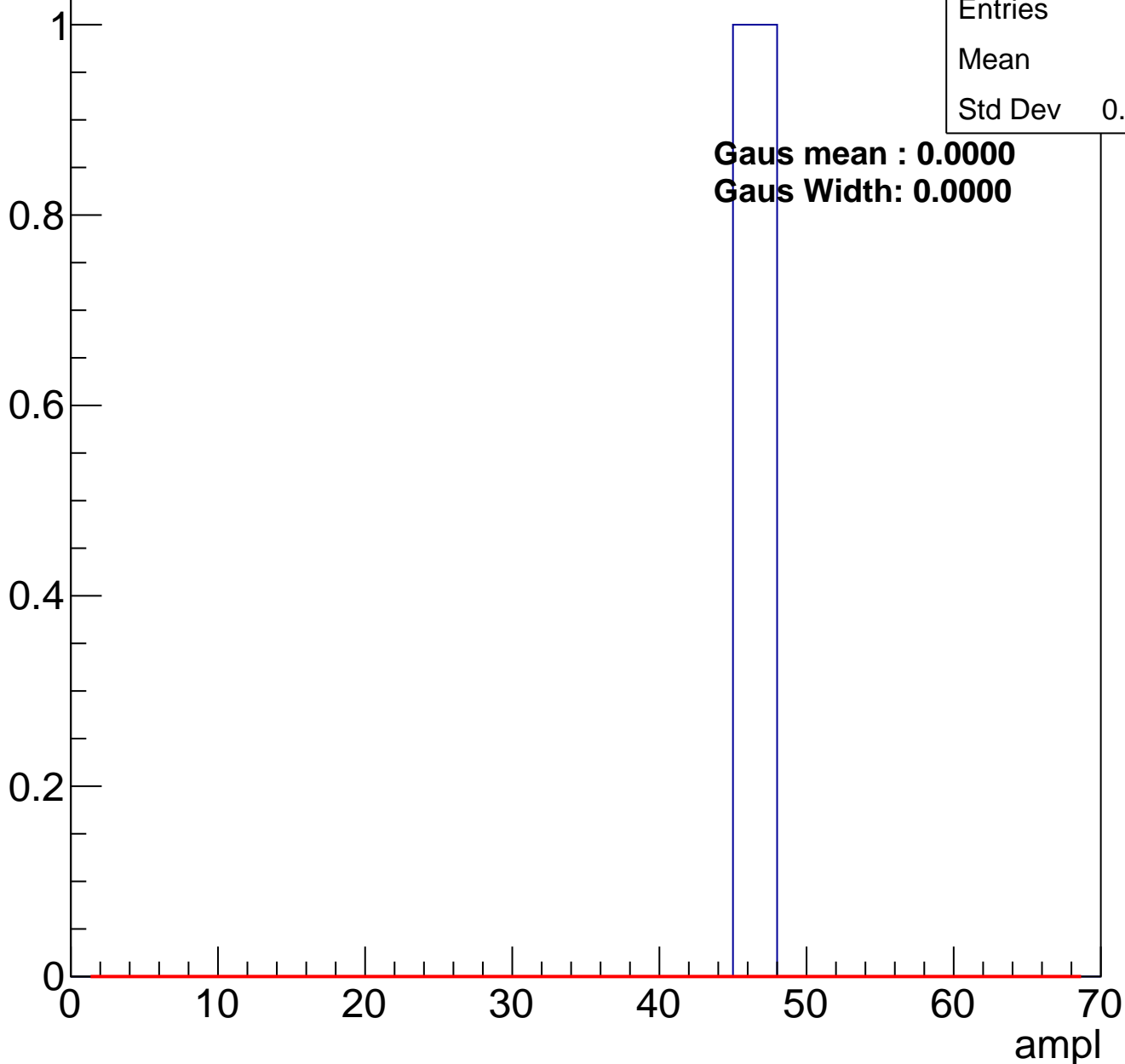
B0L100S, U5-ch27, adc2

calib_packv5_042523_0143.root, FC#6, port A1
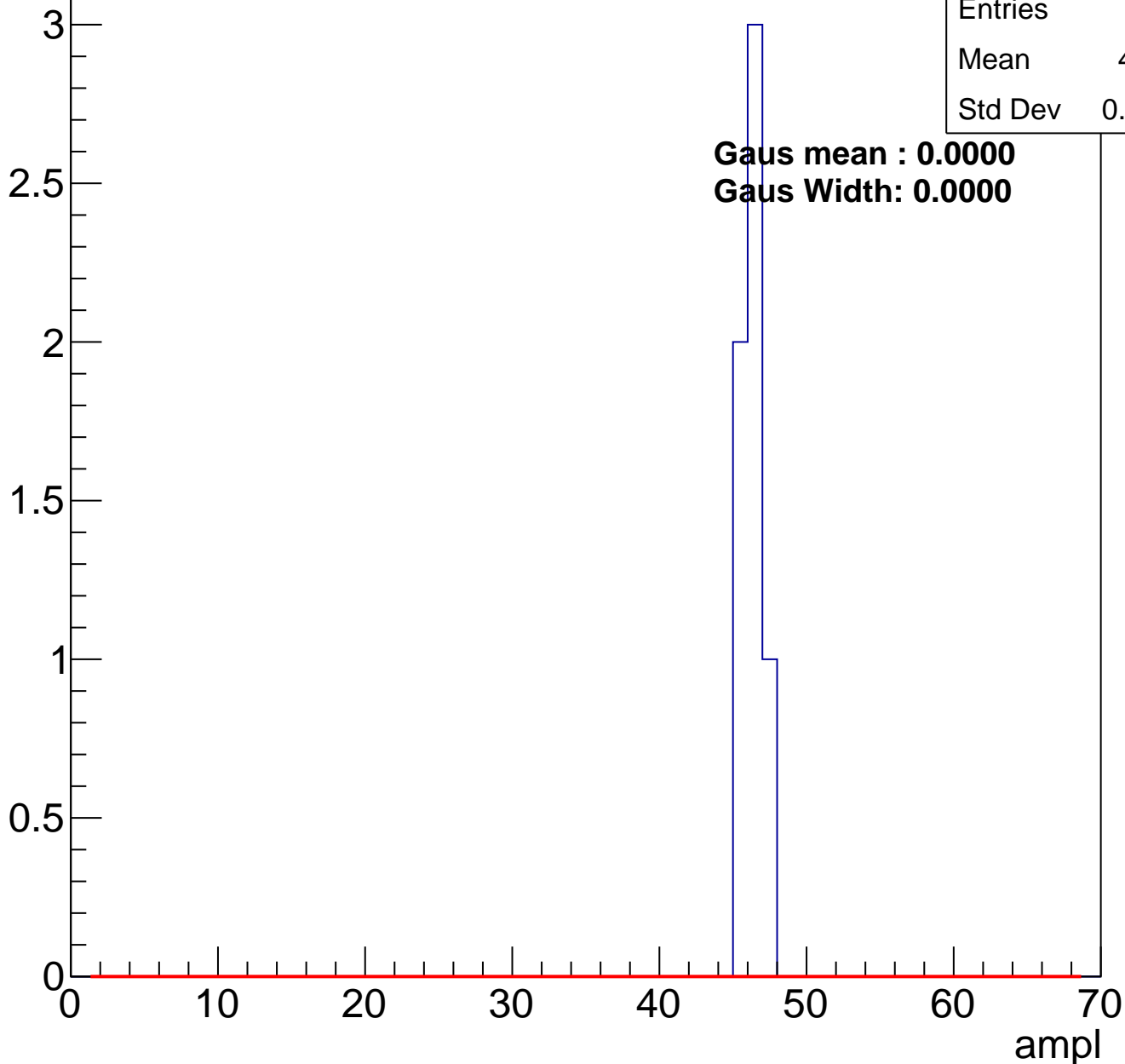
Entries	6
Mean	45.83
Std Dev	0.6872

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch27, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch27, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch27, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch27, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch27, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch28, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch28, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	48.14
Std Dev	2.231

Gaus mean : 0.0000
Gaus Width: 0.0000
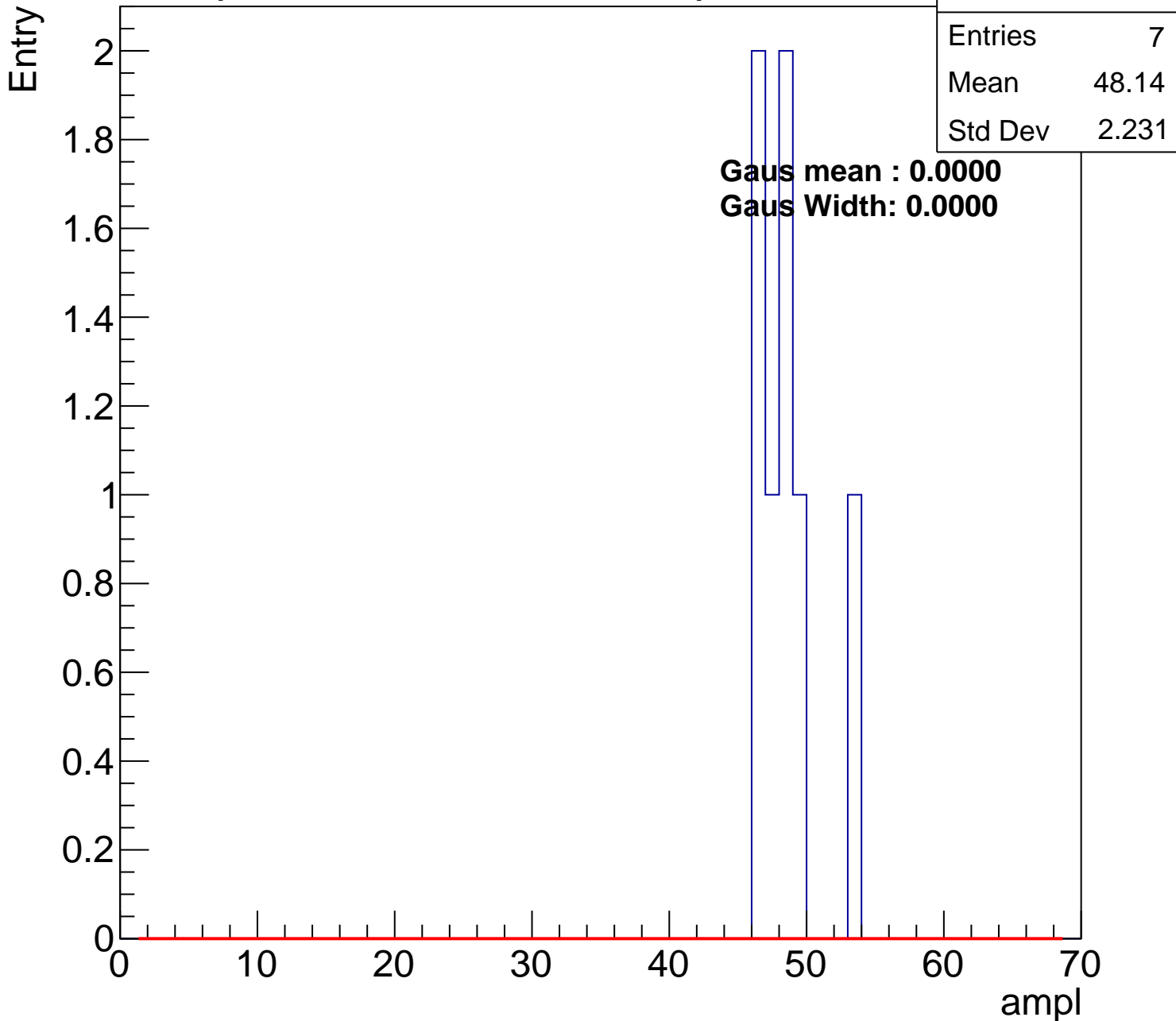
B0L100S, U5-ch28, adc2

calib_packv5_042523_0143.root, FC#6, port A1
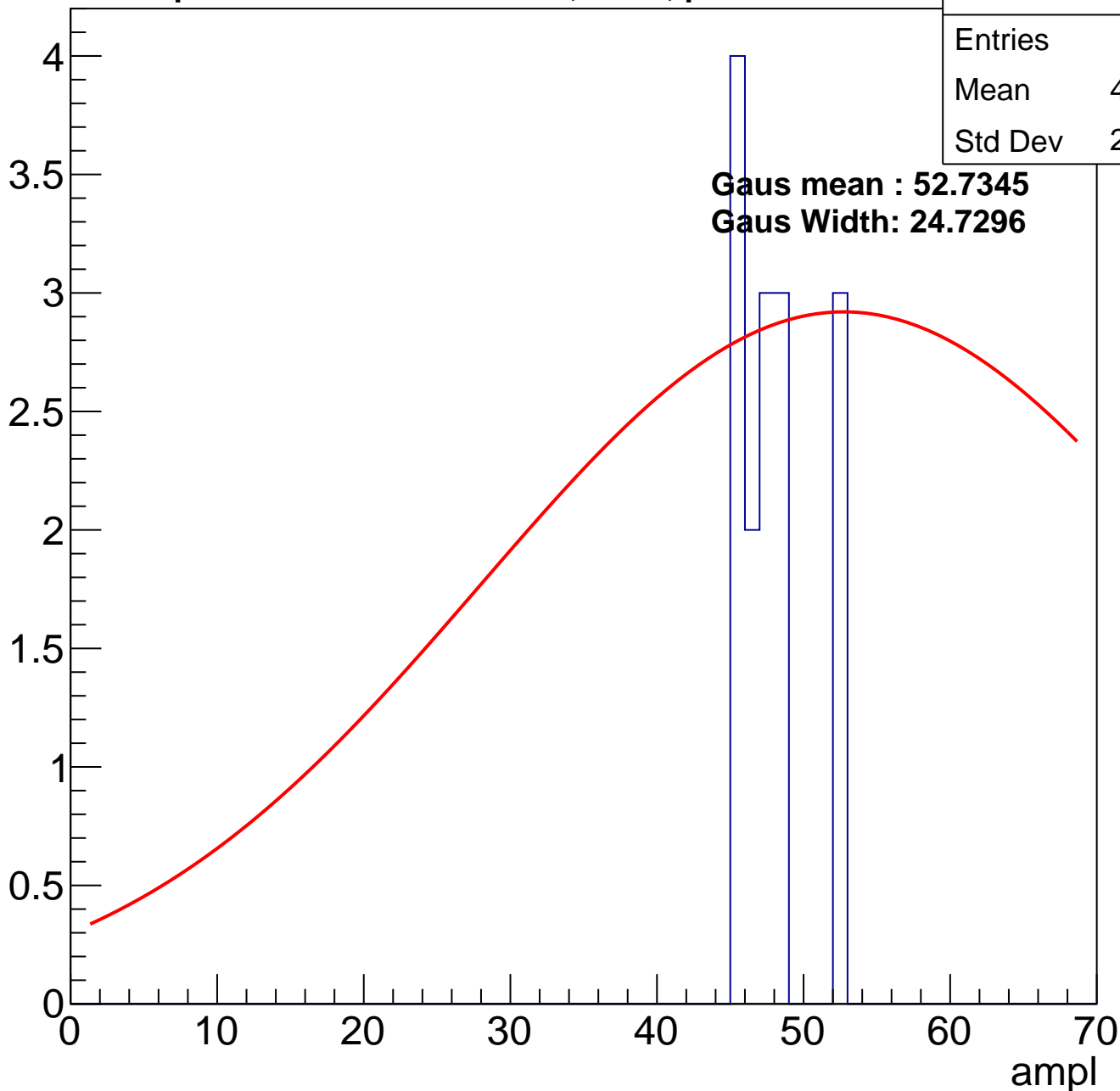
Entries	15
Mean	47.53
Std Dev	2.473

Gaus mean : 52.7345
Gaus Width: 24.7296

Entry

ampl

B0L100S, U5-ch28, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	52.22
Std Dev	2.299

ampl

Entry

B0L100S, U5-ch28, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch28, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch28, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch28, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch29, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch29, adc1
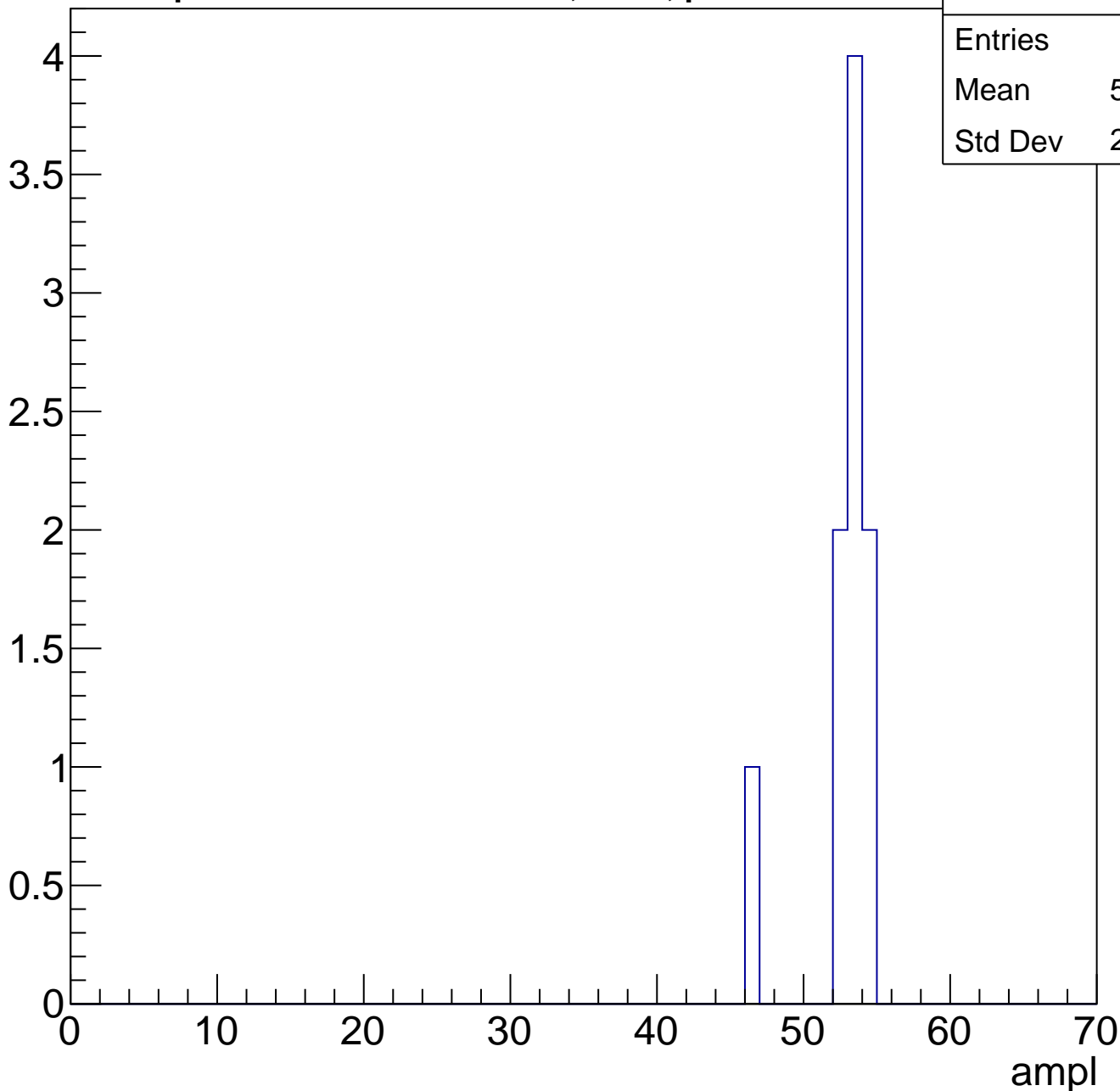
calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
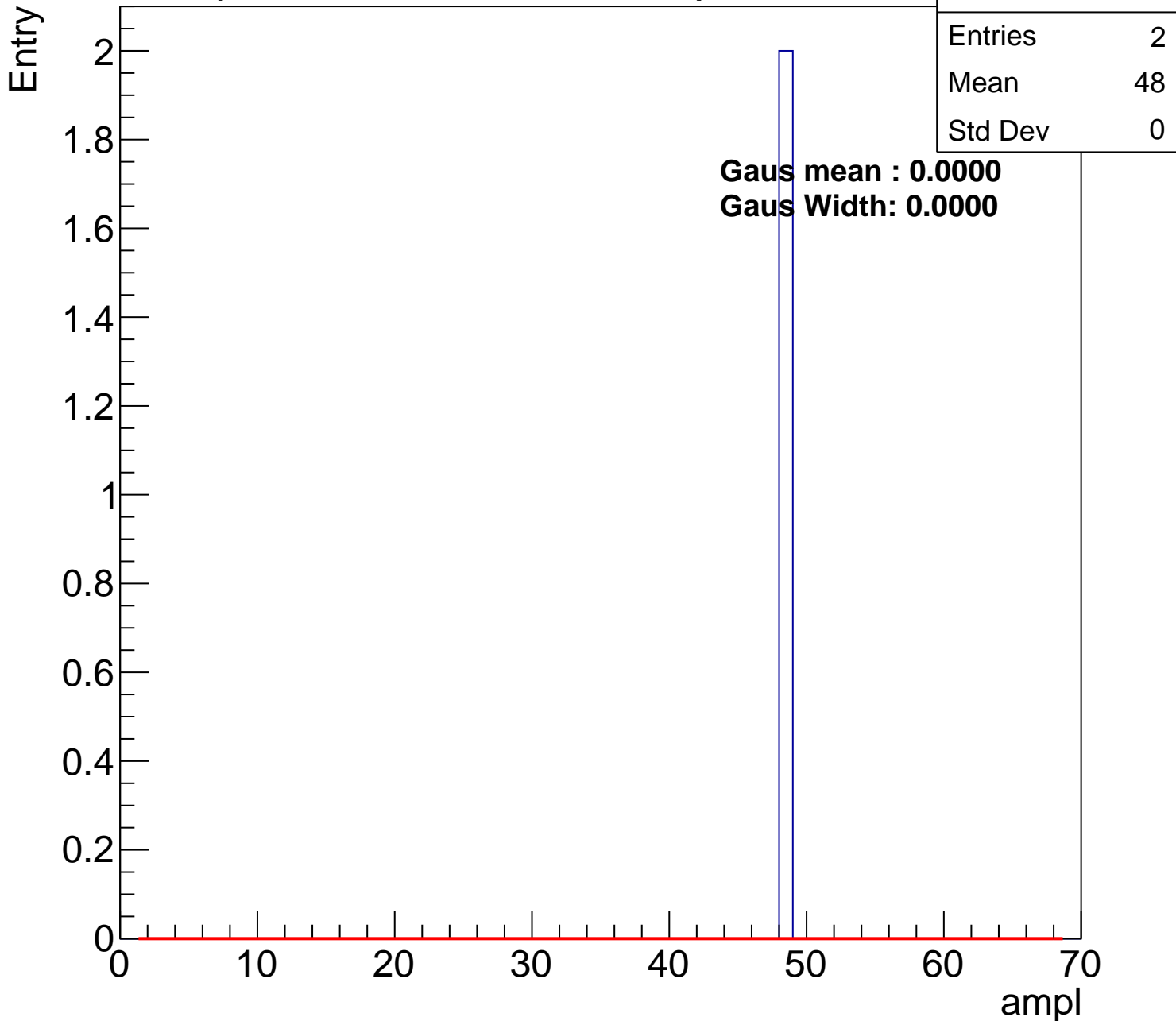
Entry

ampl

B0L100S, U5-ch29, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
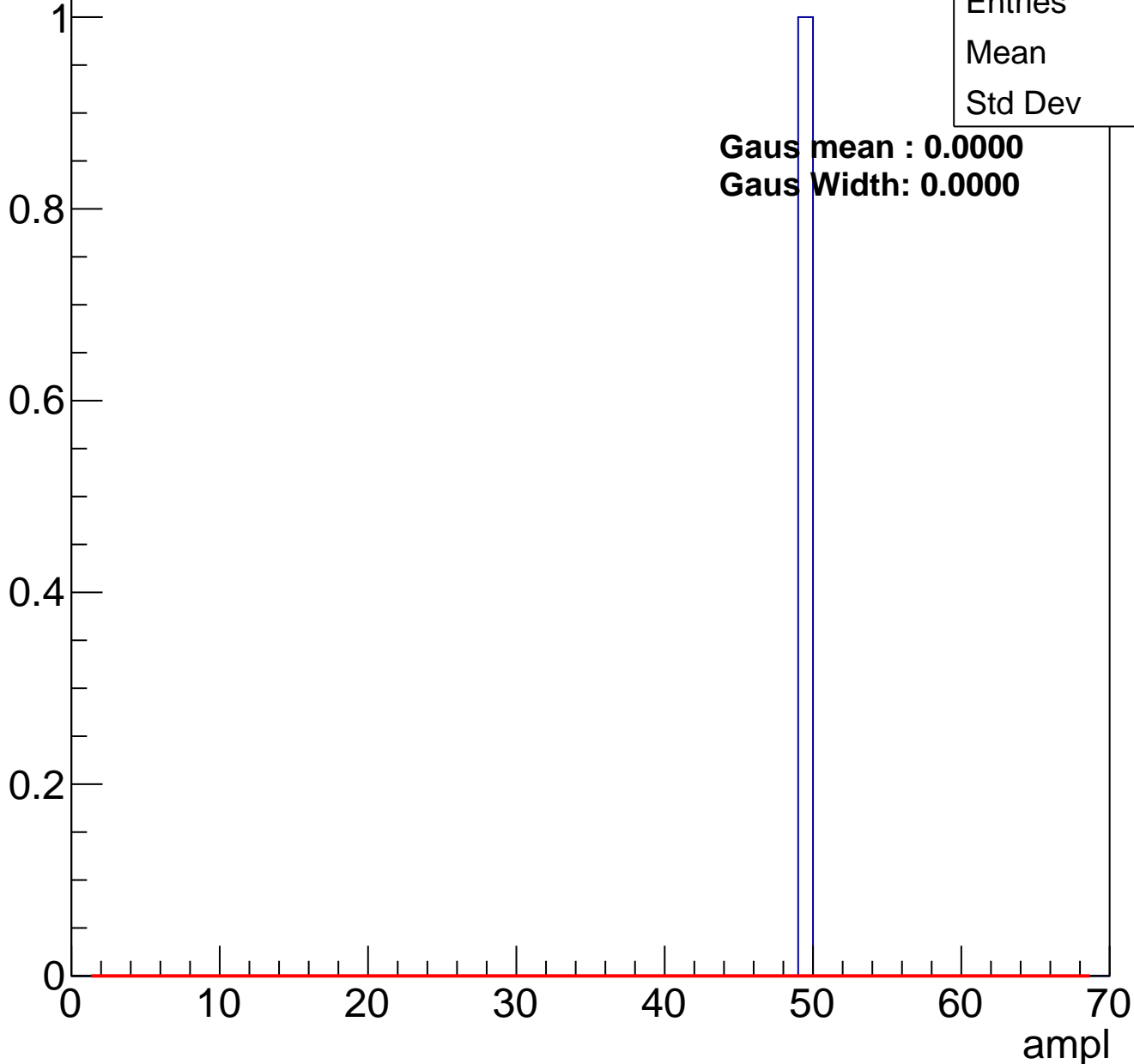
Entry

ampl

B0L100S, U5-ch29, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	48.33
Std Dev	0.4714

Entry

ampl
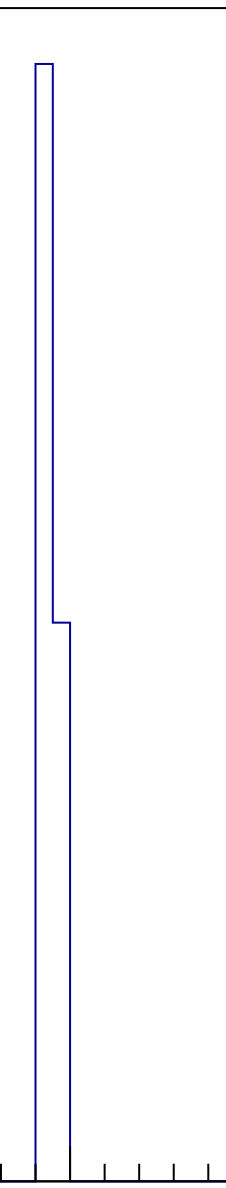
B0L100S, U5-ch29, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch29, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch29, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch29, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch30, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch30, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch30, adc2

calib_packv5_042523_0143.root, FC#6, port A1
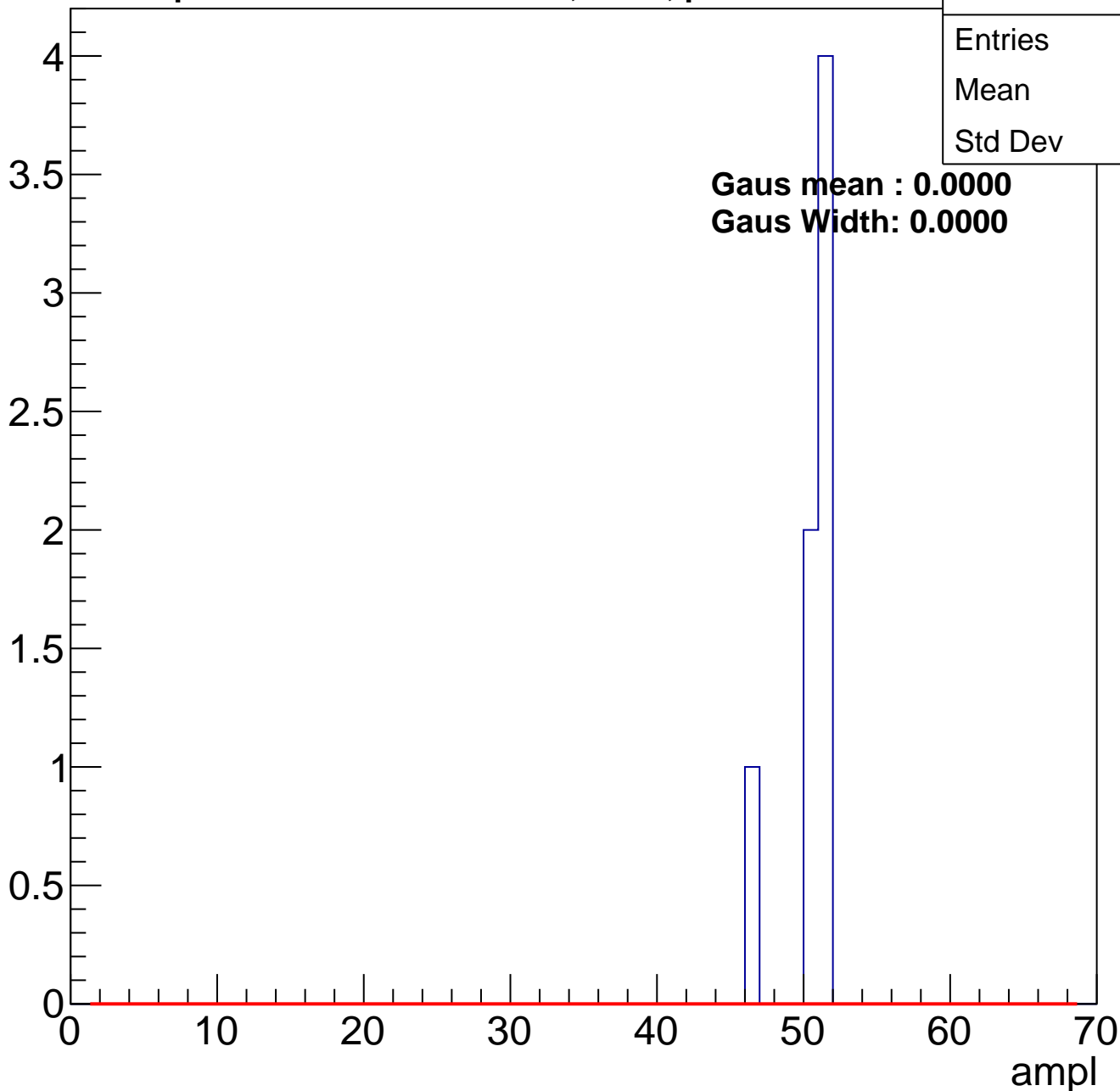
Entries	7
Mean	50
Std Dev	1.69

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch30, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.8
Std Dev	3.868

B0L100S, U5-ch30, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch30, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch30, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch30, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch31, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch31, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch31, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch31, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch31, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch31, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch31, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch31, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch32, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
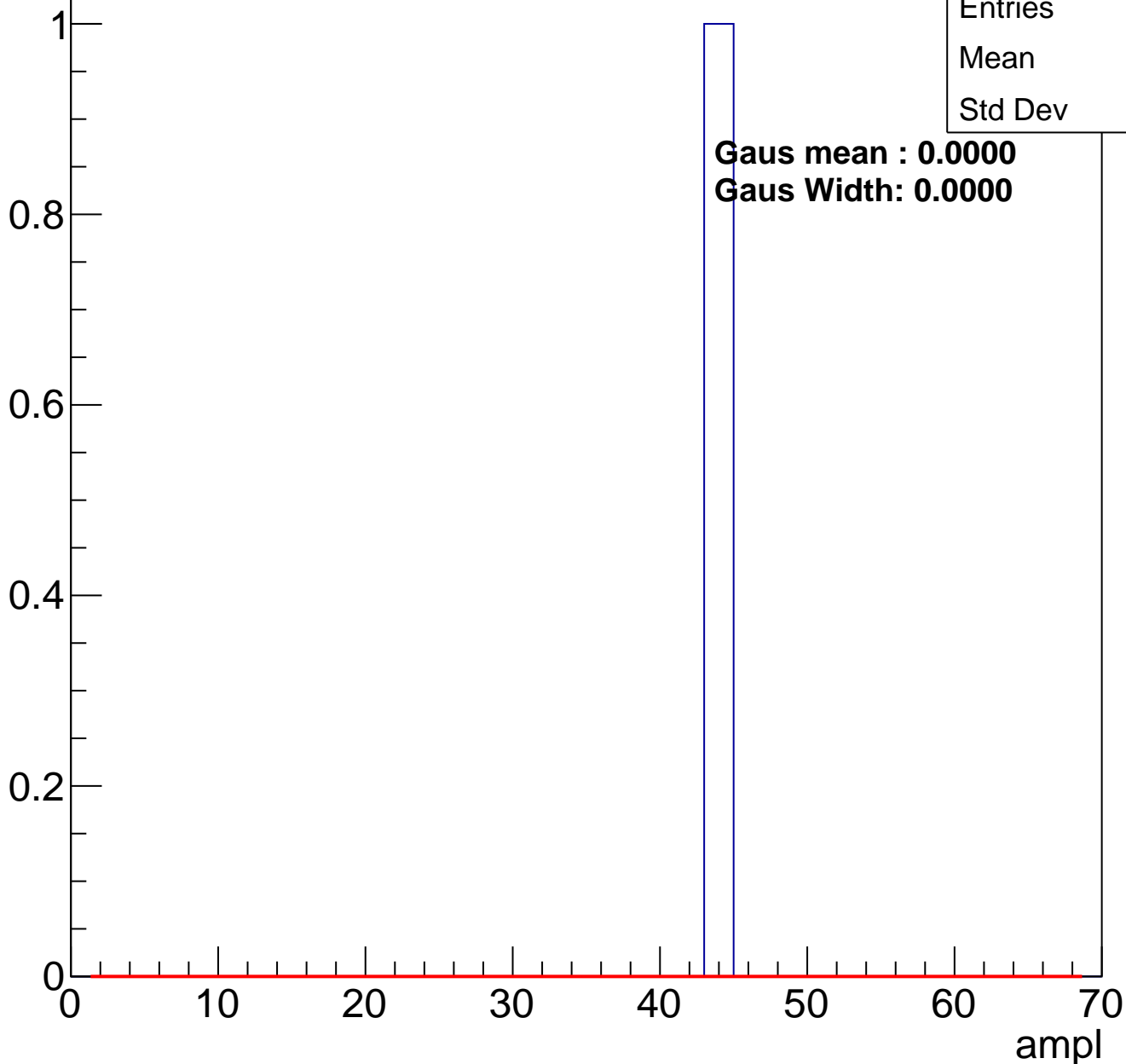
B0L100S, U5-ch32, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	2
Mean	45.5
Std Dev	2.5

Gaus mean : 0.0000
Gaus Width: 0.0000
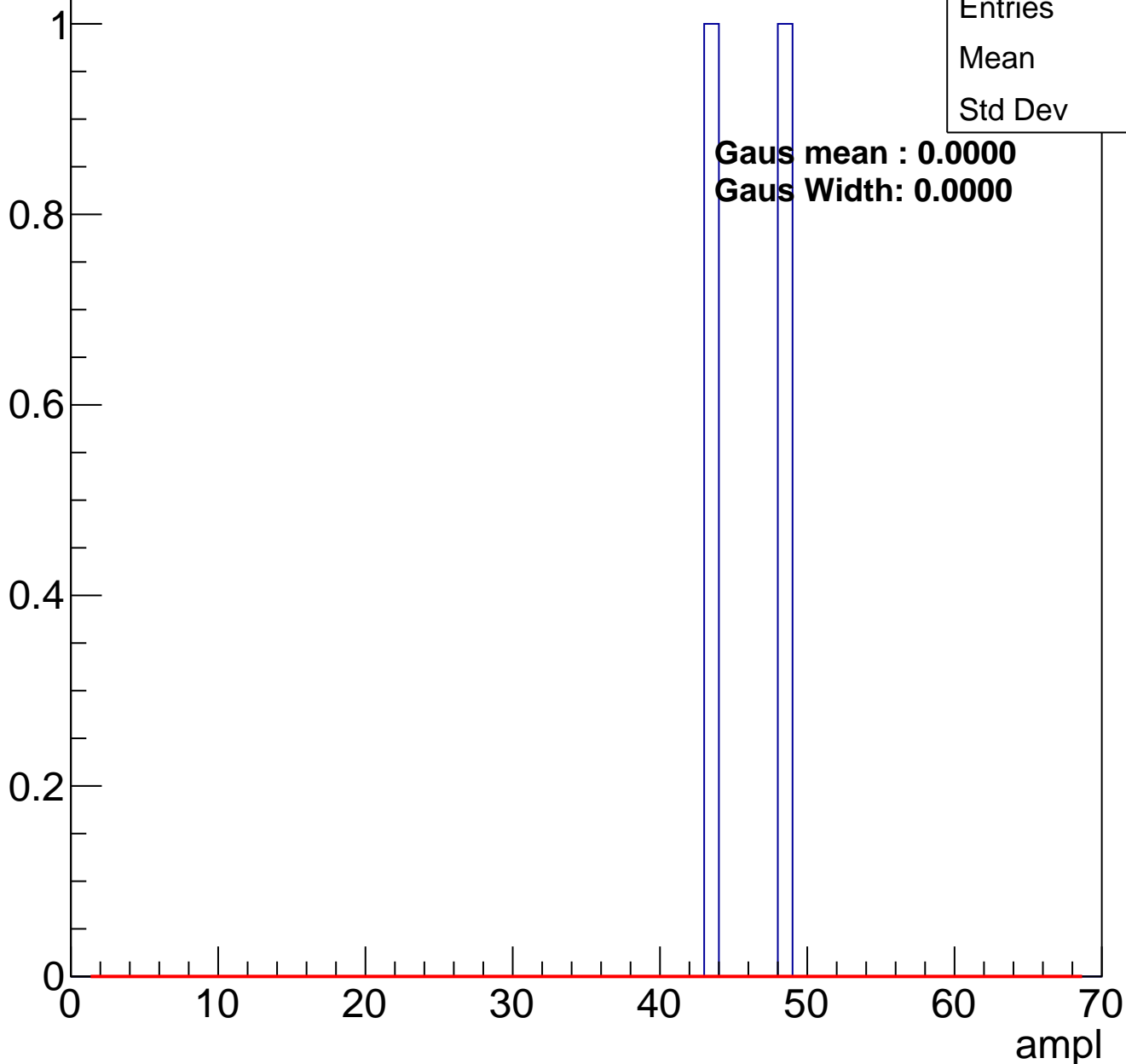
B0L100S, U5-ch32, adc2

calib_packv5_042523_0143.root, FC#6, port A1
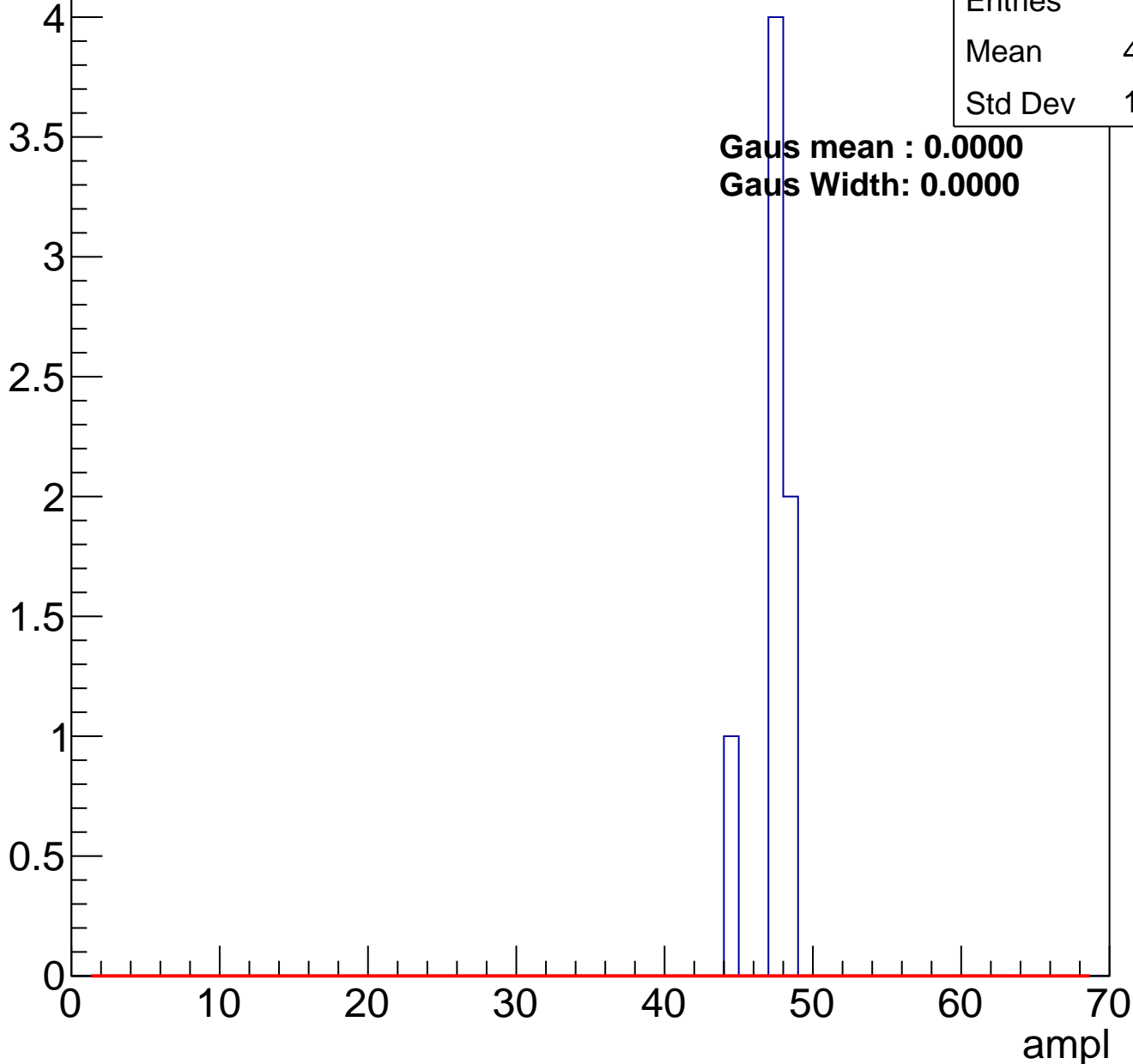
Entries	7
Mean	46.86
Std Dev	1.245

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch32, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch32, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch32, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch32, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch32, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch33, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch33, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	44.17
Std Dev	1.344

Gaus mean : 0.0000
Gaus Width: 0.0000
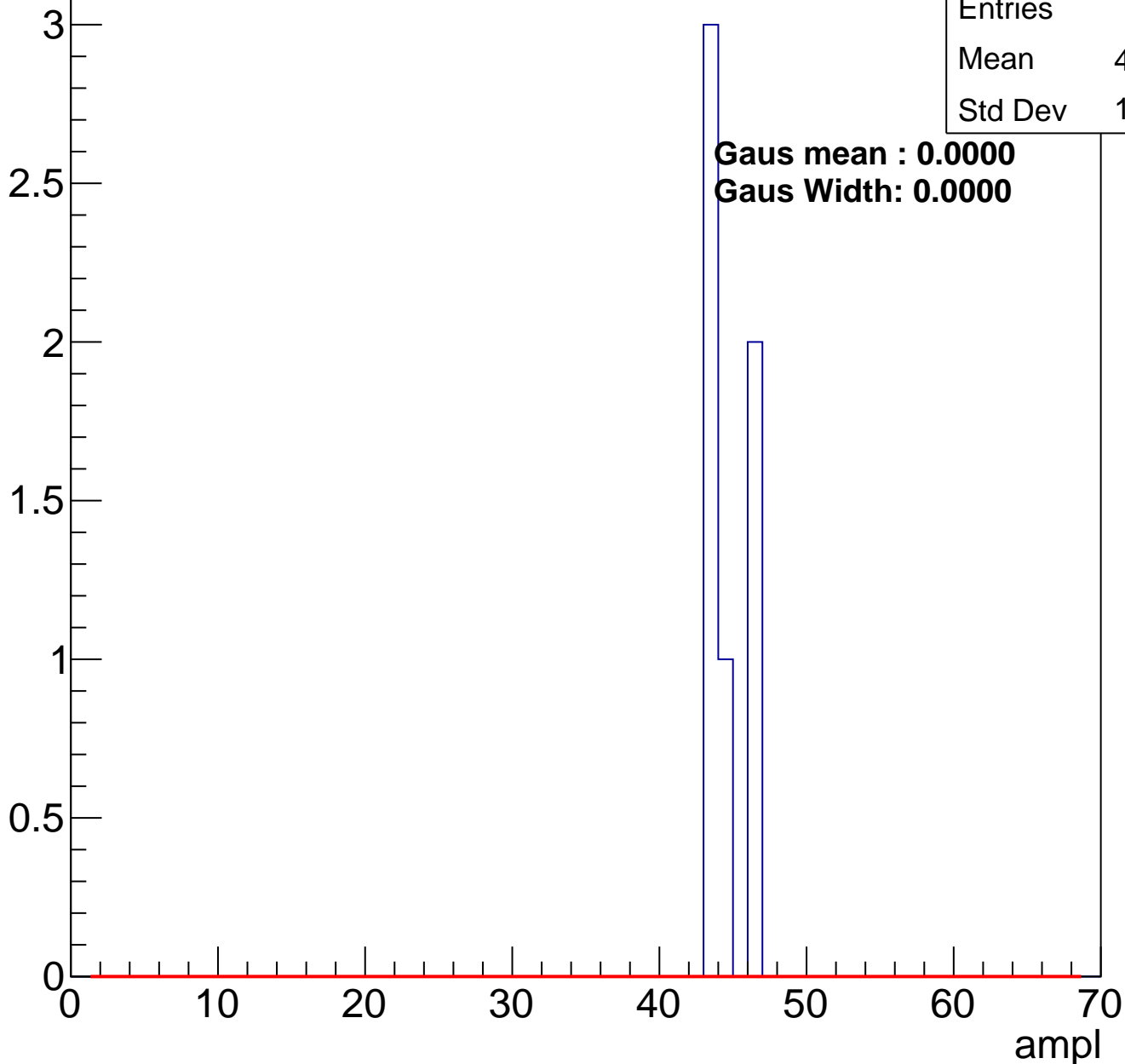
Entry

ampl

B0L100S, U5-ch33, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
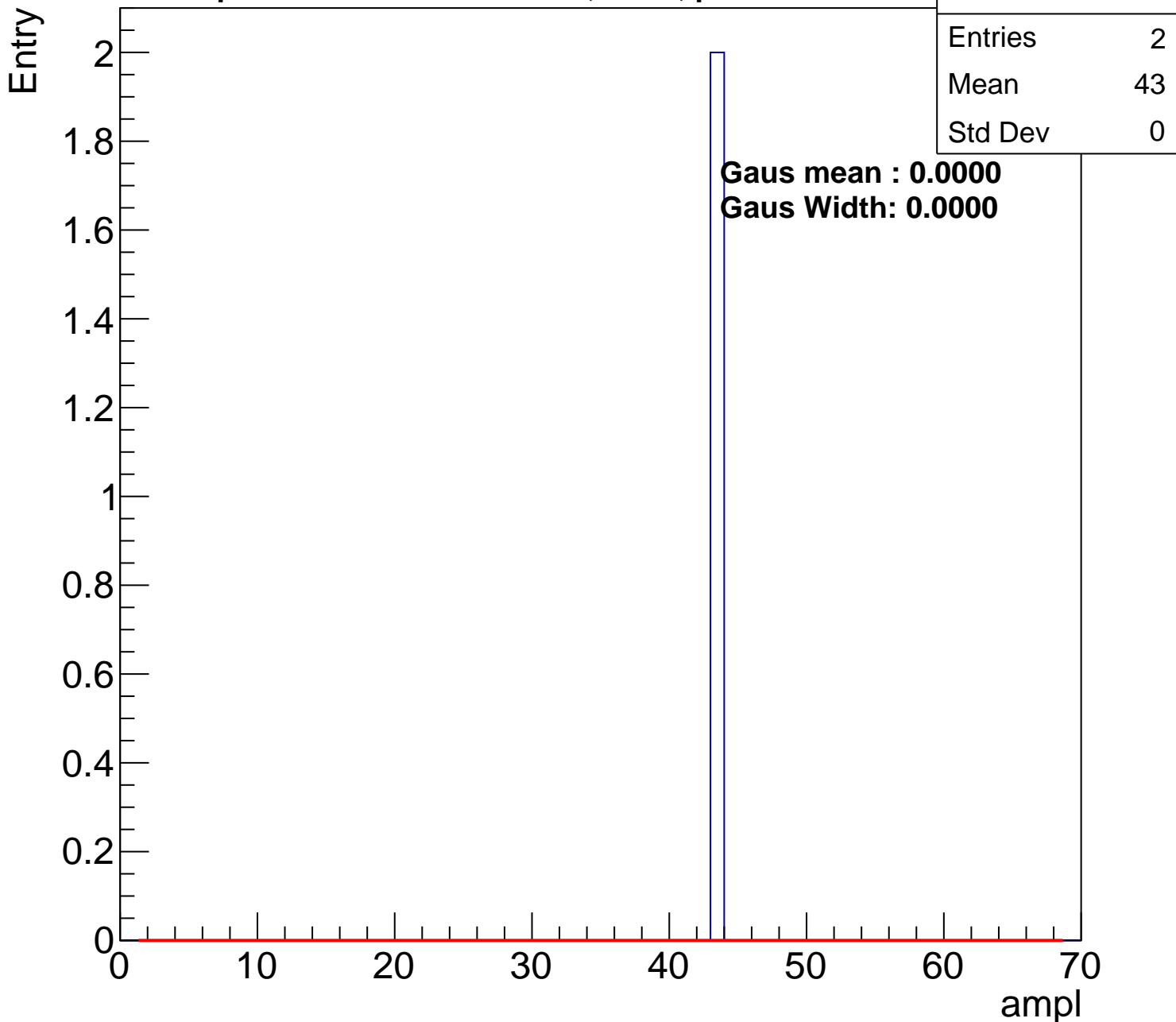
B0L100S, U5-ch33, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch33, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch33, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch33, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl
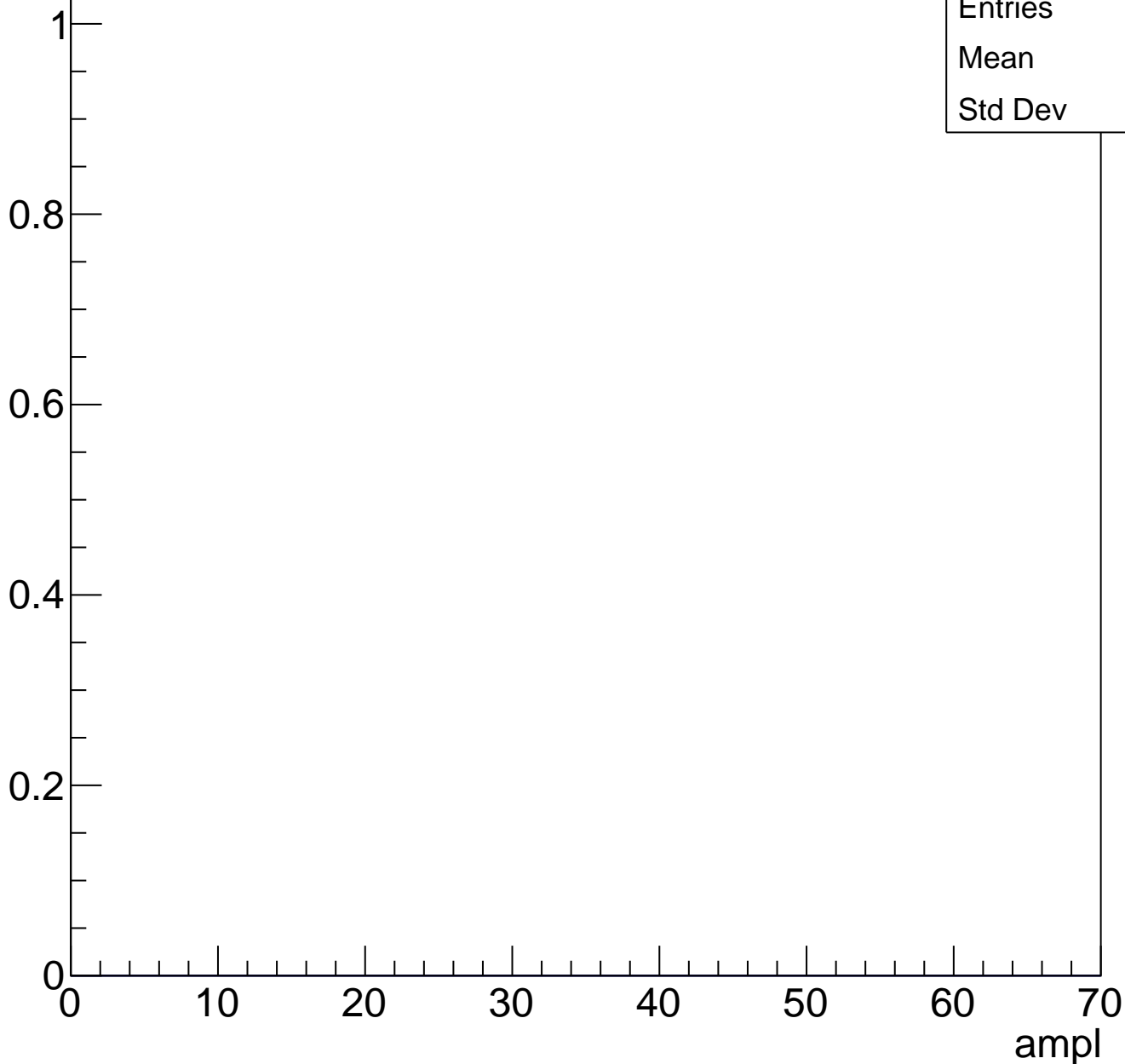
B0L100S, U5-ch33, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch34, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	42
Std Dev	1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
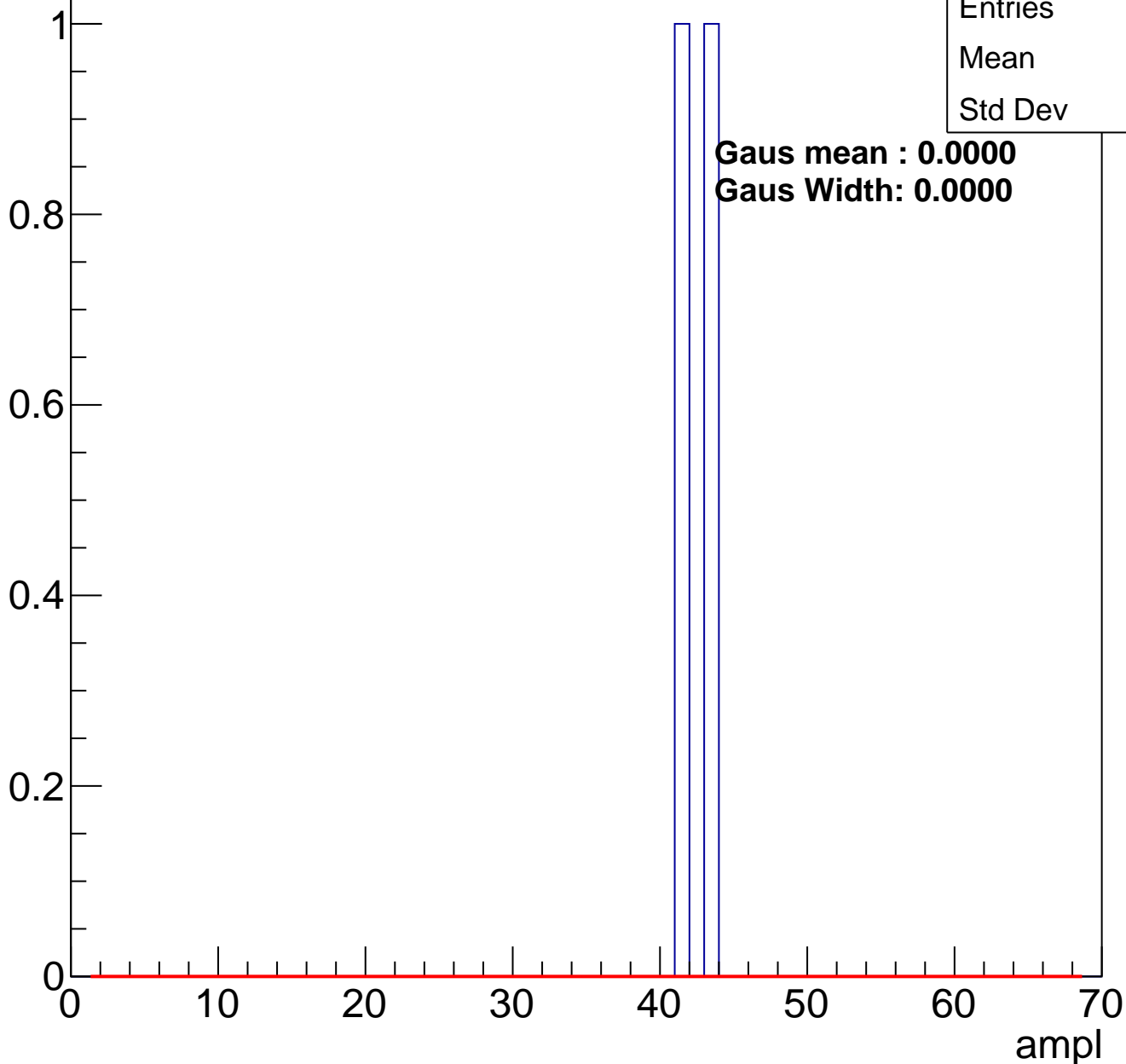
B0L100S, U5-ch34, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	22
Mean	42.82
Std Dev	1.898

Gaus mean : 42.1156
Gaus Width: 4.6577
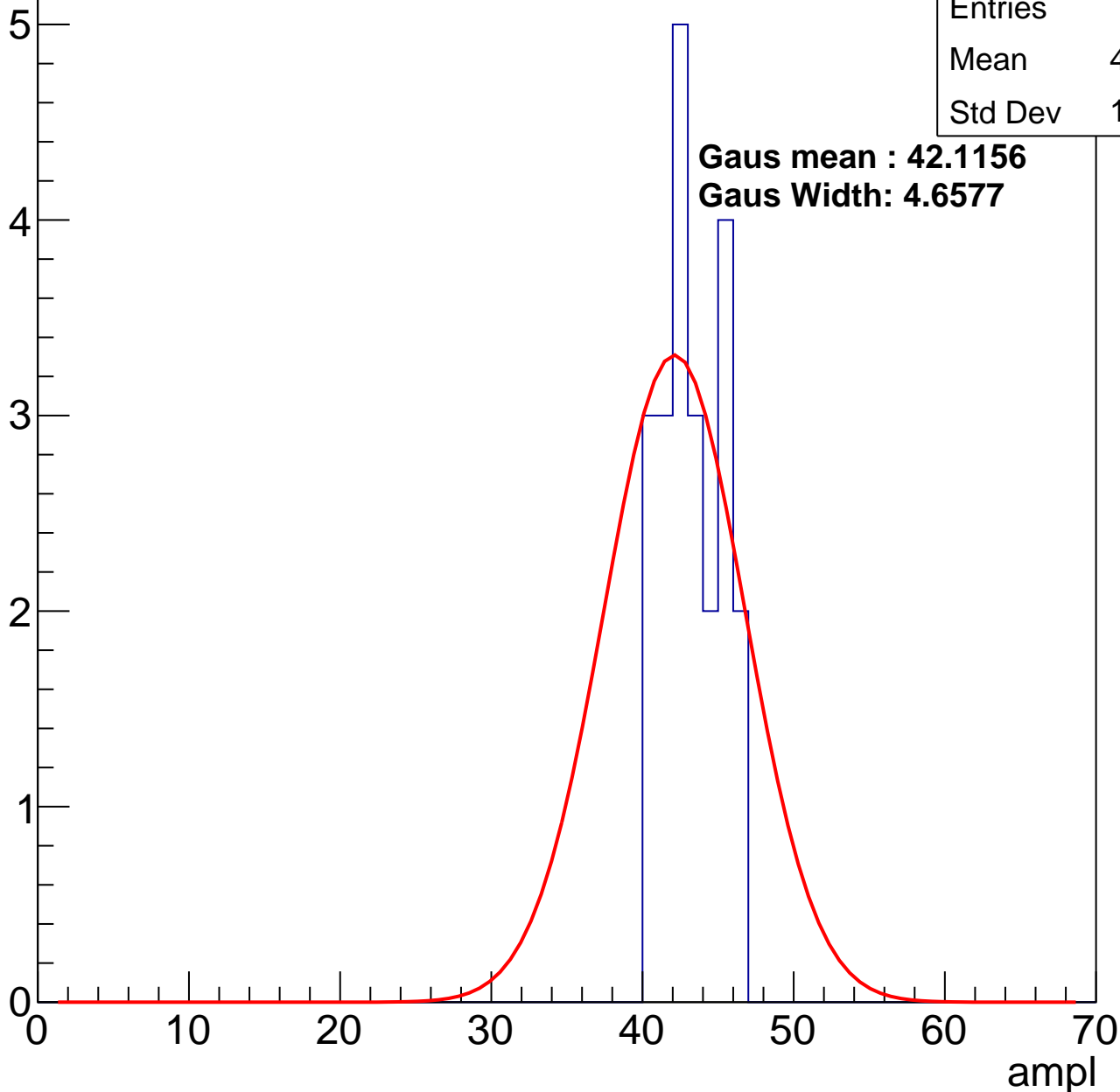
B0L100S, U5-ch34, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	9
Mean	45.89
Std Dev	2.846

Gaus mean : 0.0000
Gaus Width: 0.0000
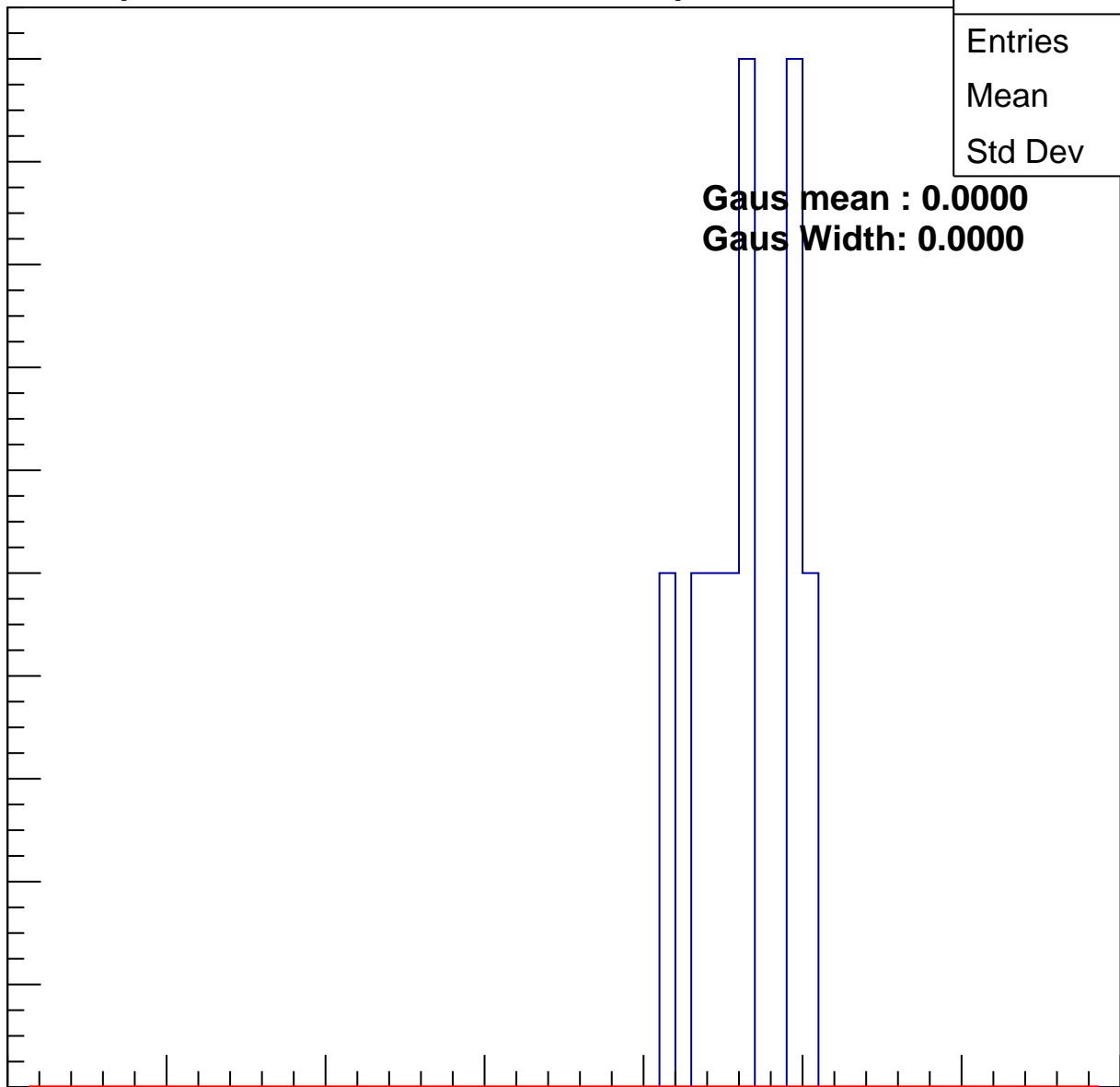
B0L100S, U5-ch34, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch34, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch34, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch34, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch34, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch35, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	25.5
Std Dev	25.5

Gaus mean : 0.0000
Gaus Width: 0.0000
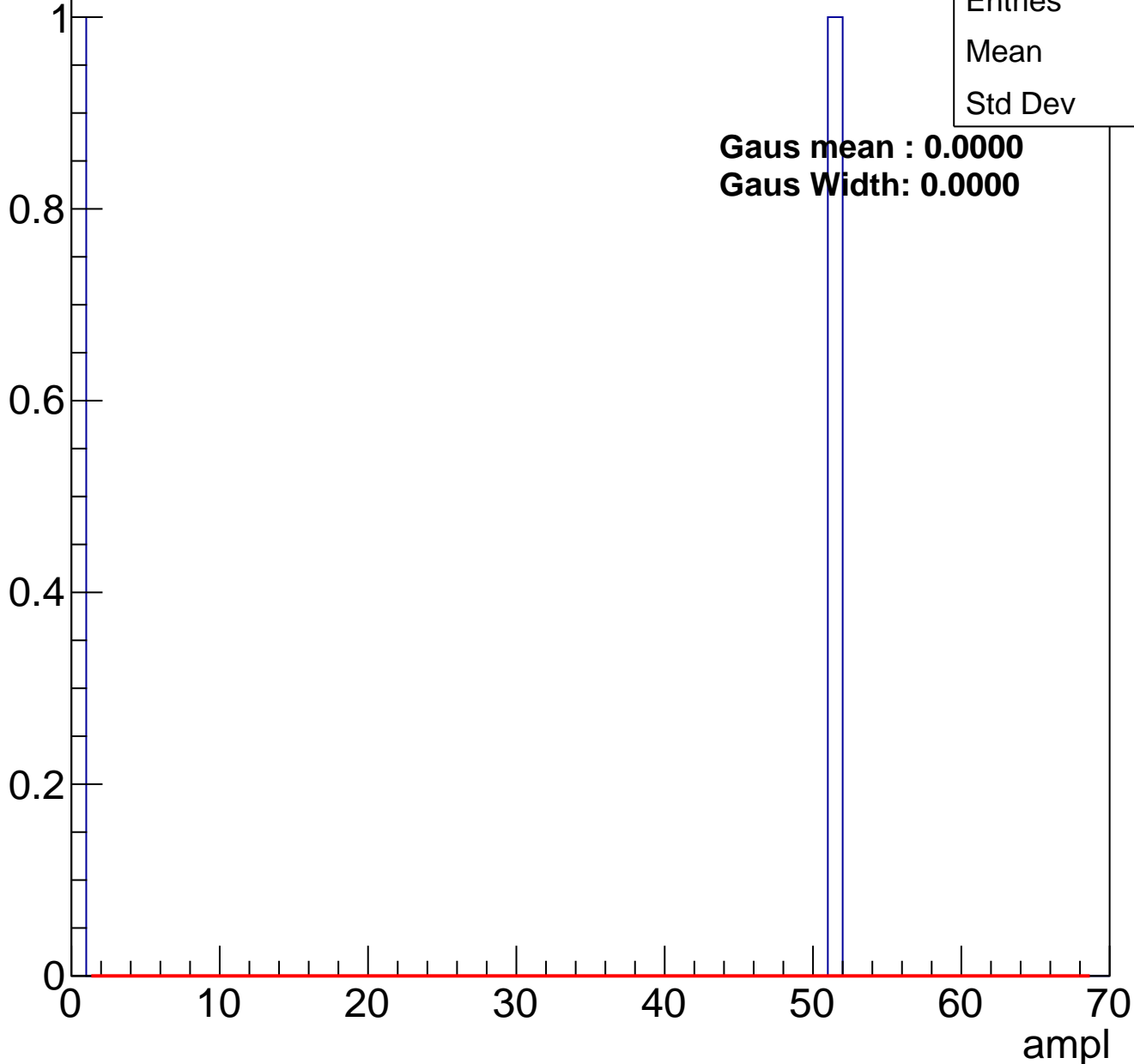
B0L100S, U5-ch35, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	47
Std Dev	0.8165

Gaus mean : 0.0000
Gaus Width: 0.0000
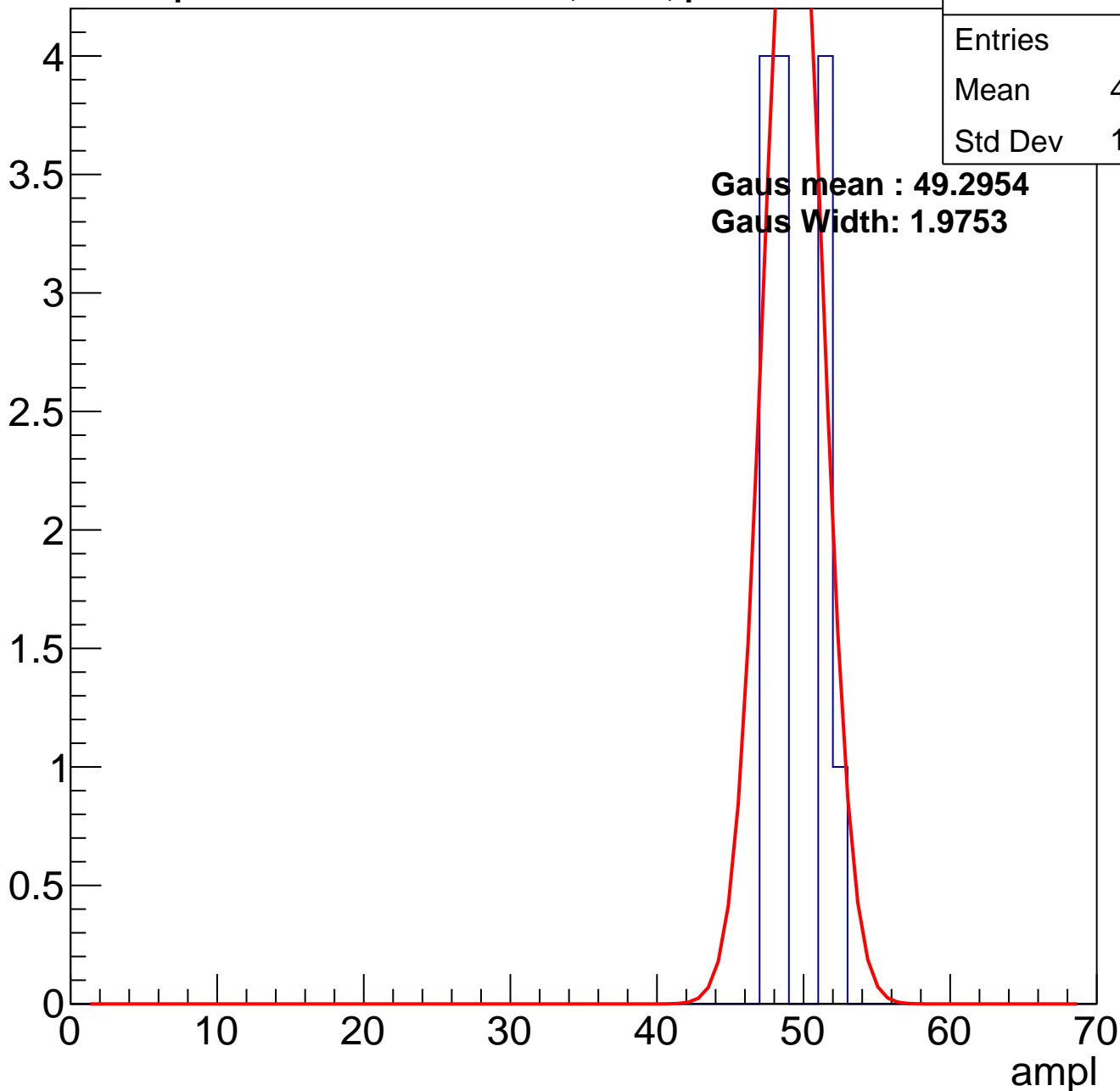
Entry

ampl

B0L100S, U5-ch35, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch35, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch35, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch35, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch35, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch36, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch36, adc1

calib_packv5_042523_0143.root, FC#6, port A1
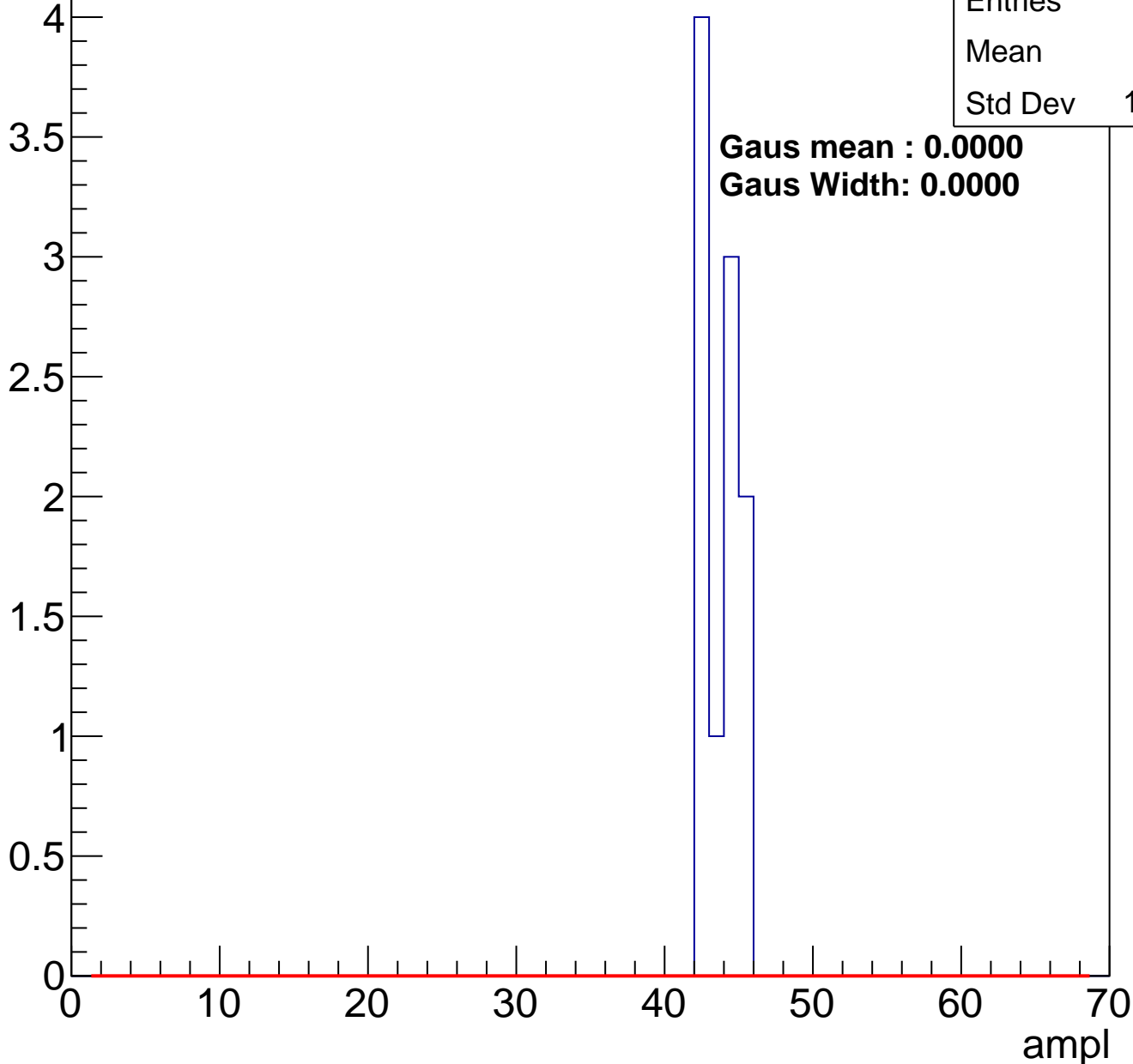
Entries	10
Mean	43.3
Std Dev	1.187

Gaus mean : 0.0000
Gaus Width: 0.0000
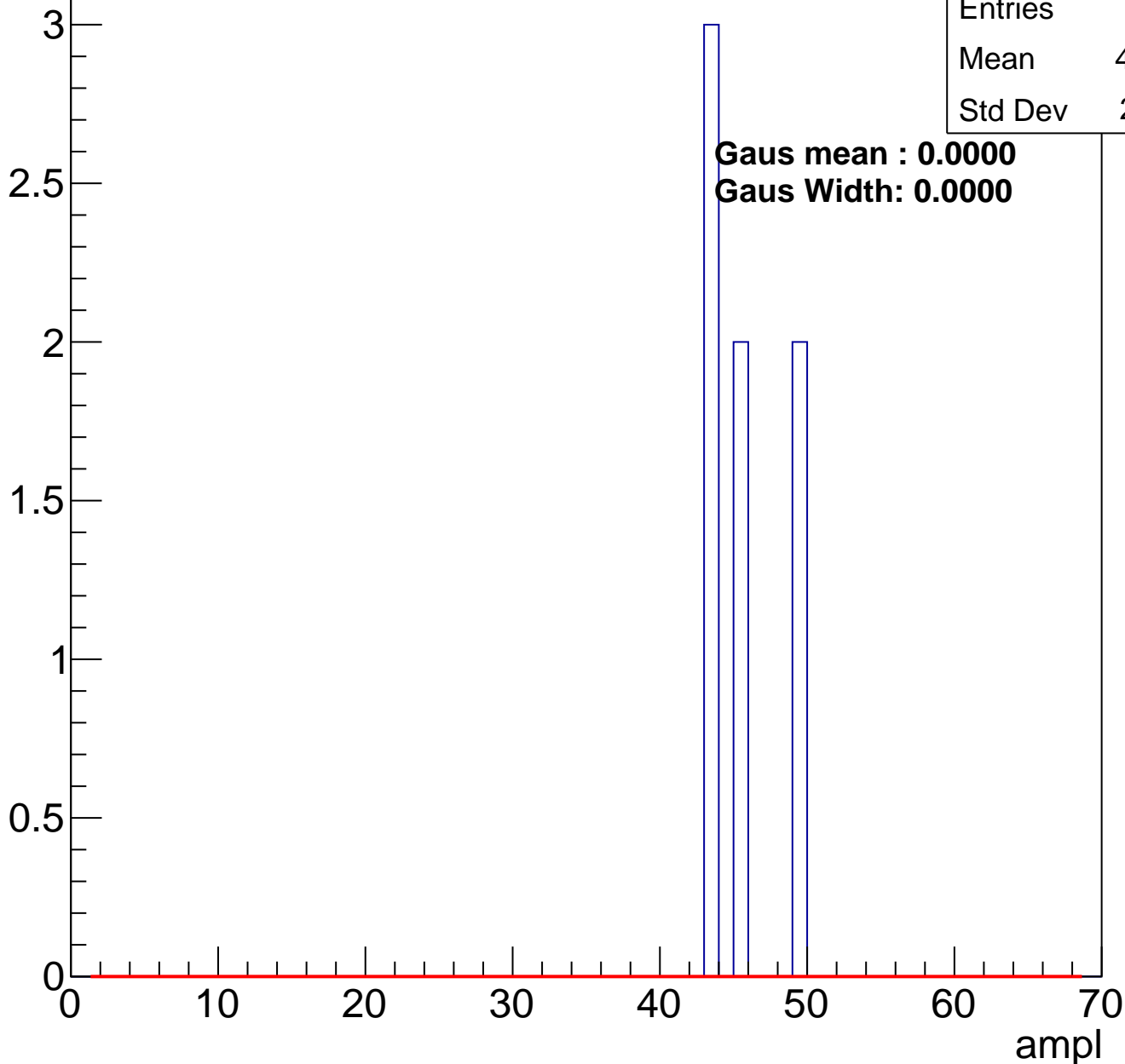
B0L100S, U5-ch36, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	47.5
Std Dev	1.5
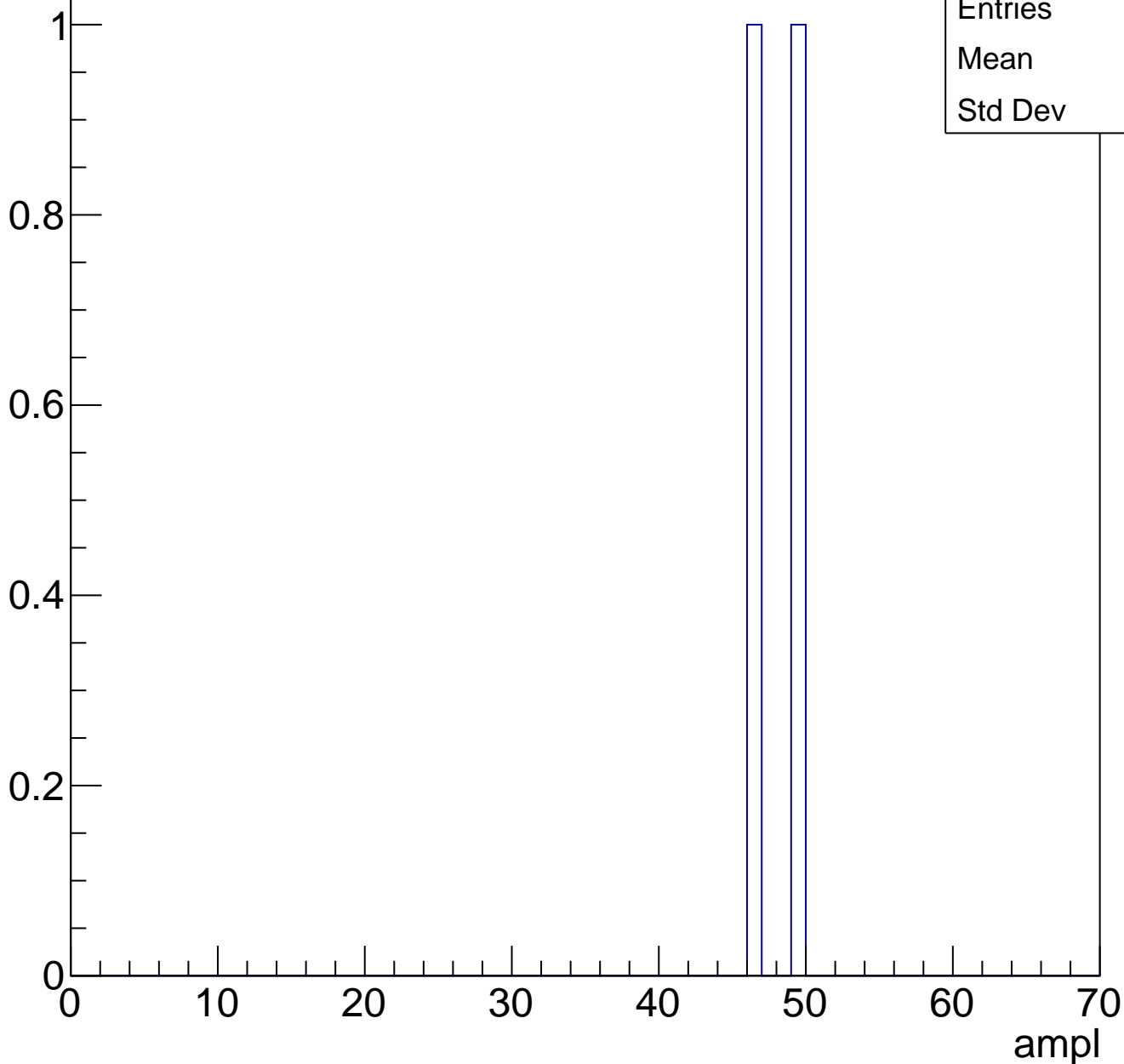
B0L100S, U5-ch36, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch36, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch36, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch36, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch37, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch37, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
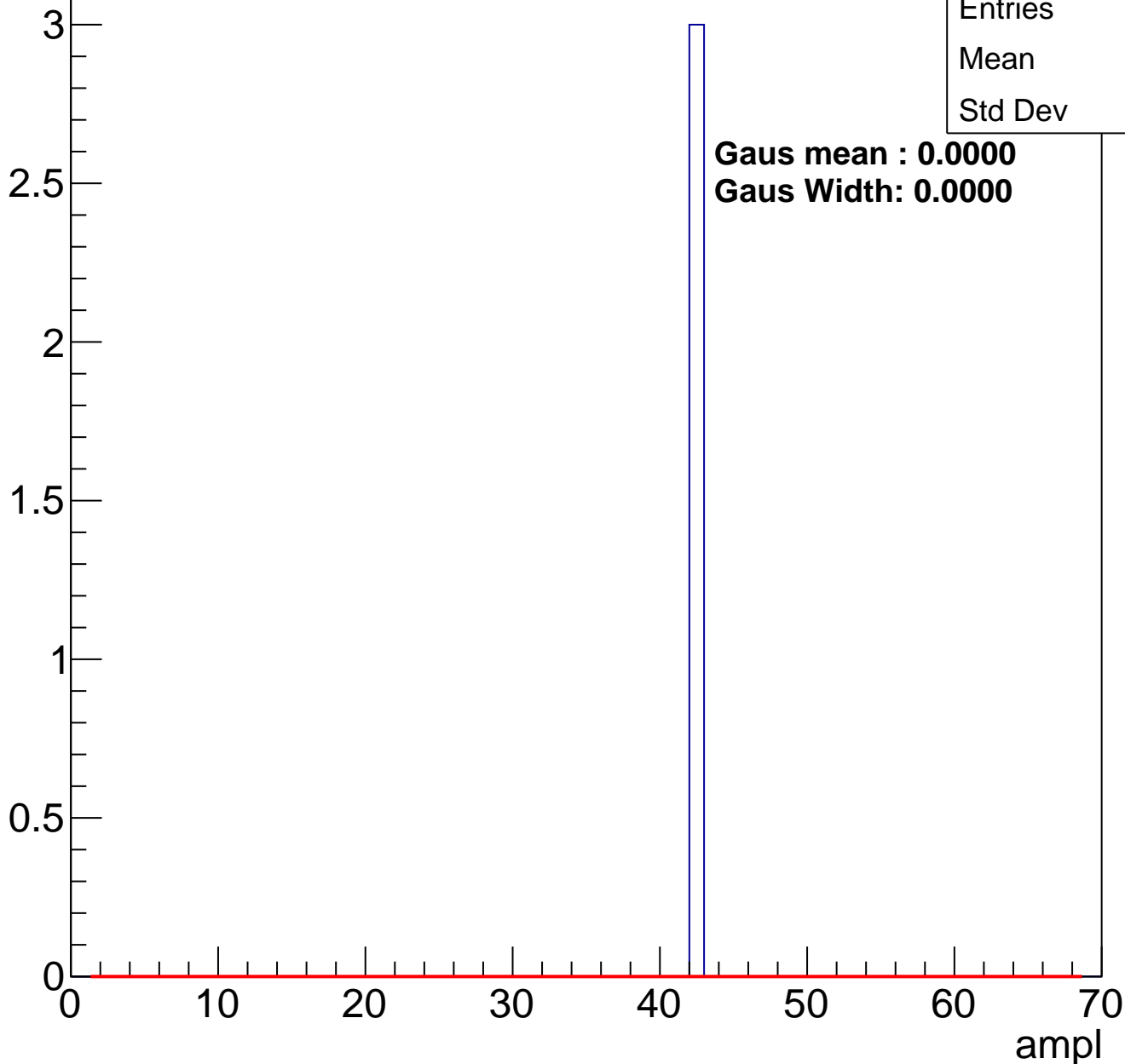
Entry

ampl

B0L100S, U5-ch37, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	55.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch37, adc3

calib_packv5_042523_0143.root, FC#6, port A1
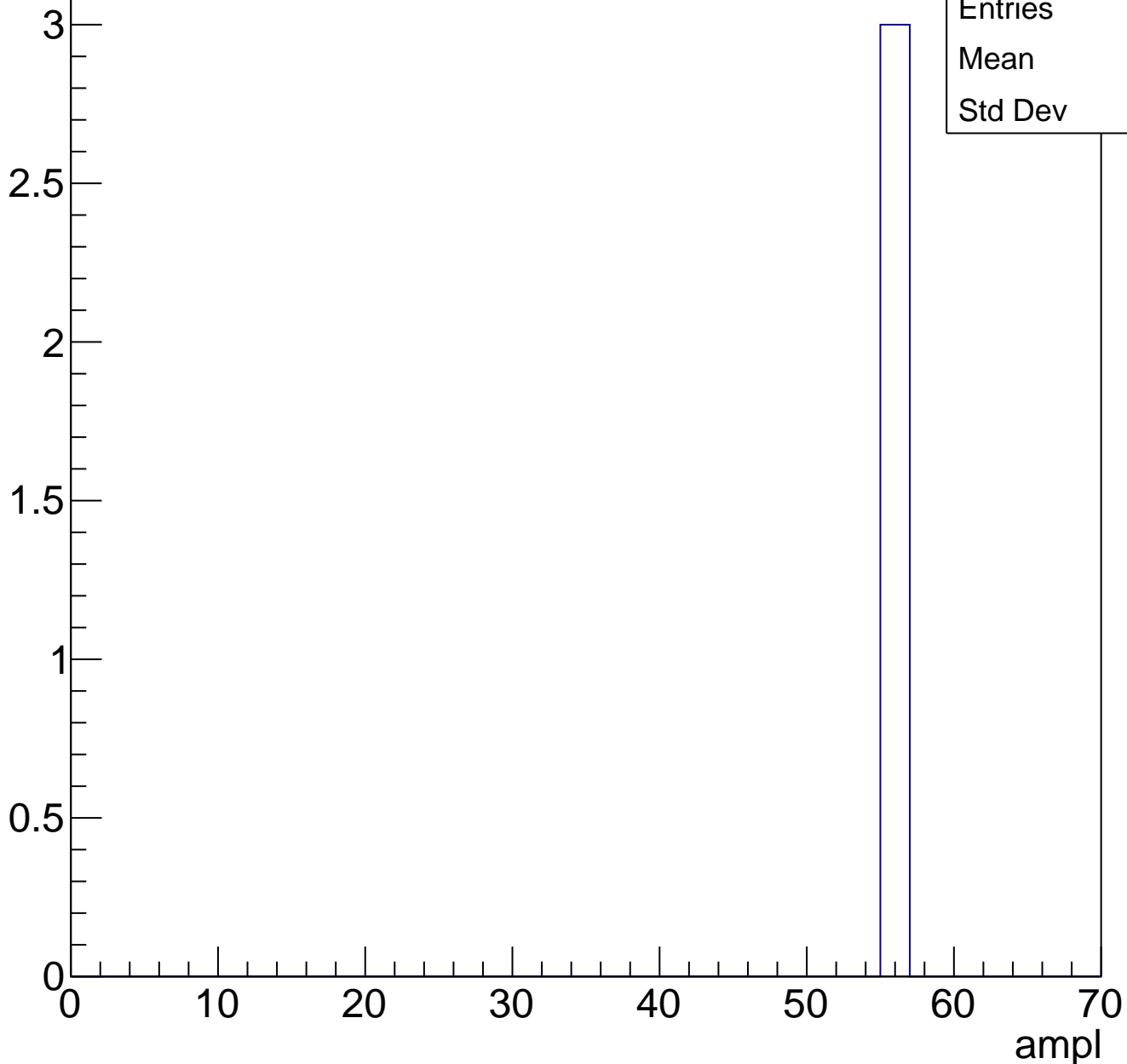
Entries	6
Mean	55.5
Std Dev	0.5

Entry

ampl

B0L100S, U5-ch37, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch37, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch37, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch38, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch38, adc1

calib_packv5_042523_0143.root, FC#6, port A1
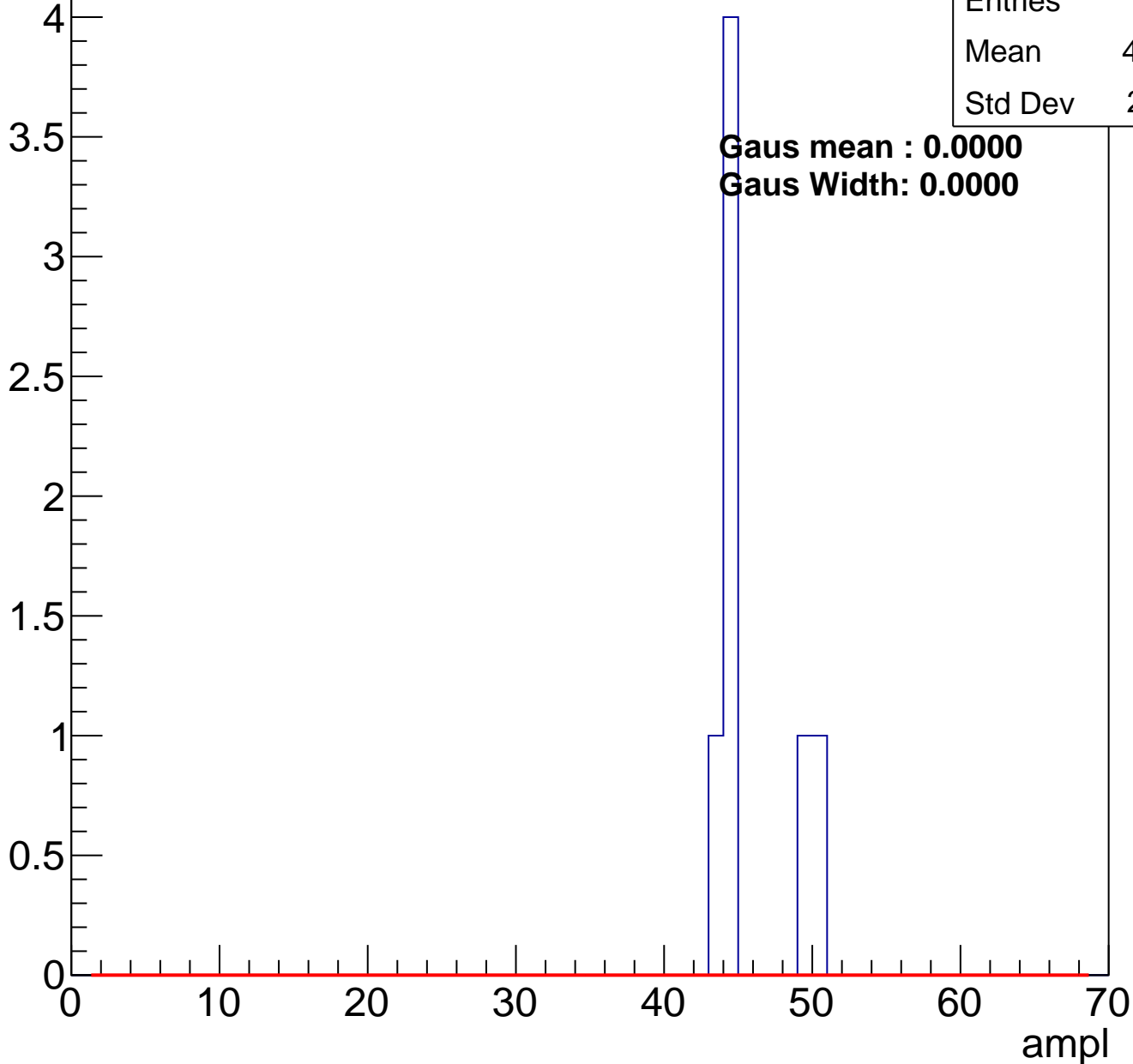
Entries	7
Mean	45.43
Std Dev	2.611

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch38, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	48.14
Std Dev	2.263

Gaus mean : 48.3936
Gaus Width: 4.3530
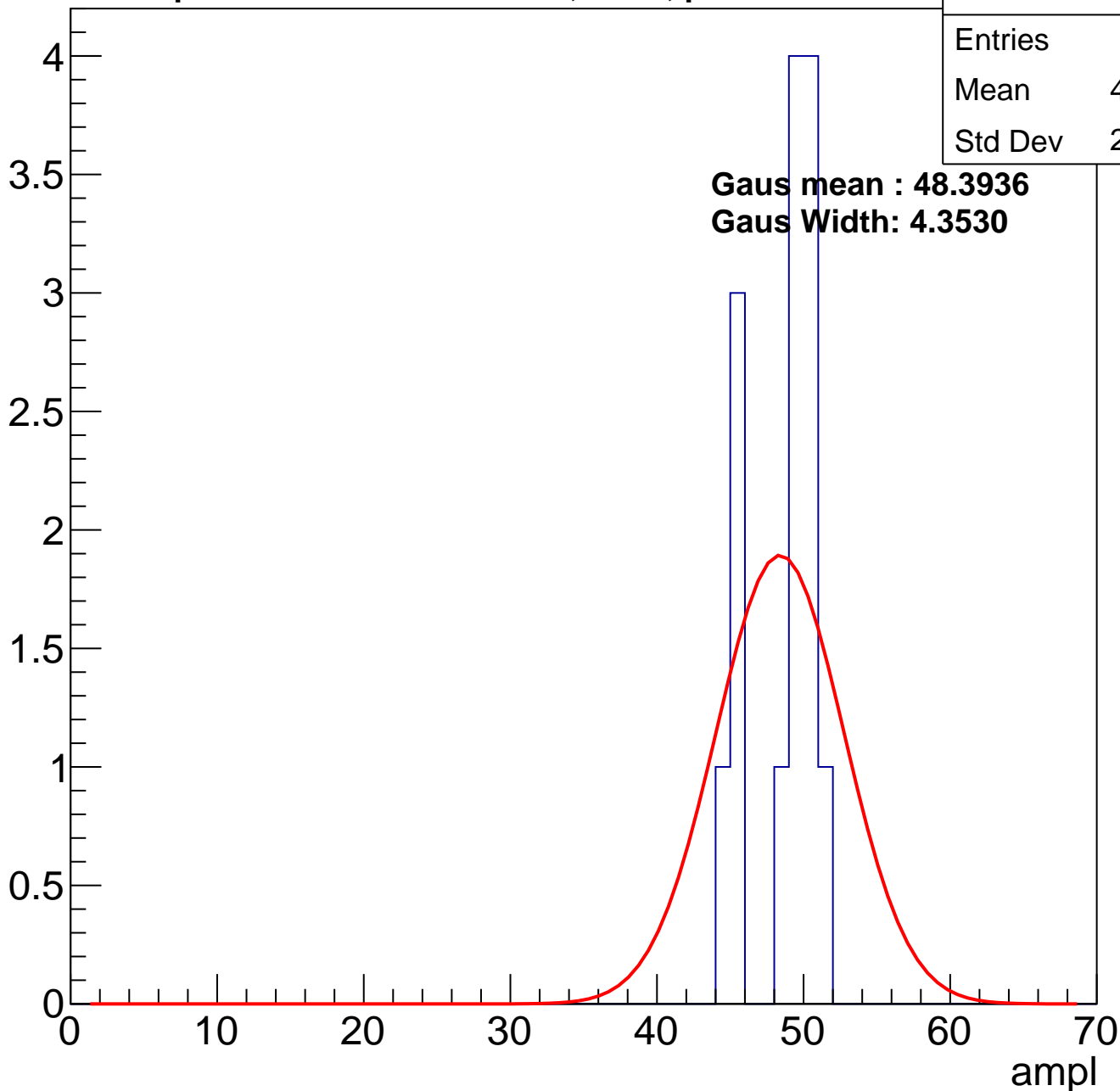
B0L100S, U5-ch38, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	51
Std Dev	0

Entry

ampl
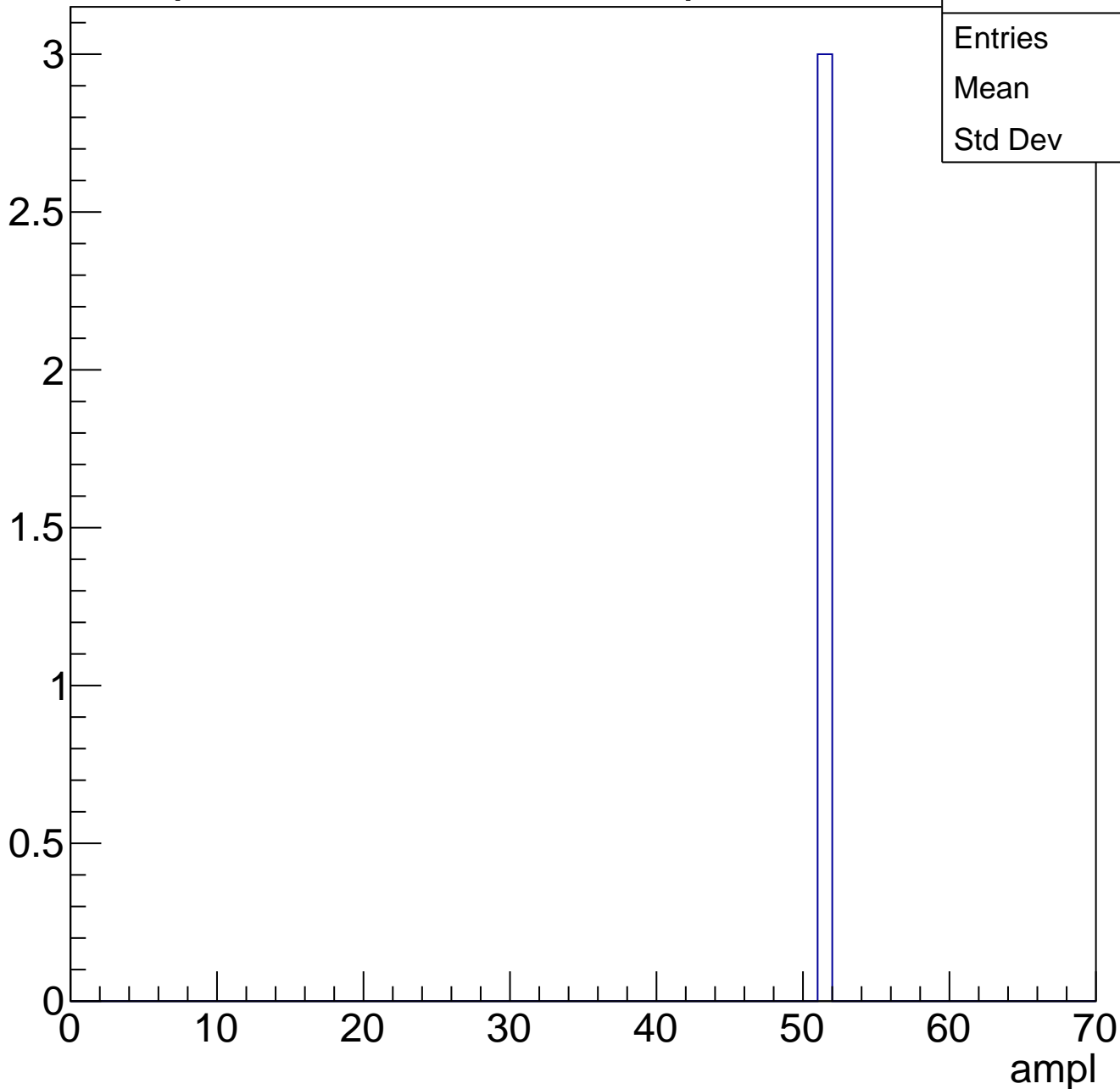
B0L100S, U5-ch38, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch38, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch38, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch38, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch39, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch39, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch39, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch39, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch39, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch39, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch39, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch39, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch40, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	46
Std Dev	0
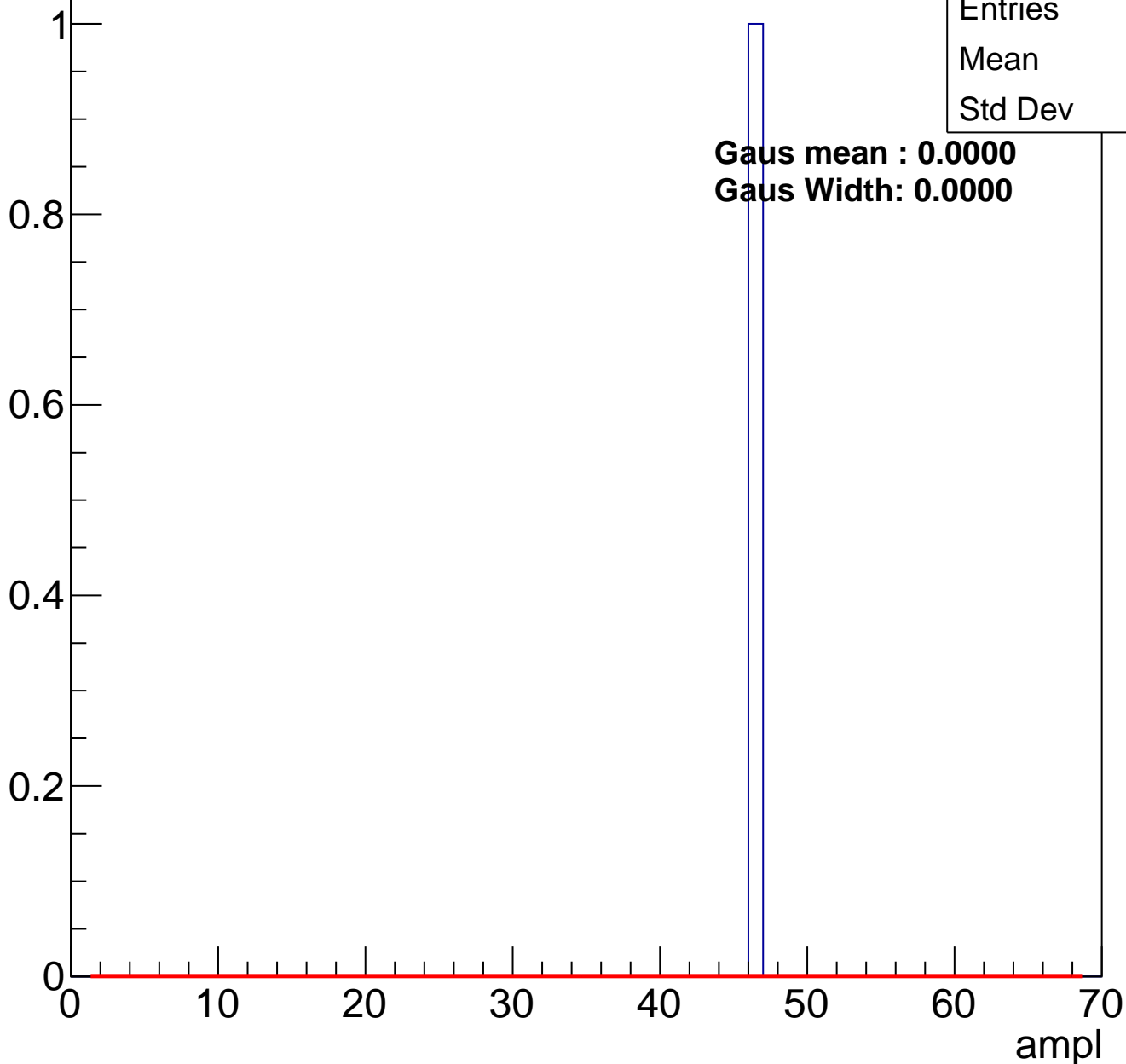
ampl

B0L100S, U5-ch40, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	47.2
Std Dev	2.315

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
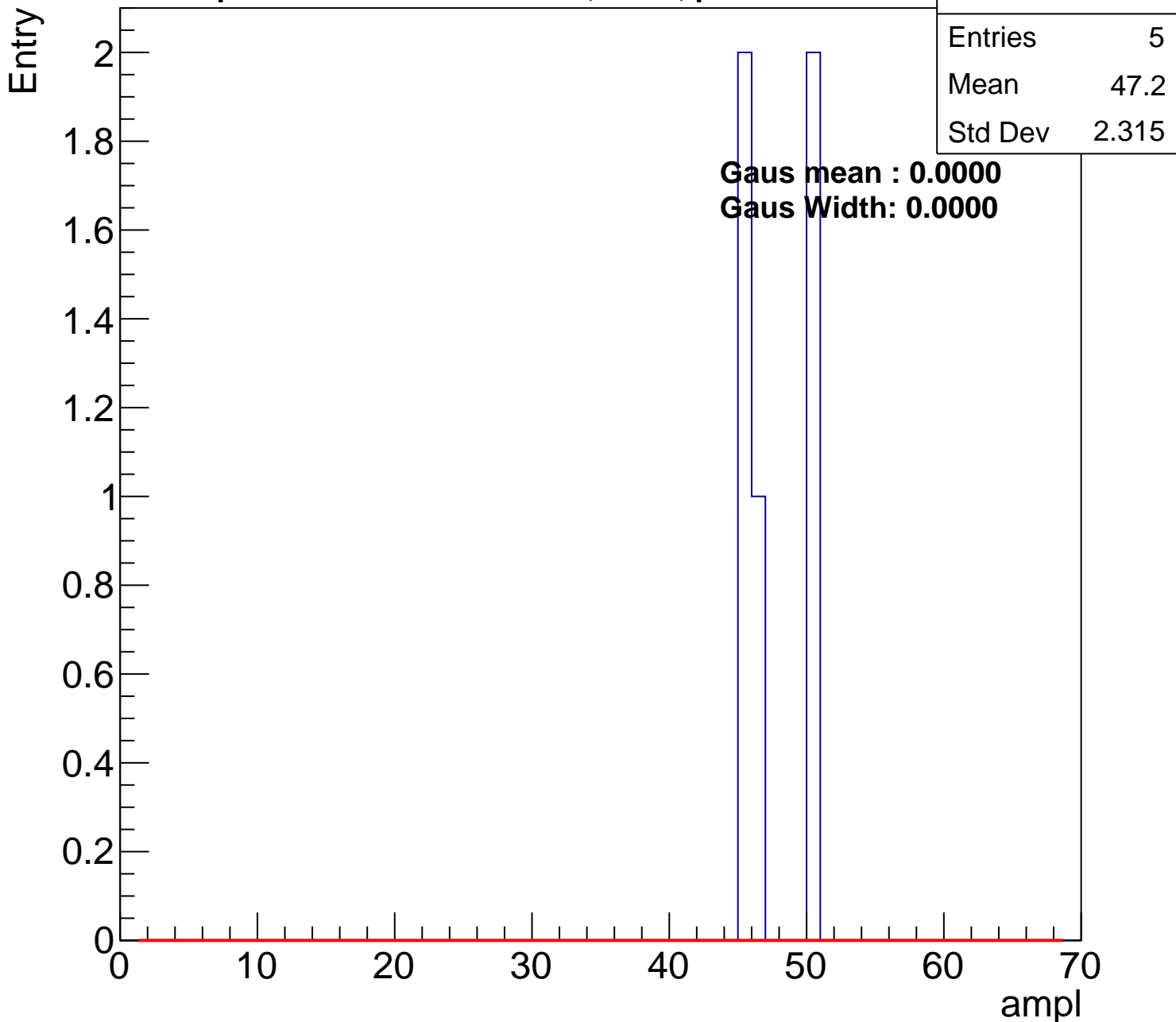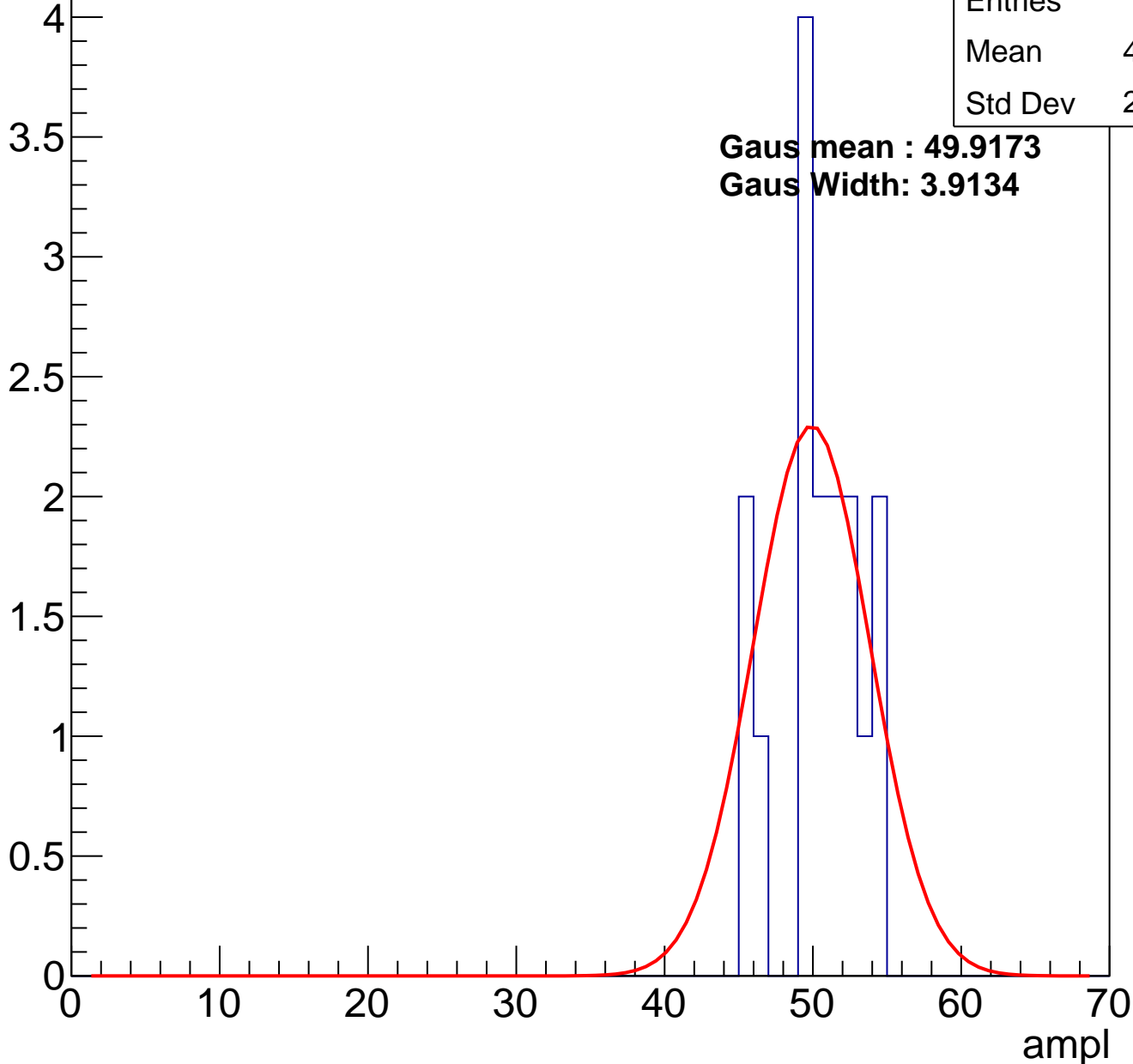
B0L100S, U5-ch40, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	52.88
Std Dev	1.536

Entry

ampl

B0L100S, U5-ch40, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch40, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch40, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch40, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch41, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch41, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch41, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch41, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch41, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch41, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch41, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch41, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch42, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
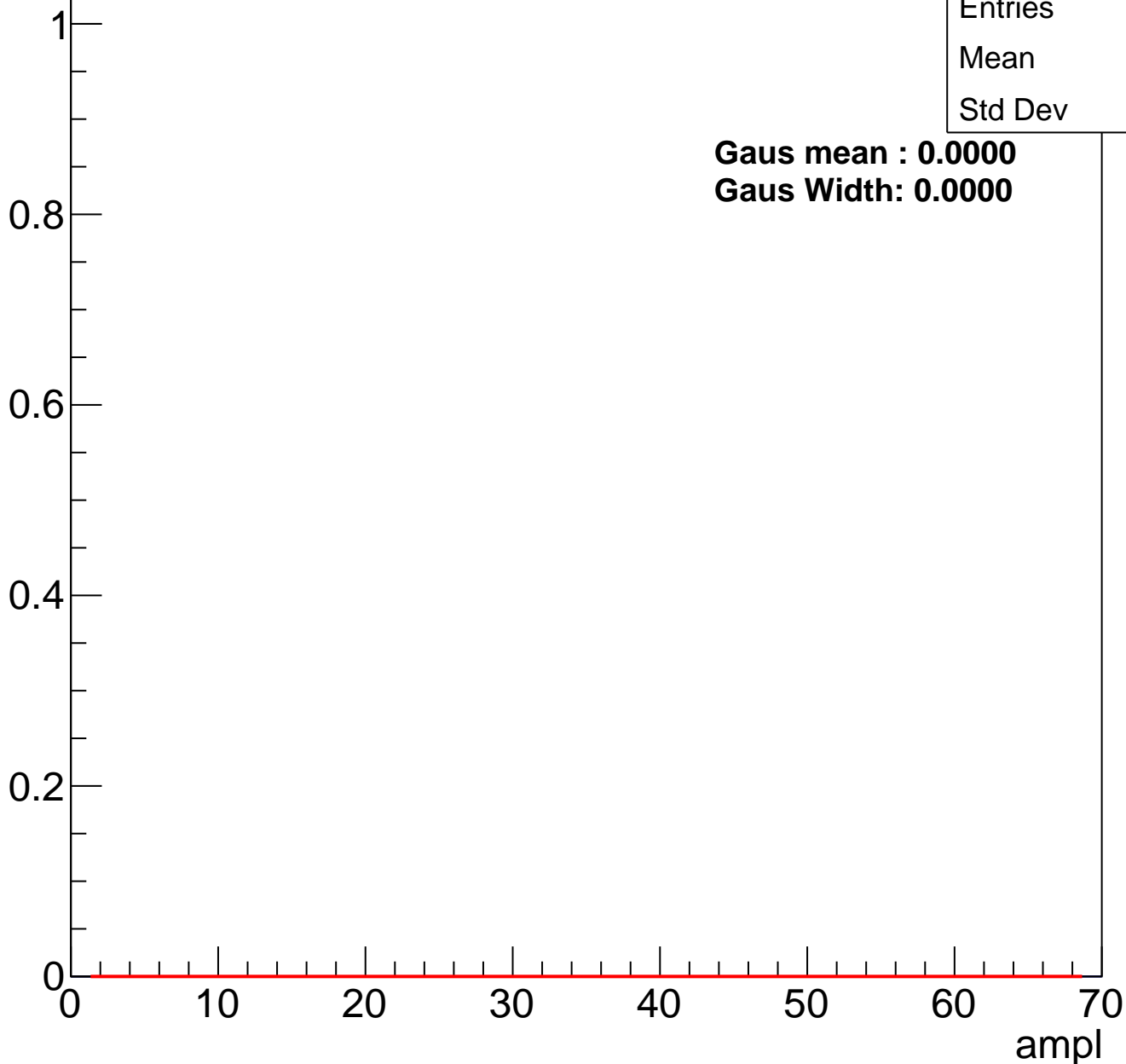
B0L100S, U5-ch42, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch42, adc2
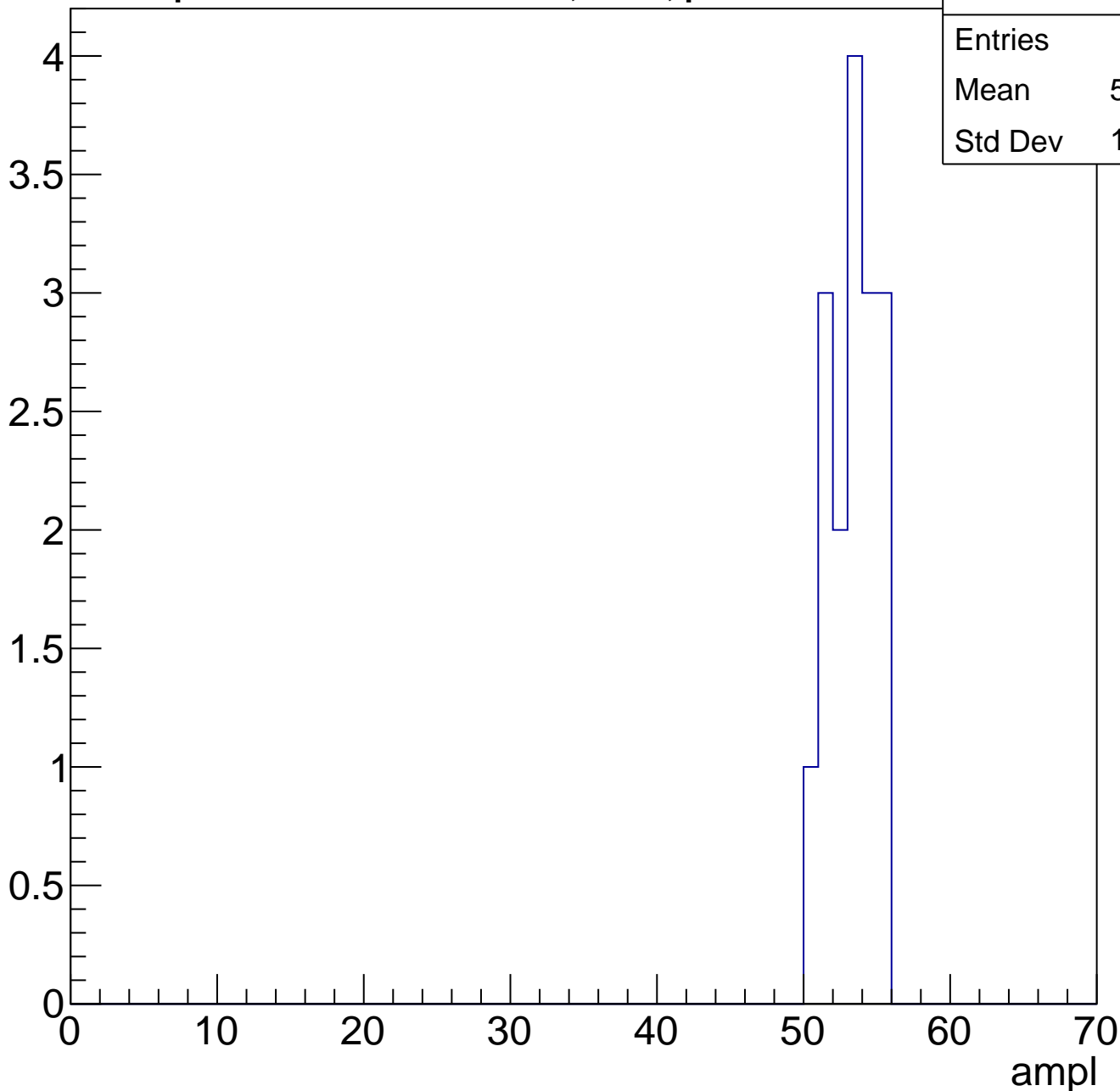
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch42, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch42, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch42, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch42, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch42, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch43, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch43, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch43, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch43, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch43, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch43, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch43, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch43, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch44, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch44, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch44, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch44, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch44, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch44, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch44, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch44, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch45, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch45, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch45, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch45, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch45, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch45, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch45, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch45, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch46, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	20.94
Std Dev	13.72

Gaus mean : 34.0132
Gaus Width: 6.0074
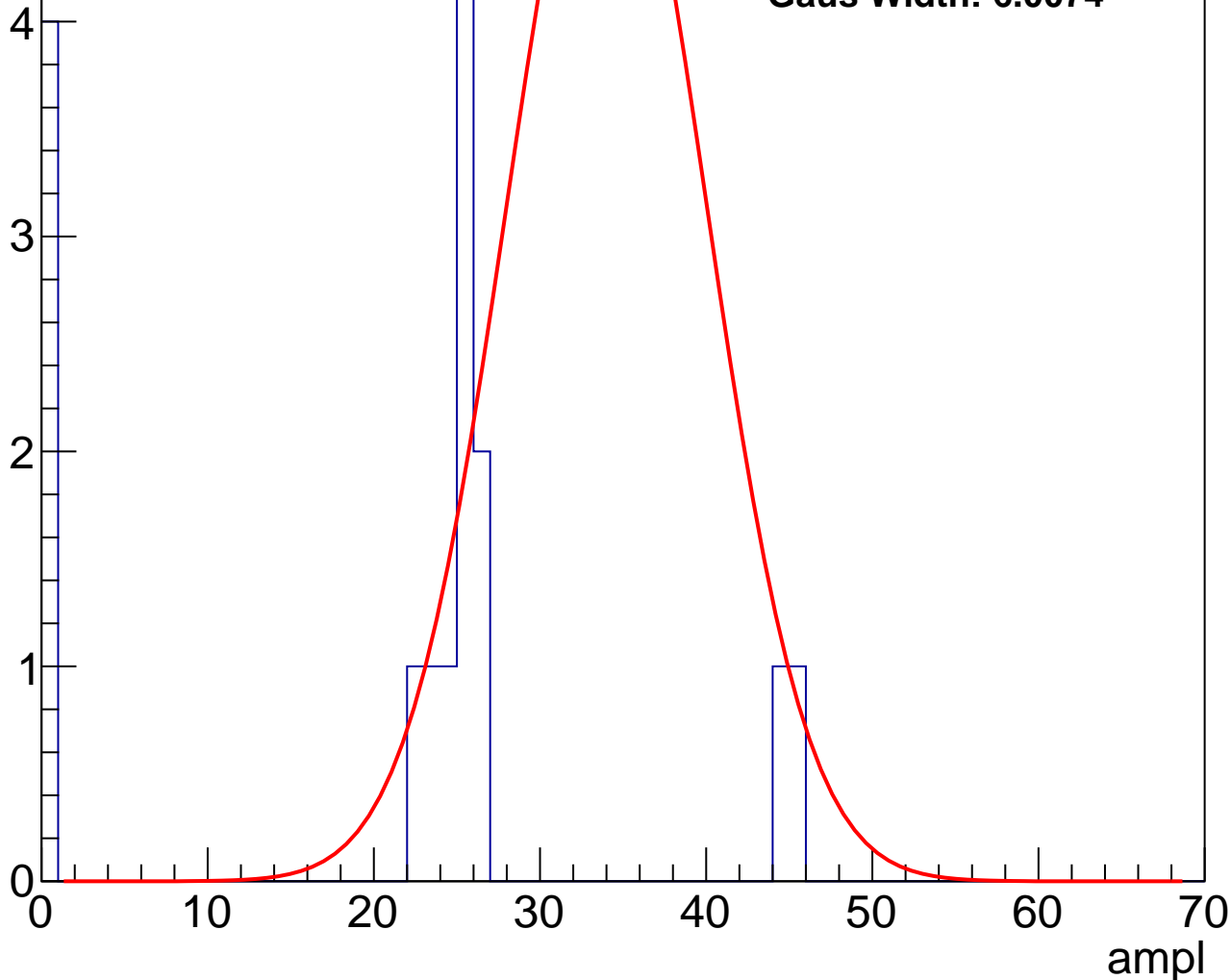
B0L100S, U5-ch46, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	8
Mean	46.12
Std Dev	0.7806

Gaus mean : 0.0000
Gaus Width: 0.0000
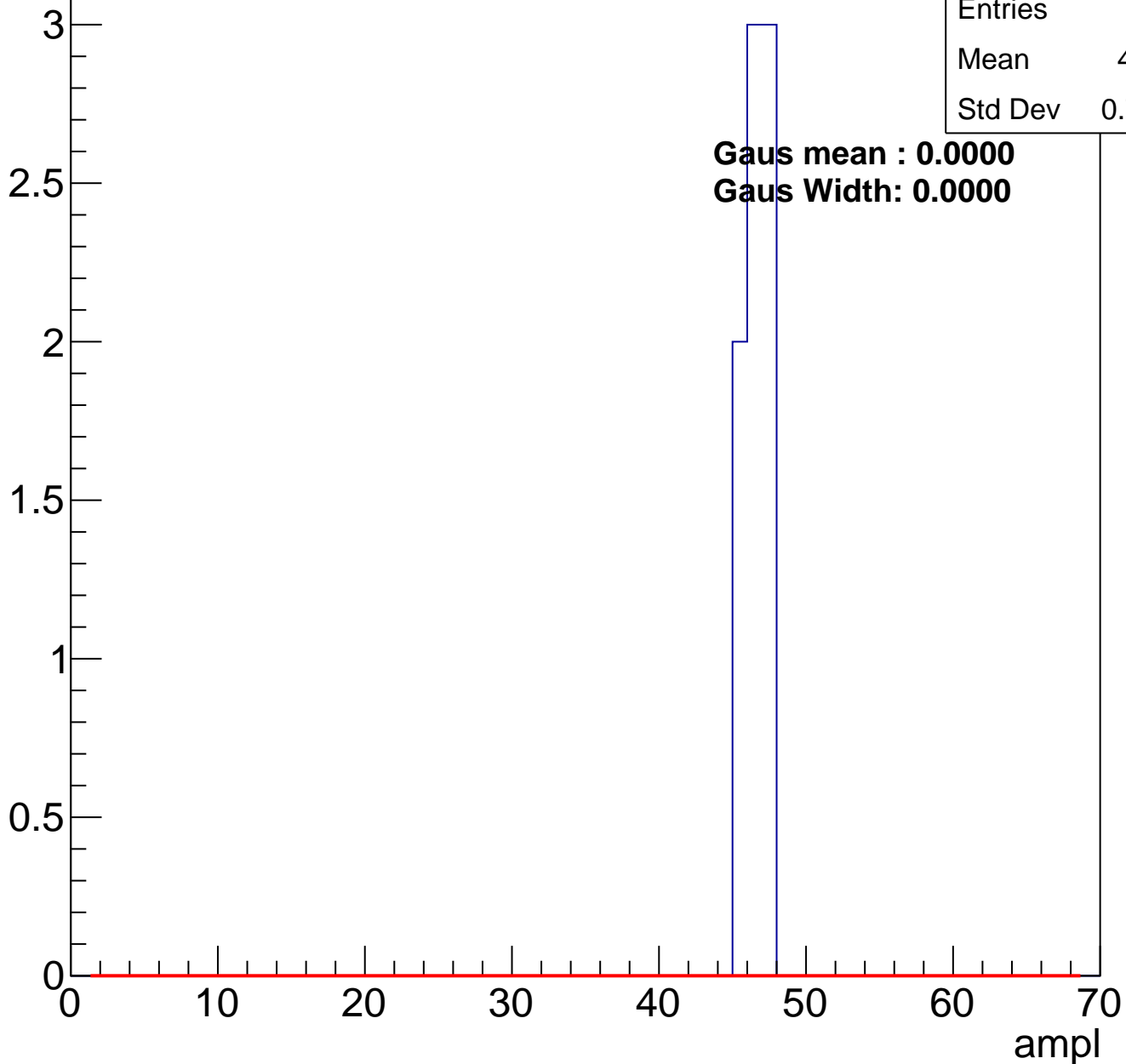
Entry

ampl

B0L100S, U5-ch46, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	38.17
Std Dev	17.08

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
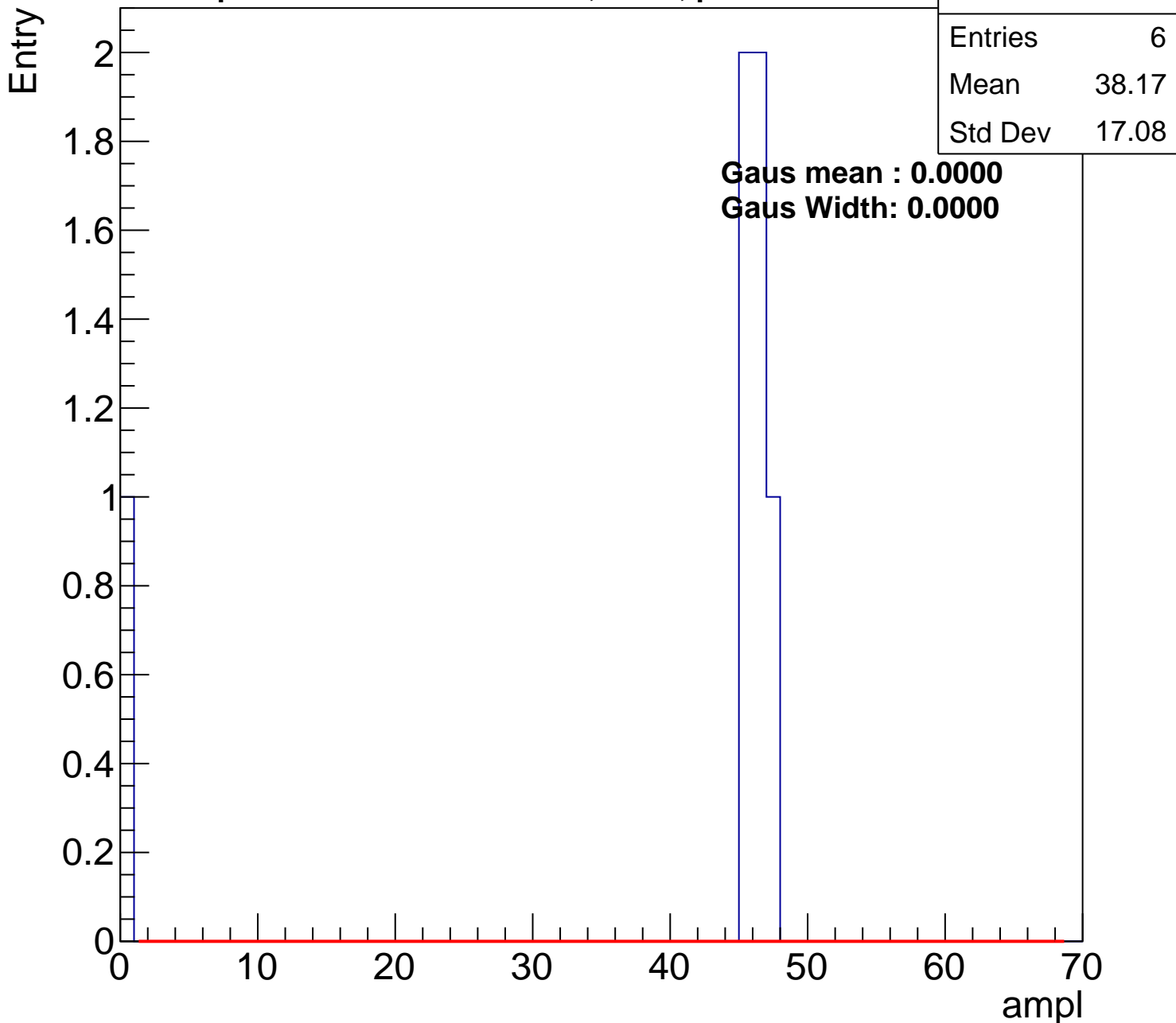
B0L100S, U5-ch46, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch46, adc4

calib_packv5_042523_0143.root, FC#6, port A1
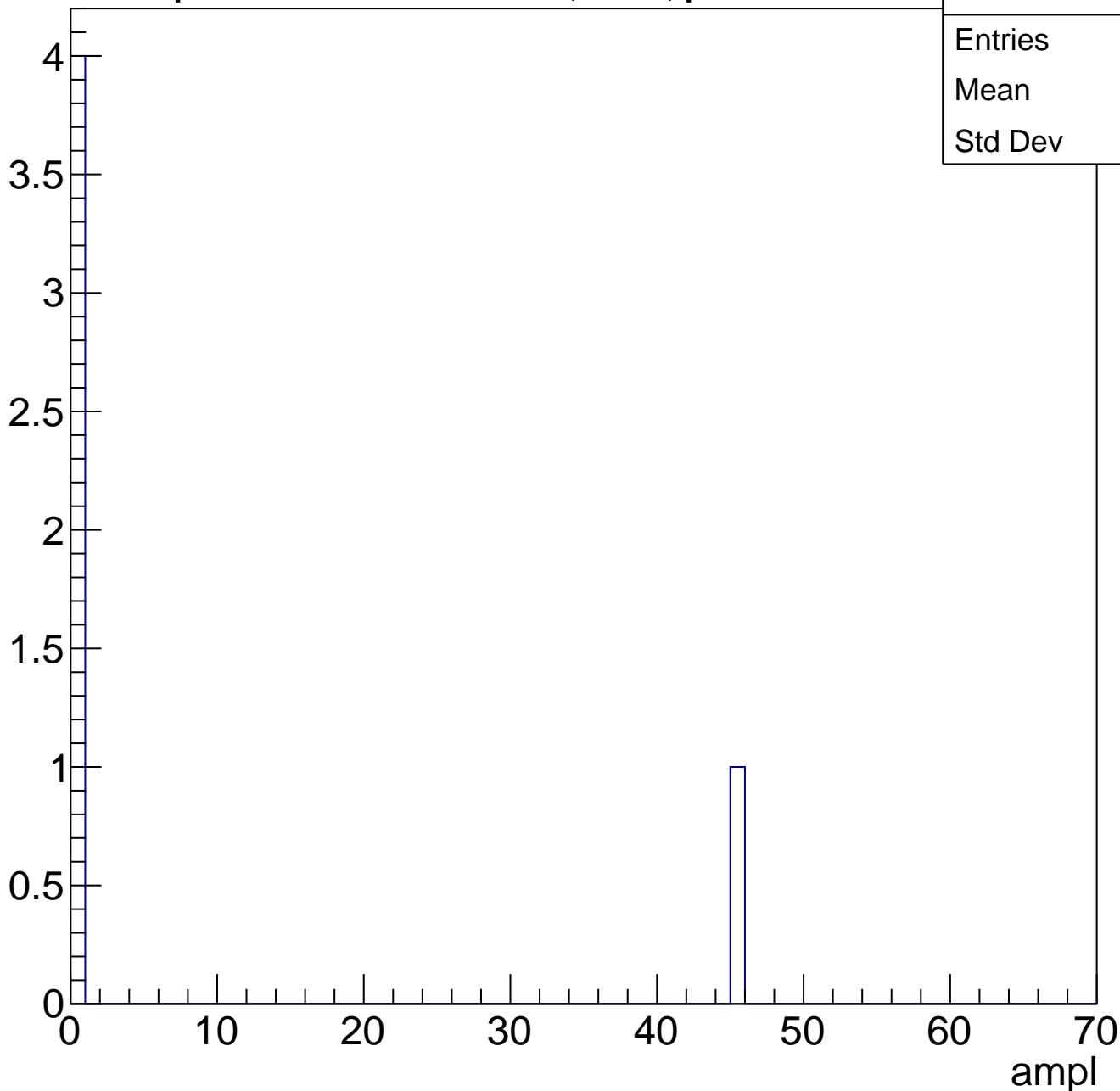
Entries	5
Mean	9
Std Dev	18

Entry

ampl

B0L100S, U5-ch46, adc5

calib_packv5_042523_0143.root, FC#6, port A1
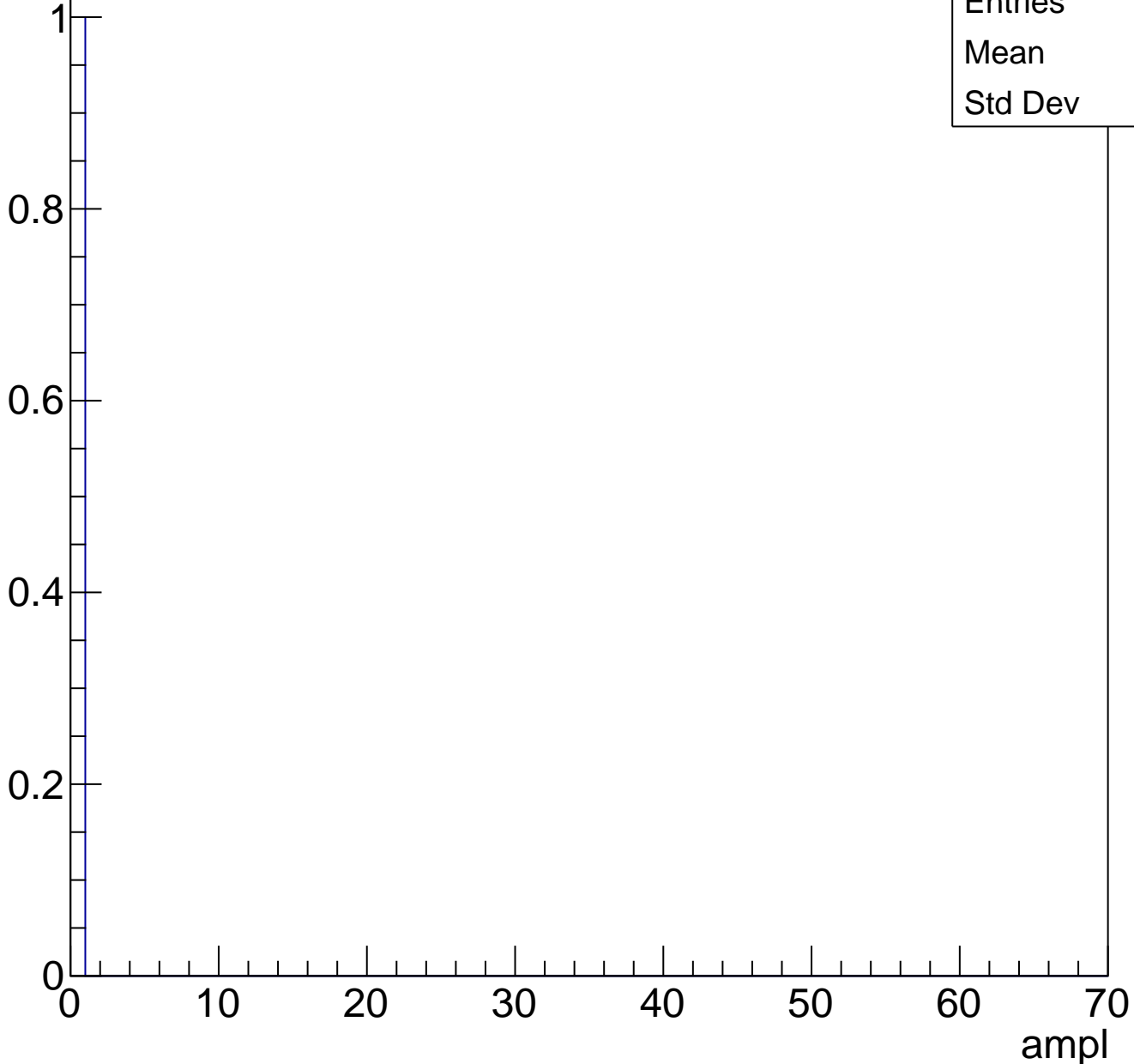
Entries	1
Mean	0
Std Dev	0

B0L100S, U5-ch46, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch46, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch47, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	42.67
Std Dev	1.247

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch47, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	19
Mean	43.32
Std Dev	2.407

Gaus mean : -27.6582
Gaus Width: 19.2859
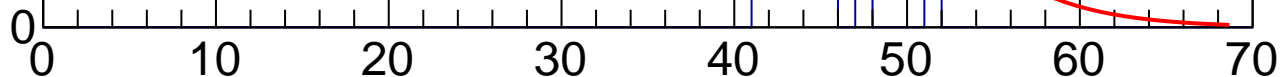
B0L100S, U5-ch47, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	33
Mean	48.03
Std Dev	2.504

Gaus mean : 48.7235
Gaus Width: 2.7880
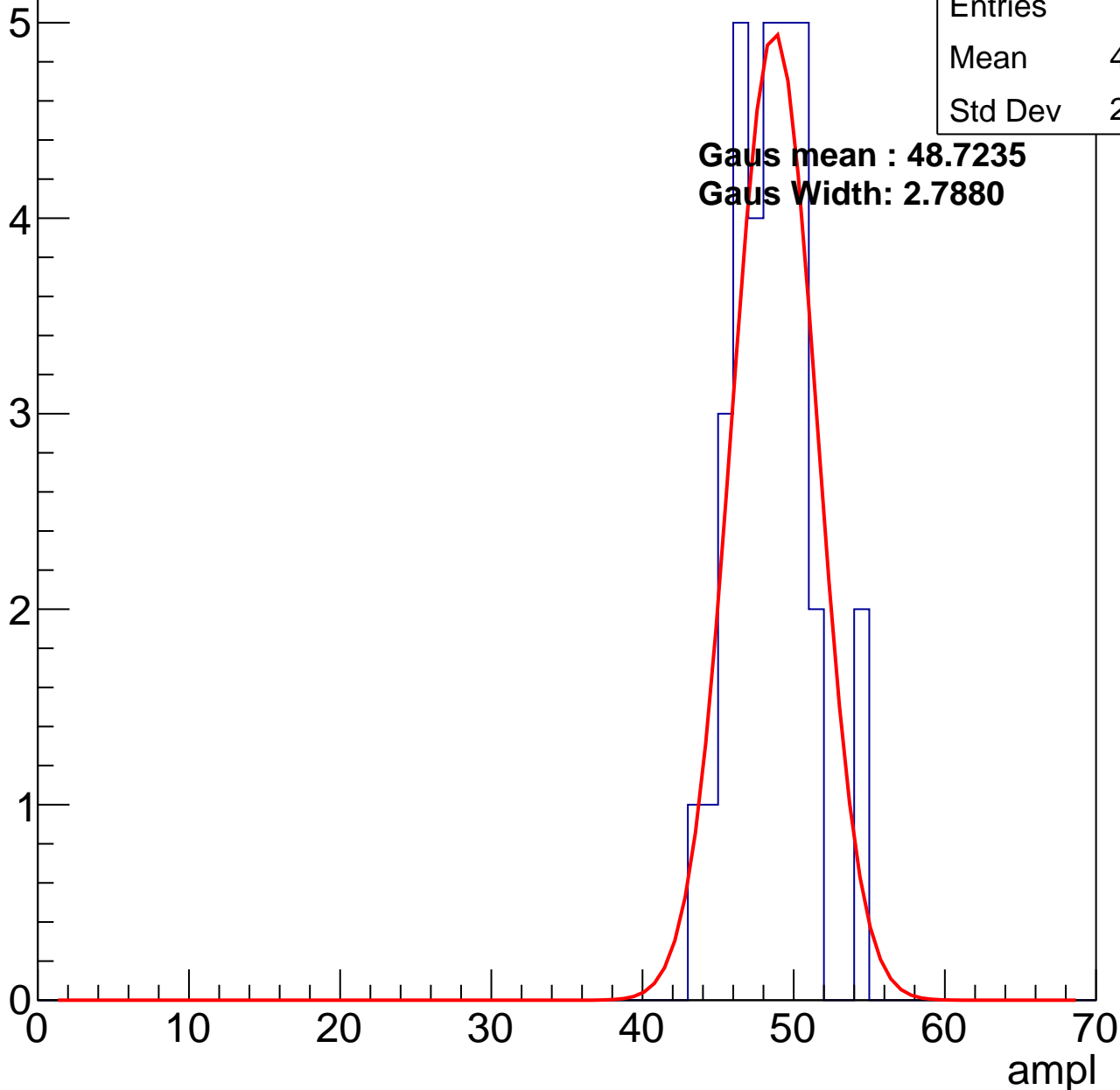
Entry

ampl

B0L100S, U5-ch47, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	54.33
Std Dev	0.4714

Entry

ampl
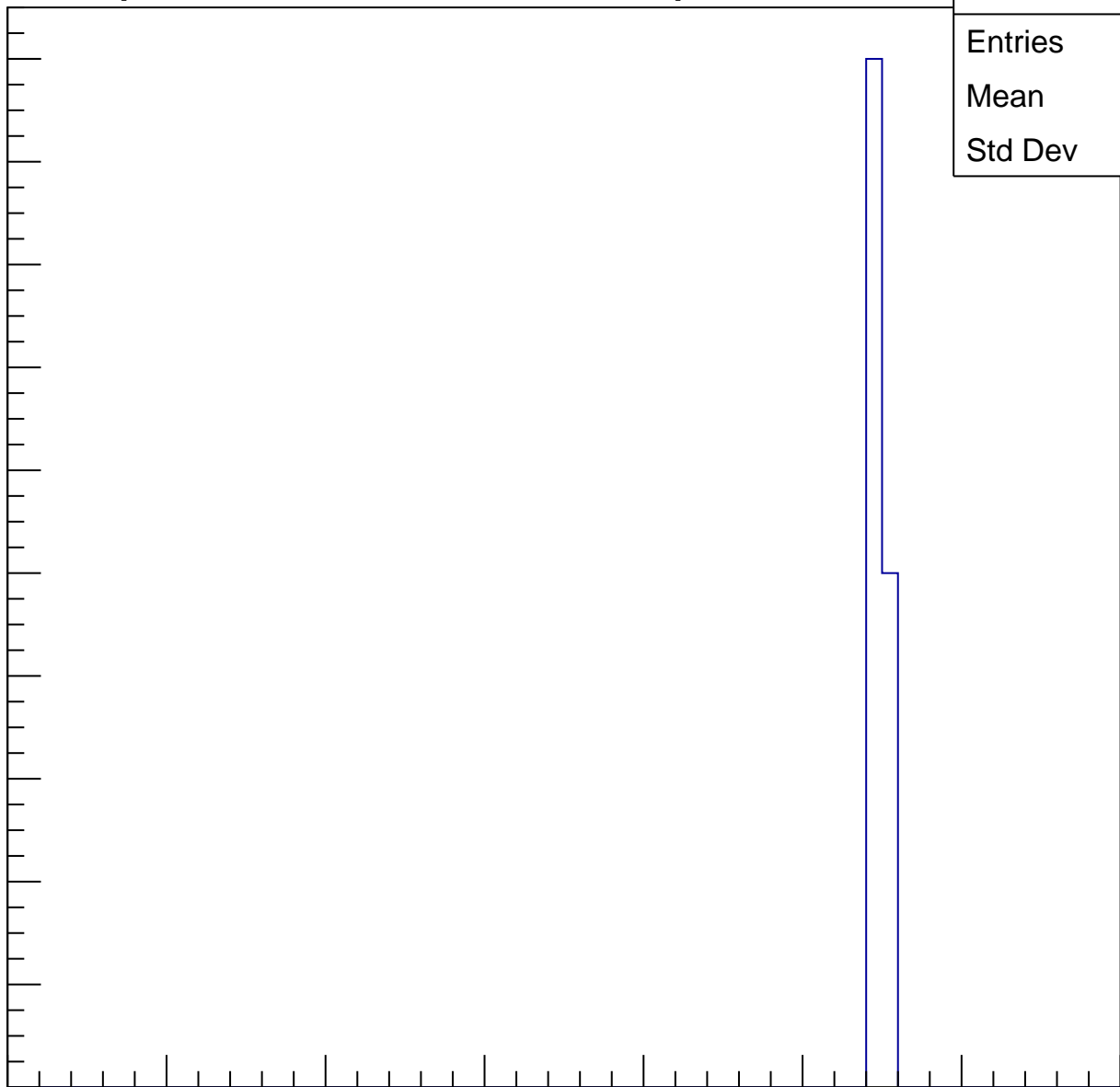
B0L100S, U5-ch47, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch47, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch47, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch47, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch48, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch48, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch48, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch48, adc3

calib_packv5_042523_0143.root, FC#6, port A1

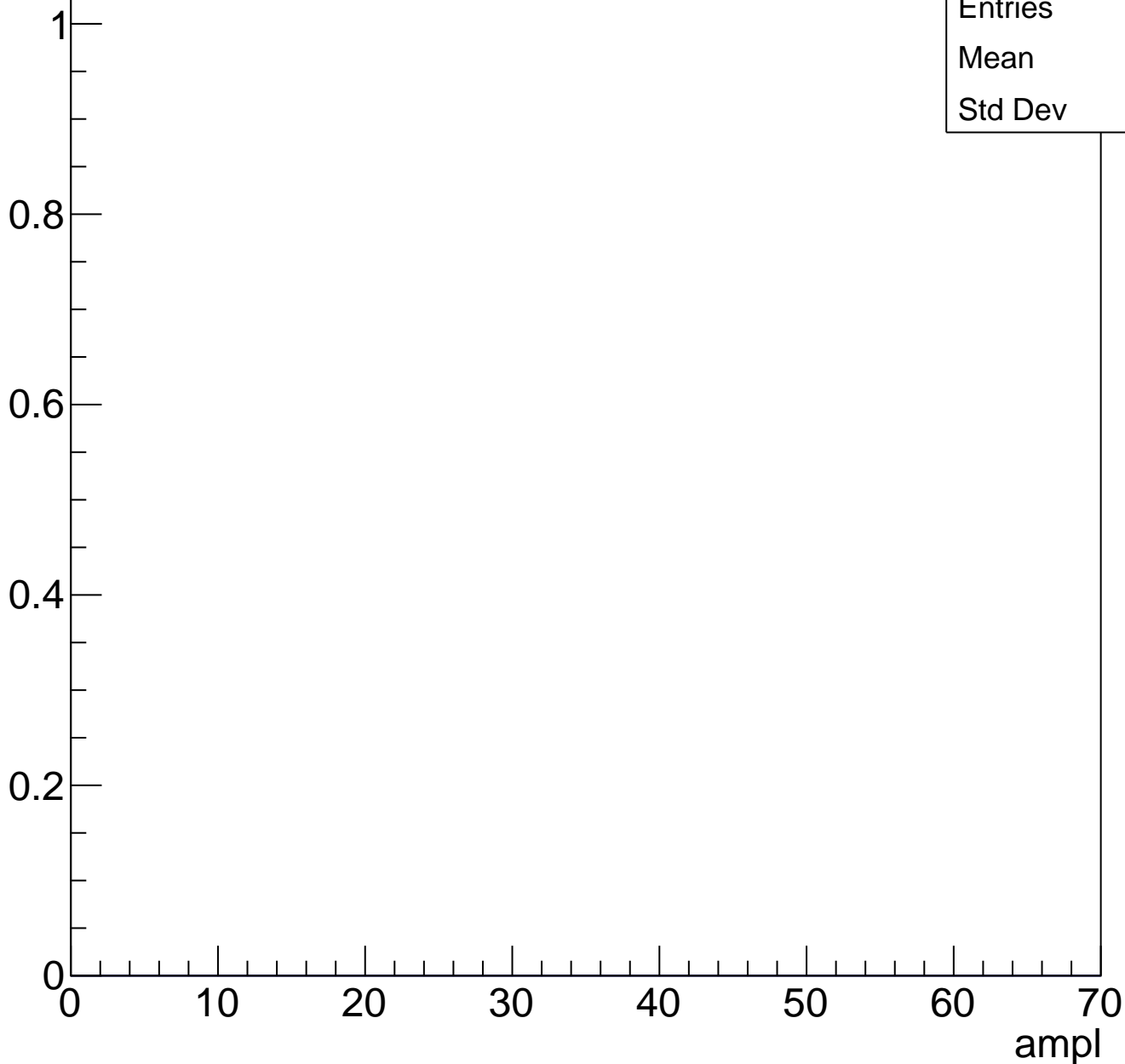|	
Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch48, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch48, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch48, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch48, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch49, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch49, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch49, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch49, adc3

calib_packv5_042523_0143.root, FC#6, port A1

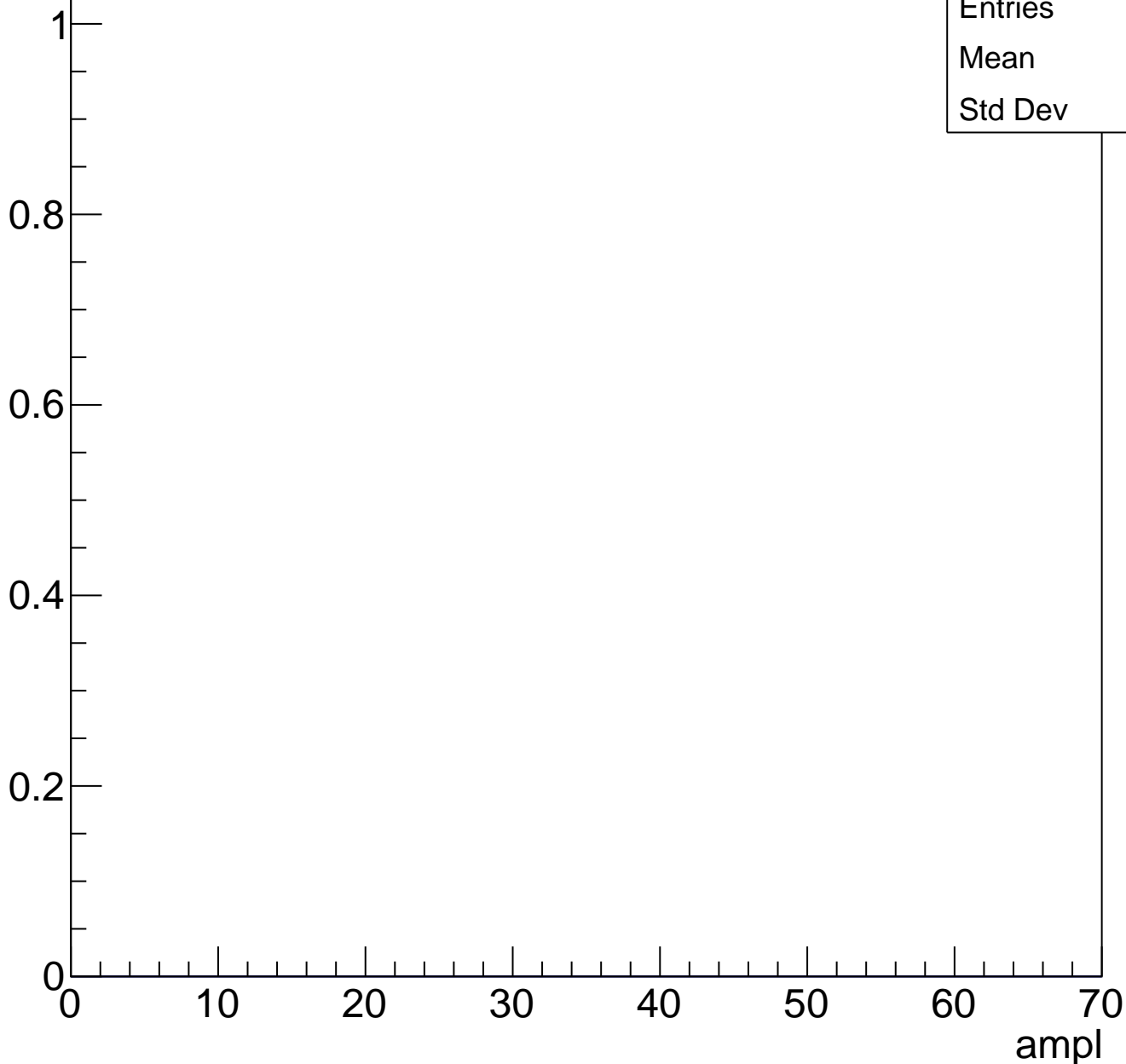|	
Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch49, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch49, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch49, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch49, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch50, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch50, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	45
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch50, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch50, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch50, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch50, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch50, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch50, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch51, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch51, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	46.57
Std Dev	1.591

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
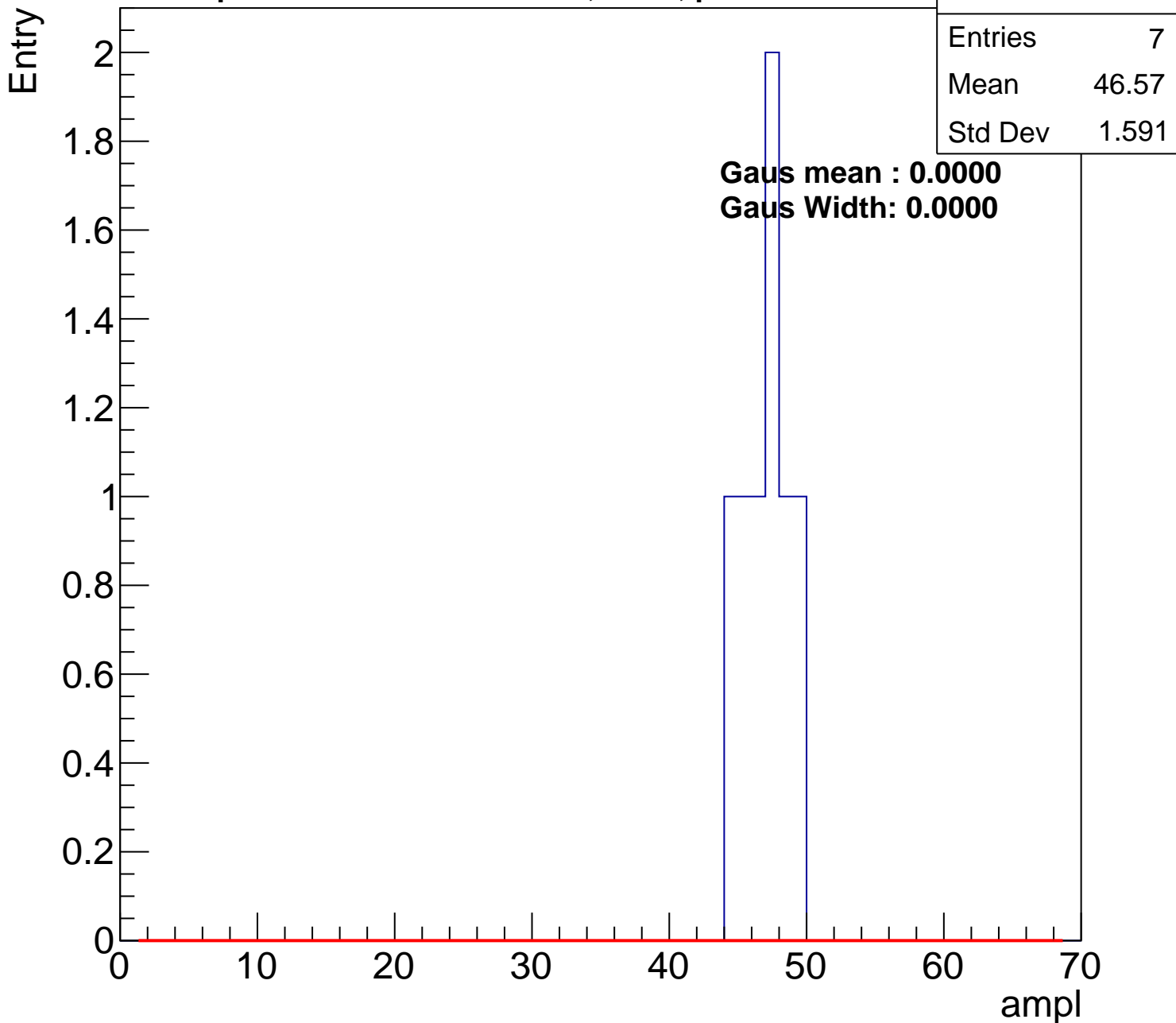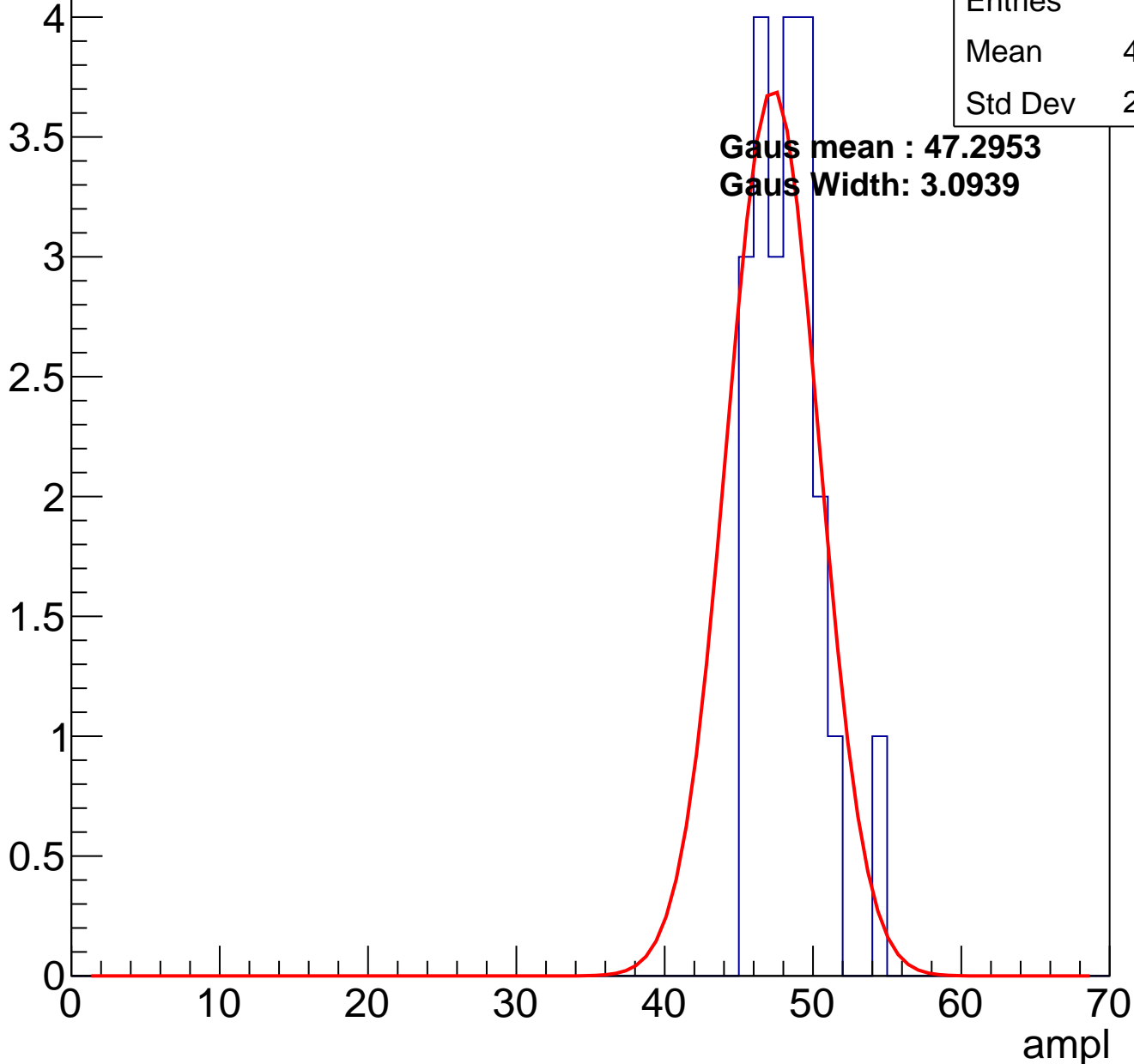
B0L100S, U5-ch51, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	51.5
Std Dev	1.803

Entry

ampl

B0L100S, U5-ch51, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch51, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch51, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch51, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch52, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch52, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch52, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch52, adc3

calib_packv5_042523_0143.root, FC#6, port A1

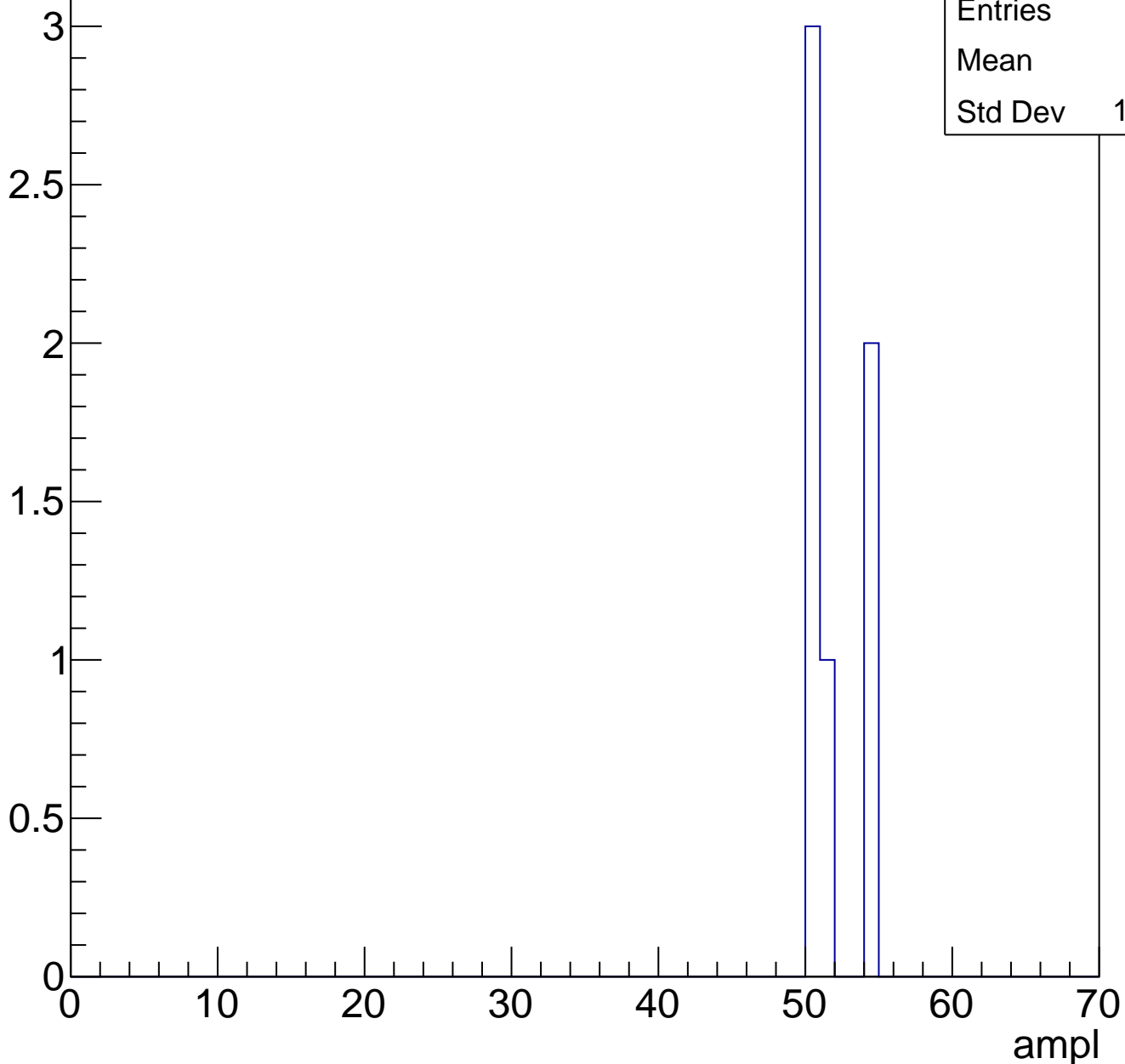|	
Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch52, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch52, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch52, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch52, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch53, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch53, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch53, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch53, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch53, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch53, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch53, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch53, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch54, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries		2
Mean		41.5
Std Dev		1.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch54, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	42.88
Std Dev	1.691

Gaus mean : 43.2855
Gaus Width: 3.1895

Entry

ampl
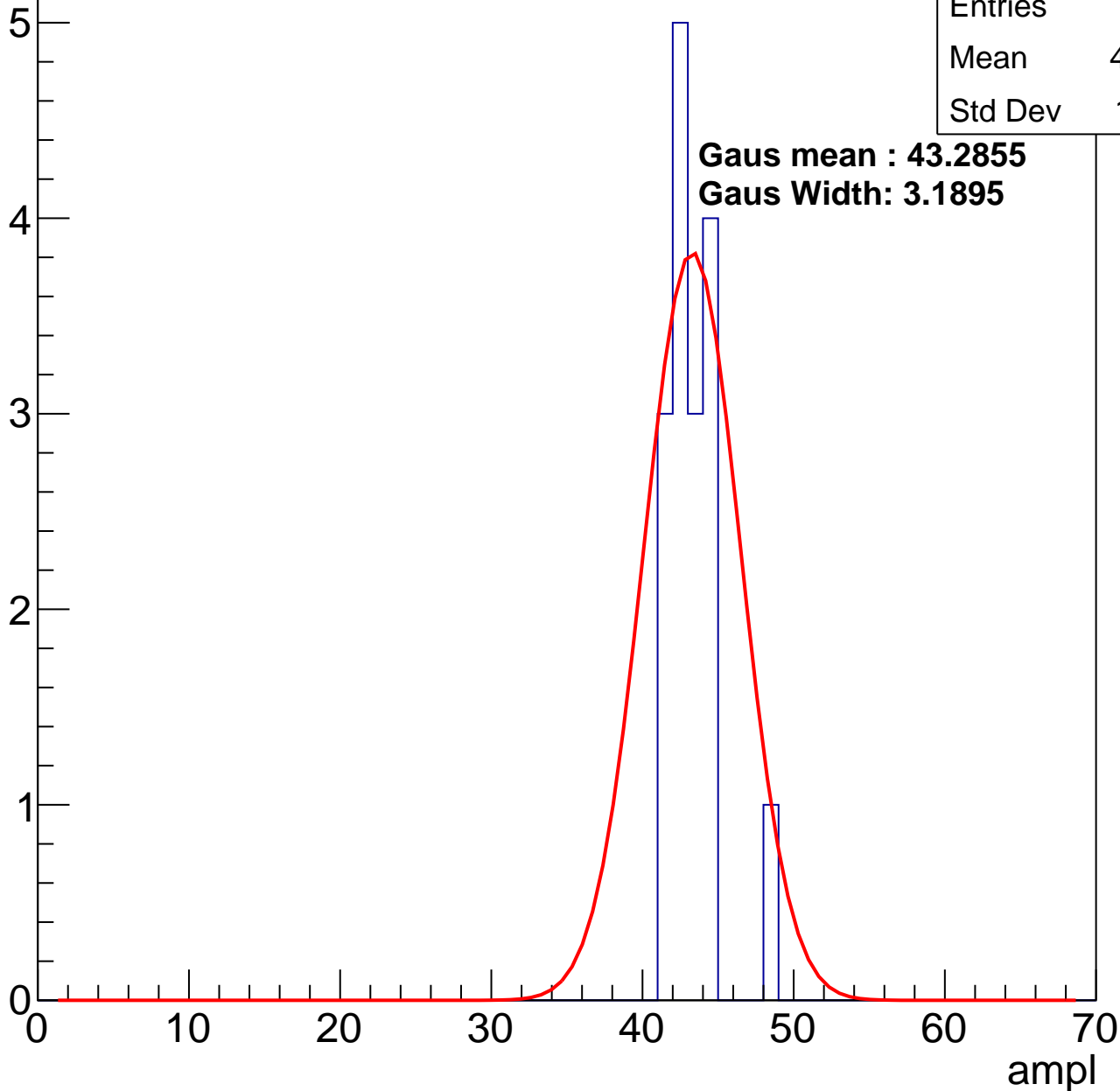
B0L100S, U5-ch54, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 47.9710
Gaus Width: 5.5988

Entries	12
Mean	47
Std Dev	3.808

Entry

ampl
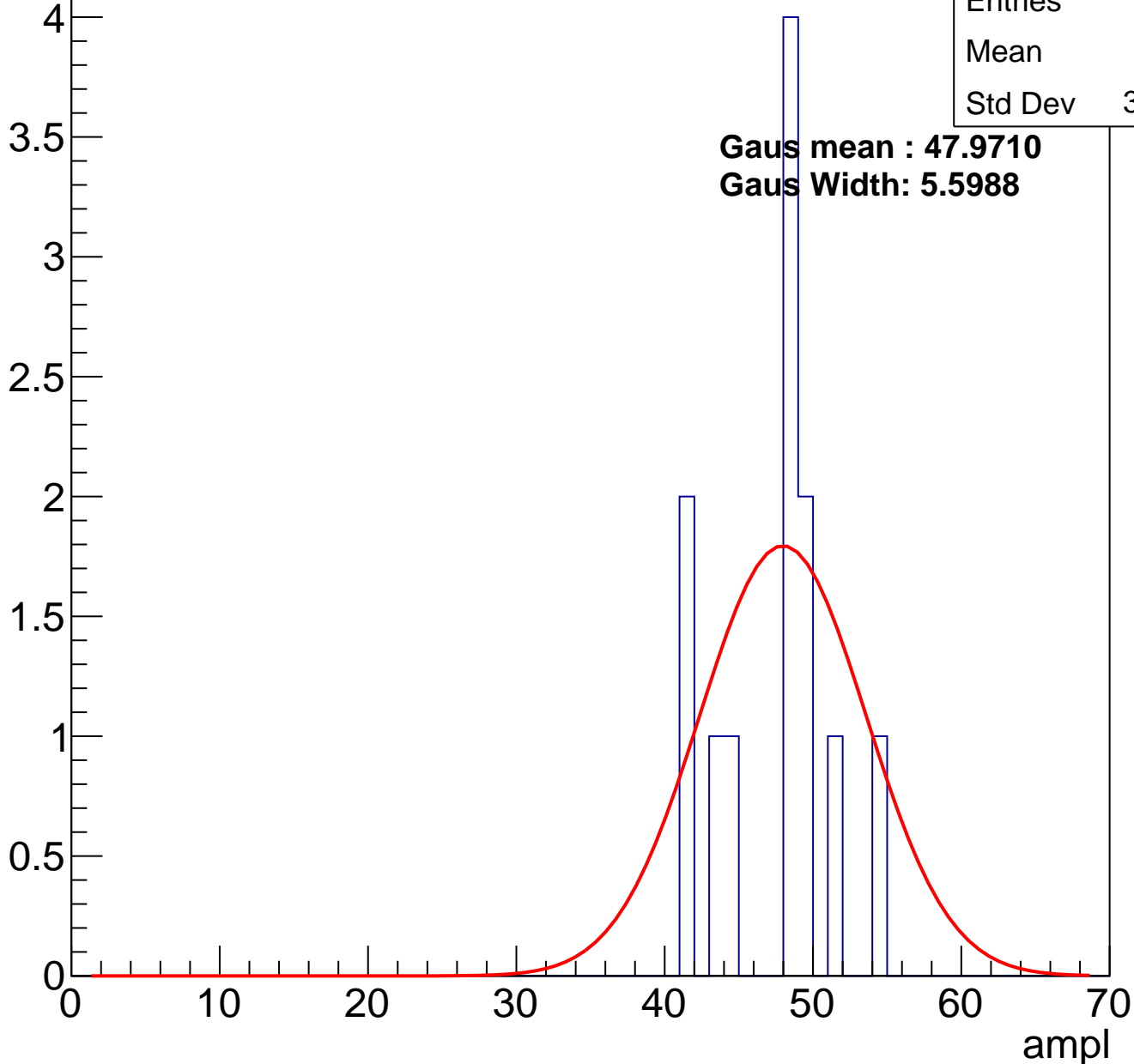
B0L100S, U5-ch54, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	13
Mean	51.23
Std Dev	3.886

Entry

ampl
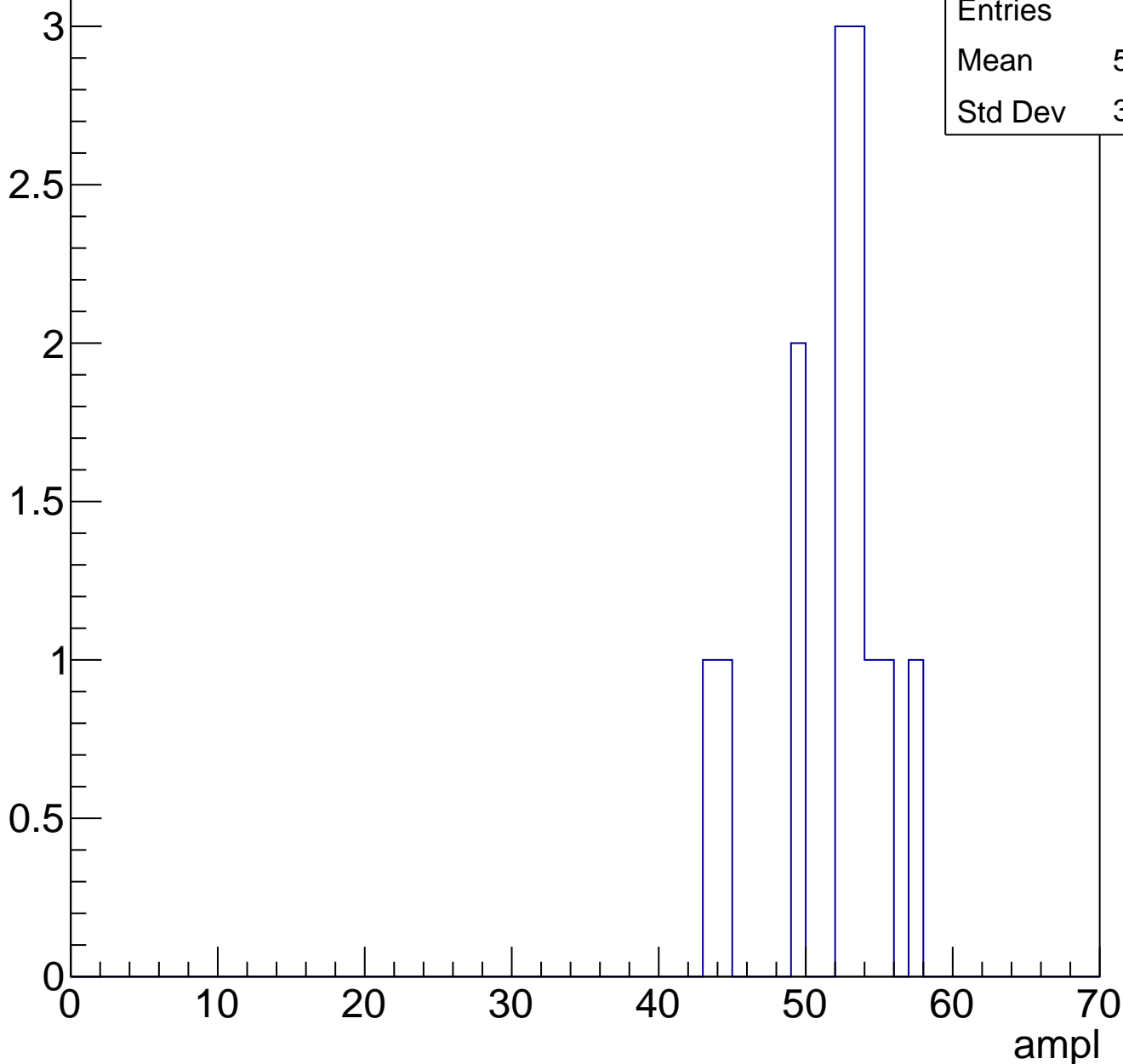
B0L100S, U5-ch54, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	52.33
Std Dev	2.055

Entry

ampl
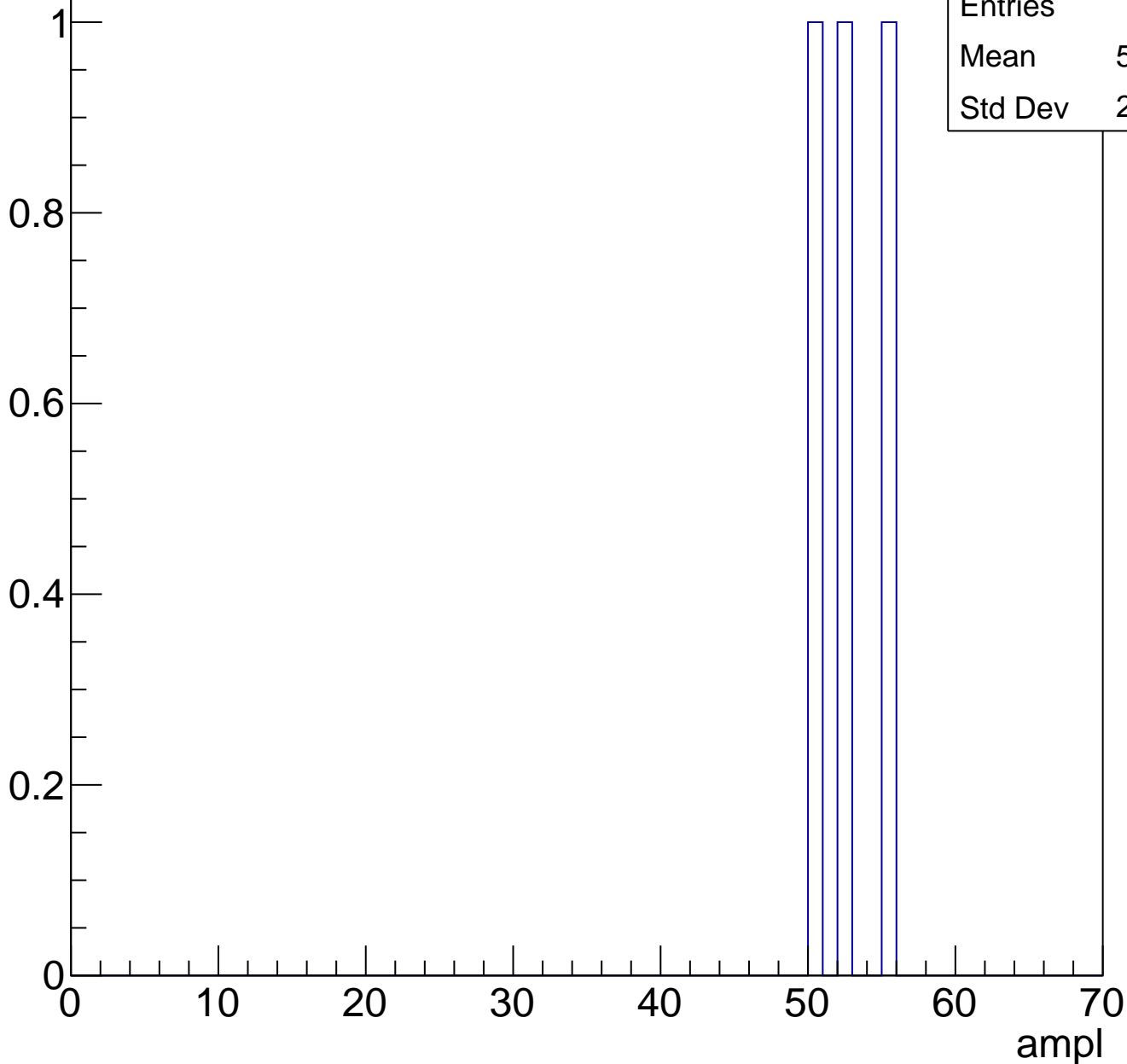
B0L100S, U5-ch54, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch54, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch54, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch55, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
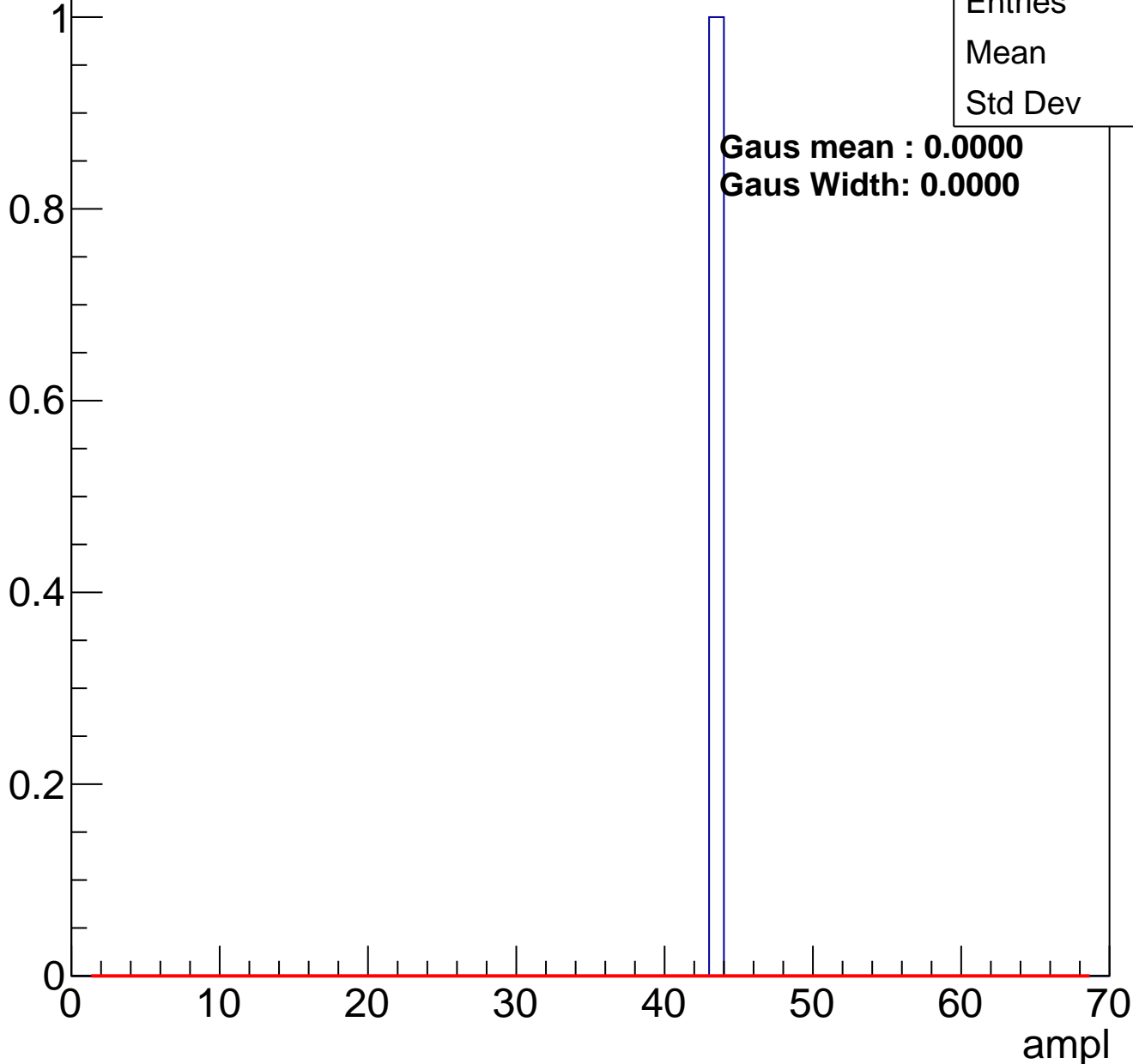
Entry

ampl

B0L100S, U5-ch55, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	43
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
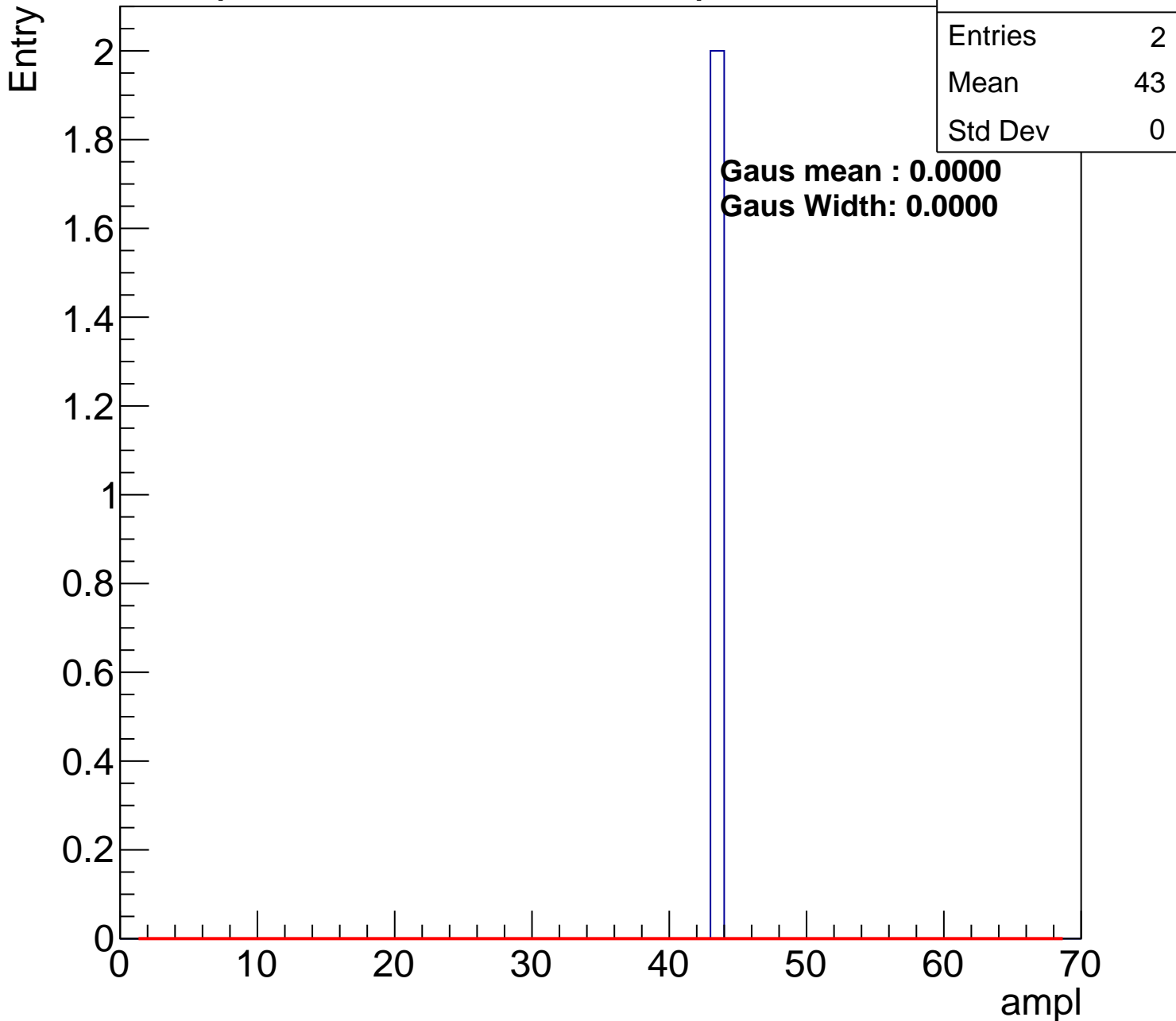
B0L100S, U5-ch55, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch55, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Entry

ampl

B0L100S, U5-ch55, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch55, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch55, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch55, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl
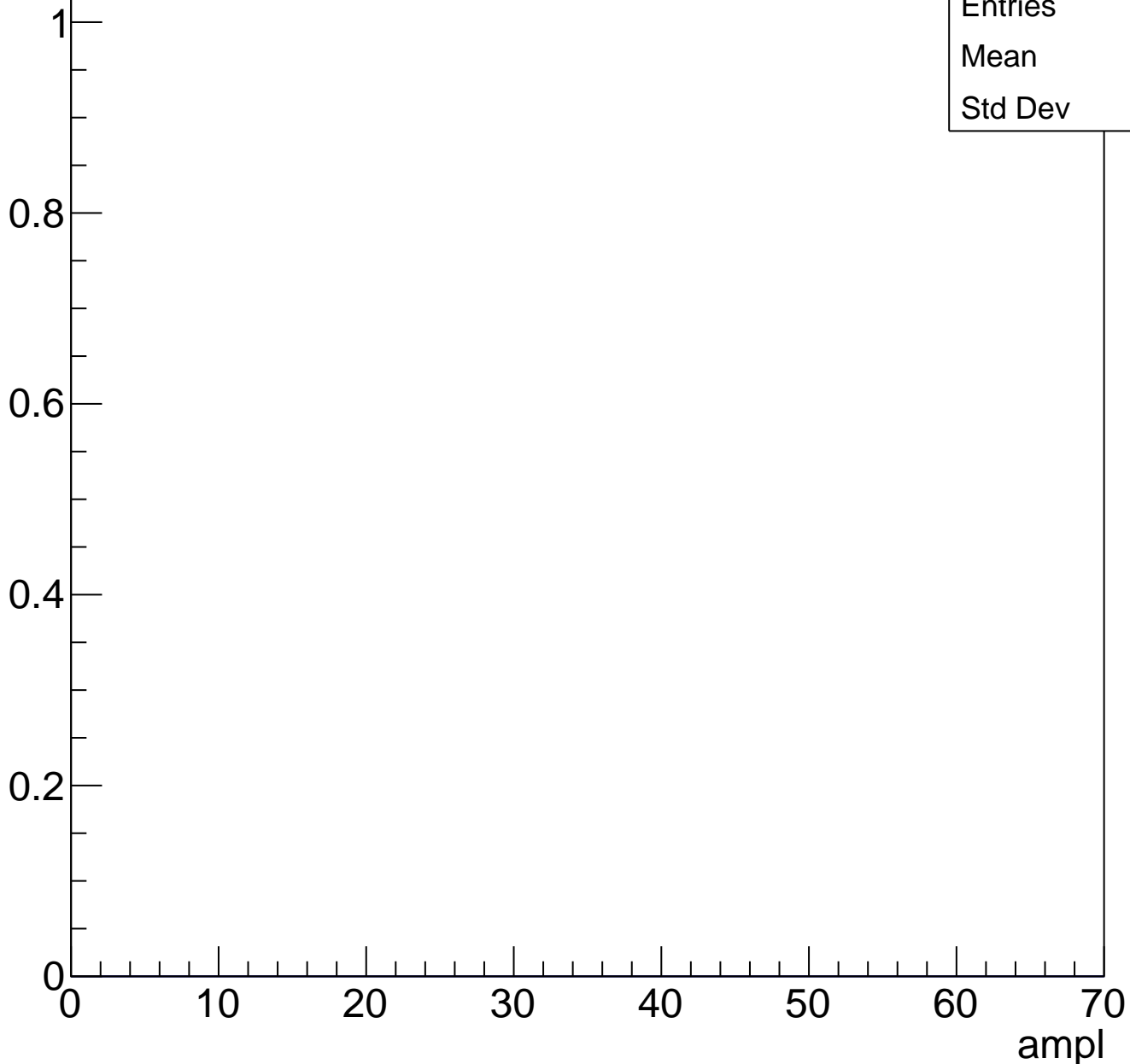
B0L100S, U5-ch56, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch56, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch56, adc2

calib_packv5_042523_0143.root, FC#6, port A1

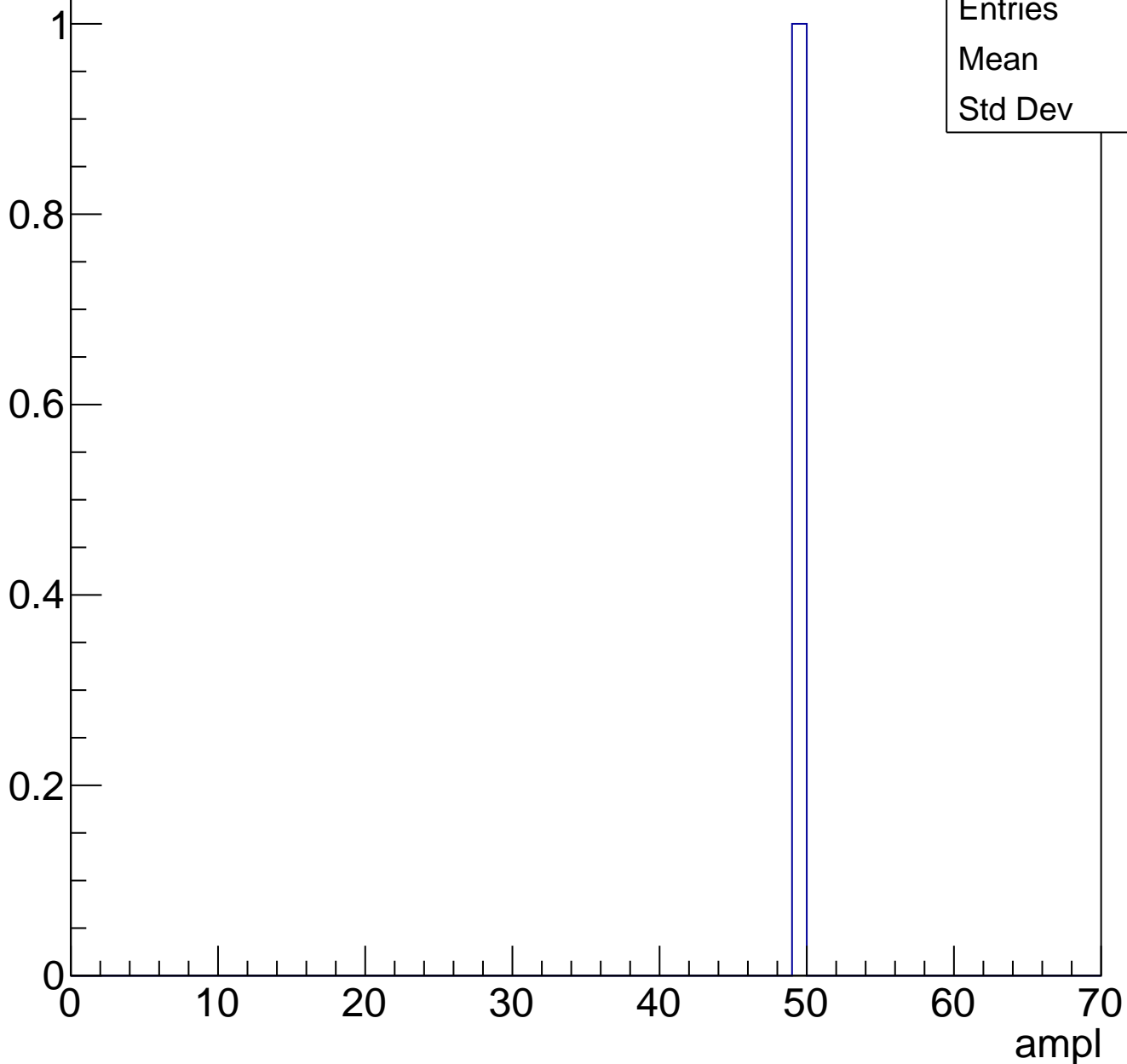| Entries | 7 |
| Mean | 47.14 |
| Std Dev | 1.355 |

Gaus mean : 0.0000
Gaus Width: 0.0000
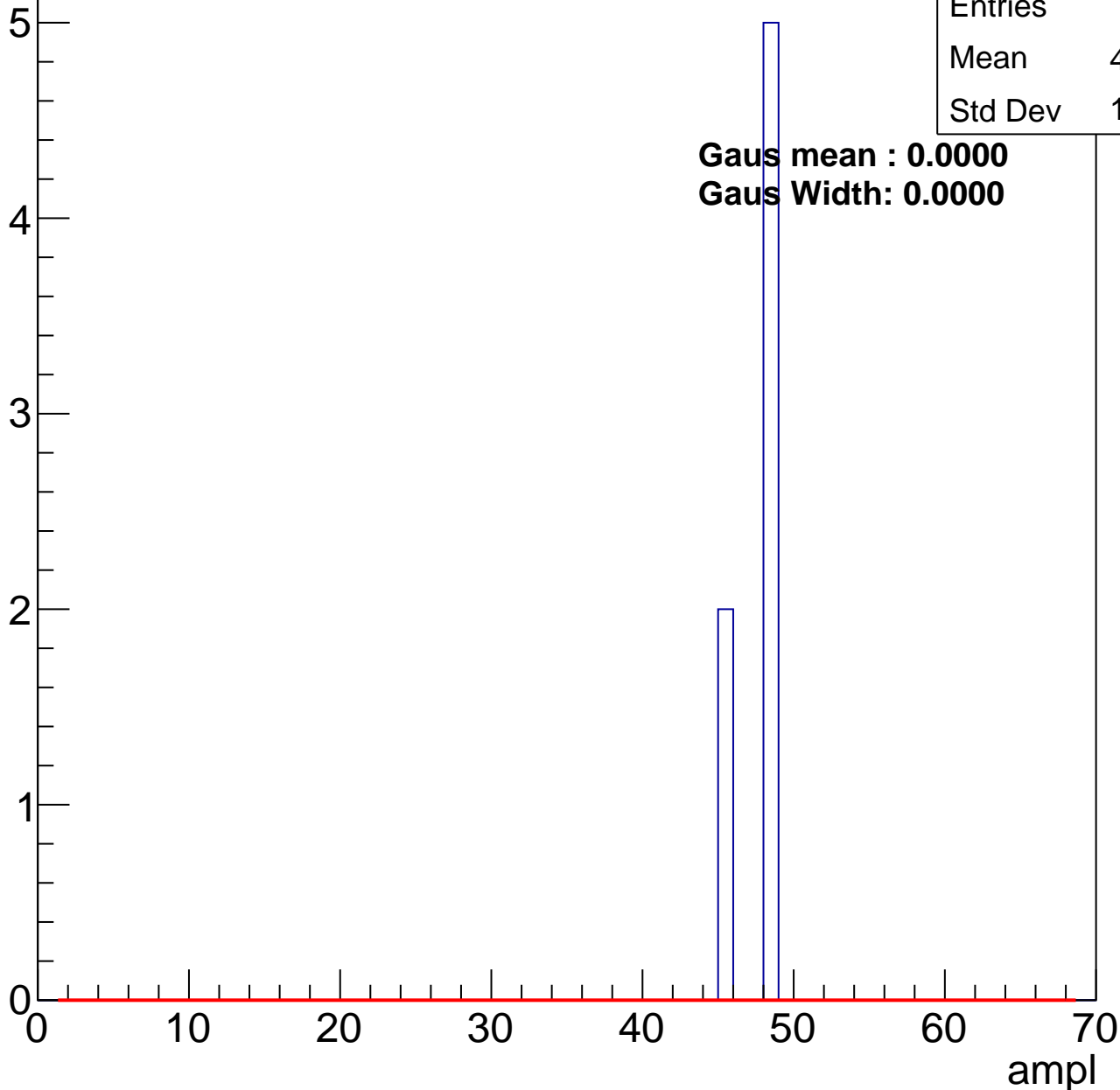
B0L100S, U5-ch56, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch56, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch56, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch56, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch56, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

B0L100S, U5-ch57, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch57, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch57, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch57, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch57, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch57, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch57, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch57, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch58, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch58, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	40
Std Dev	0

ampl
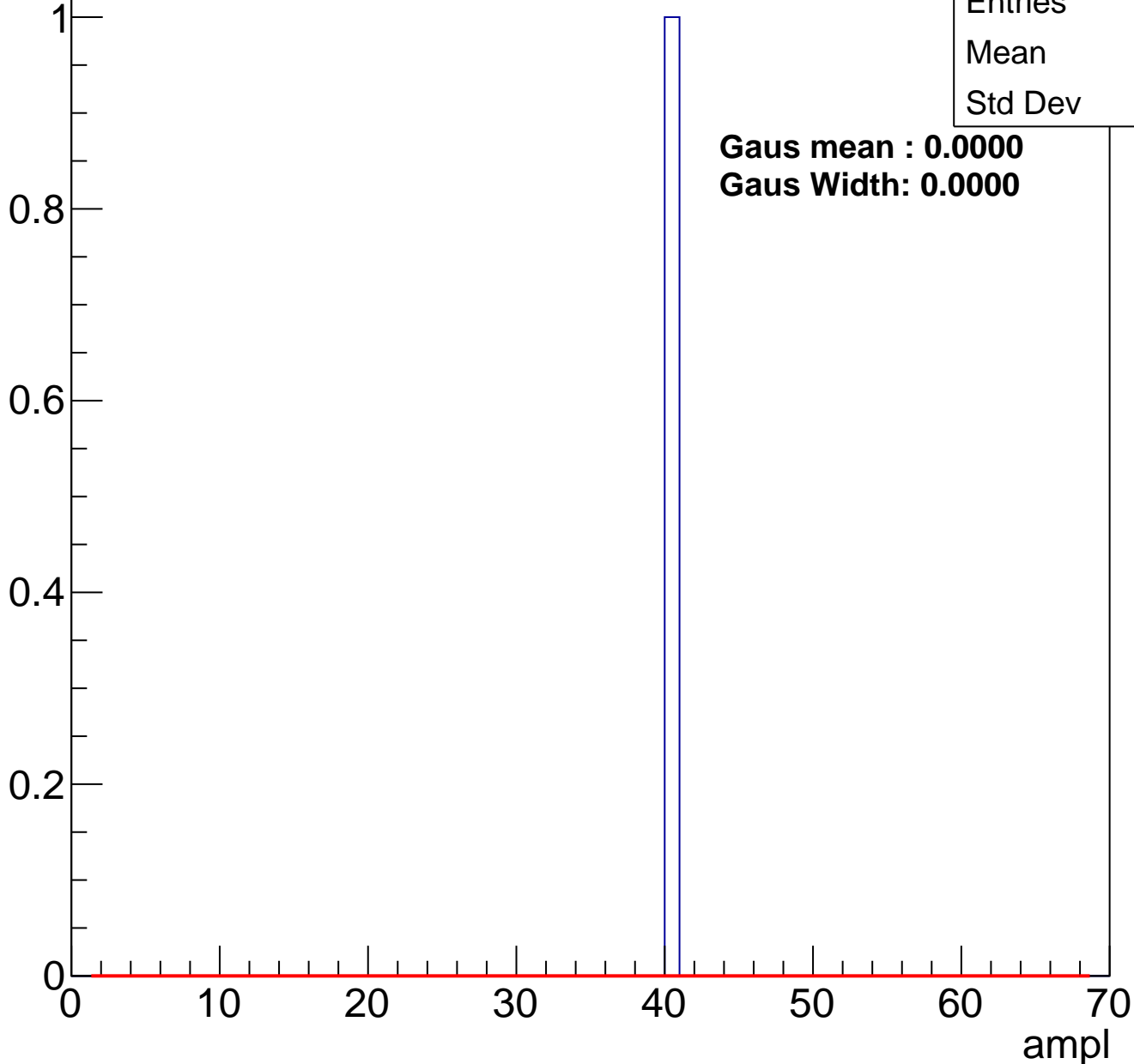
B0L100S, U5-ch58, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
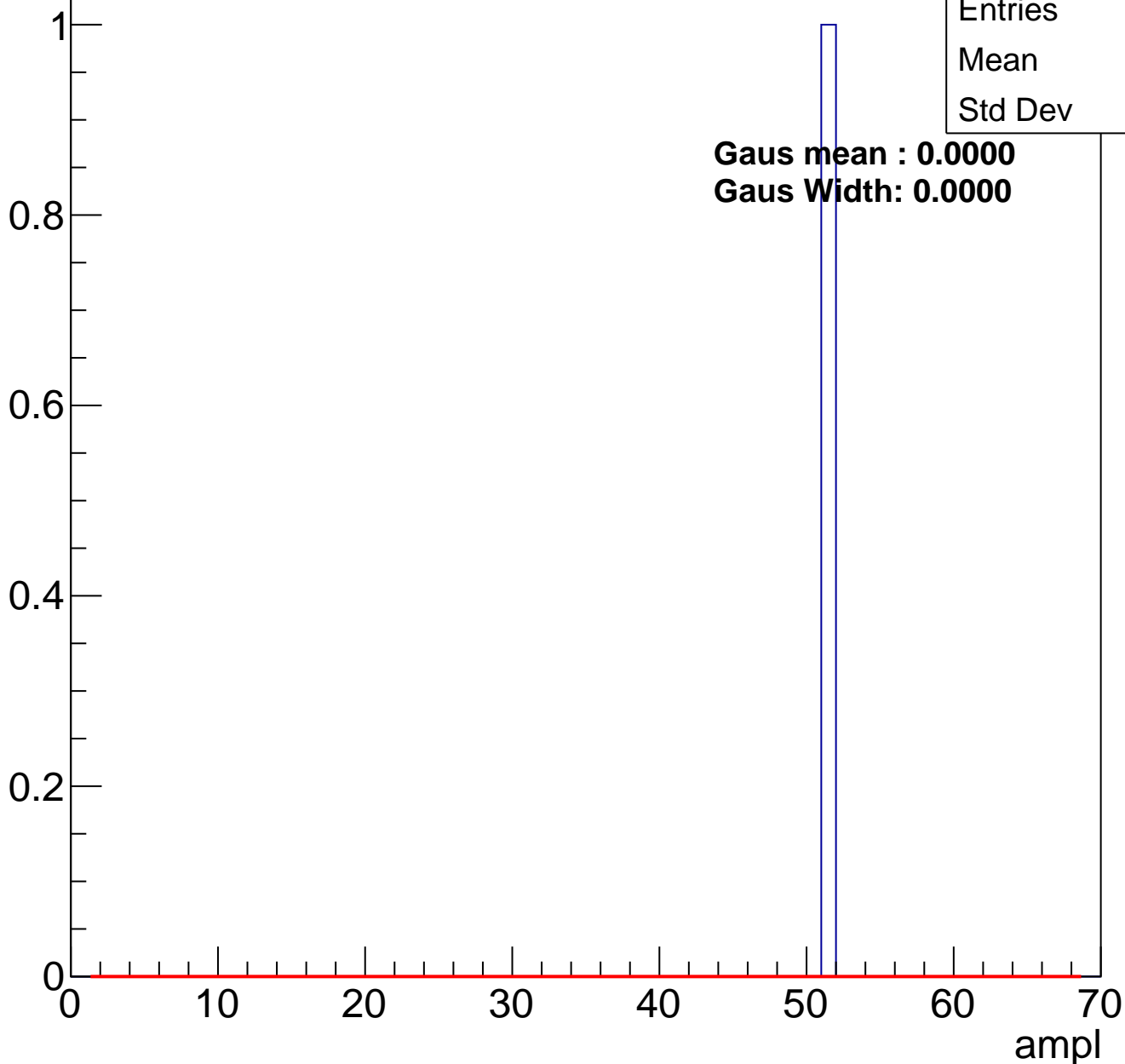
B0L100S, U5-ch58, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

ampl
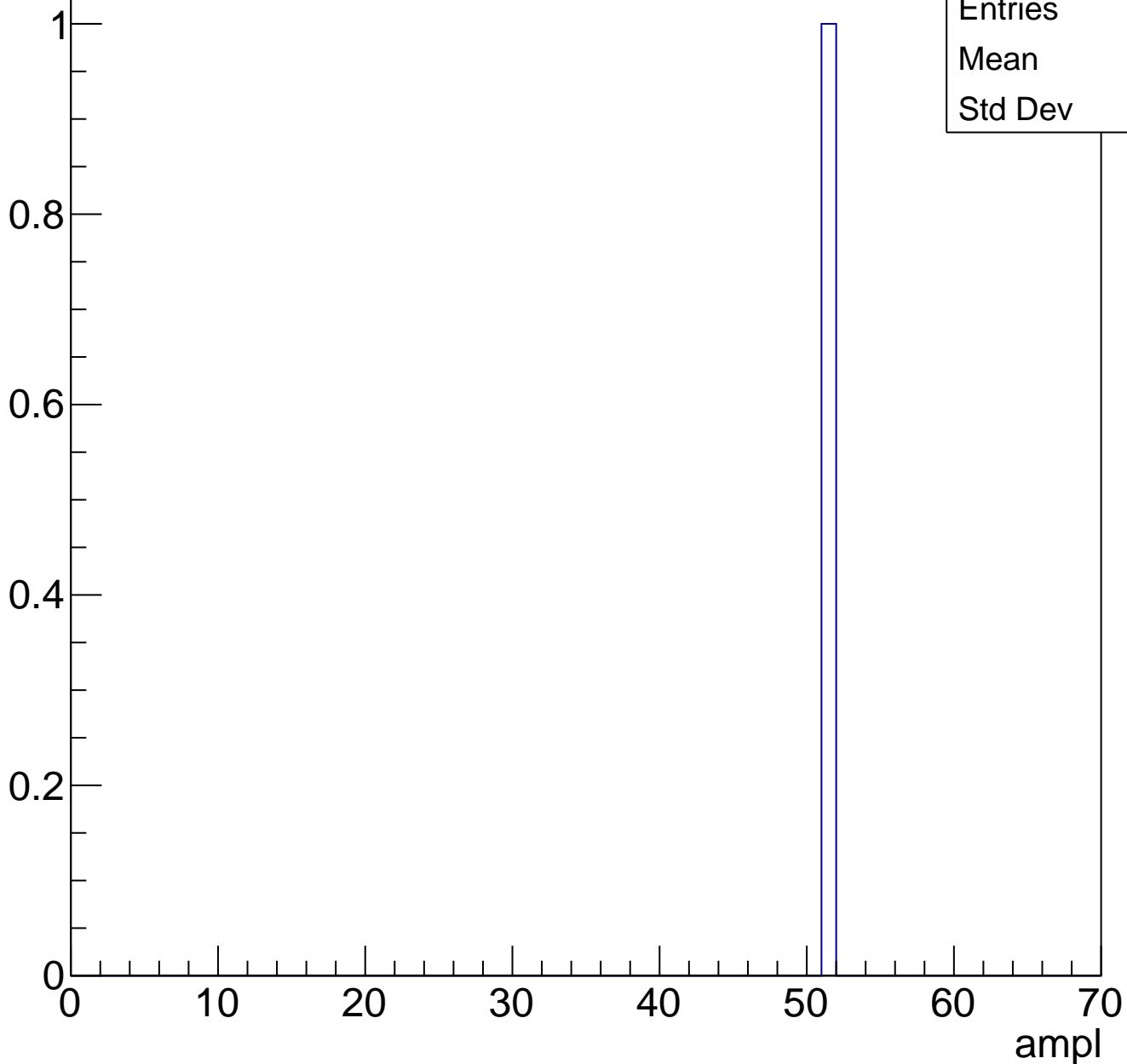
B0L100S, U5-ch58, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch58, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch58, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch58, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch59, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	45
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch59, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	10
Mean	46.8
Std Dev	1.778

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch59, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	47.64
Std Dev	2.532

Gaus mean : 70.5370
Gaus Width: 25.2681
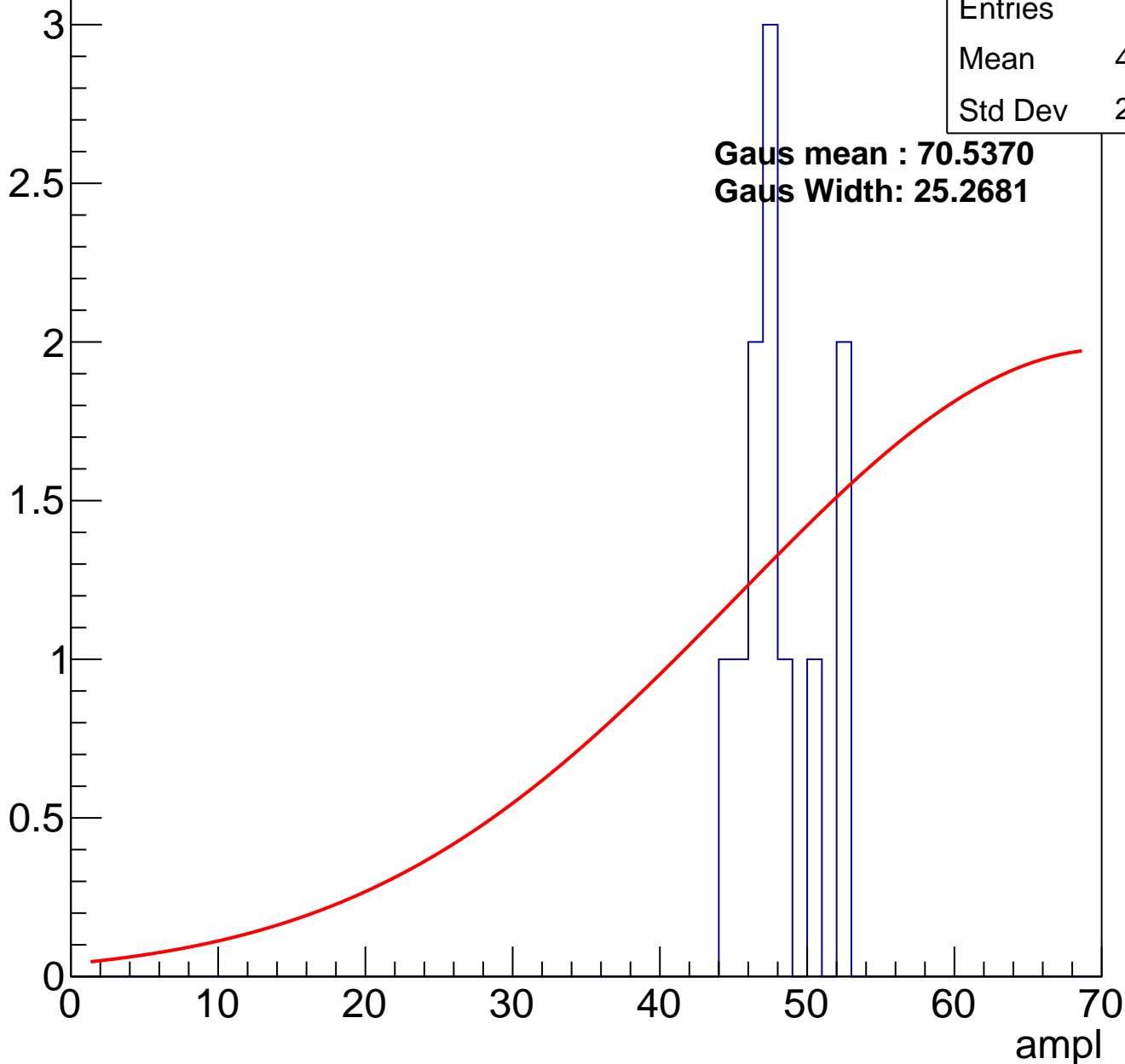
B0L100S, U5-ch59, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	50.2
Std Dev	2.135

Entry

ampl
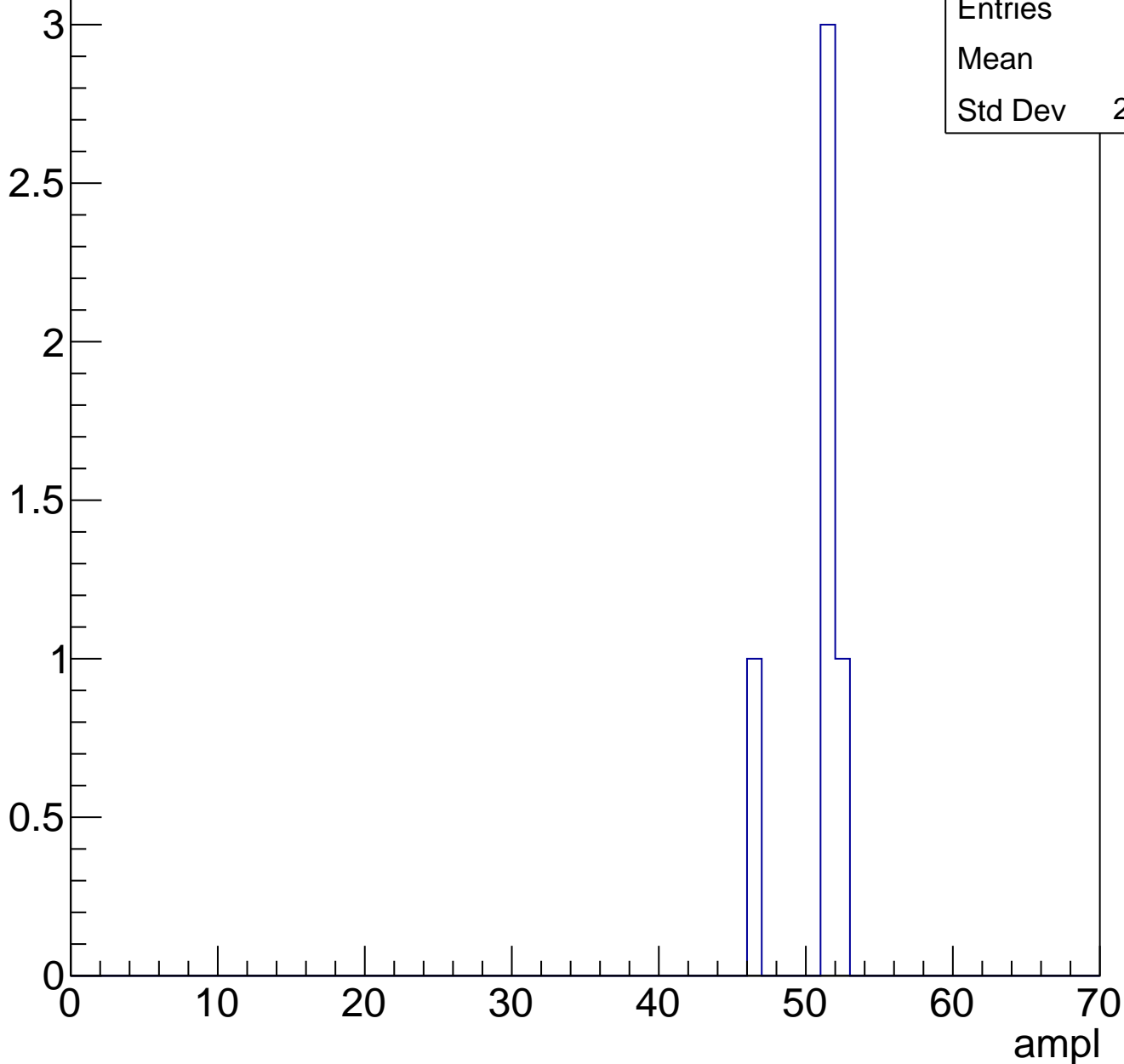
B0L100S, U5-ch59, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch59, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch59, adc6
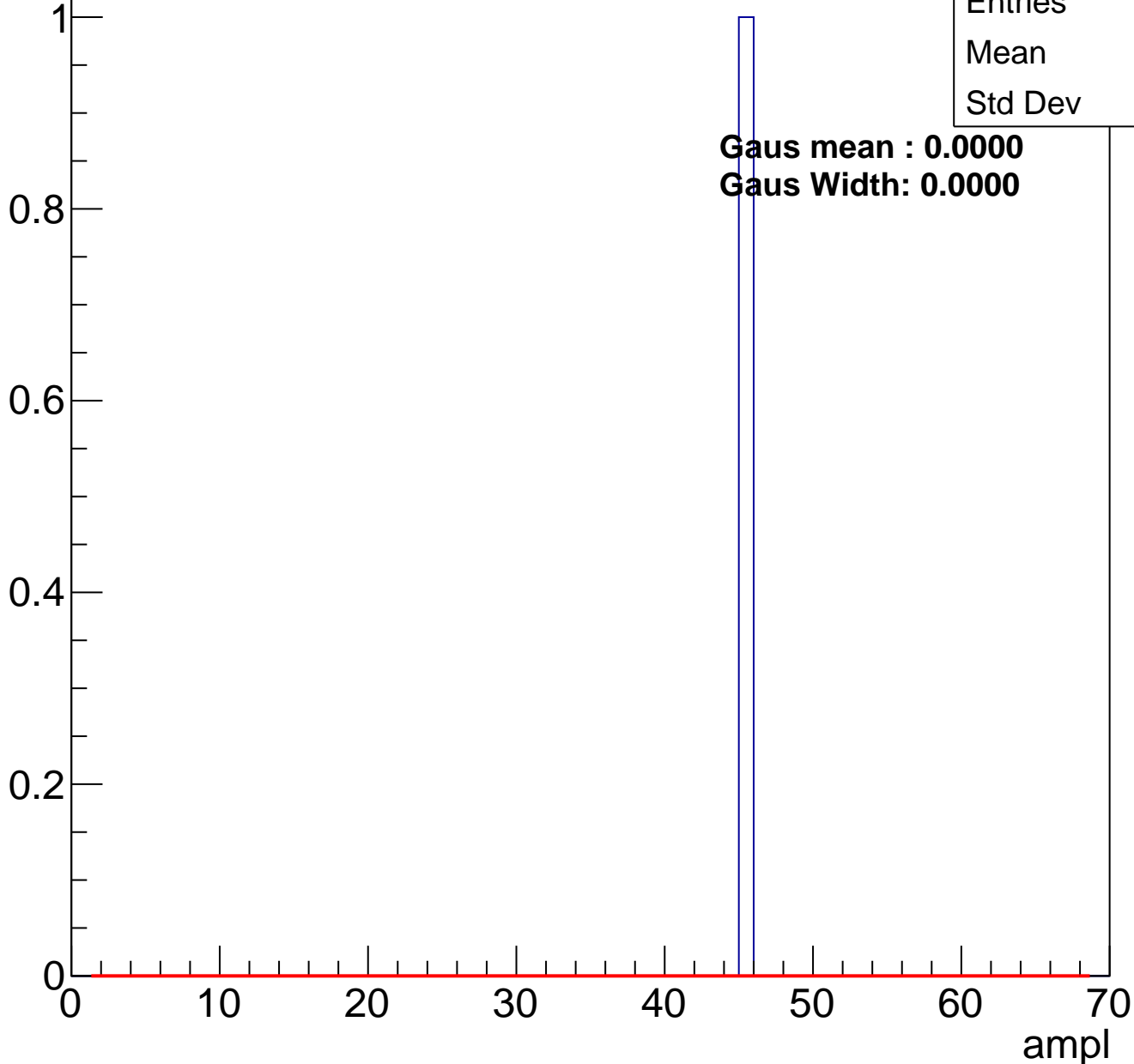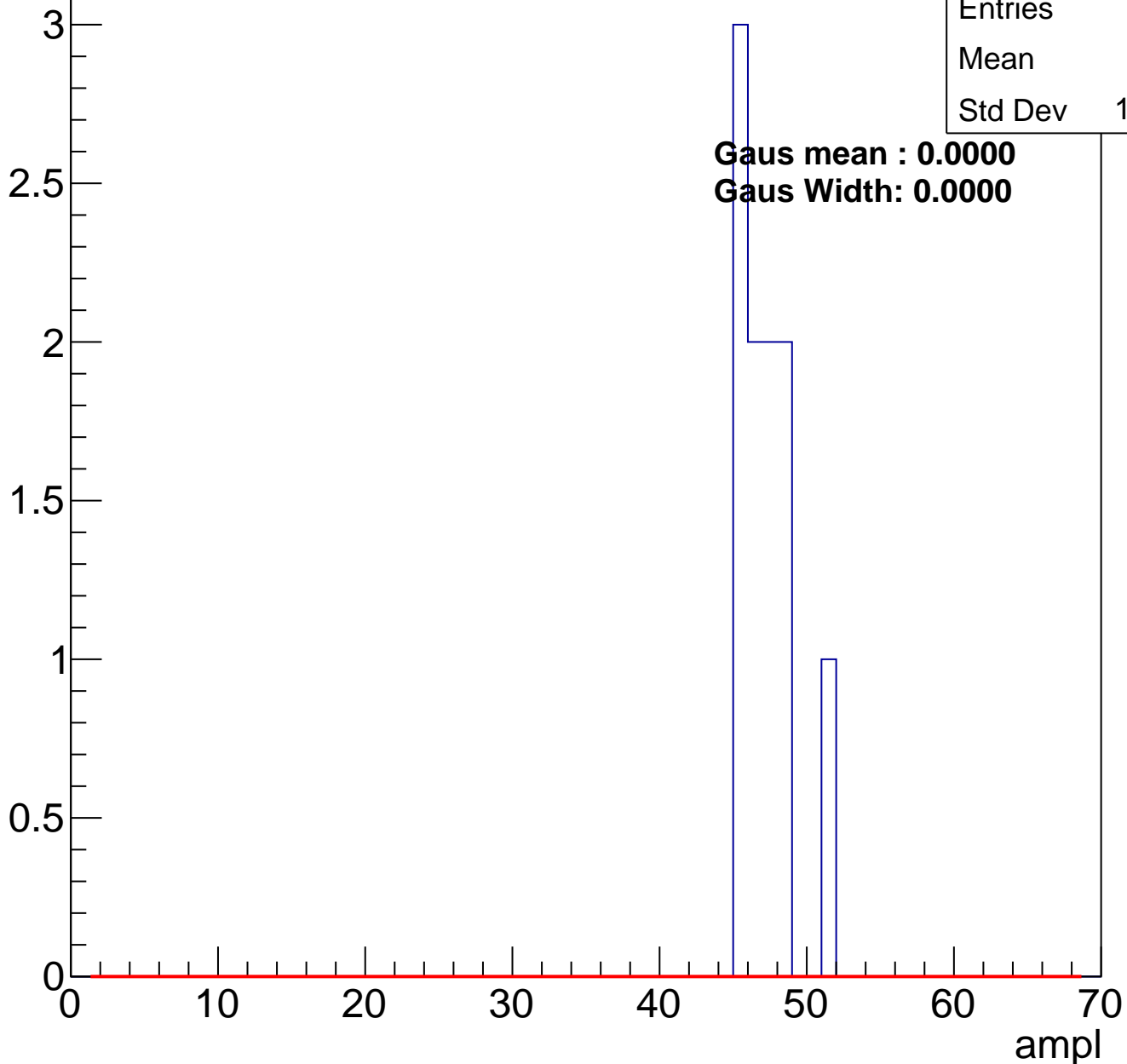
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch59, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch60, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch60, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch60, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
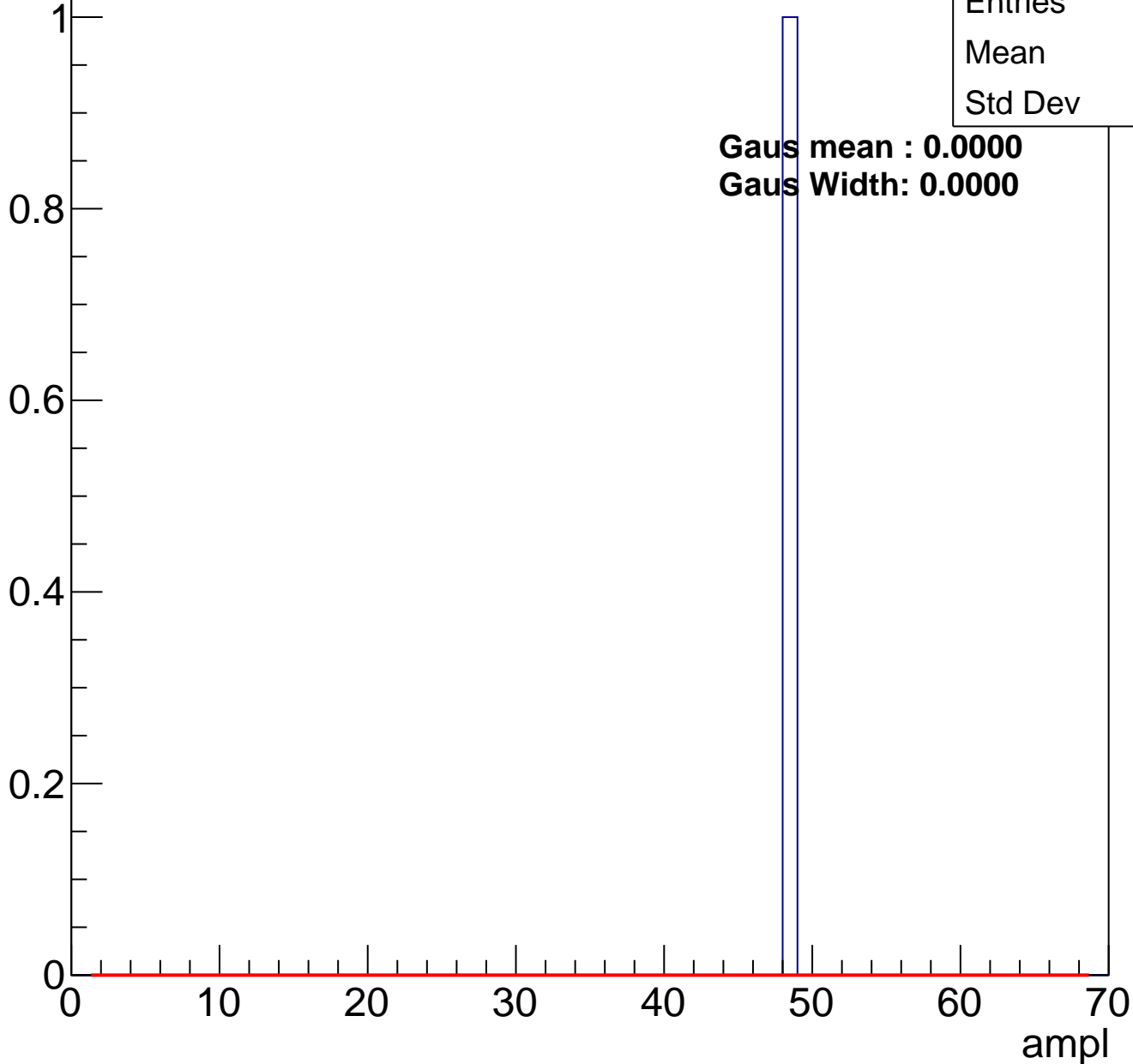
B0L100S, U5-ch60, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch60, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch60, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch60, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch60, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch61, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch61, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch61, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch61, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch61, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch61, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch61, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch61, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch62, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
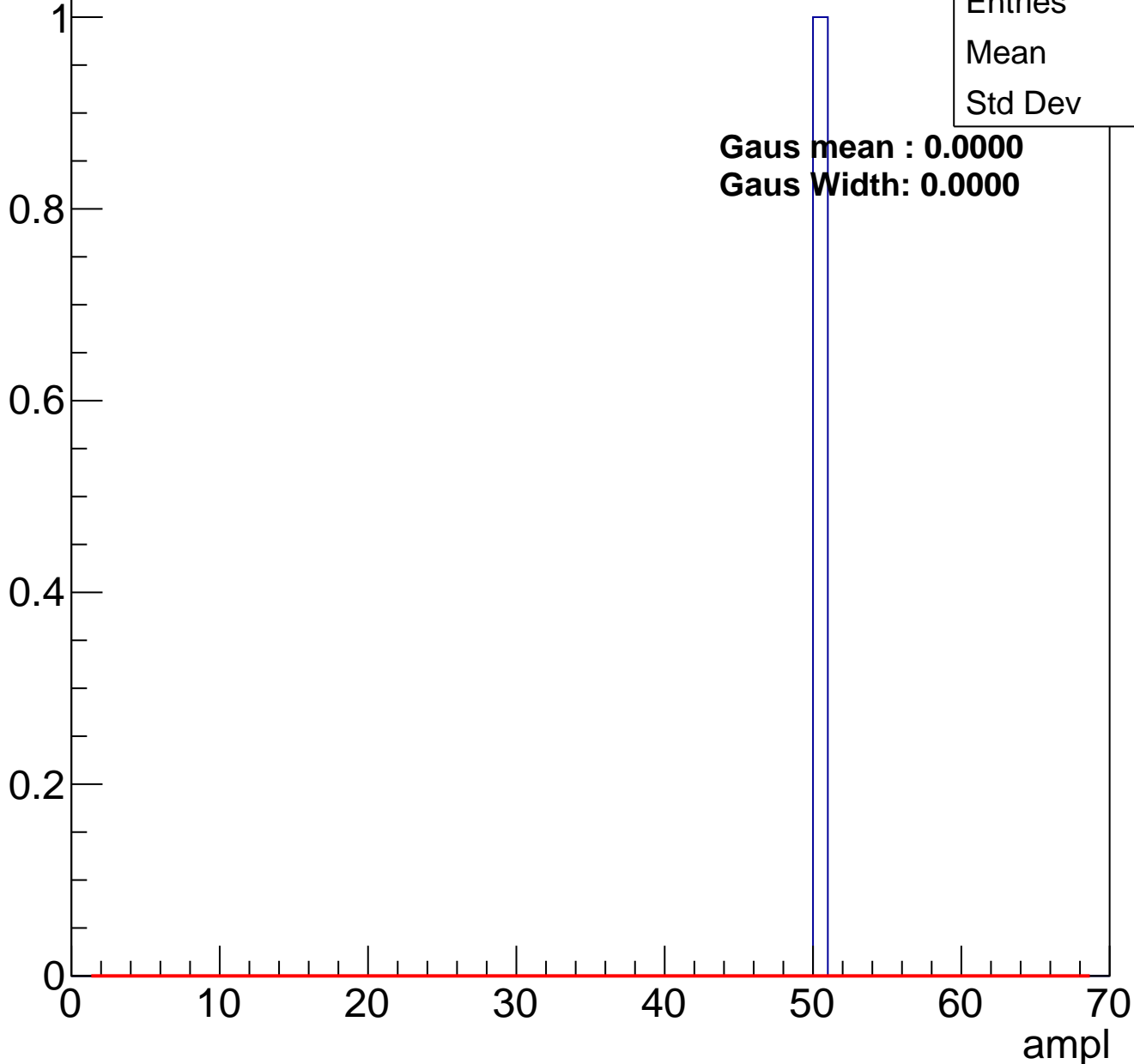
B0L100S, U5-ch62, adc1

calib_packv5_042523_0143.root, FC#6, port A1
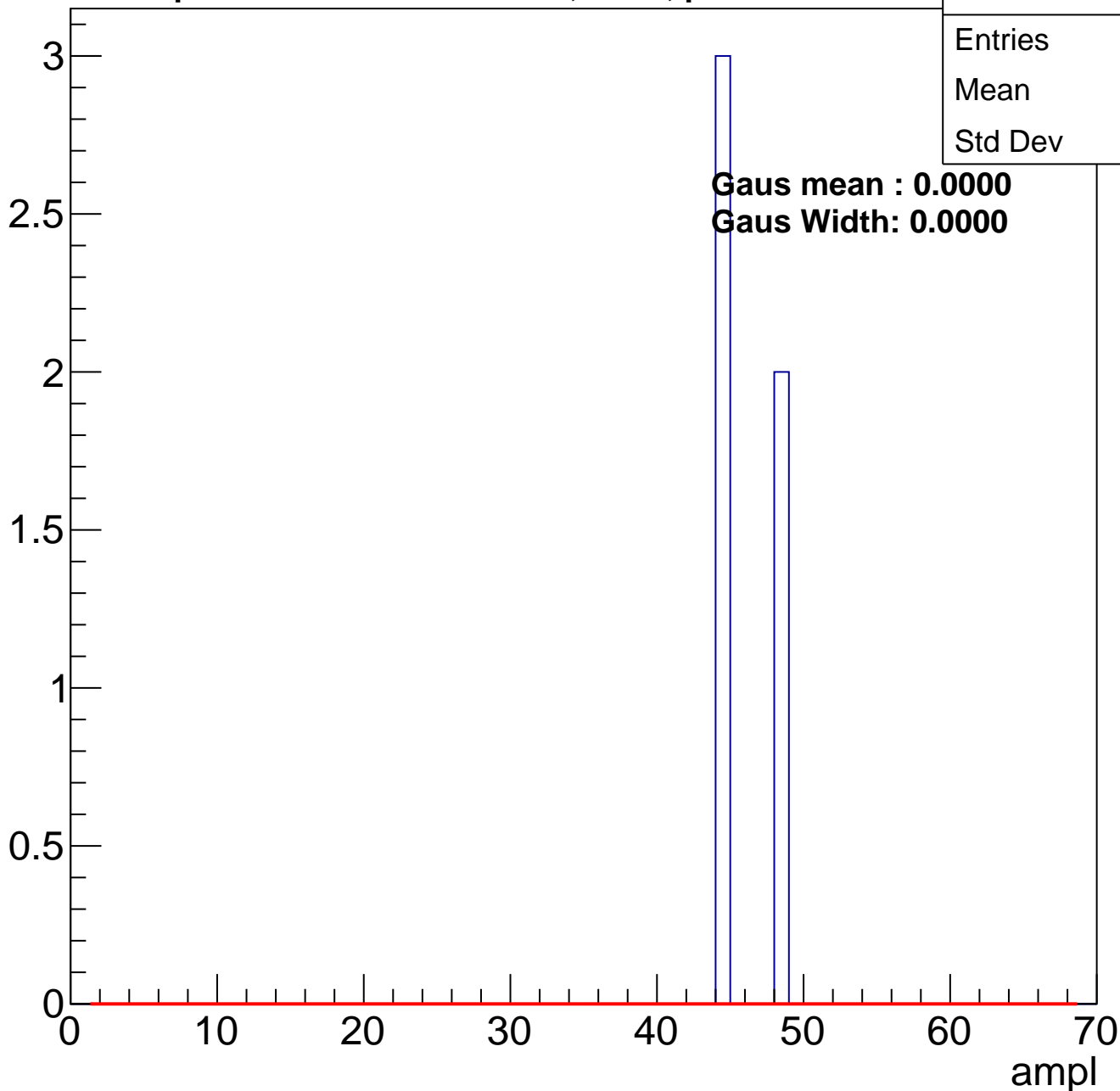
Entries	5
Mean	45.6
Std Dev	1.96

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch62, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 46.0279
Gaus Width: 7.6029
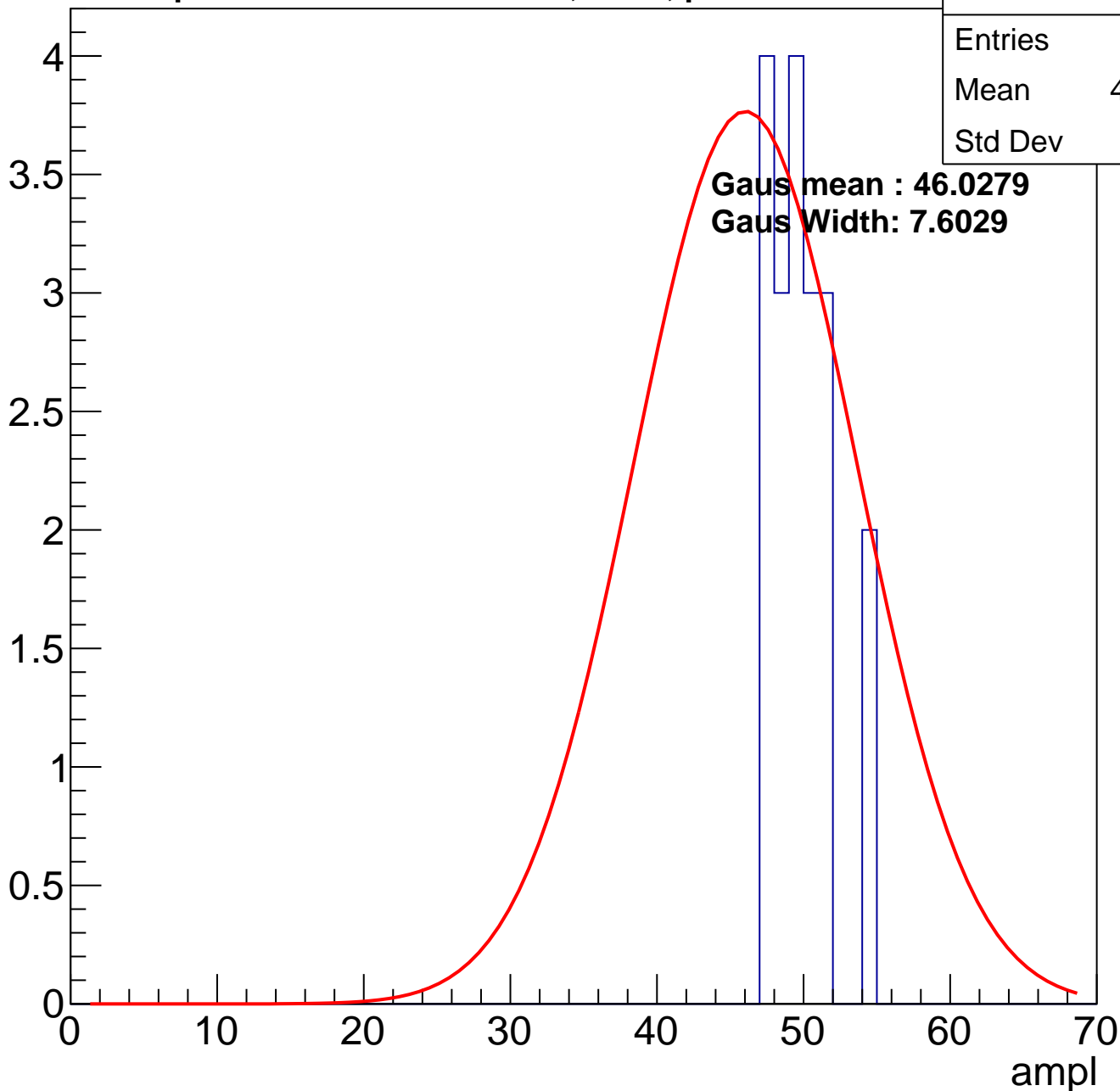
Entries	19
Mean	49.42
Std Dev	2.06

Entry

ampl

B0L100S, U5-ch62, adc3

calib_packv5_042523_0143.root, FC#6, port A1
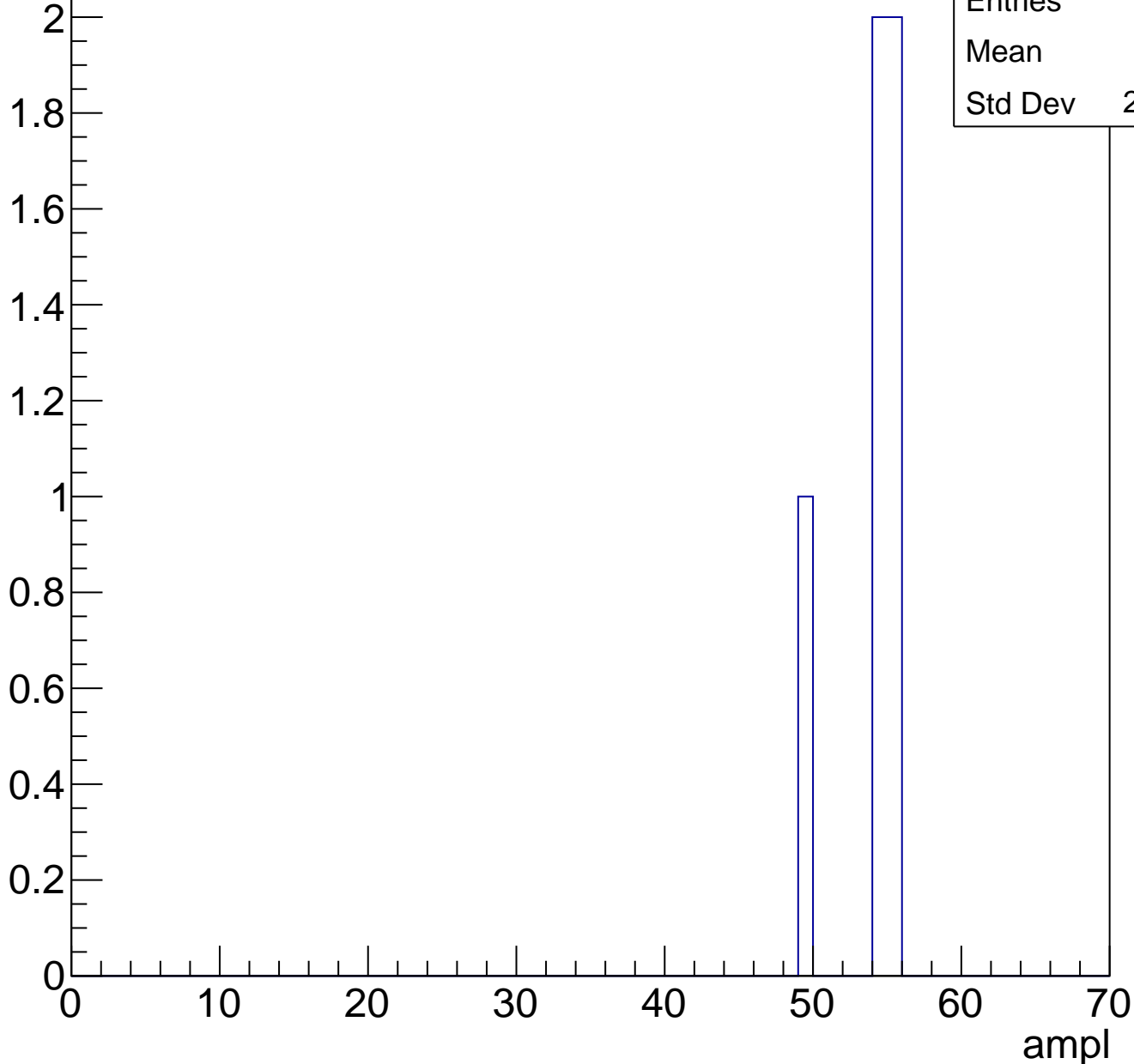
Entries	5
Mean	53.4
Std Dev	2.245

ampl

B0L100S, U5-ch62, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch62, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch62, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch62, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch63, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch63, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	18
Mean	43.89
Std Dev	1.792

Gaus mean : 44.5571
Gaus Width: 2.4247

Entry

ampl
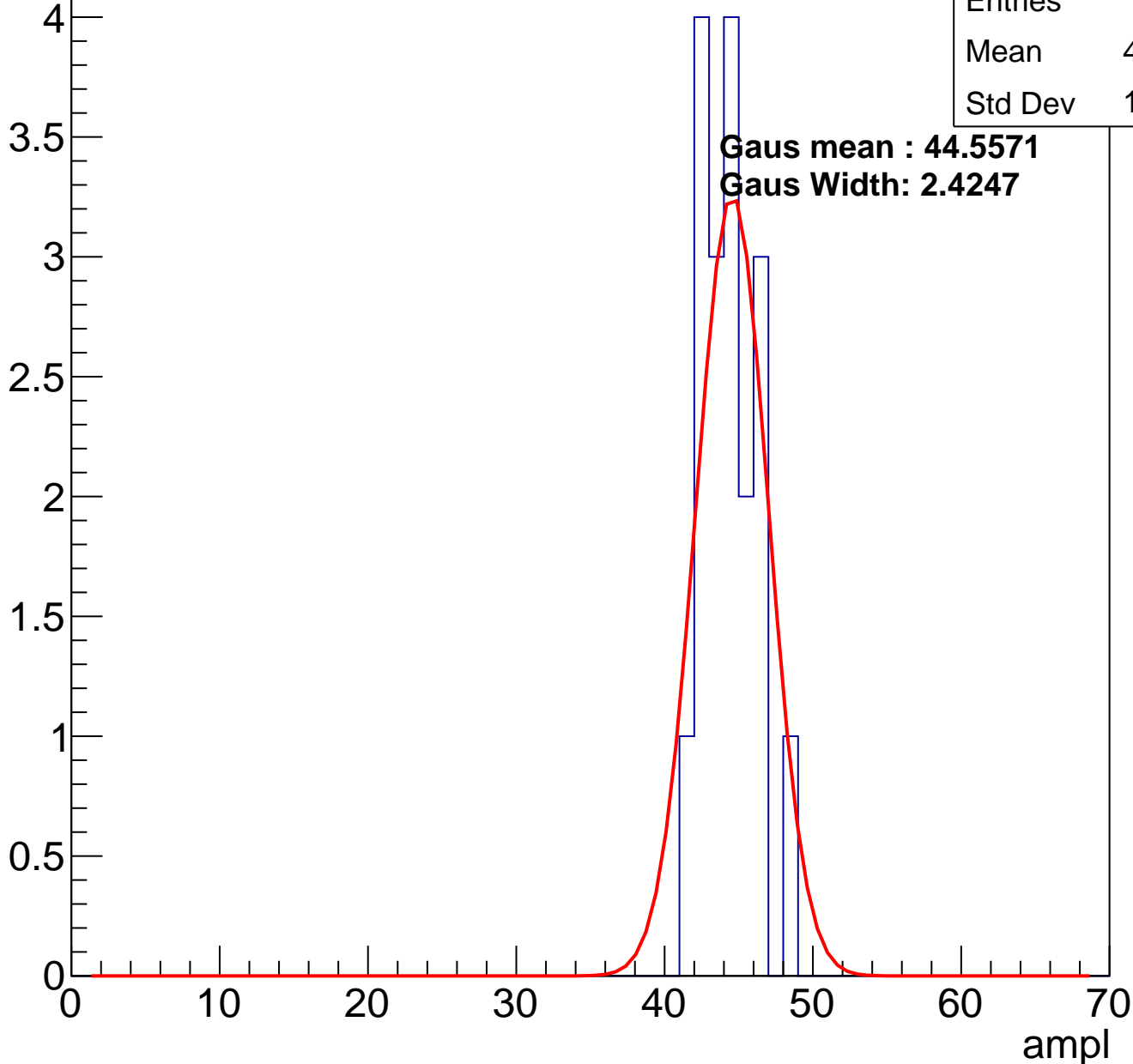
B0L100S, U5-ch63, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	23
Mean	46.78
Std Dev	2.358

Gaus mean : 47.6829
Gaus Width: 2.8660
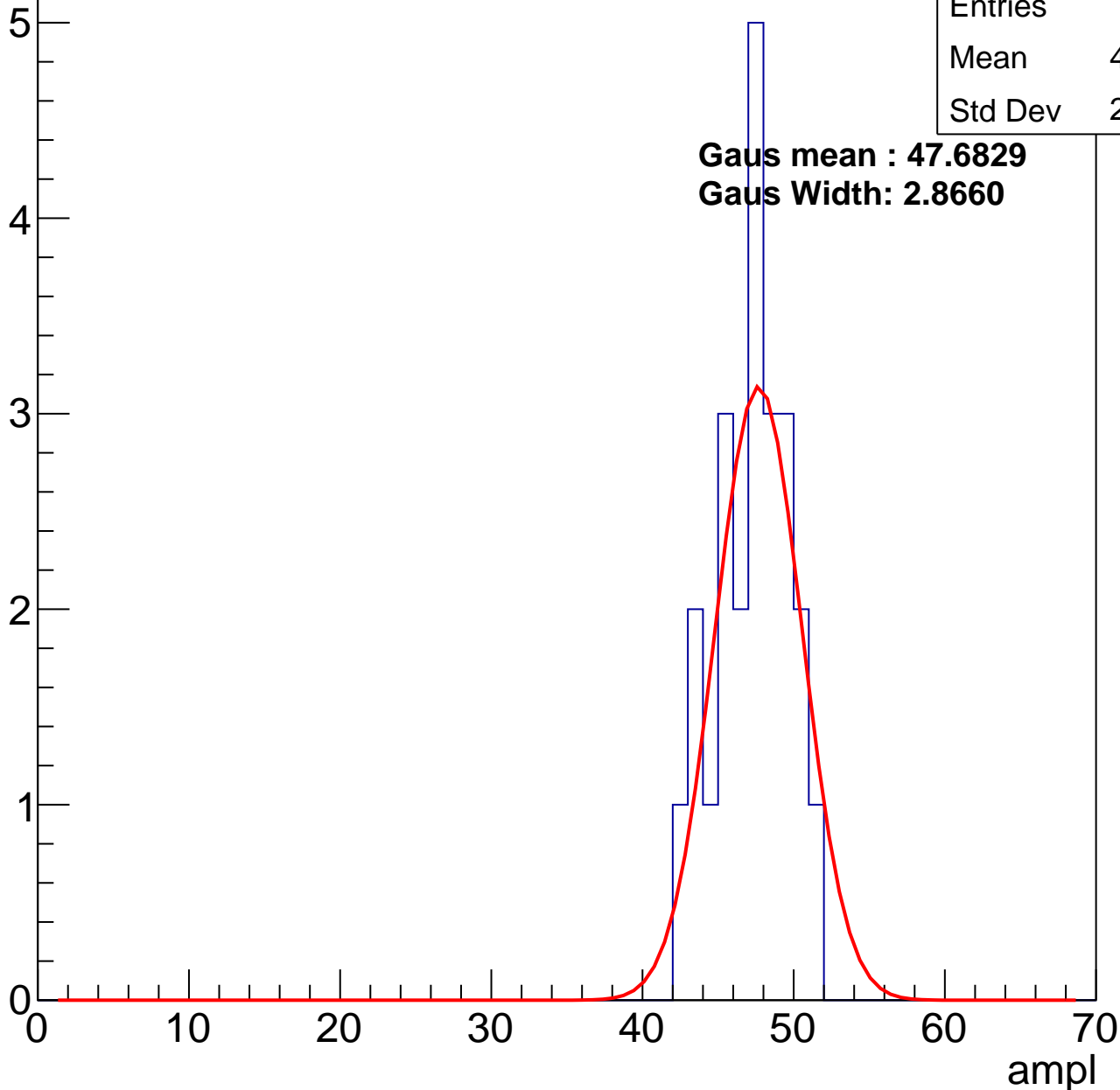
Entry

ampl

B0L100S, U5-ch63, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	49.5
Std Dev	0.9574
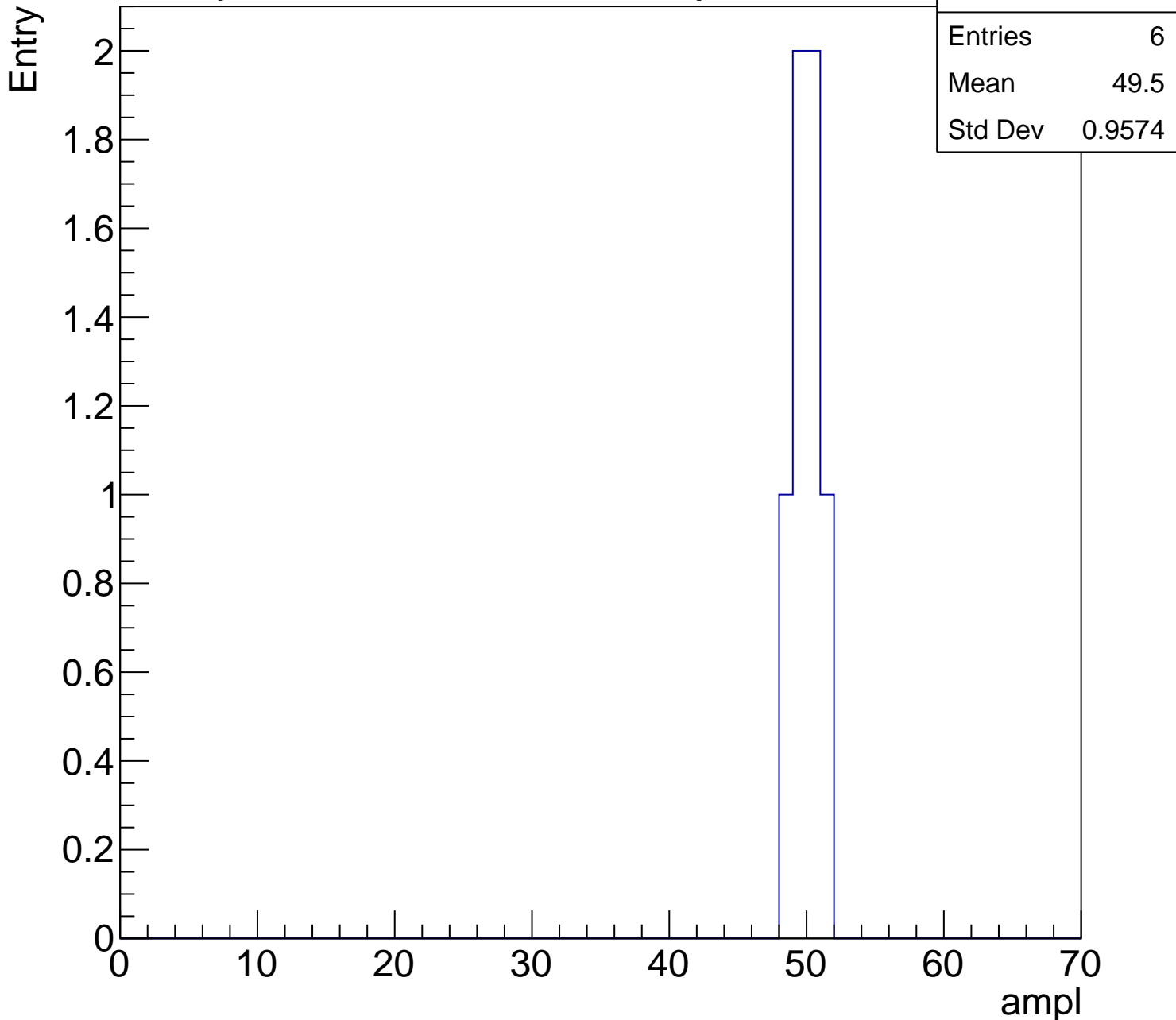
B0L100S, U5-ch63, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch63, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch63, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch63, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch64, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	39.33
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
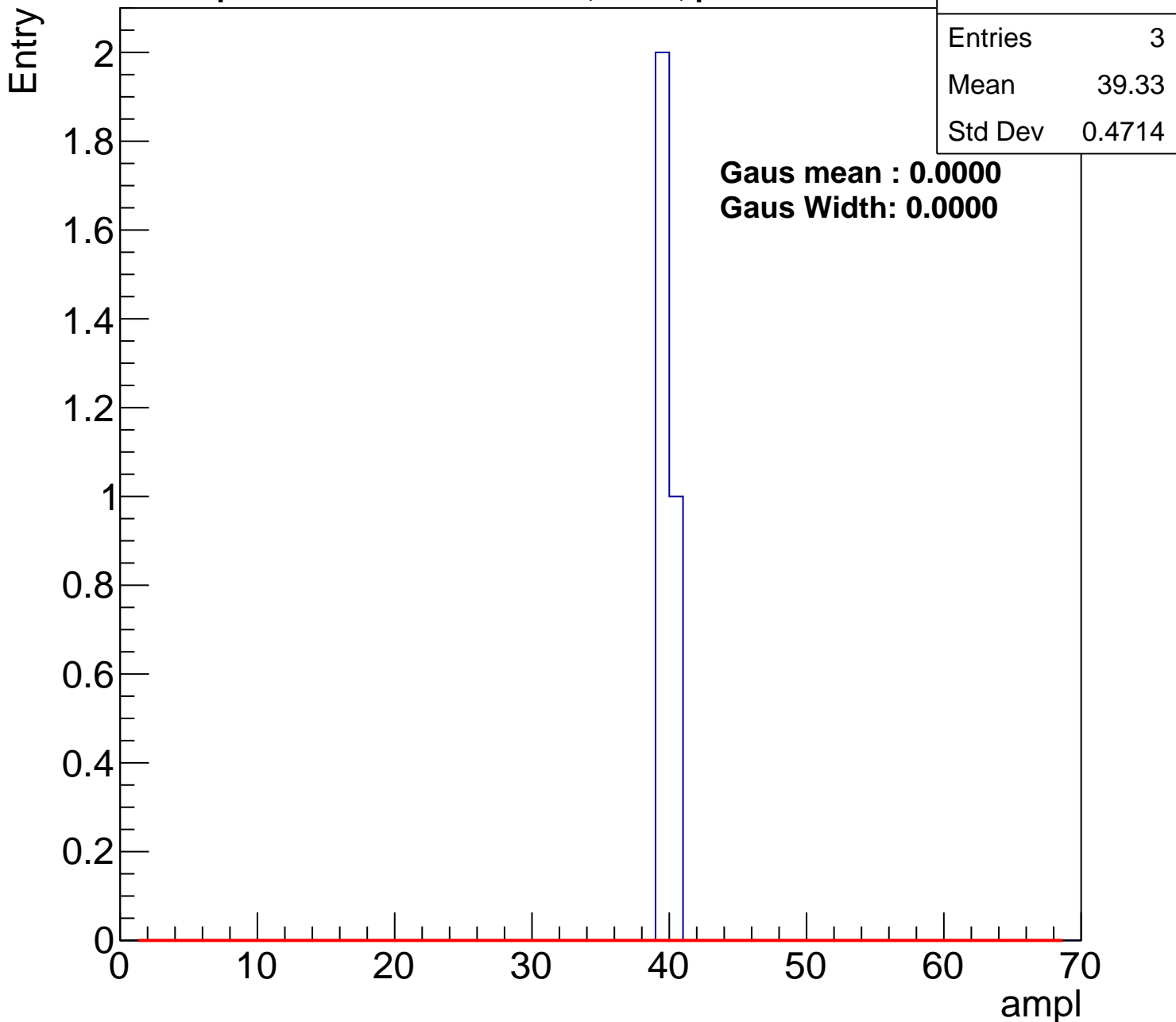
B0L100S, U5-ch64, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	16
Mean	44.5
Std Dev	3.182

Gaus mean : 23.9708
Gaus Width: 31.8182
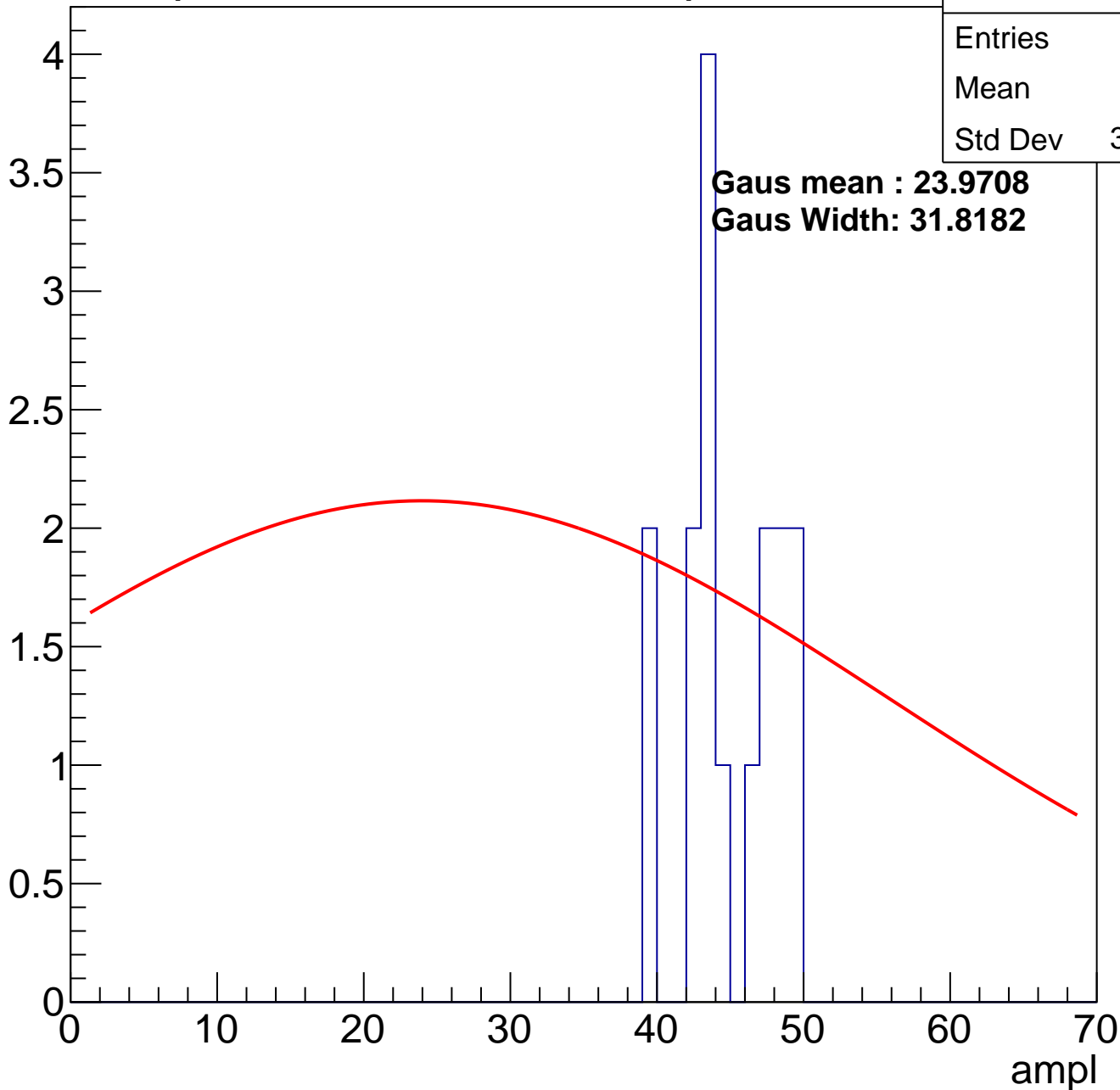
B0L100S, U5-ch64, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 48.9207
Gaus Width: 3.6433

Entries	20
Mean	48
Std Dev	2.739

Entry

ampl

B0L100S, U5-ch64, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50.33
Std Dev	2.055

ampl
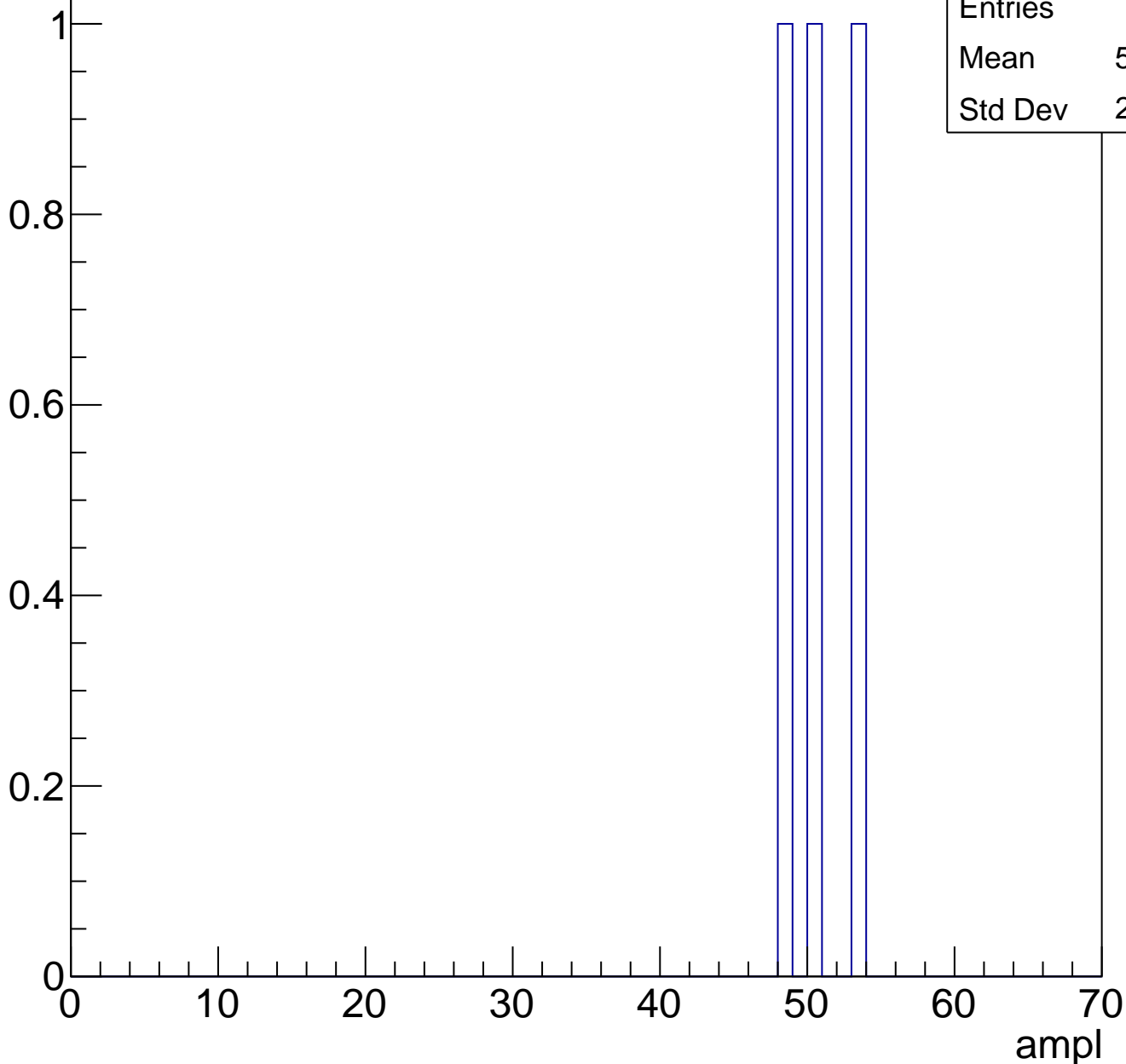
B0L100S, U5-ch64, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch64, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch64, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch64, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch65, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch65, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch65, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch65, adc3

calib_packv5_042523_0143.root, FC#6, port A1

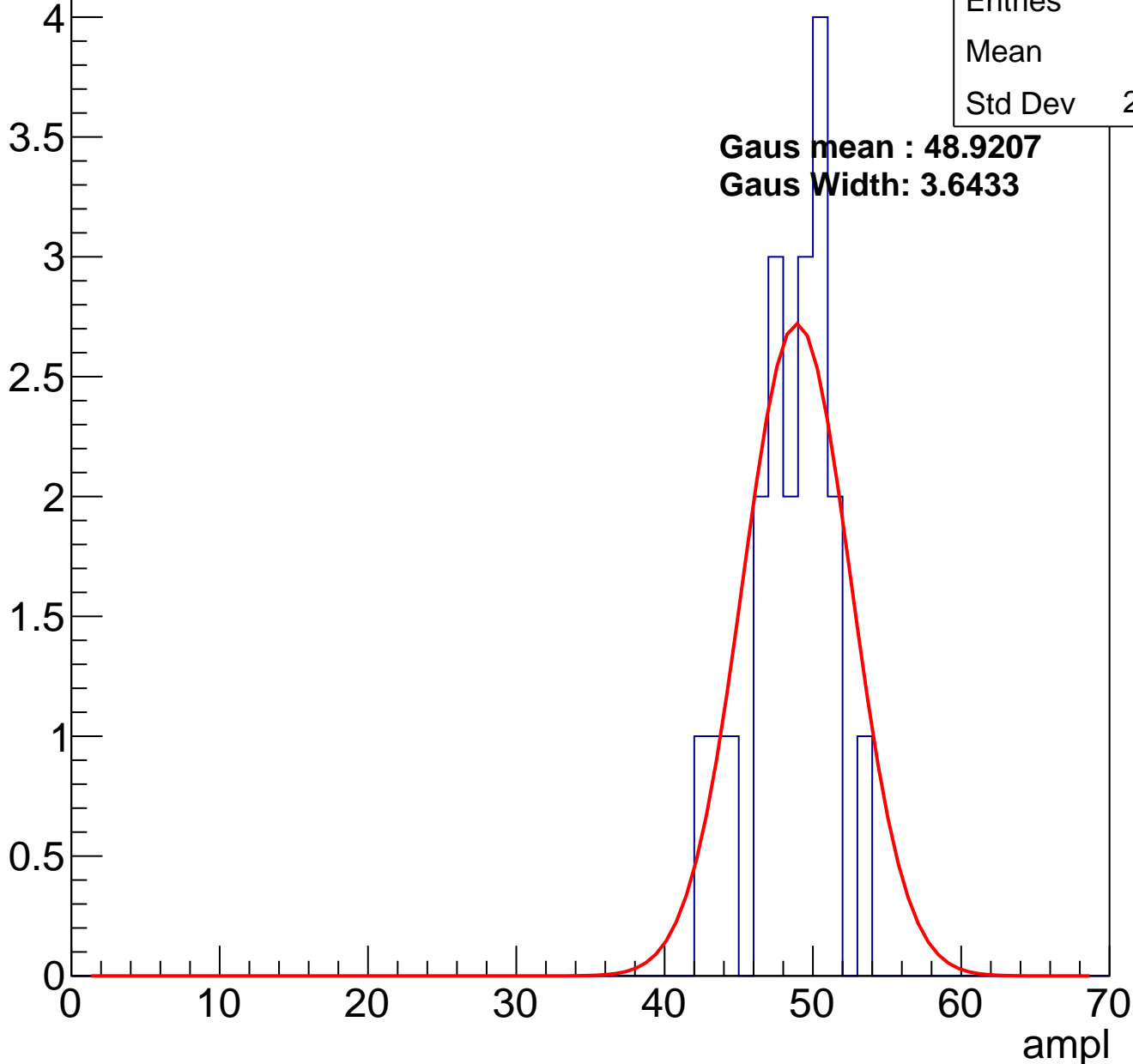|	
Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch65, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch65, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch65, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch65, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch66, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch66, adc1

calib_packv5_042523_0143.root, FC#6, port A1
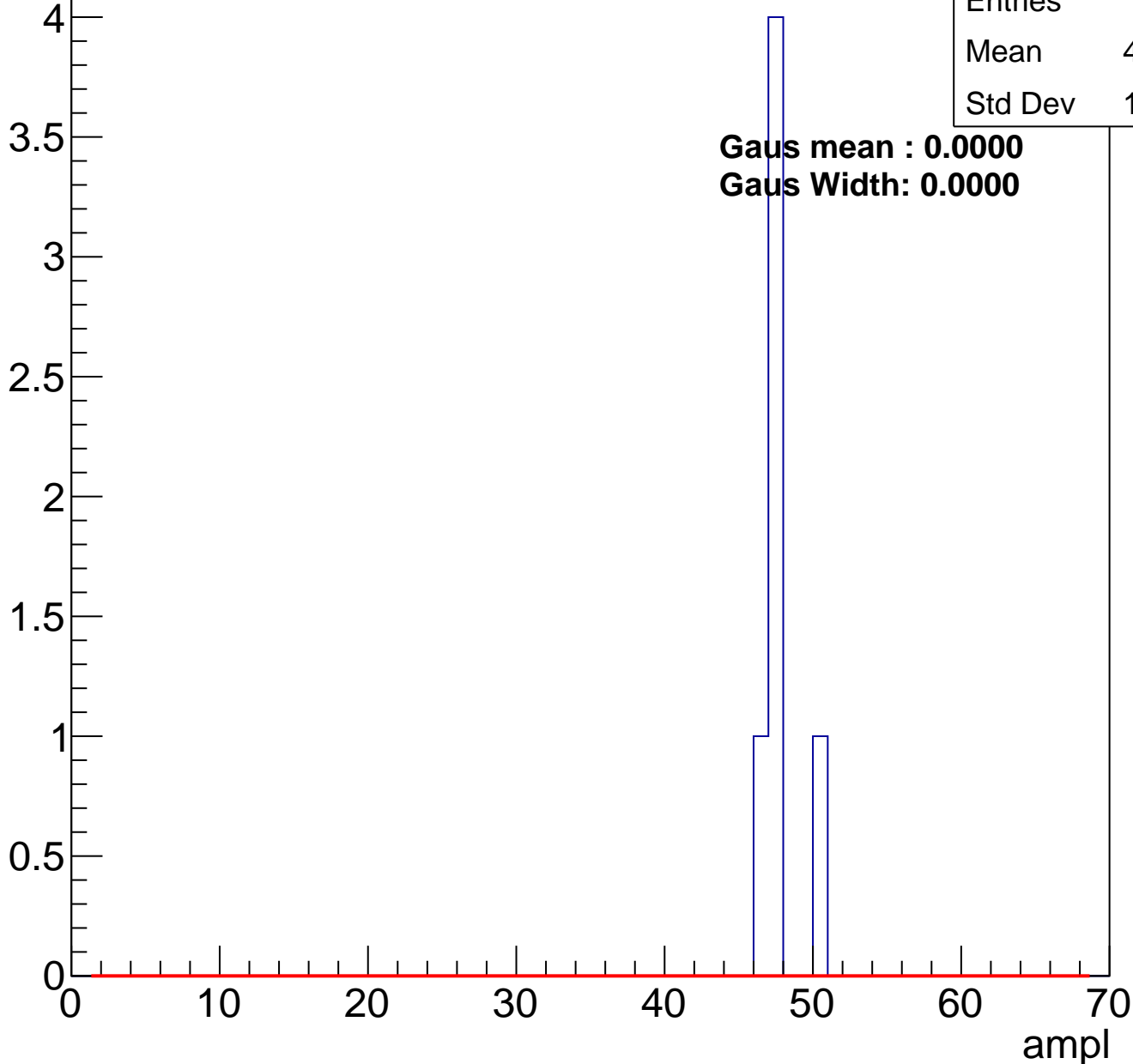
Entries	6
Mean	47.33
Std Dev	1.247

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
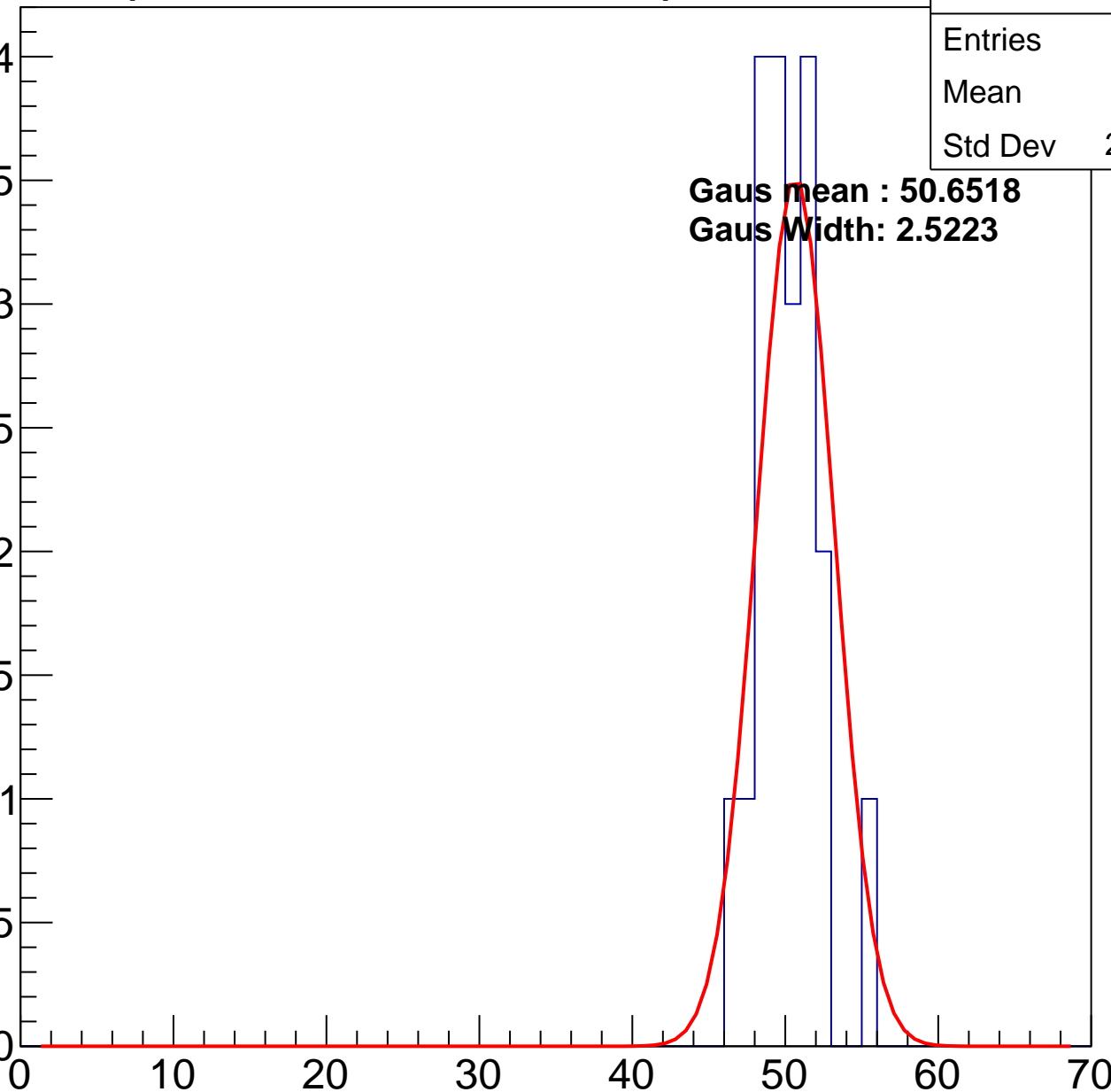
B0L100S, U5-ch66, adc3

calib_packv5_042523_0143.root, FC#6, port A1
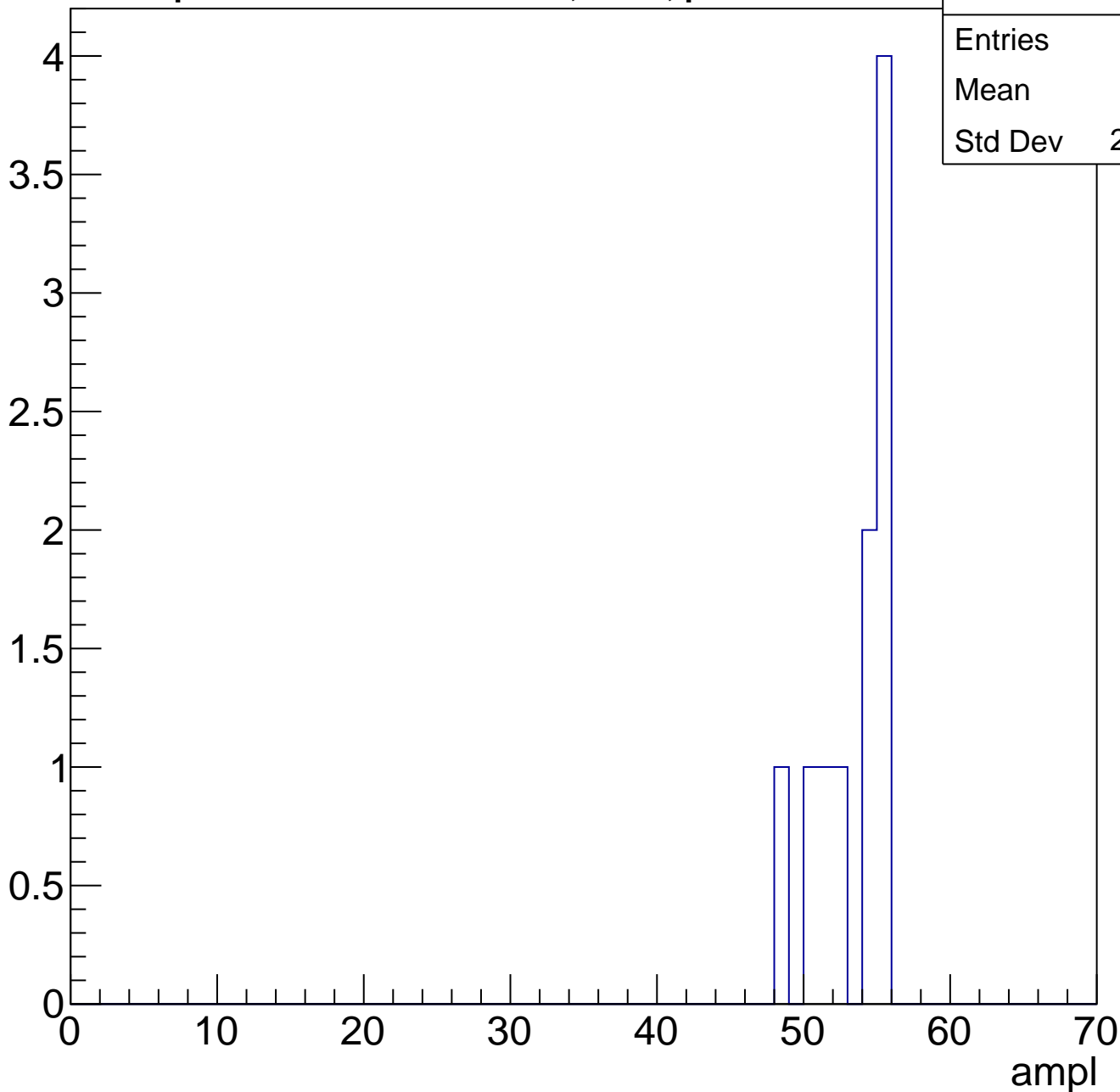
Entries	10
Mean	52.9
Std Dev	2.385

B0L100S, U5-ch66, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch66, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch66, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch66, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch67, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch67, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch67, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch67, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch67, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch67, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch67, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch67, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch68, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch68, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	2
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
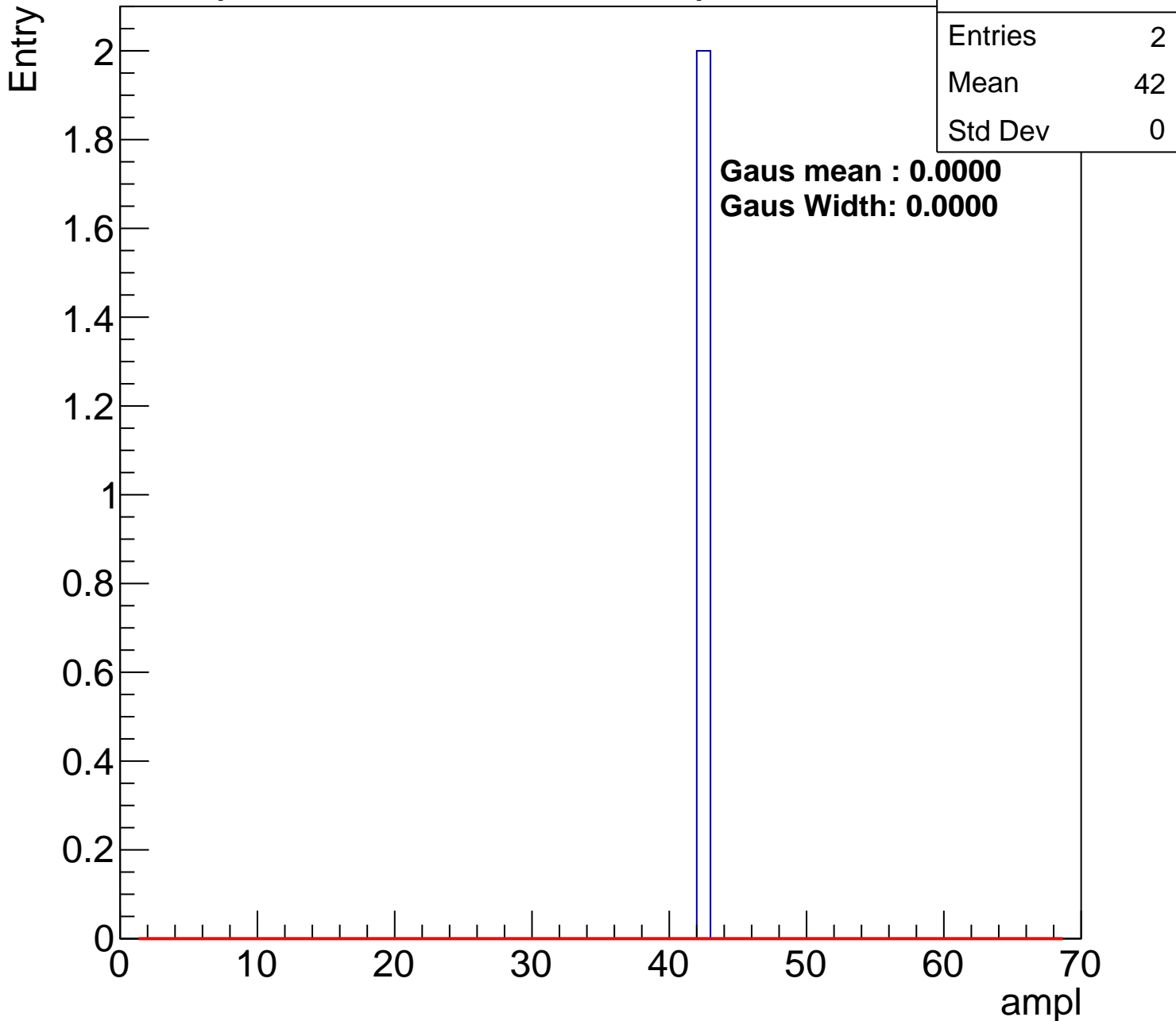
B0L100S, U5-ch68, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
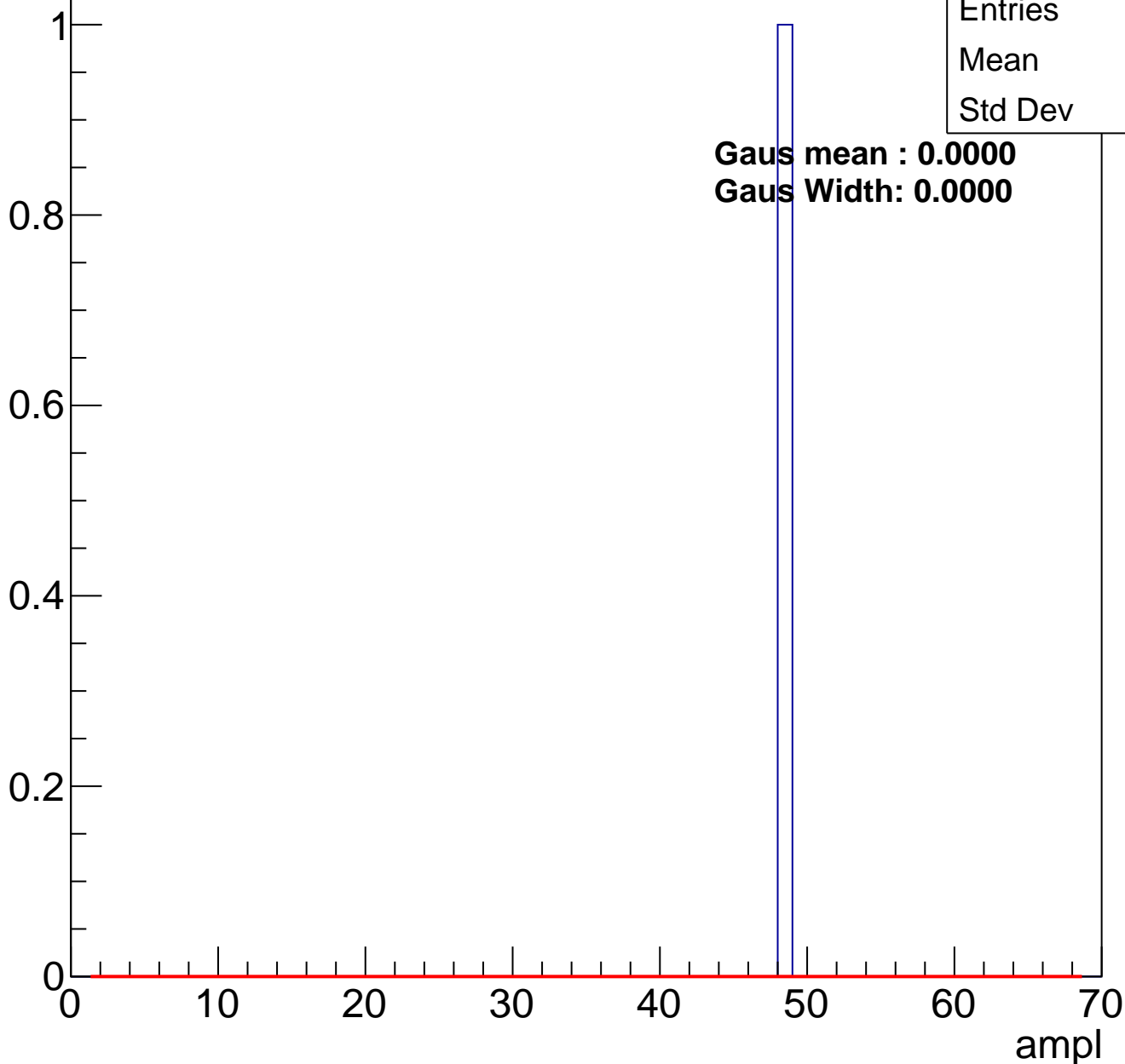
B0L100S, U5-ch68, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch68, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch68, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch68, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch68, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch69, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	44.5
Std Dev	1.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch69, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	19
Mean	43.89
Std Dev	2.447

Gaus mean : 41.3821
Gaus Width: 5.5040
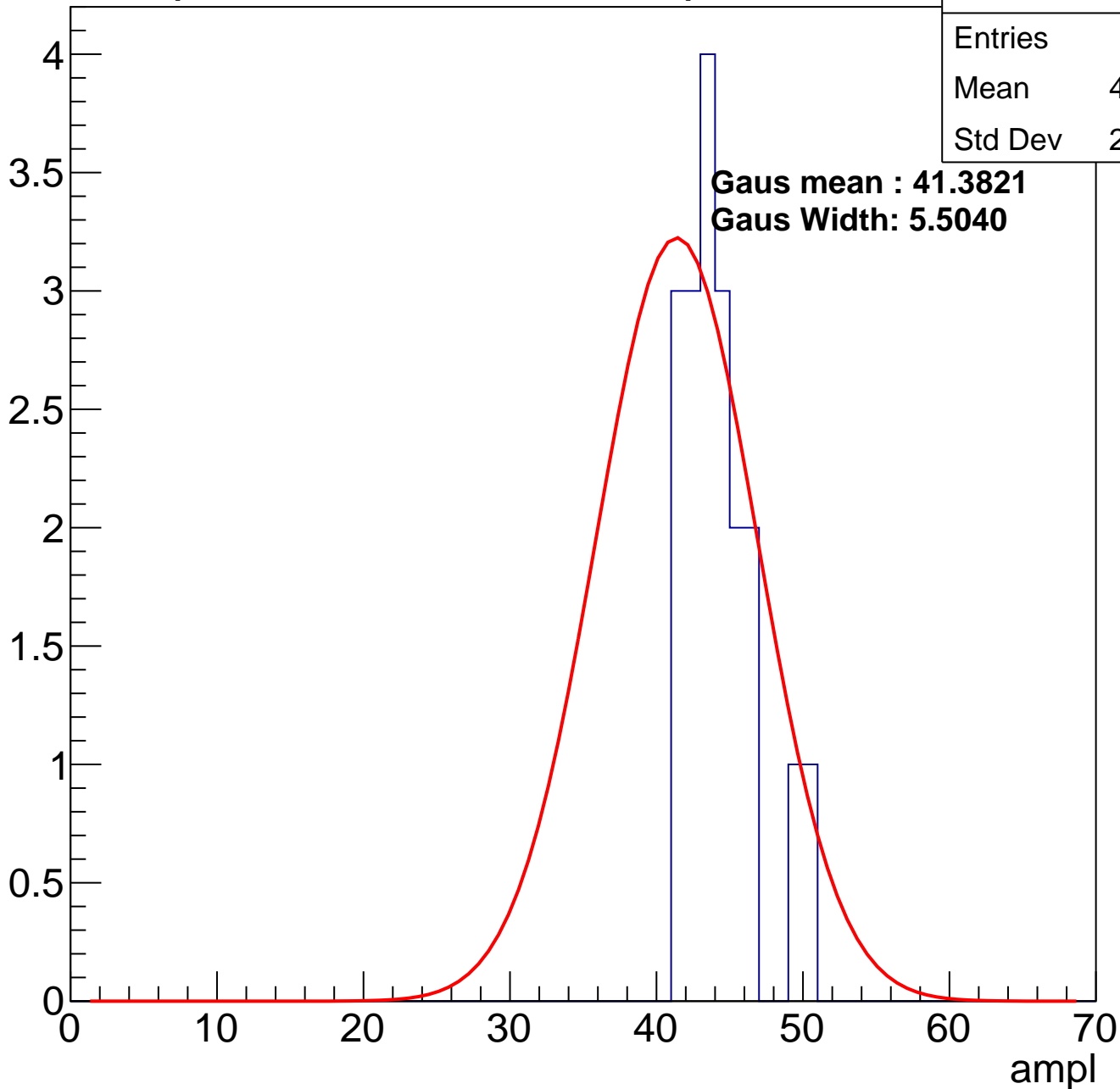
Entry

ampl

B0L100S, U5-ch69, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : -6.3720
Gaus Width: 21.4731

Entries	11
Mean	46.45
Std Dev	2.147

Entry

ampl
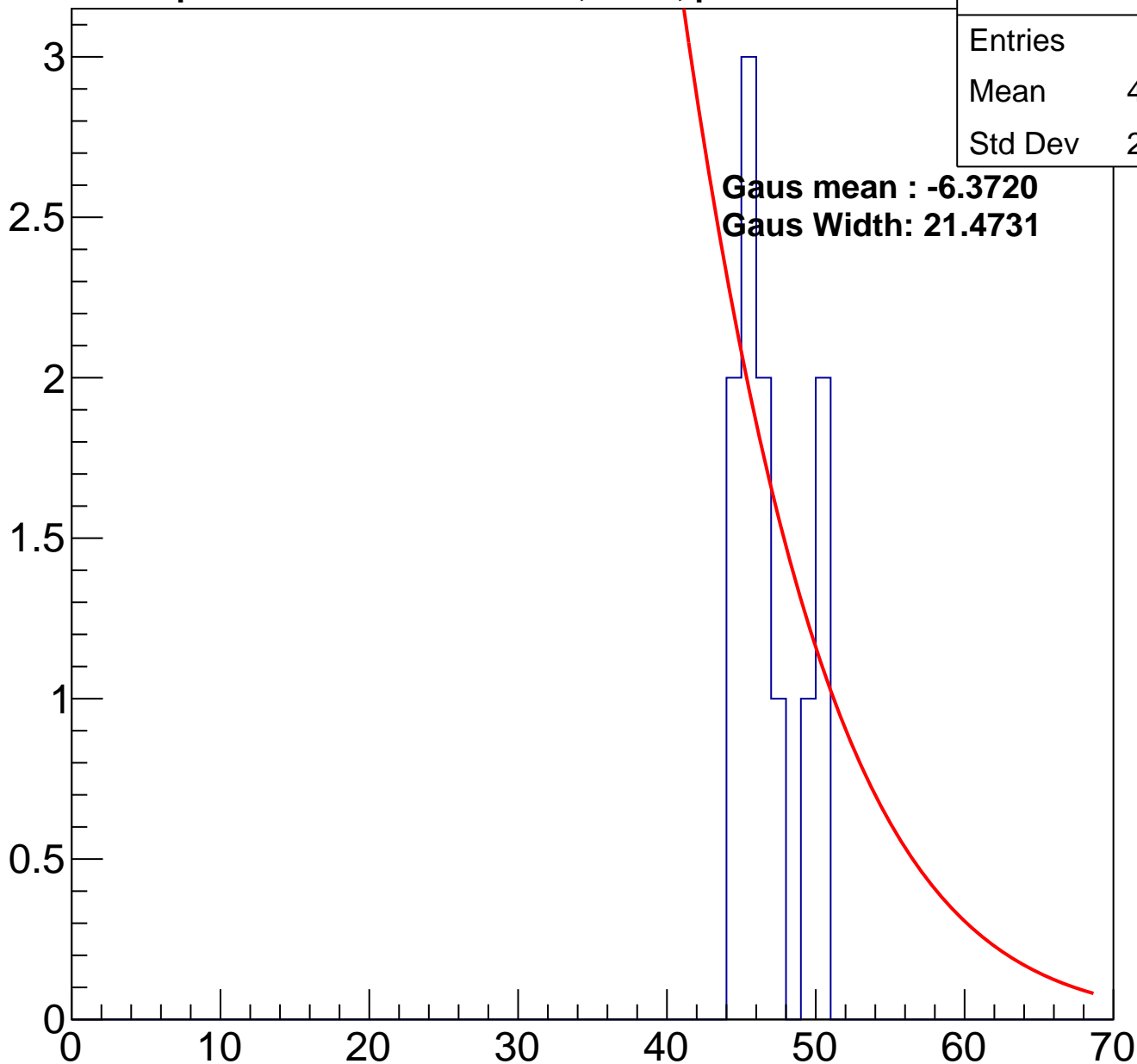
B0L100S, U5-ch69, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Entry

ampl
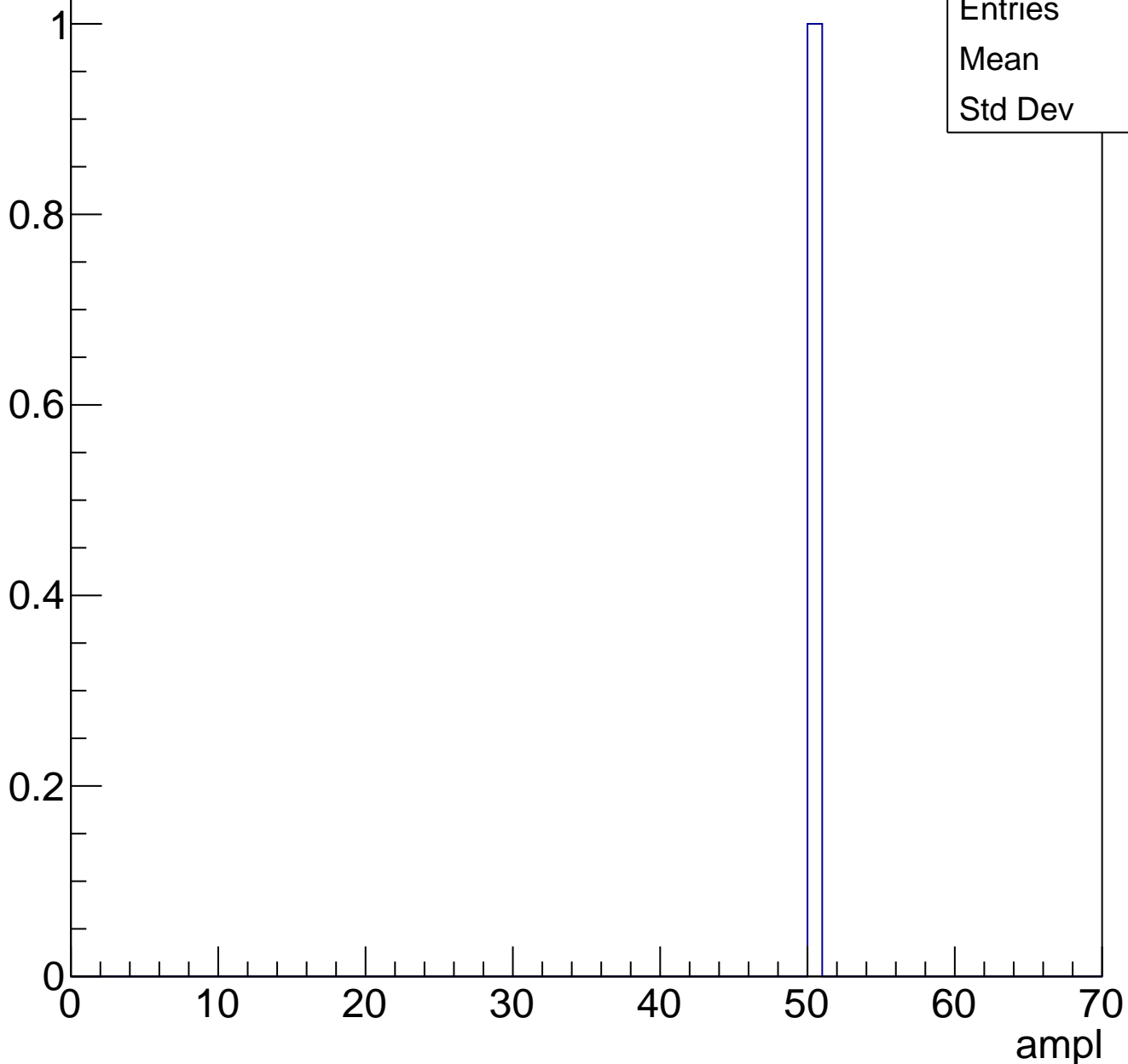
B0L100S, U5-ch69, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch69, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch69, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch69, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch70, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch70, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch70, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch70, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch70, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch70, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch70, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch70, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch71, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch71, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch71, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
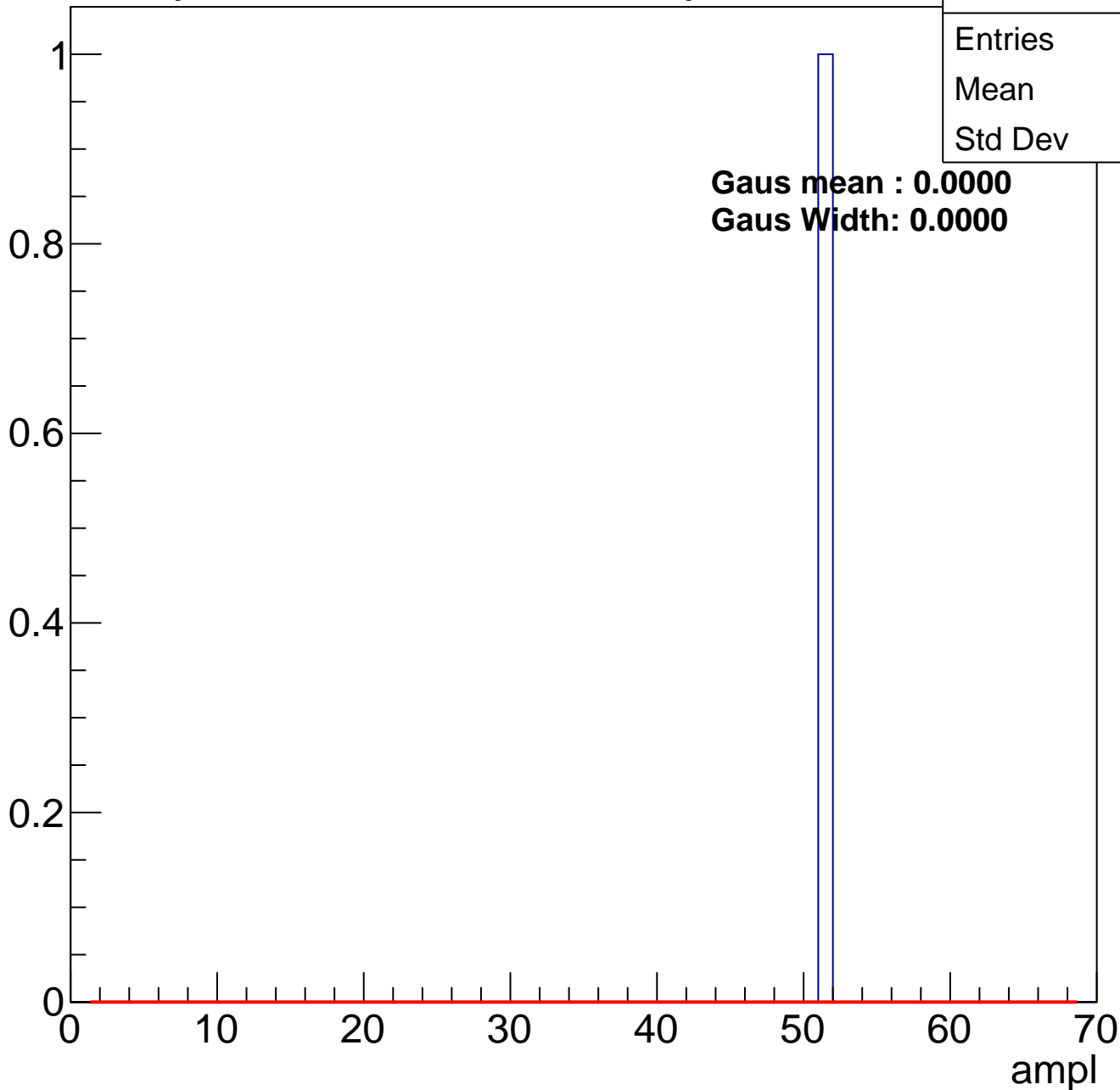
B0L100S, U5-ch71, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	51
Std Dev	0
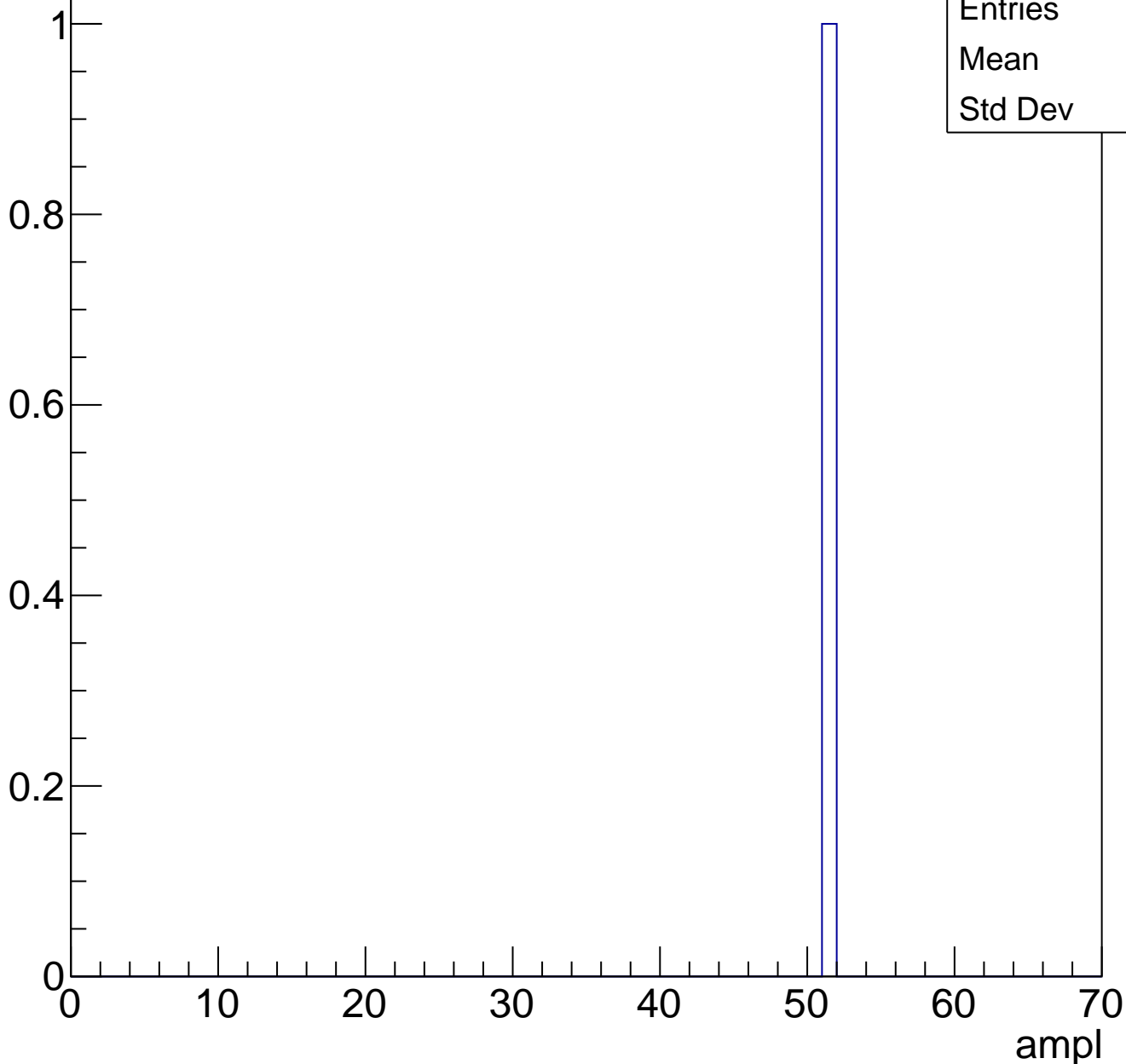
B0L100S, U5-ch71, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch71, adc5

calib_packv5_042523_0143.root, FC#6, port A1

	Entries	0
	Mean	0
	Std Dev	0

B0L100S, U5-ch71, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch71, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch72, adc0
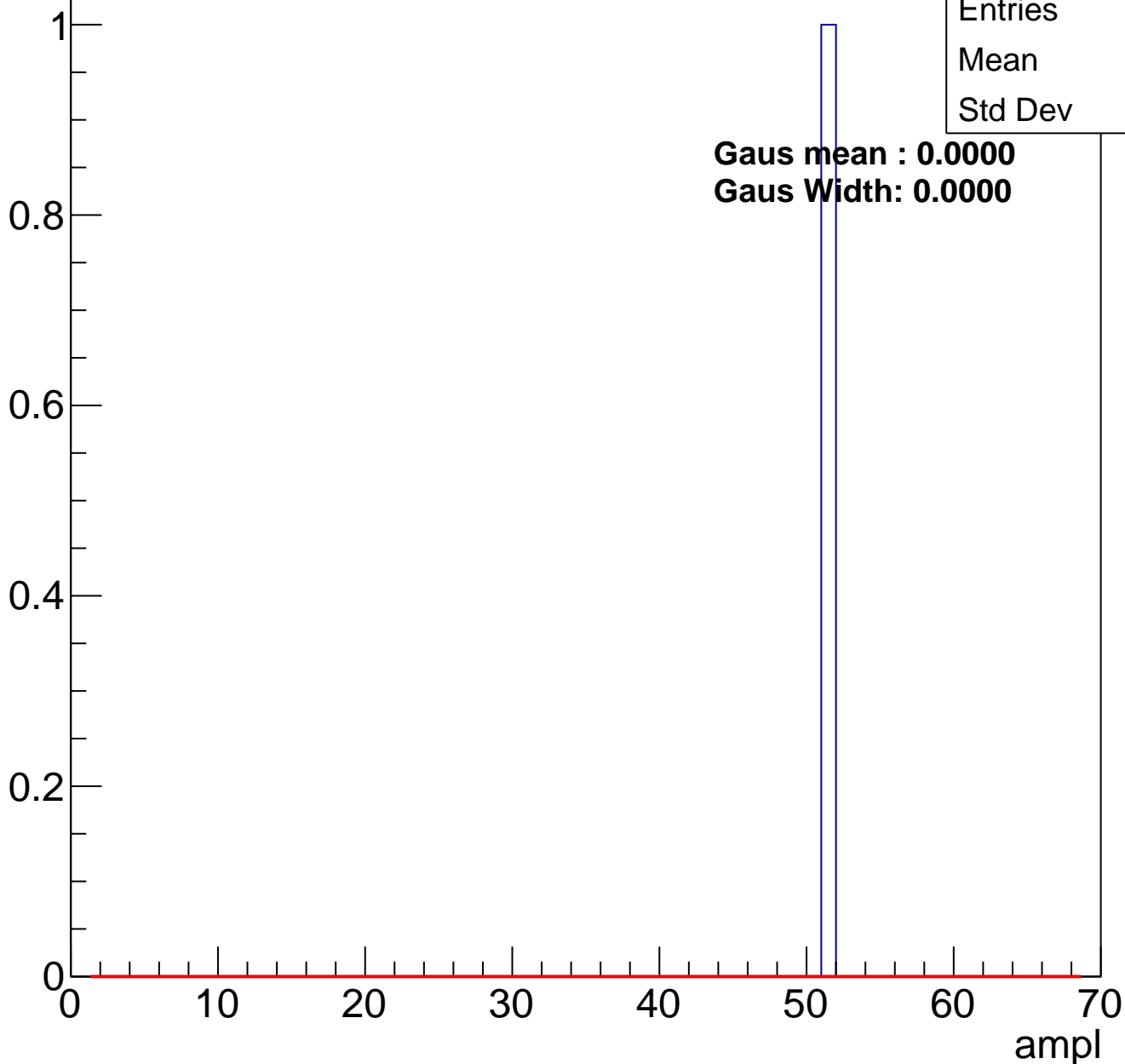
calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	42
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
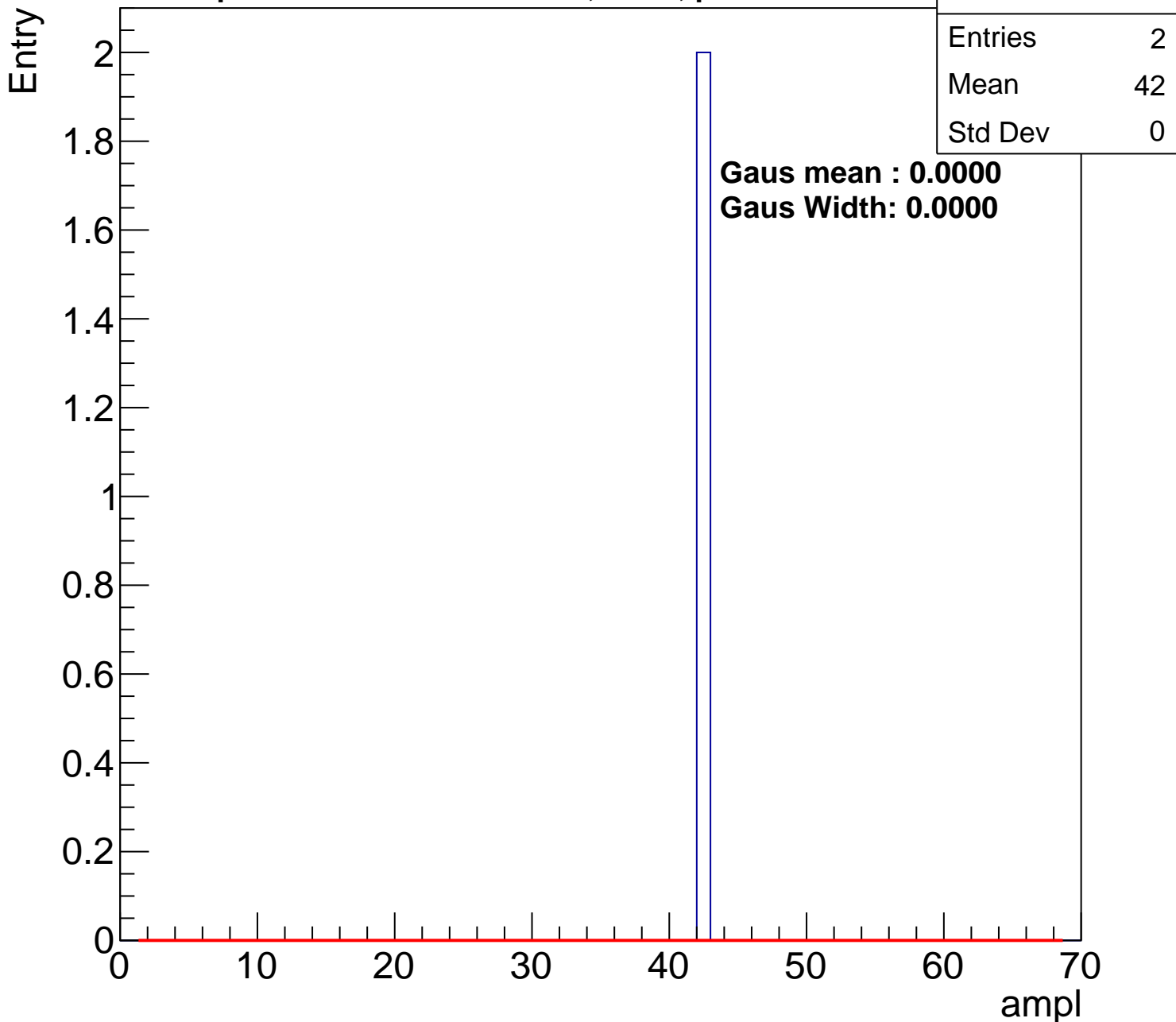
Entry

ampl

B0L100S, U5-ch72, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	43.79
Std Dev	2.242

Gaus mean : 43.5869
Gaus Width: 3.2630
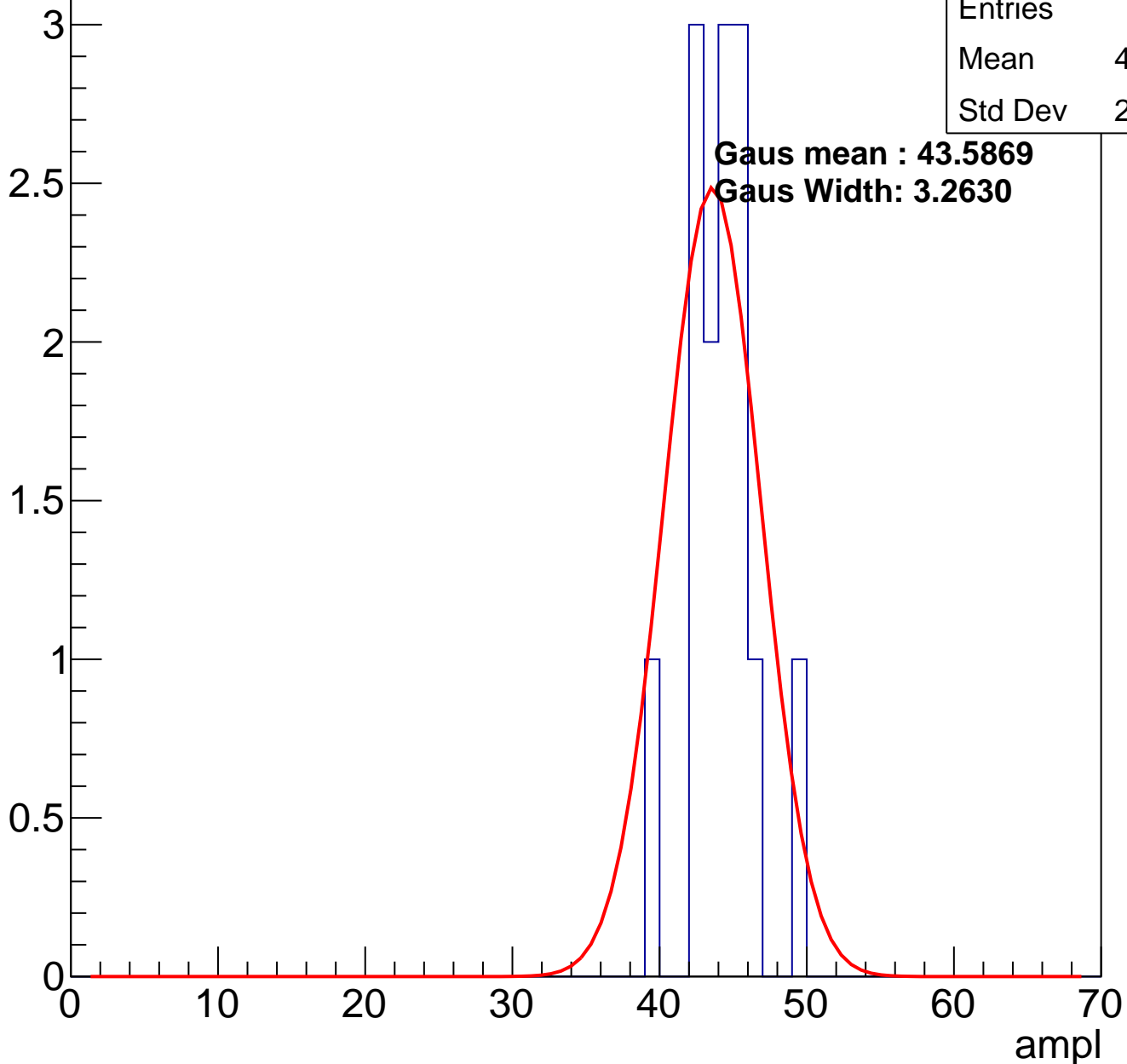
B0L100S, U5-ch72, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	27
Mean	46.89
Std Dev	2.347

Gaus mean : 48.4362
Gaus Width: 3.3648

B0L100S, U5-ch72, adc3

calib_packv5_042523_0143.root, FC#6, port A1
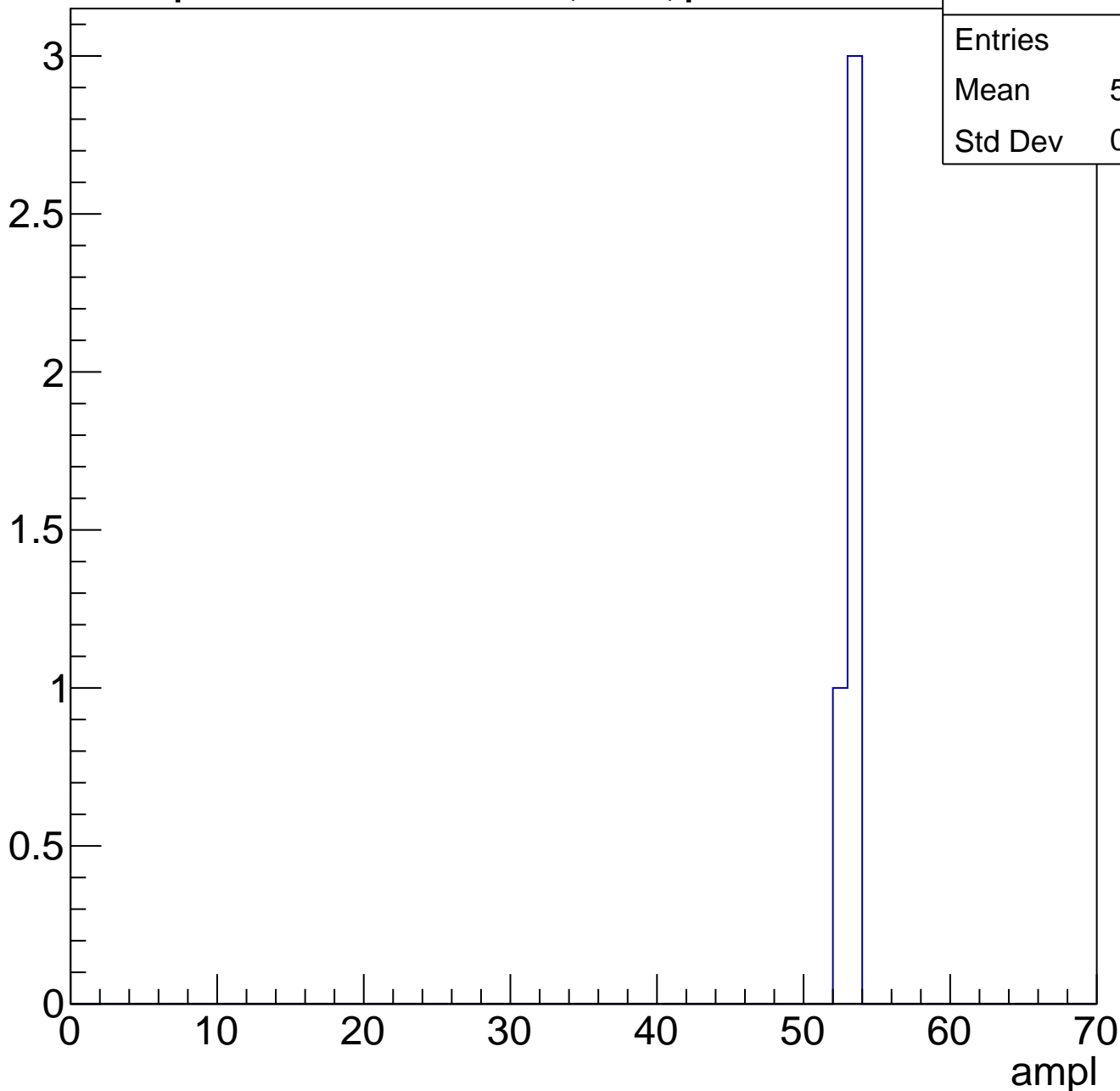
Entries	4
Mean	52.75
Std Dev	0.433

B0L100S, U5-ch72, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch72, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch72, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch72, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch73, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch73, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch73, adc2

calib_packv5_042523_0143.root, FC#6, port A1
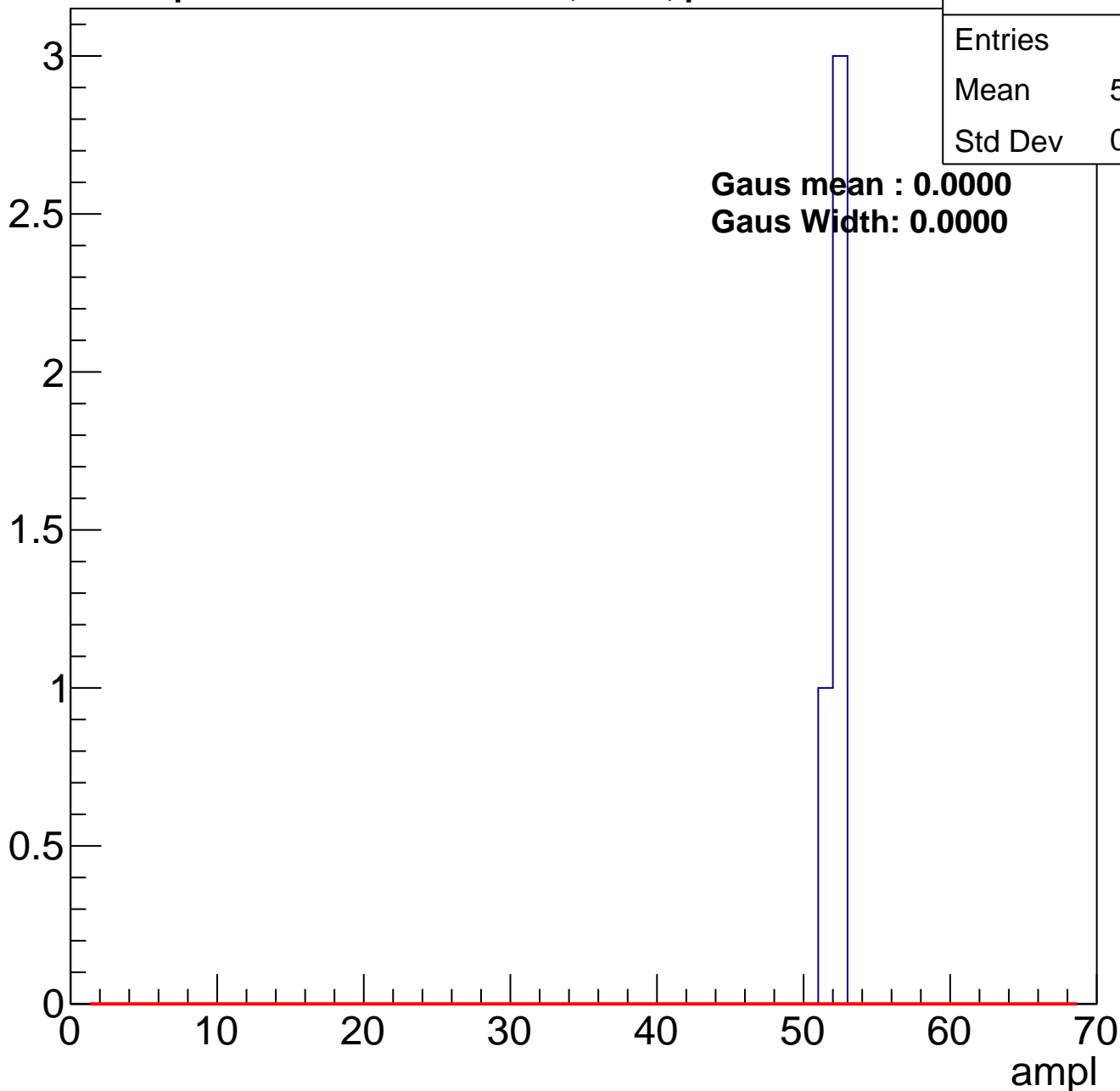
Entries	4
Mean	51.75
Std Dev	0.433

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch73, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch73, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch73, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch73, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch73, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch74, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch74, adc1

calib_packv5_042523_0143.root, FC#6, port A1
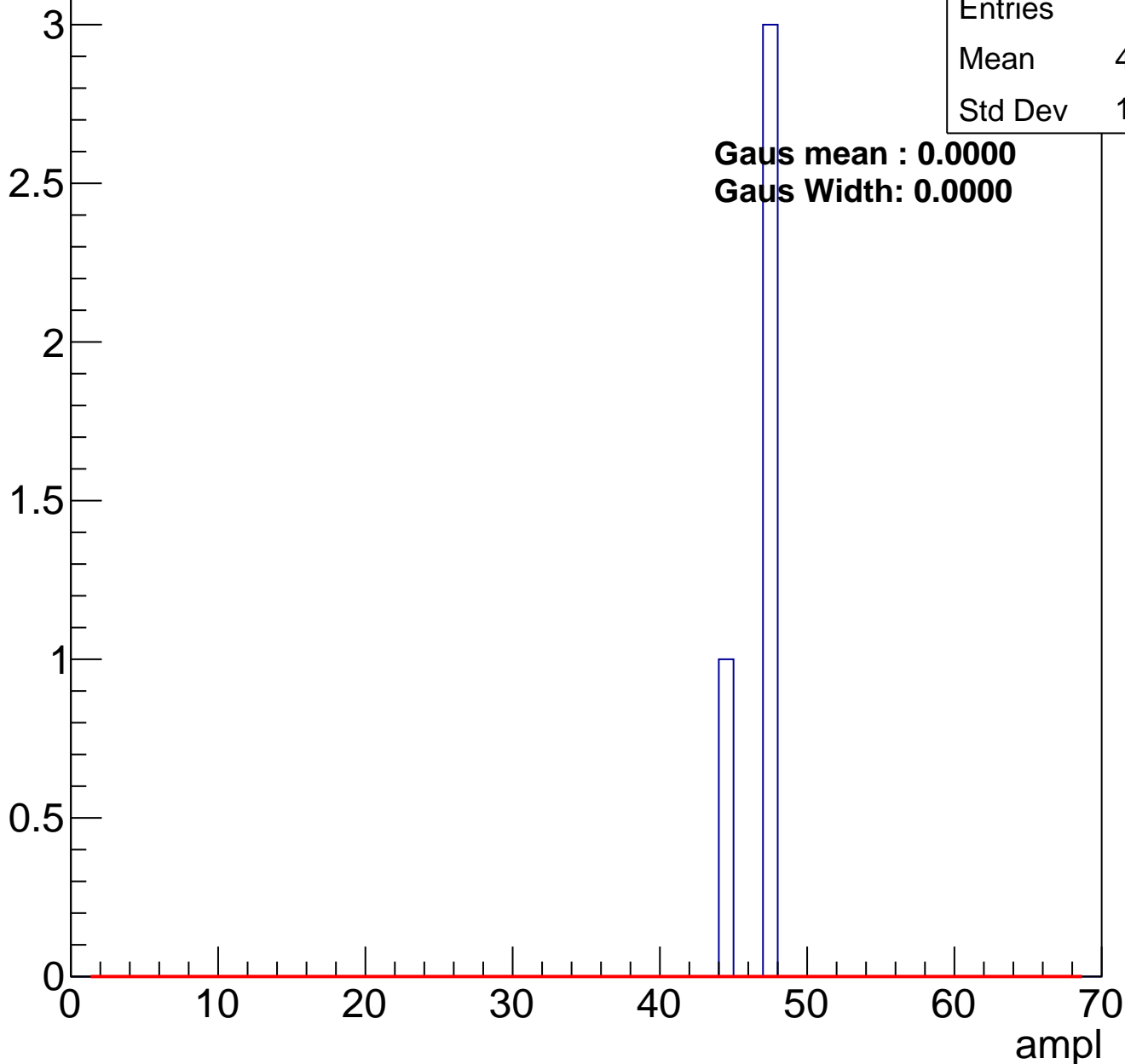
Entries	4
Mean	46.25
Std Dev	1.299

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch74, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	3
Mean	46
Std Dev	1.414

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl
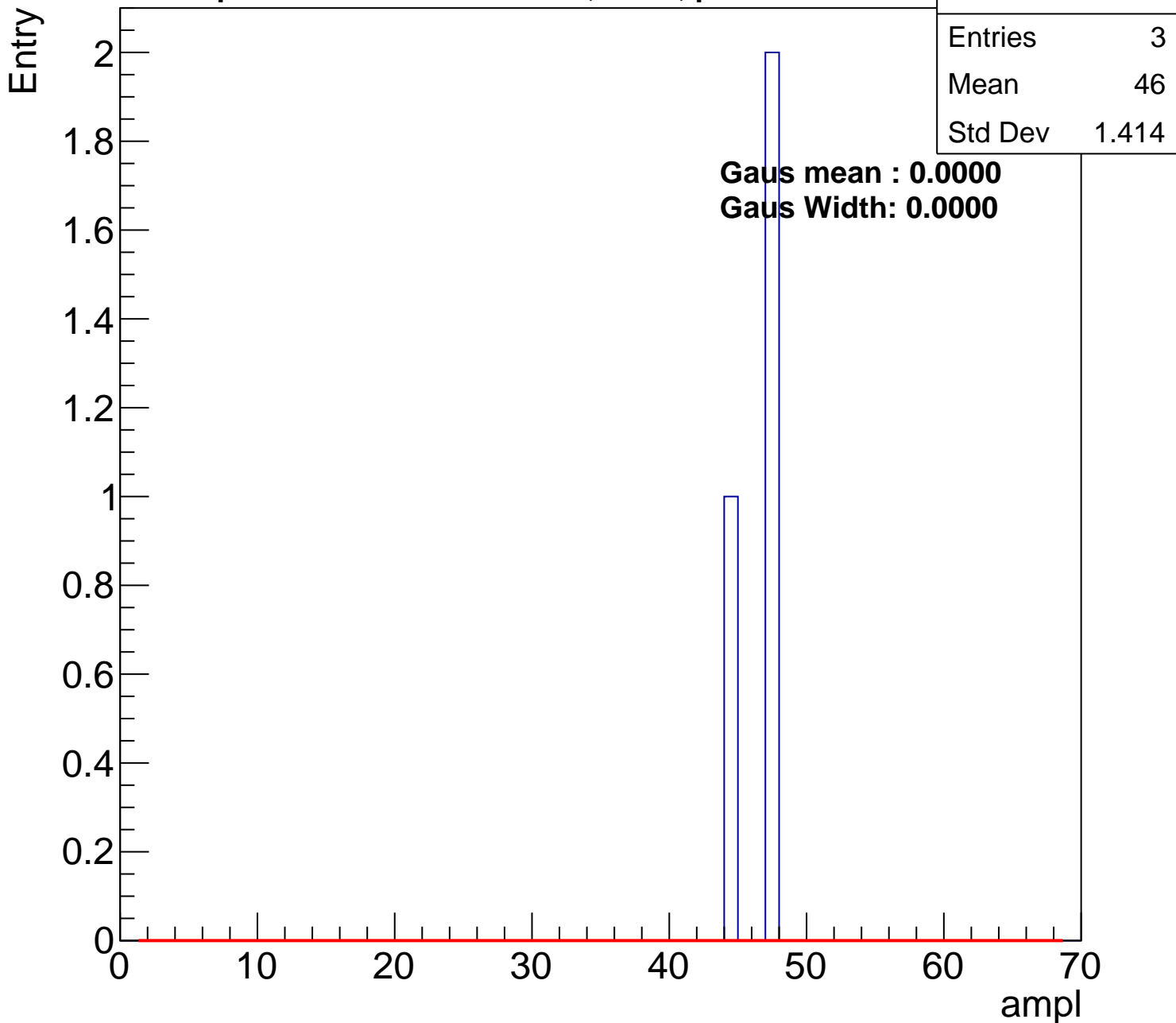
B0L100S, U5-ch74, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch74, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch74, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch74, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch74, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch75, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch75, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch75, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch75, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch75, adc4

calib_packv5_042523_0143.root, FC#6, port A1
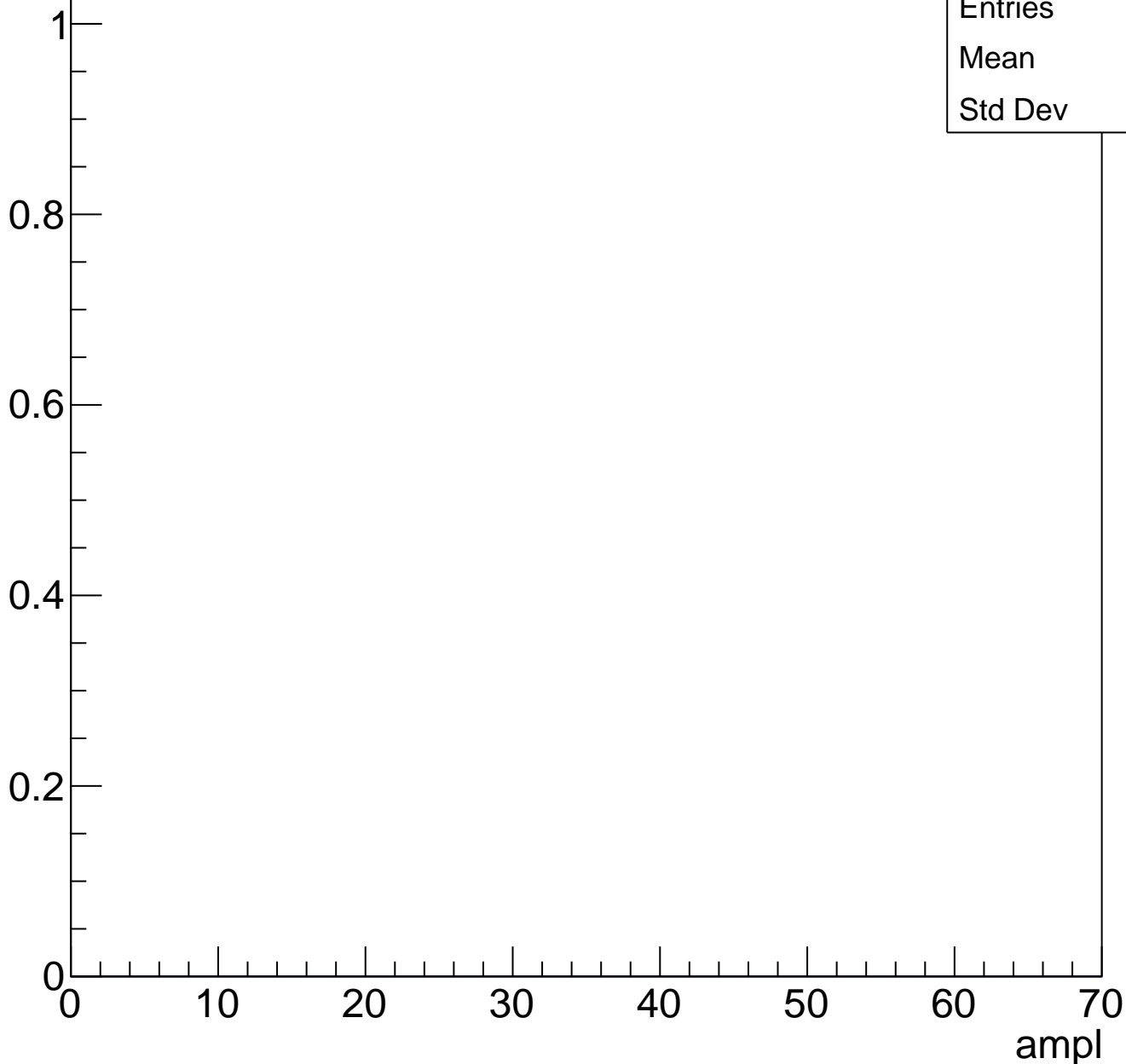
Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch75, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch75, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch75, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch76, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	25
Mean	34.64
Std Dev	2.037

Gaus mean : 35.3652
Gaus Width: 3.0906
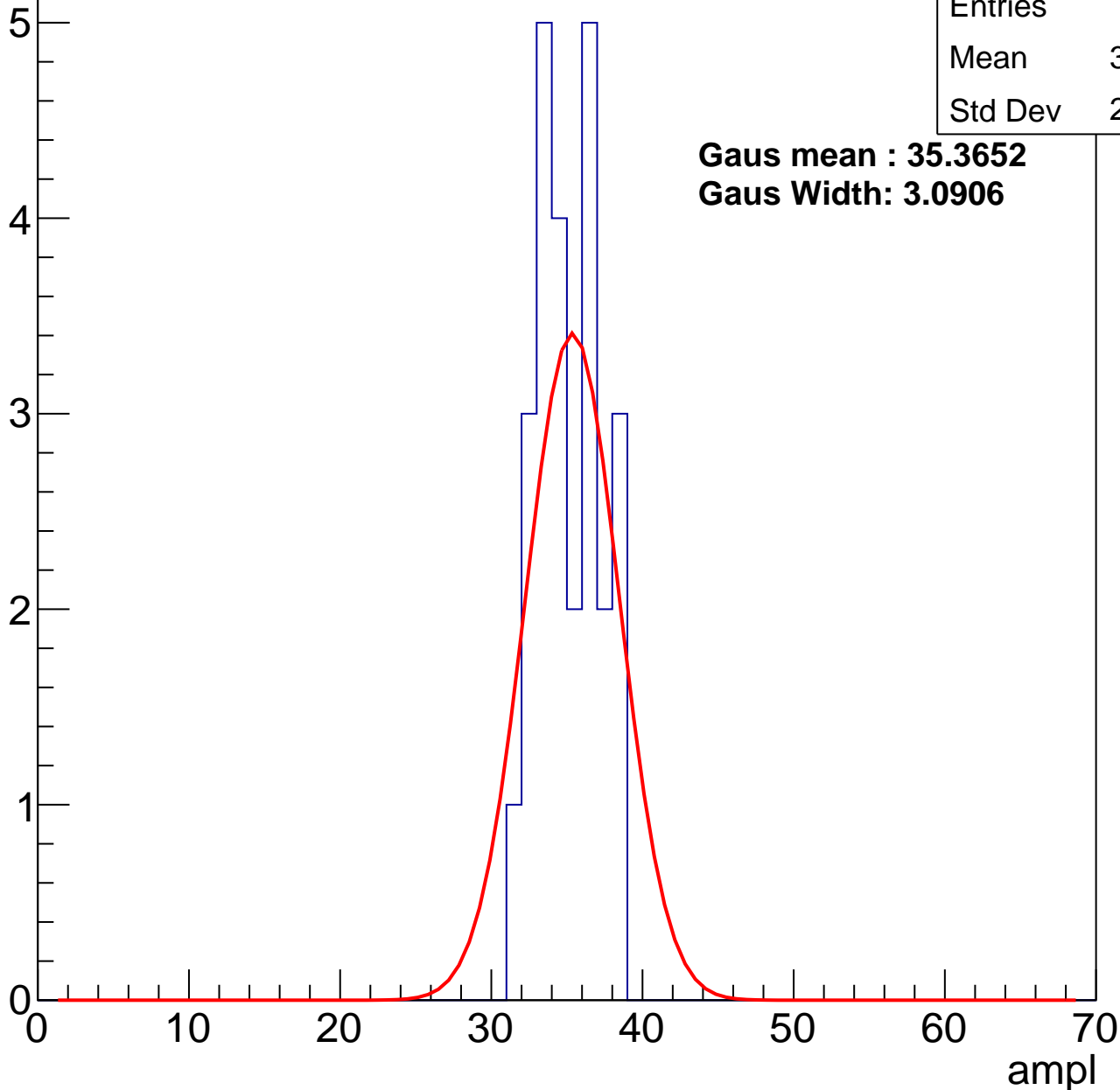
B0L100S, U5-ch76, adc1

calib_packv5_042523_0143.root, FC#6, port A1
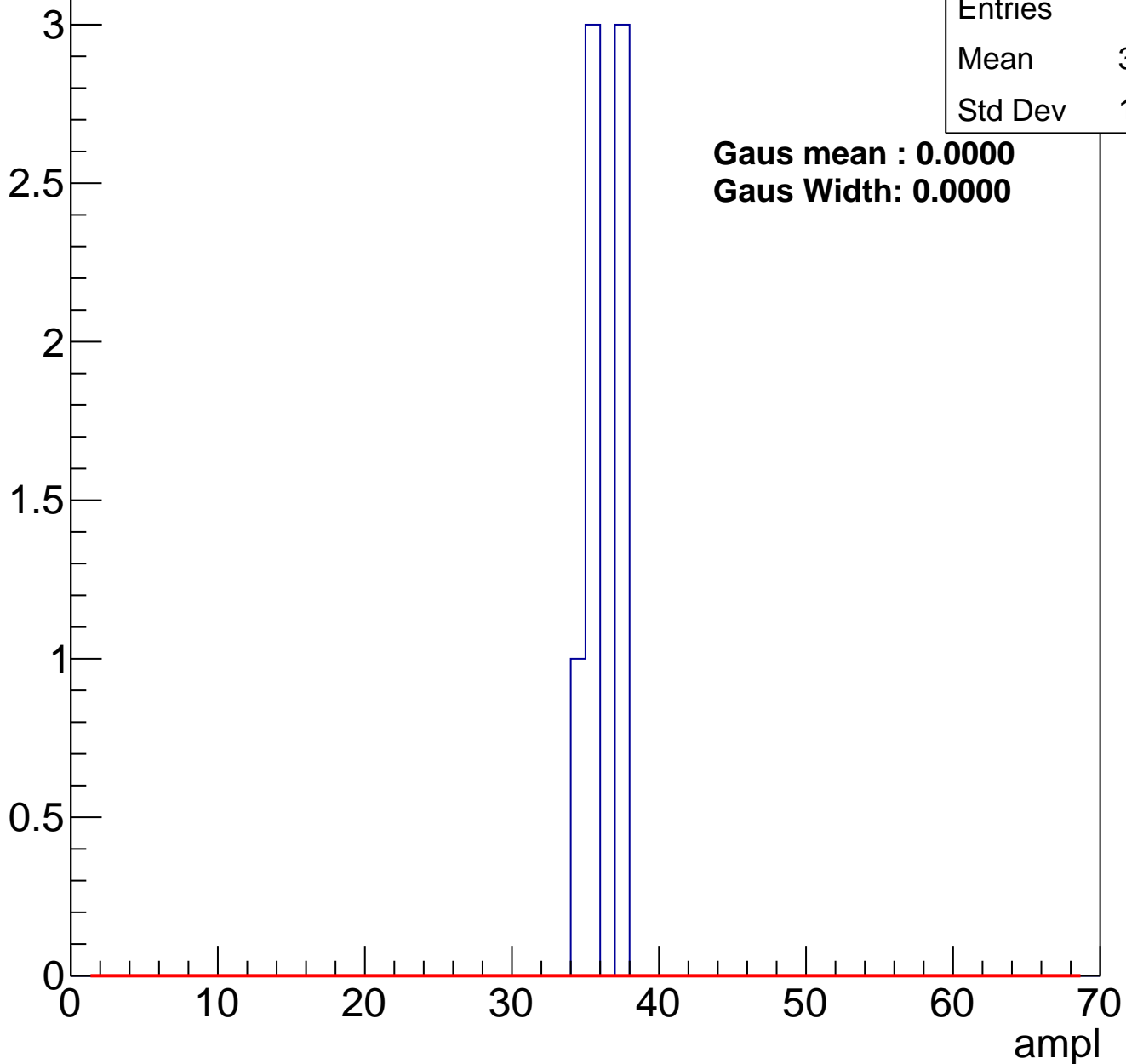
Entries	7
Mean	35.71
Std Dev	1.161

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch76, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch76, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch76, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch76, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch76, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch76, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch77, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch77, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch77, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch77, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch77, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch77, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch77, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch77, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch78, adc0

calib_packv5_042523_0143.root, FC#6, port A1
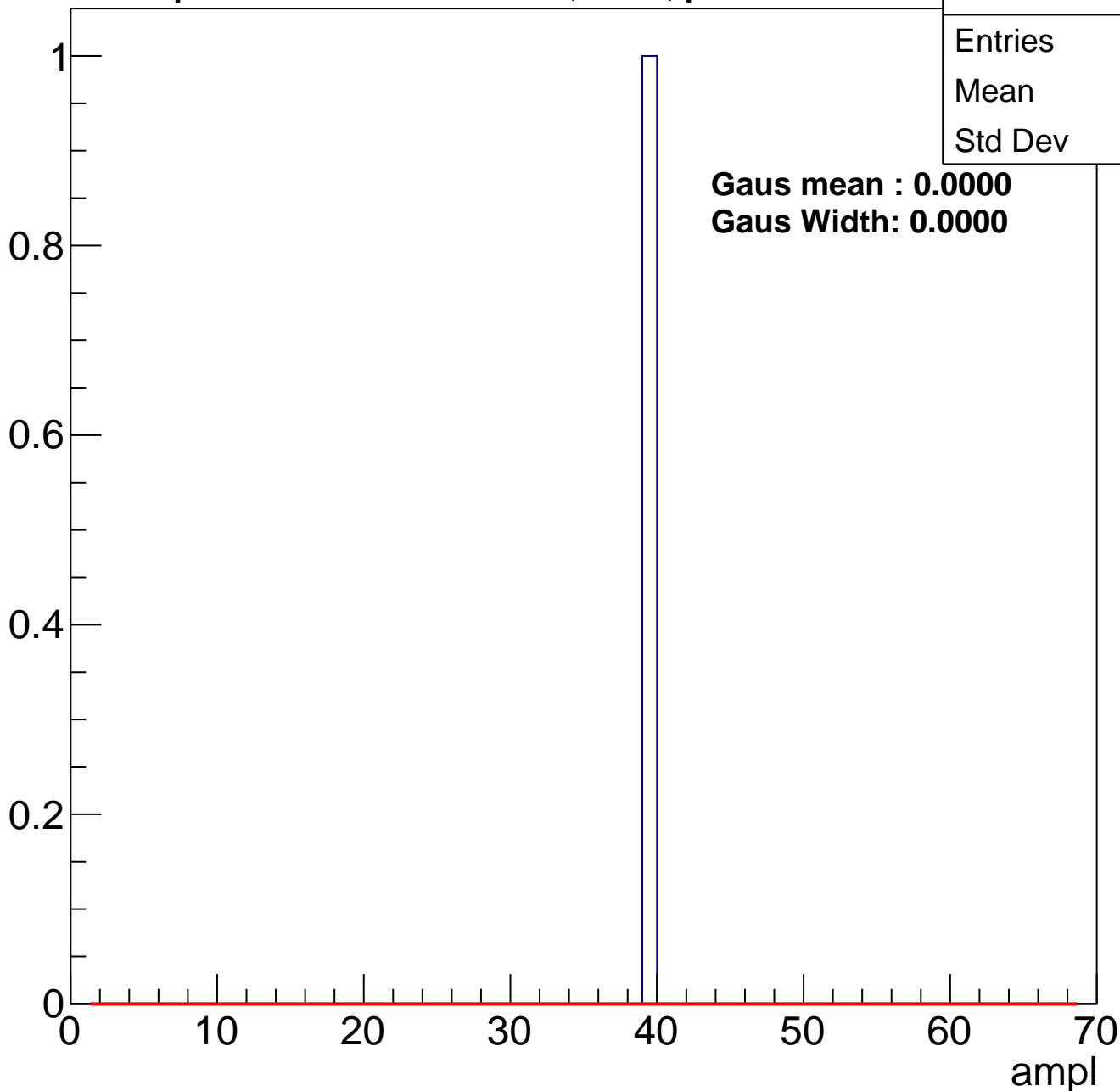
Entries	1
Mean	39
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch78, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	12
Mean	45.42
Std Dev	2.691

Gaus mean : 45.2853
Gaus Width: 3.6806
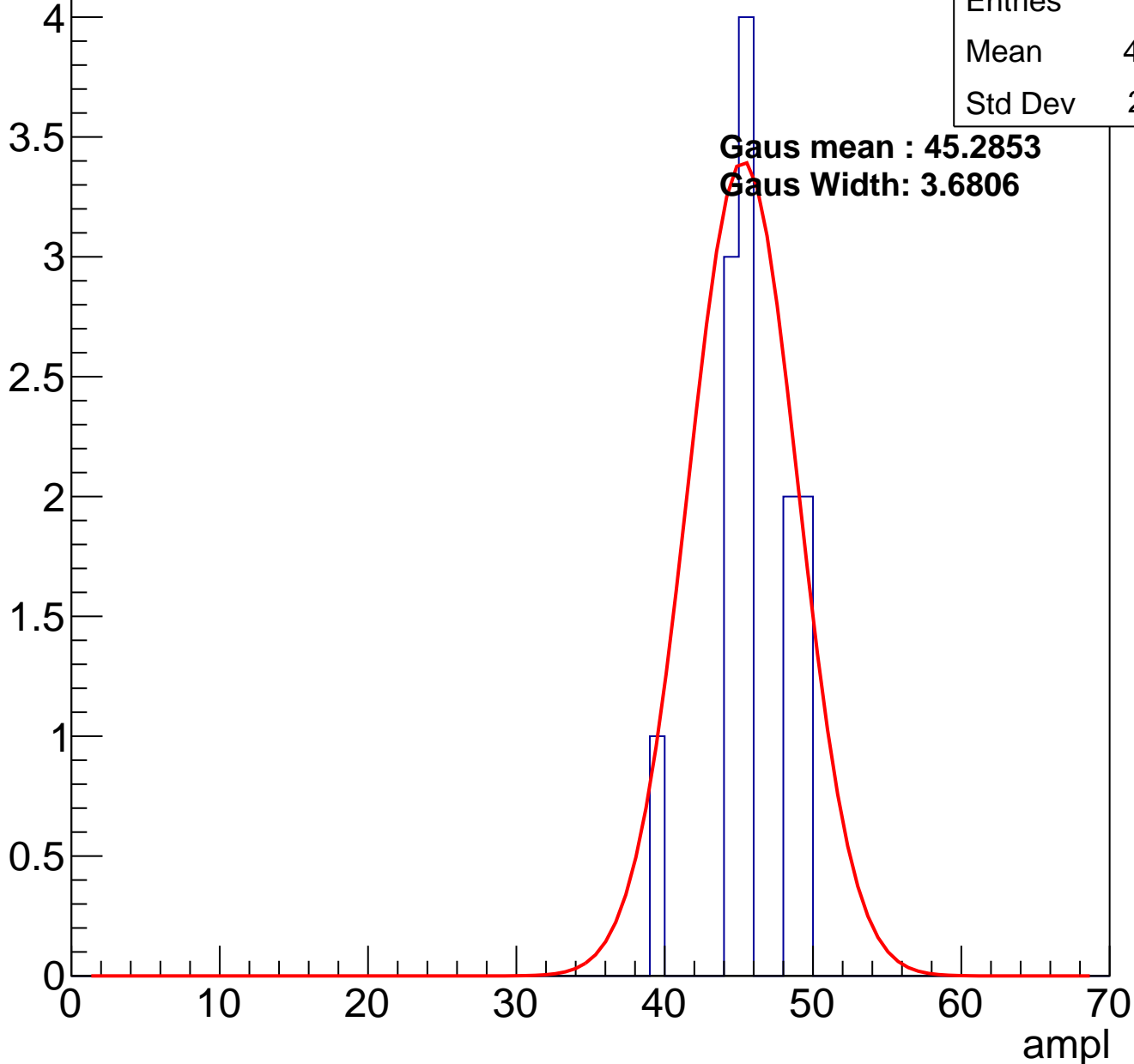
B0L100S, U5-ch78, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	12
Mean	47.92
Std Dev	2.396

Gaus mean : 63.6314
Gaus Width: 23.9314
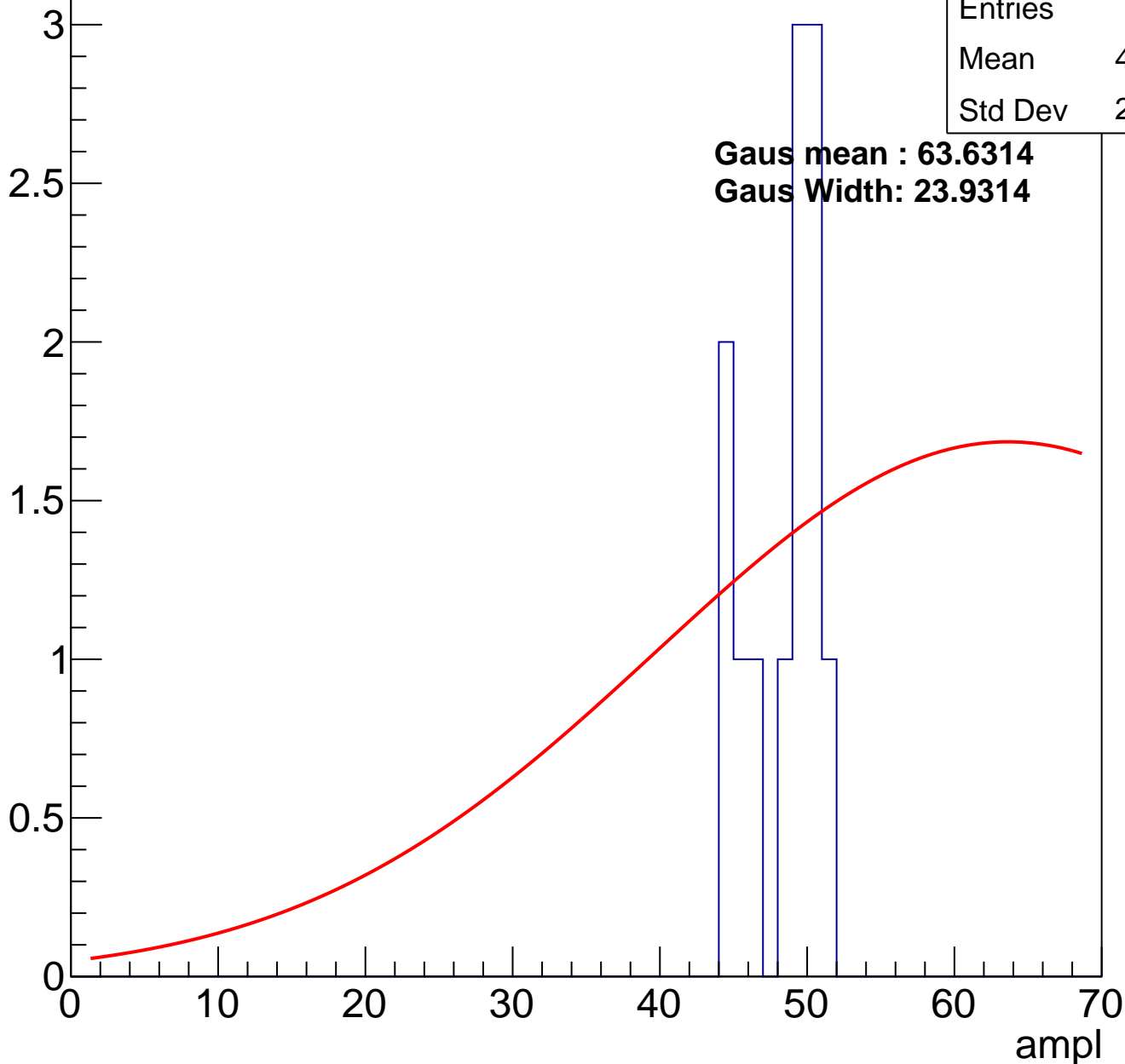
B0L100S, U5-ch78, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	50.33
Std Dev	0.4714

B0L100S, U5-ch78, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch78, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch78, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch78, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch79, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch79, adc1

calib_packv5_042523_0143.root, FC#6, port A1
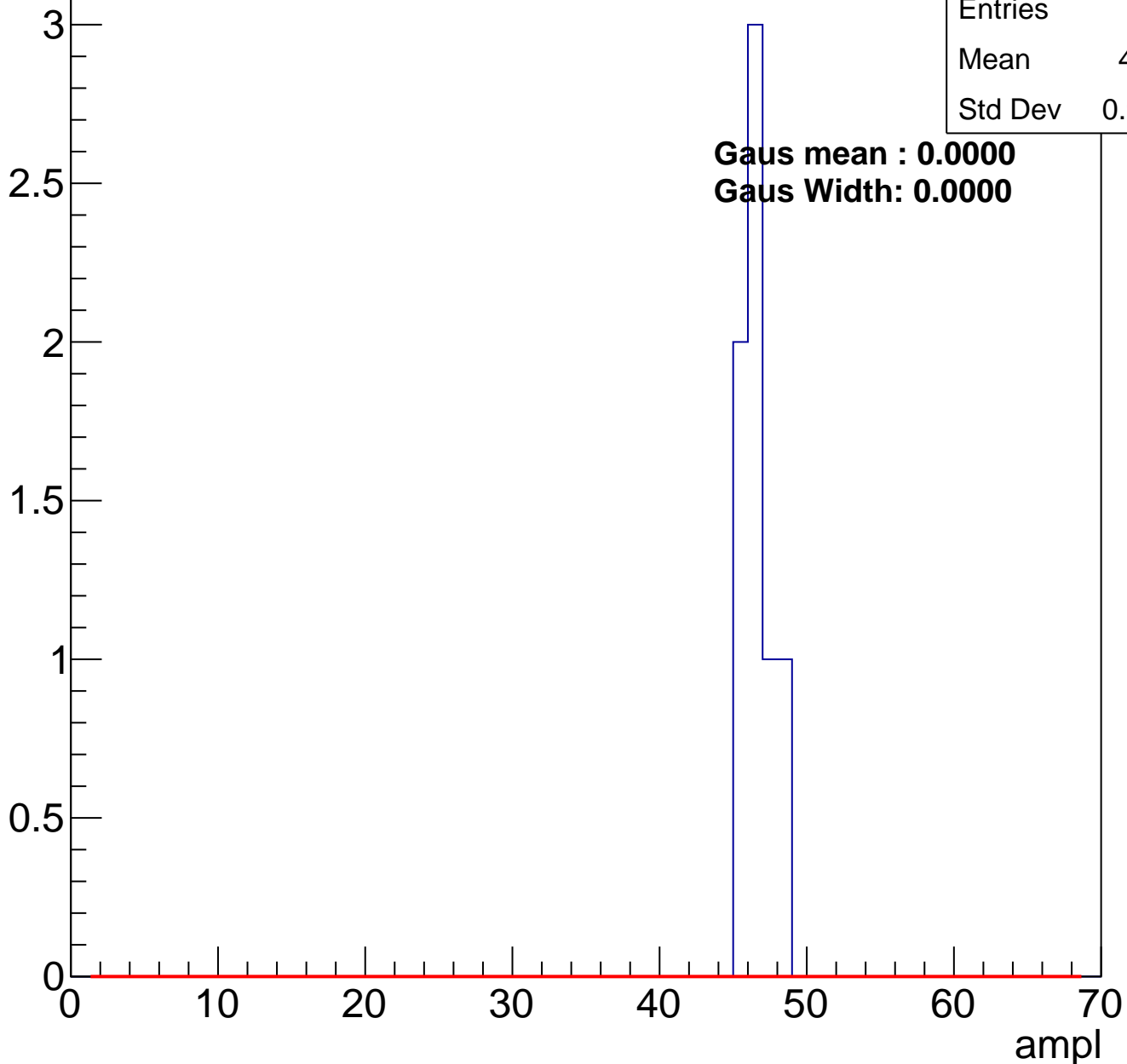
Entries	7
Mean	46.14
Std Dev	0.9897

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch79, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 53.5332
Gaus Width: 4.7895

Entries	12
Mean	46.83
Std Dev	1.067

Entry

ampl
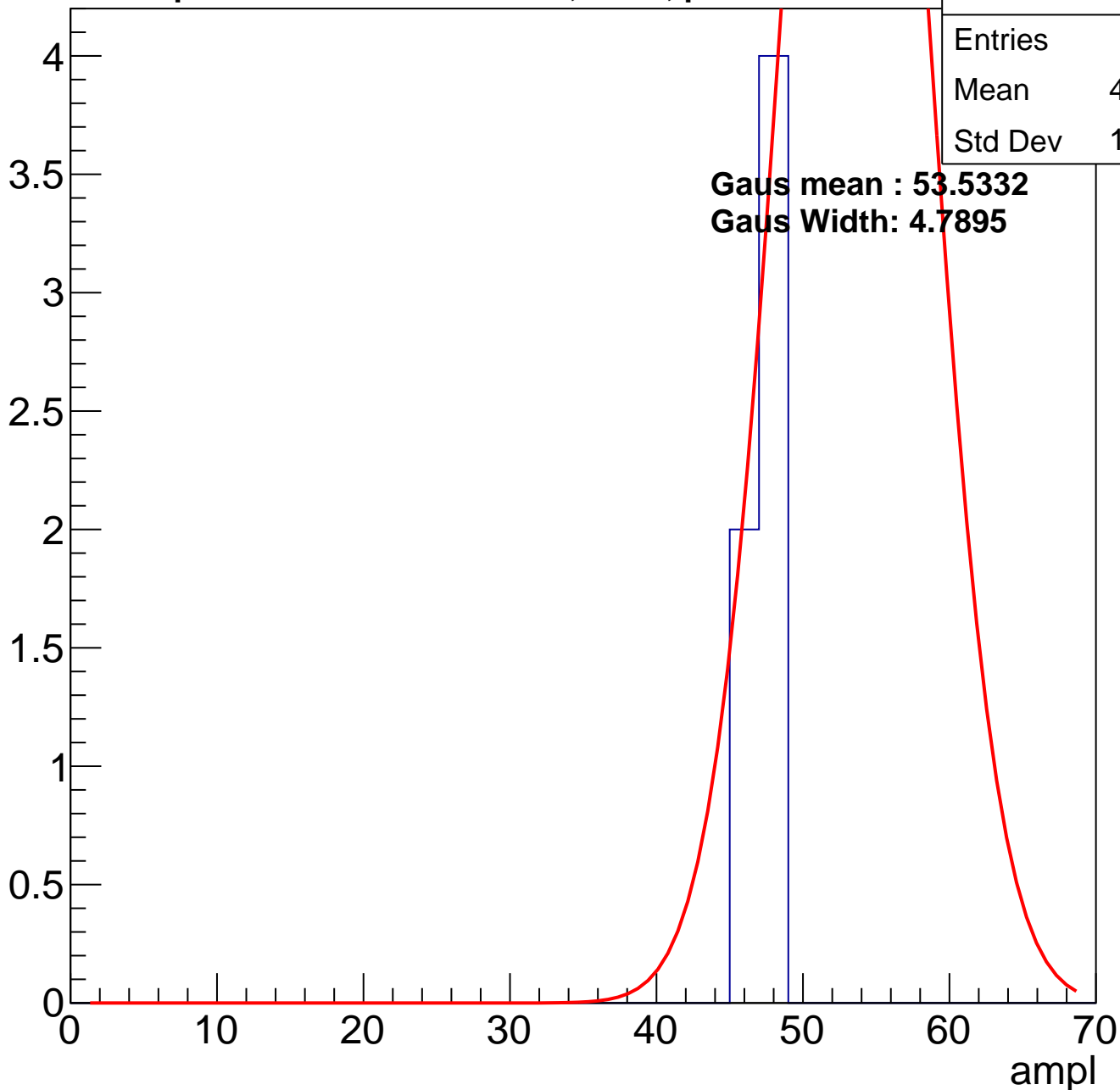
B0L100S, U5-ch79, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Entry

ampl
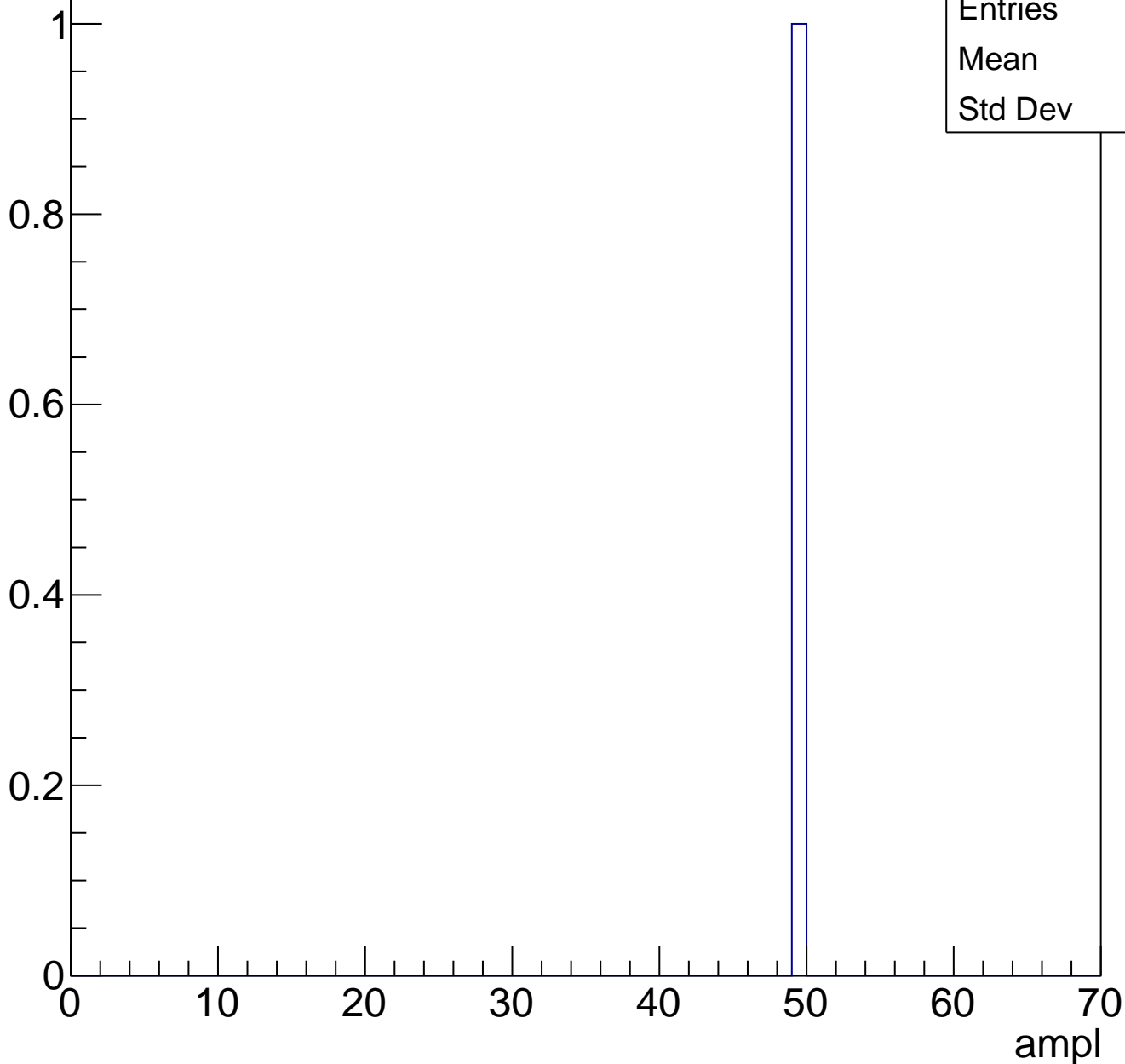
B0L100S, U5-ch79, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch79, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch79, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch79, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch80, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch80, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch80, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	52
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
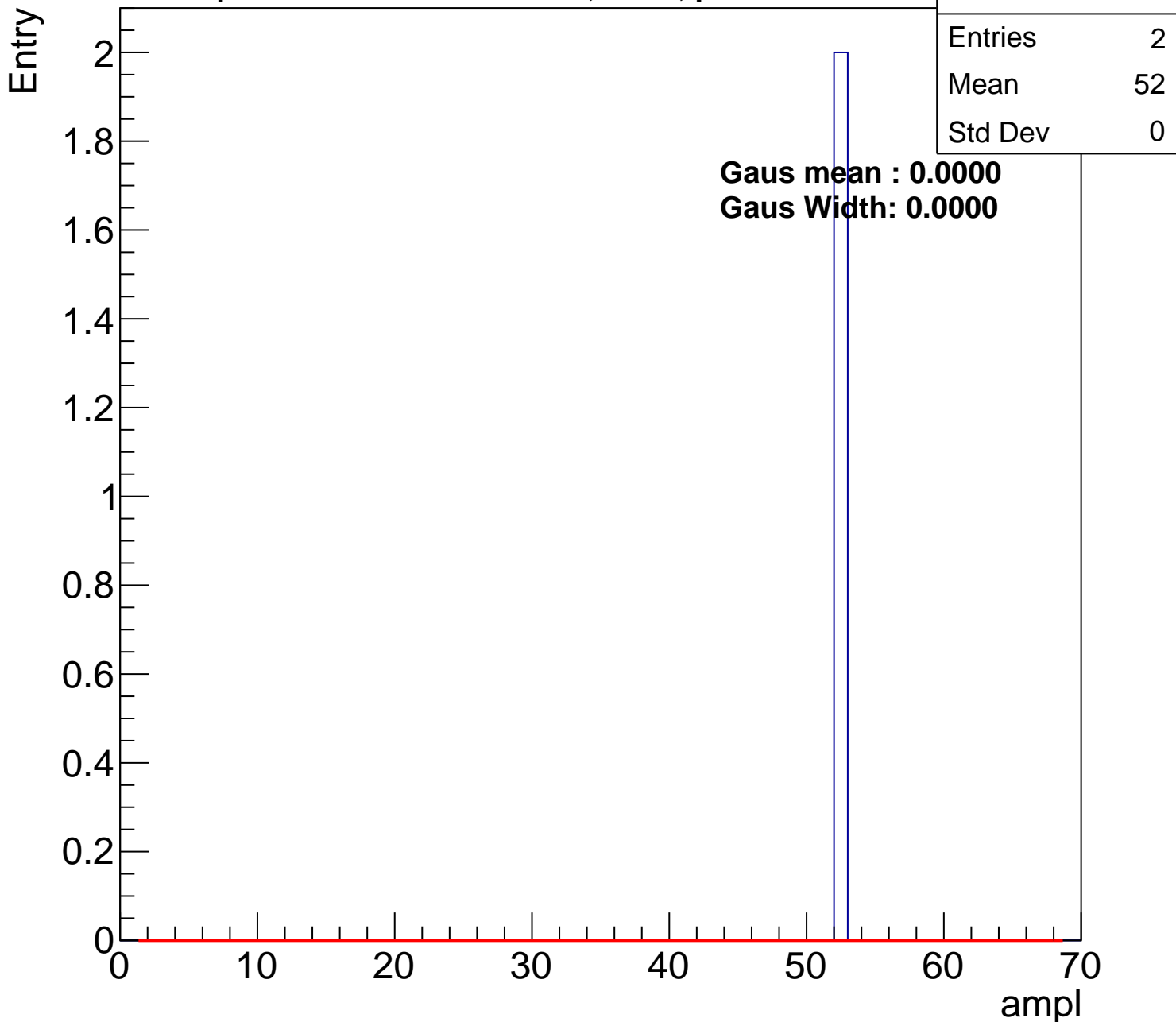
B0L100S, U5-ch80, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.4
Std Dev	0.4899

Entry

ampl
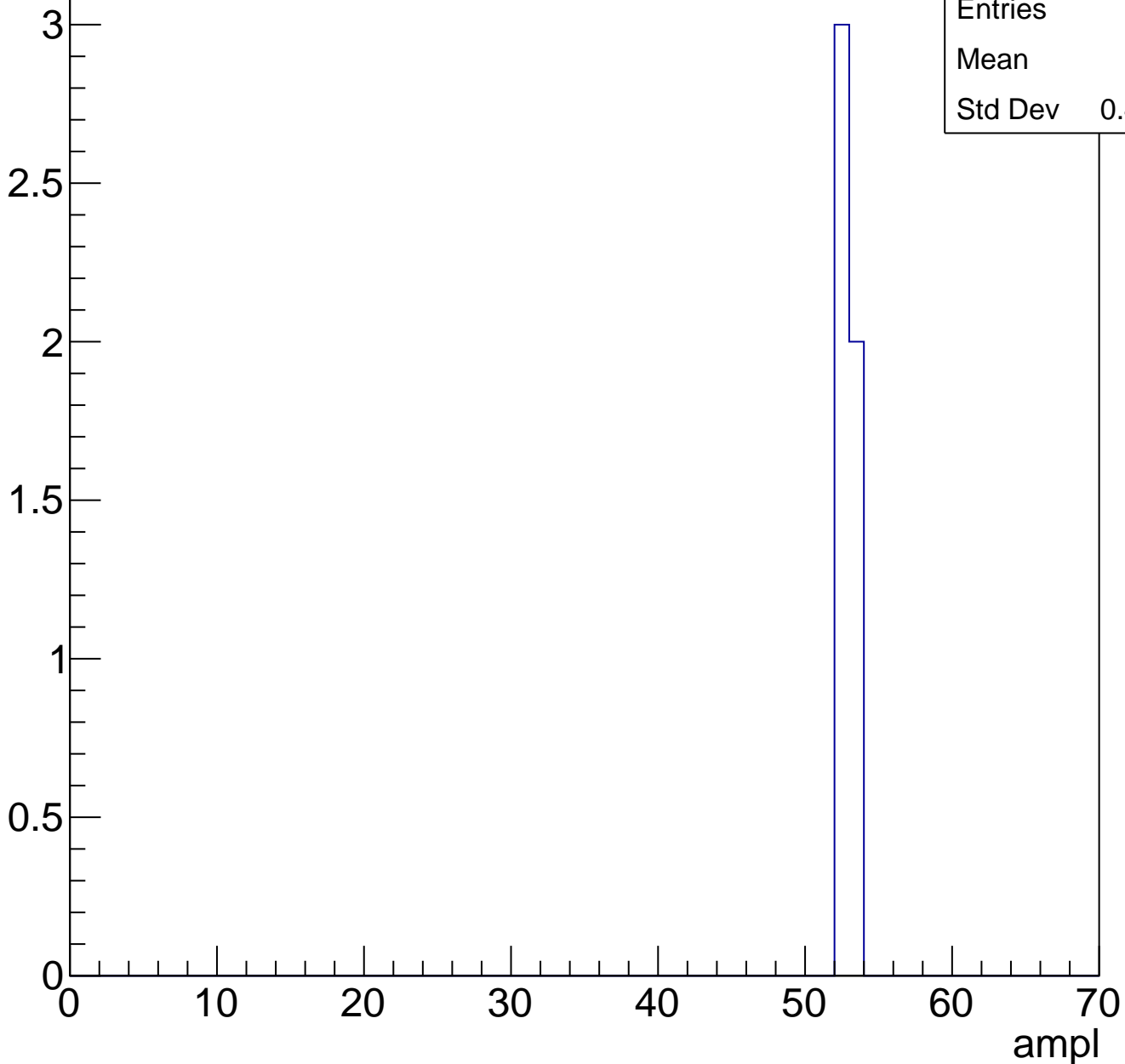
B0L100S, U5-ch80, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch80, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch80, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch80, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch81, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch81, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
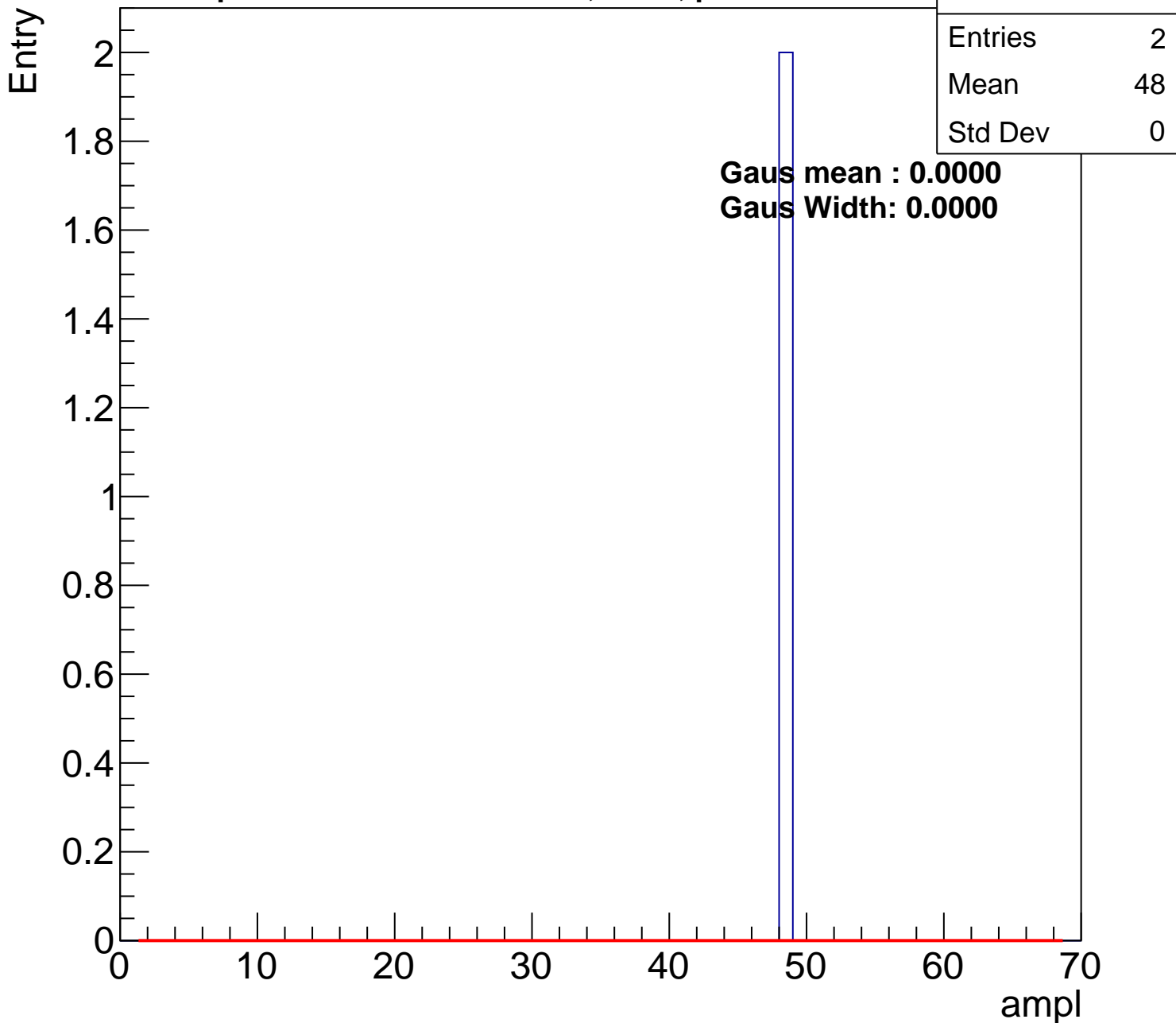
Entry

ampl

B0L100S, U5-ch81, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	48.6
Std Dev	1.625

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
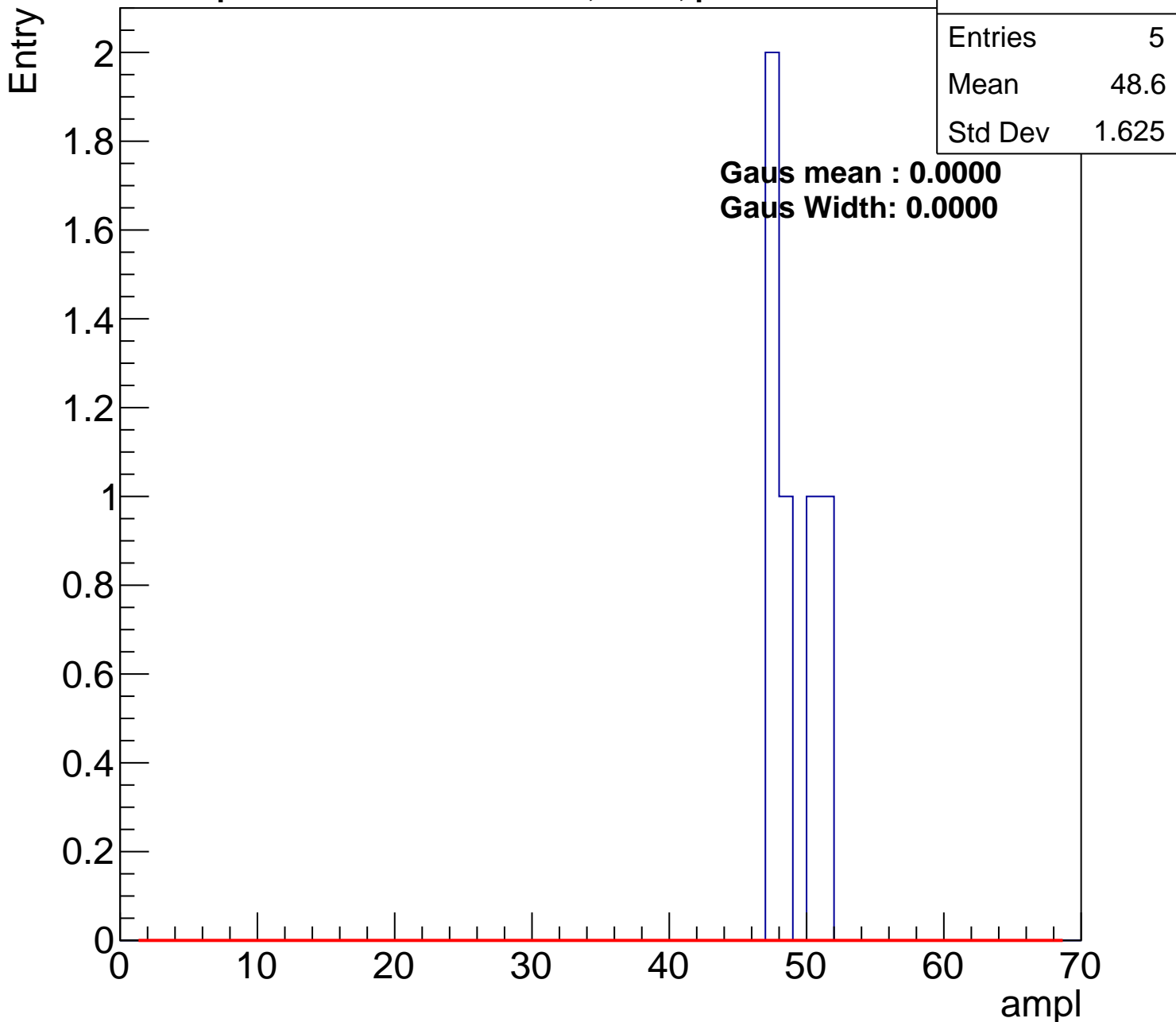
B0L100S, U5-ch81, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

ampl
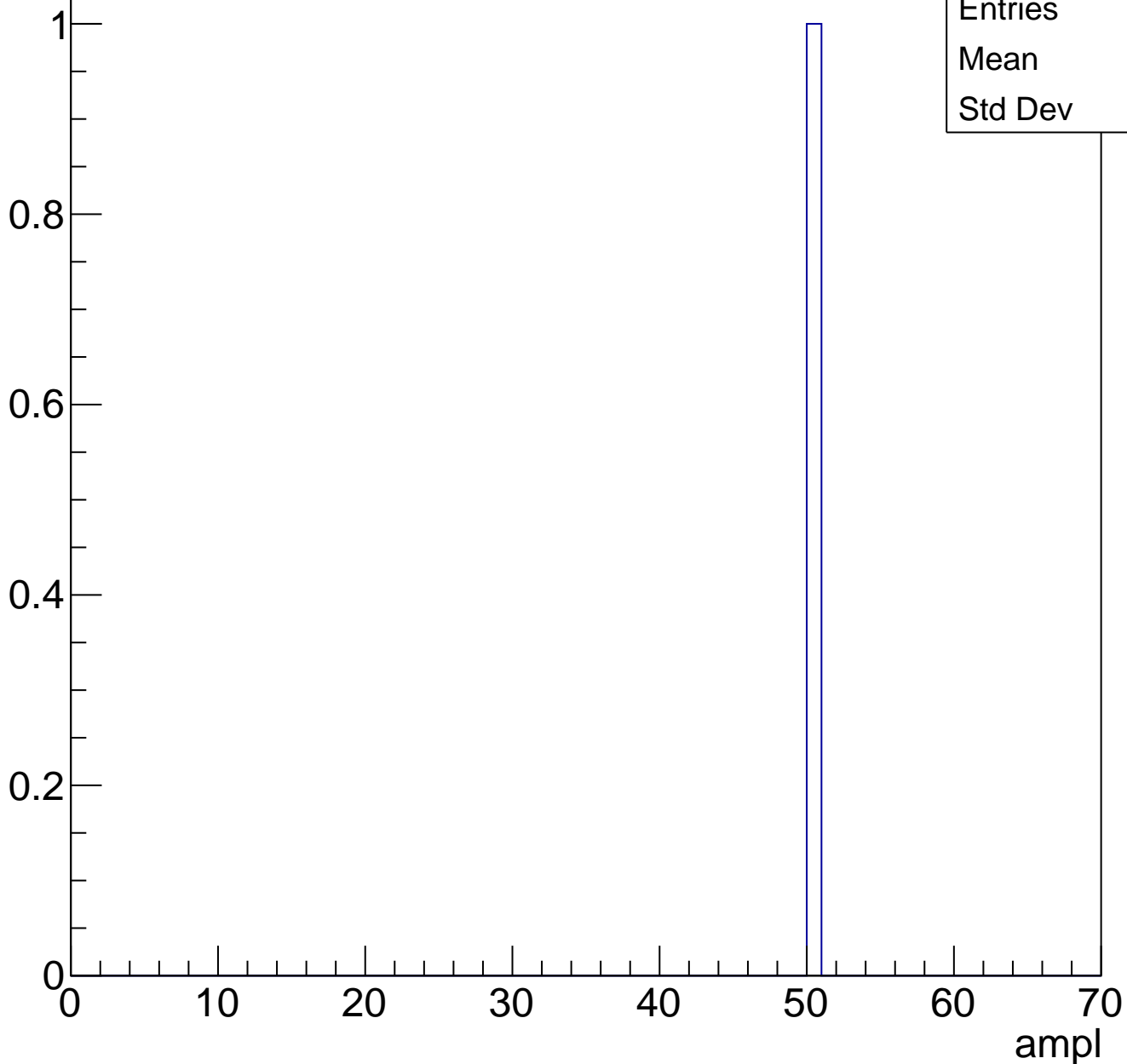
B0L100S, U5-ch81, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch81, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch81, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch81, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch82, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch82, adc1

calib_packv5_042523_0143.root, FC#6, port A1
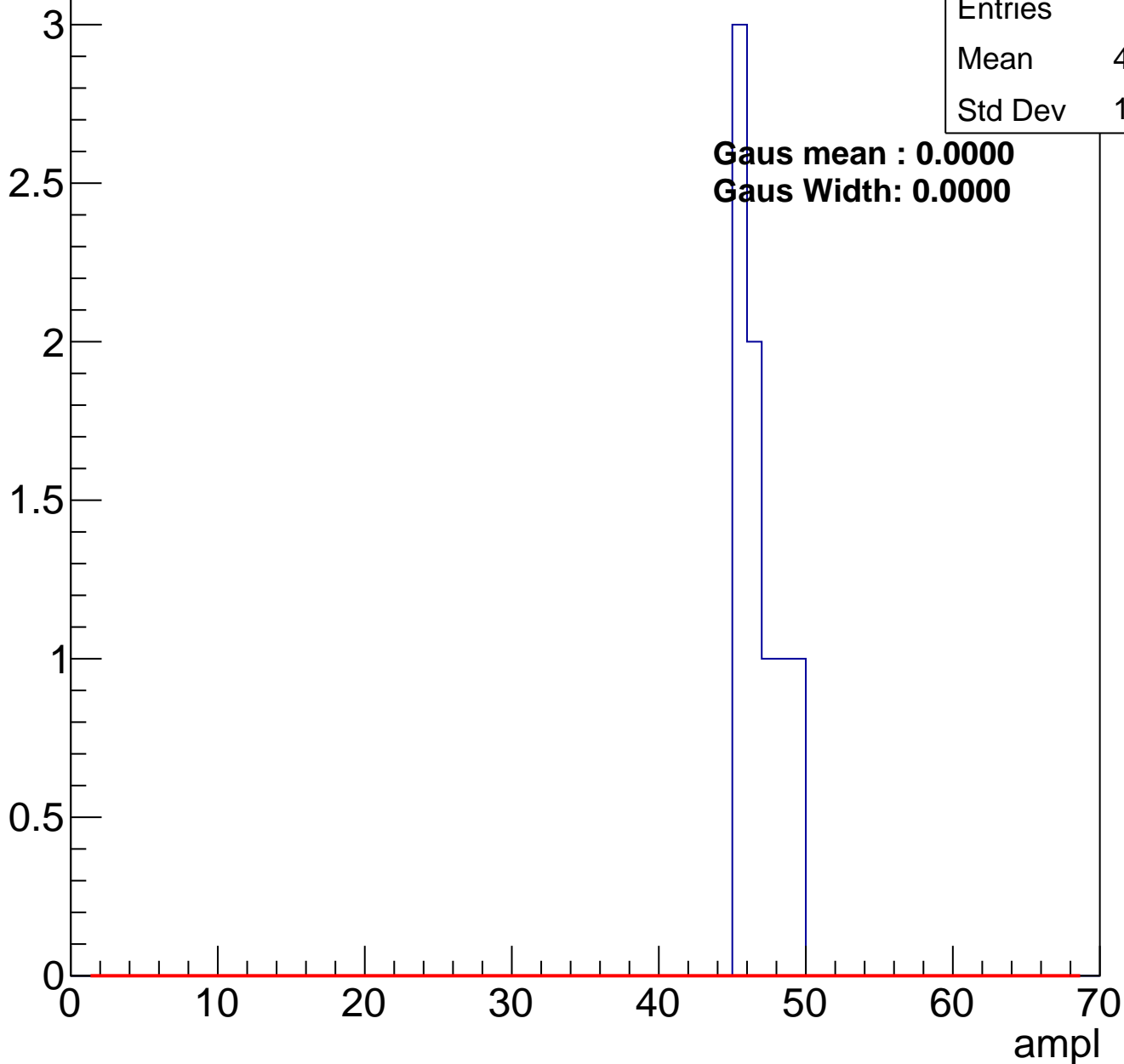
Entries	8
Mean	46.38
Std Dev	1.409

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch82, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	10
Mean	46.7
Std Dev	1.1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
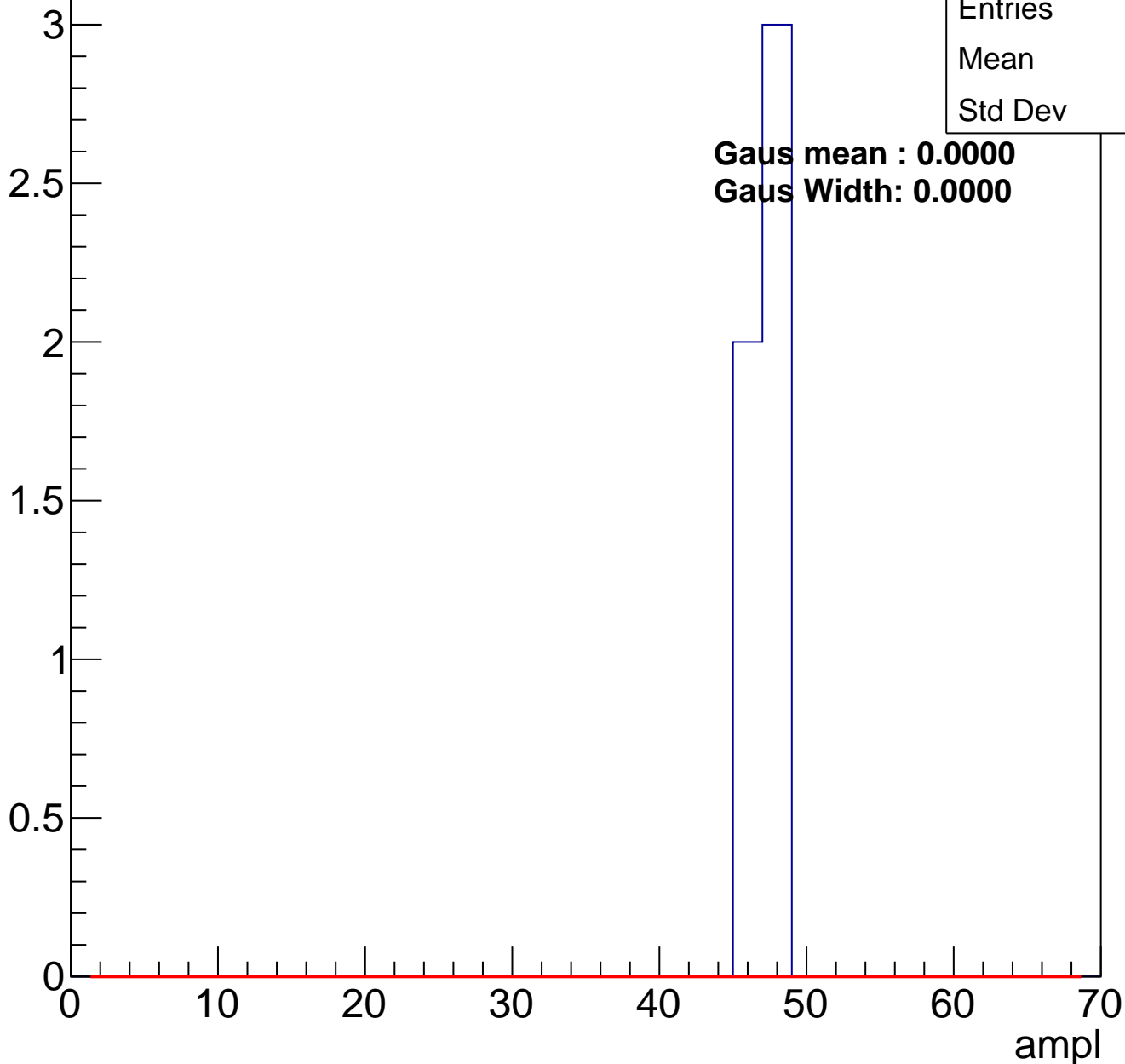
B0L100S, U5-ch82, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	47
Std Dev	0.8165

Entry

ampl
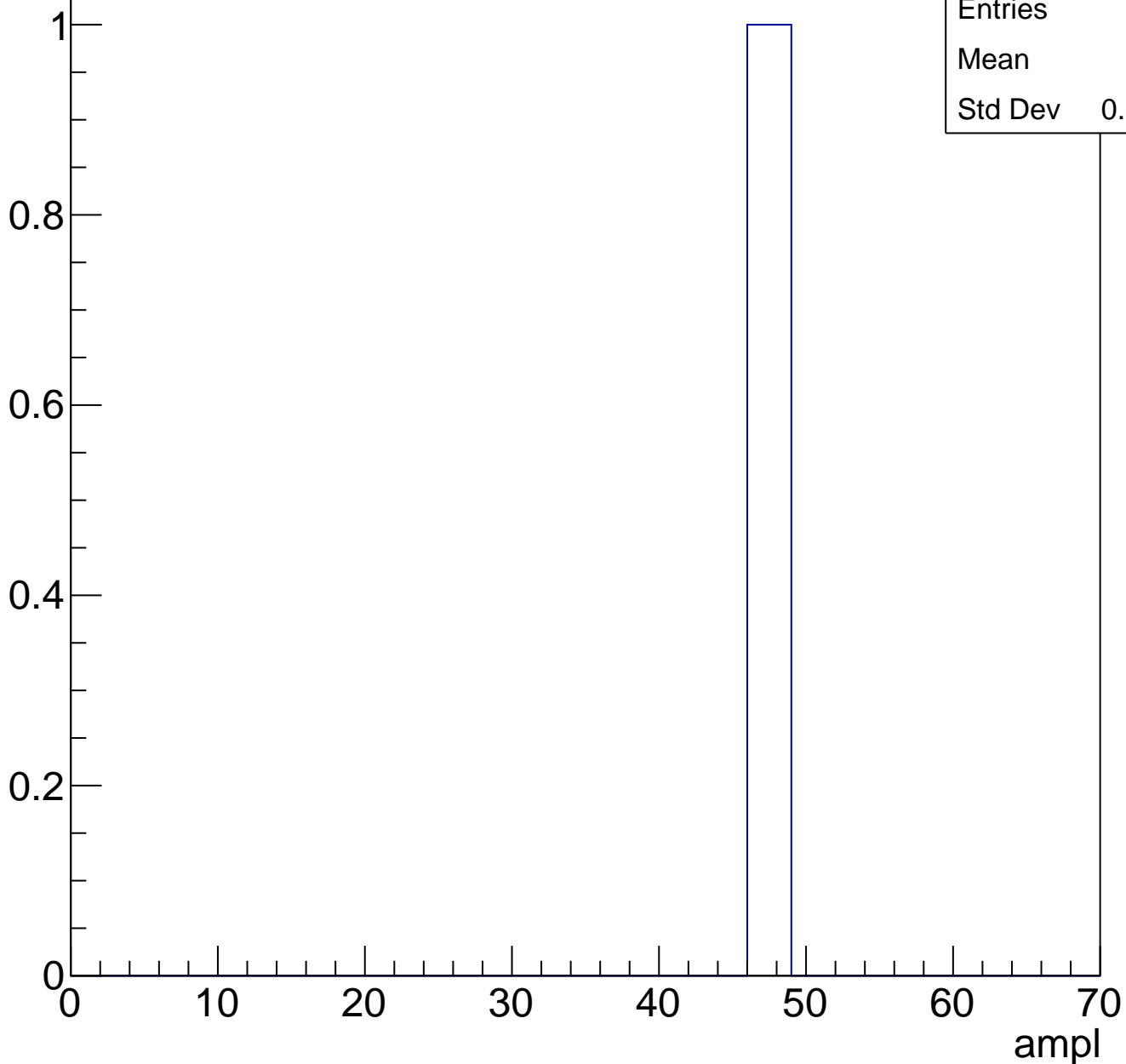
B0L100S, U5-ch82, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch82, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch82, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch82, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch83, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch83, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch83, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch83, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch83, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch83, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch83, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch83, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch84, adc0

calib_packv5_042523_0143.root, FC#6, port A1
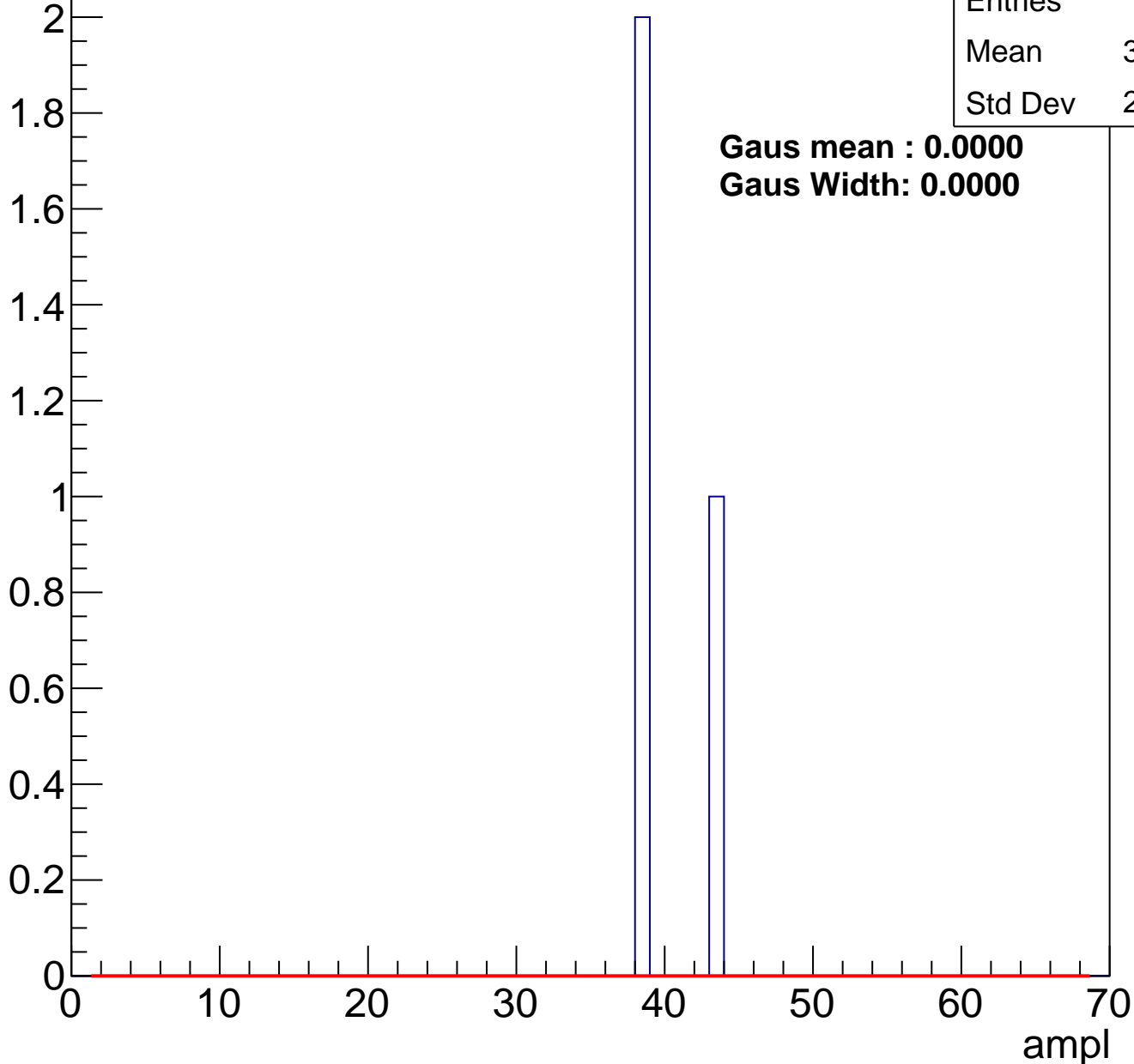
Entries	3
Mean	39.67
Std Dev	2.357

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch84, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	44
Std Dev	3.505

Gaus mean : 42.7889
Gaus Width: 3.4676
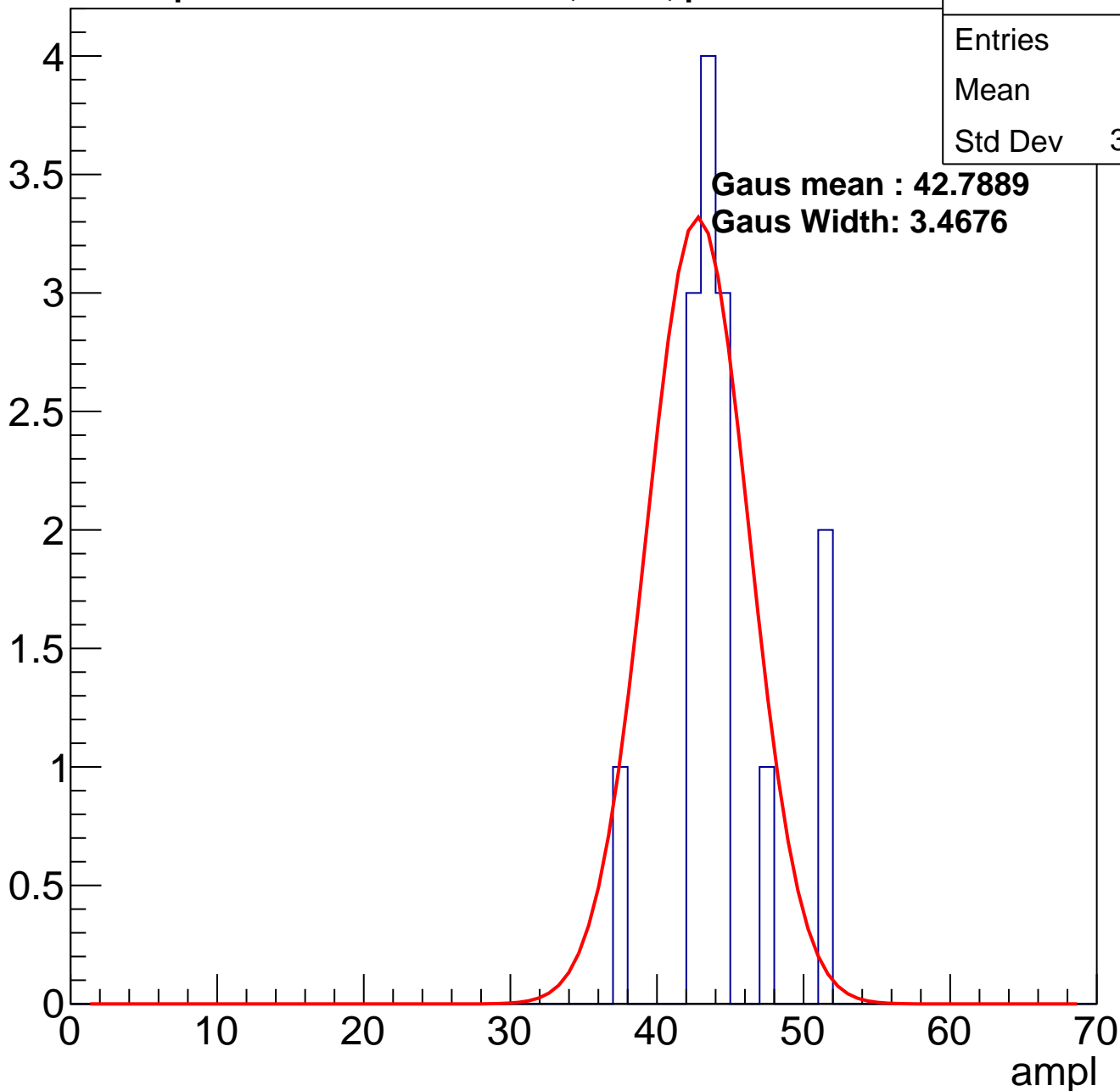
B0L100S, U5-ch84, adc2

calib_packv5_042523_0143.root, FC#6, port A1
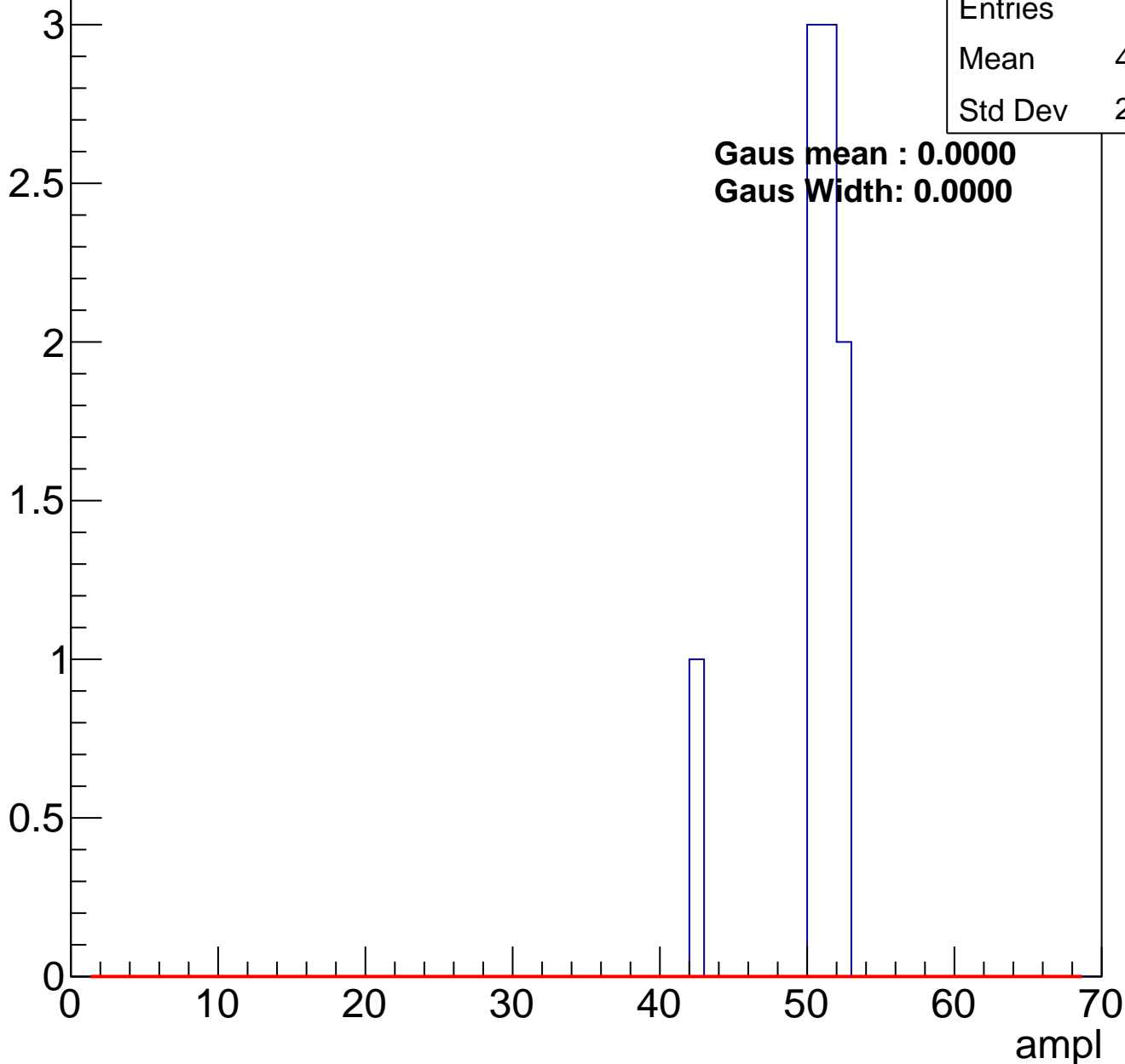
Entries	9
Mean	49.89
Std Dev	2.885

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch84, adc3

calib_packv5_042523_0143.root, FC#6, port A1
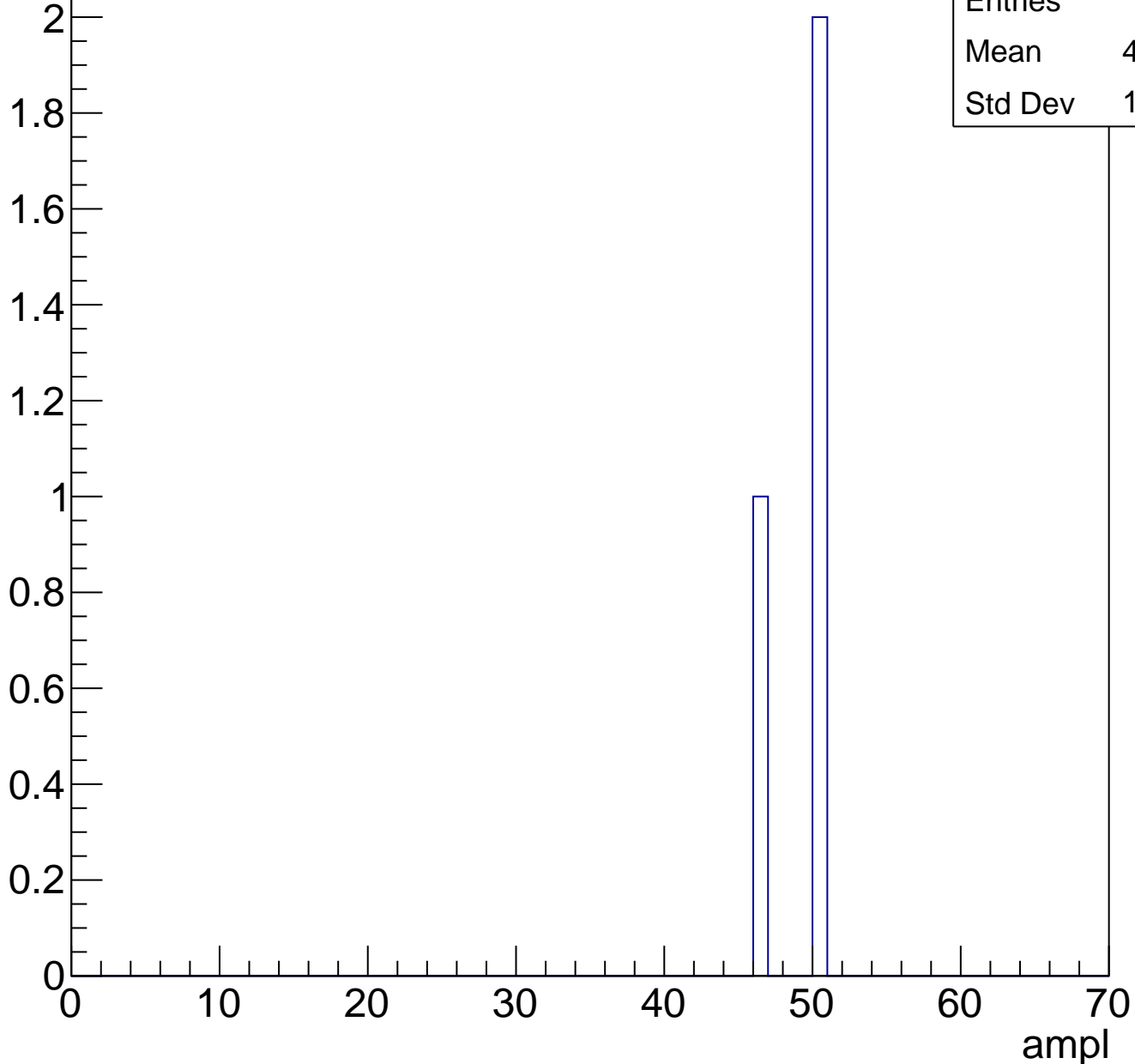
Entries	3
Mean	48.67
Std Dev	1.886

Entry

ampl

B0L100S, U5-ch84, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch84, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch84, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch84, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch85, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch85, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	14
Mean	47.79
Std Dev	2.425

Gaus mean : 47.6996
Gaus Width: 6.3457
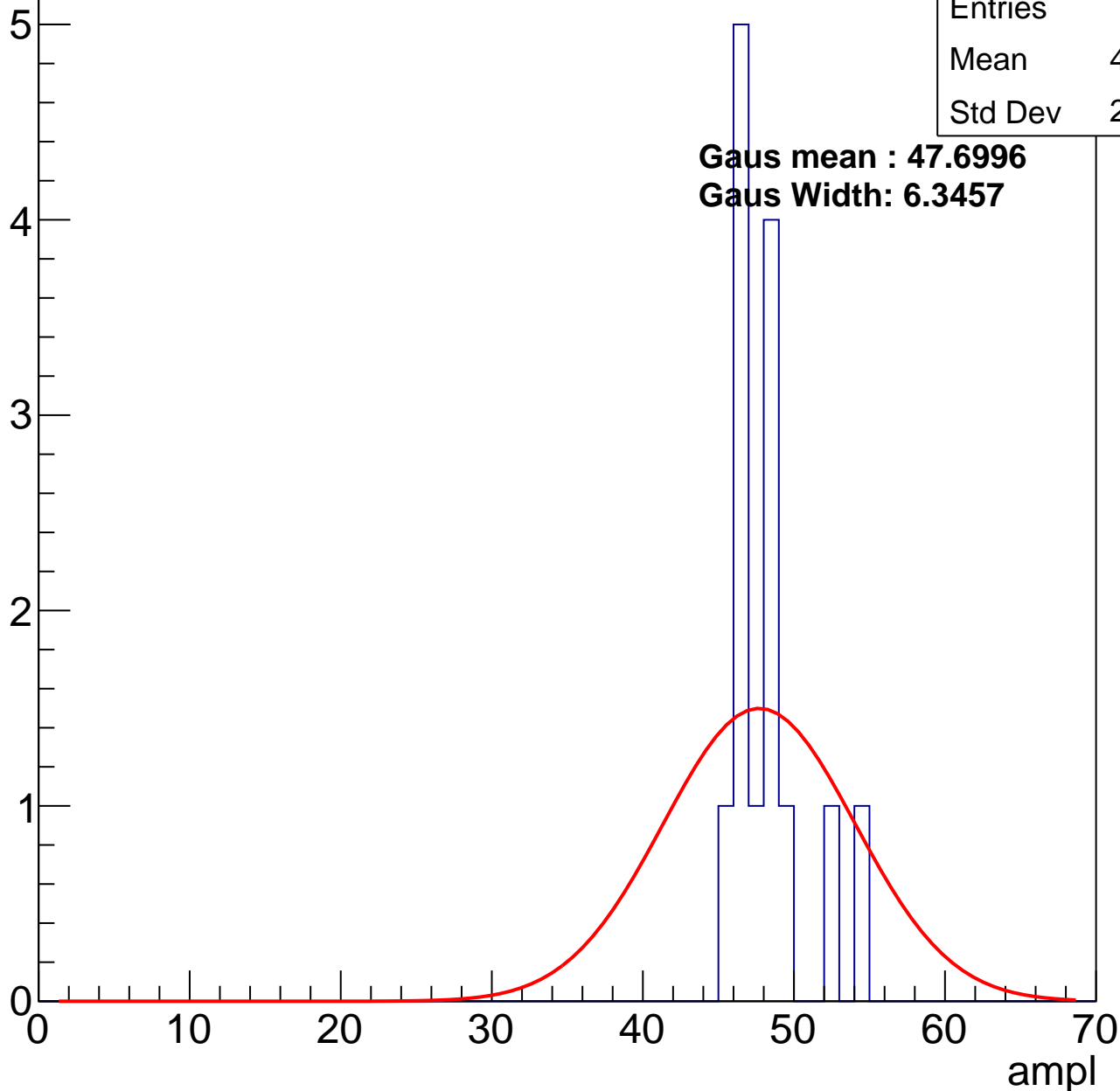
B0L100S, U5-ch85, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	23
Mean	49.83
Std Dev	2.014

Gaus mean : 51.0376
Gaus Width: 2.1275
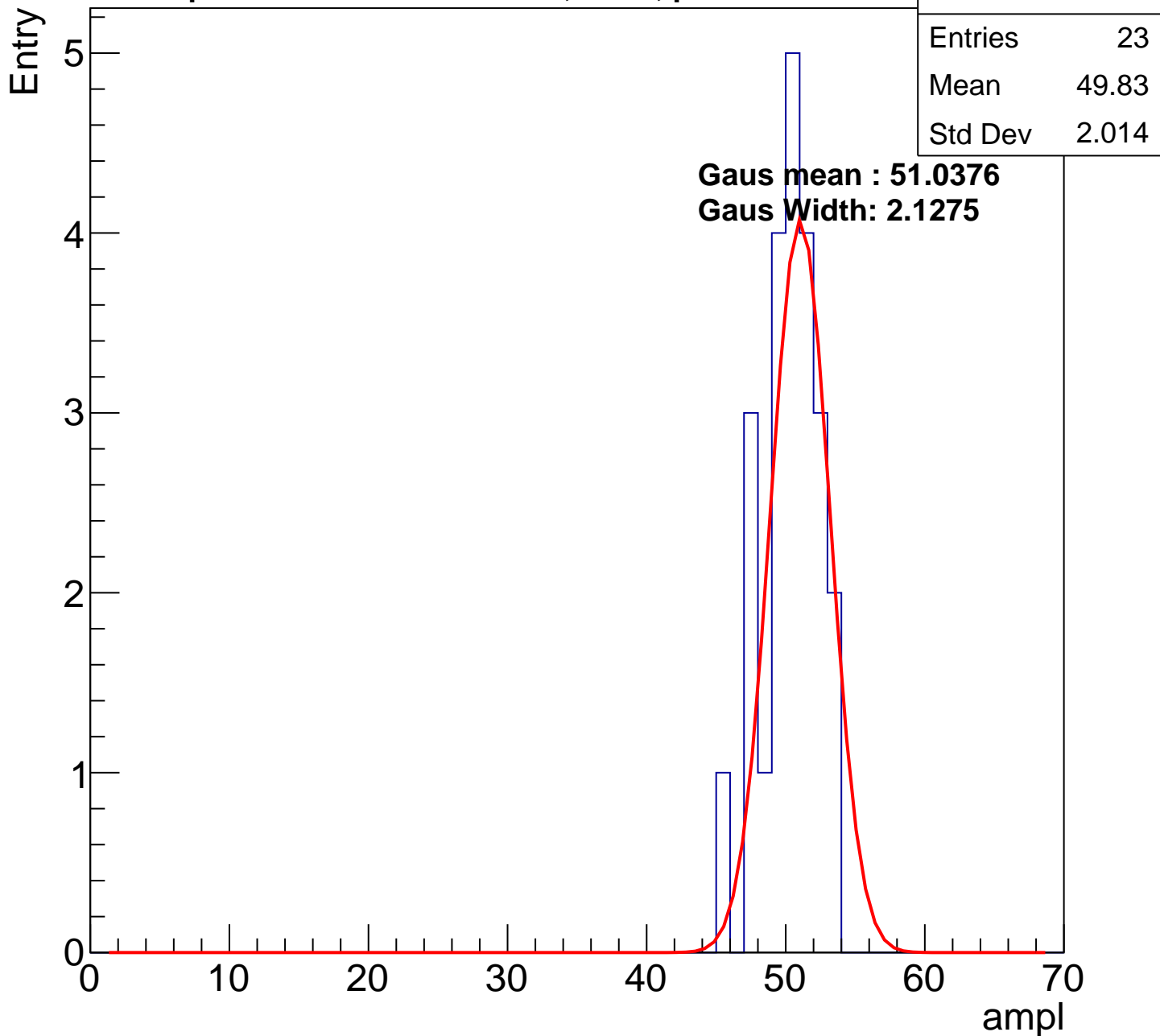
B0L100S, U5-ch85, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	51.5
Std Dev	2.141
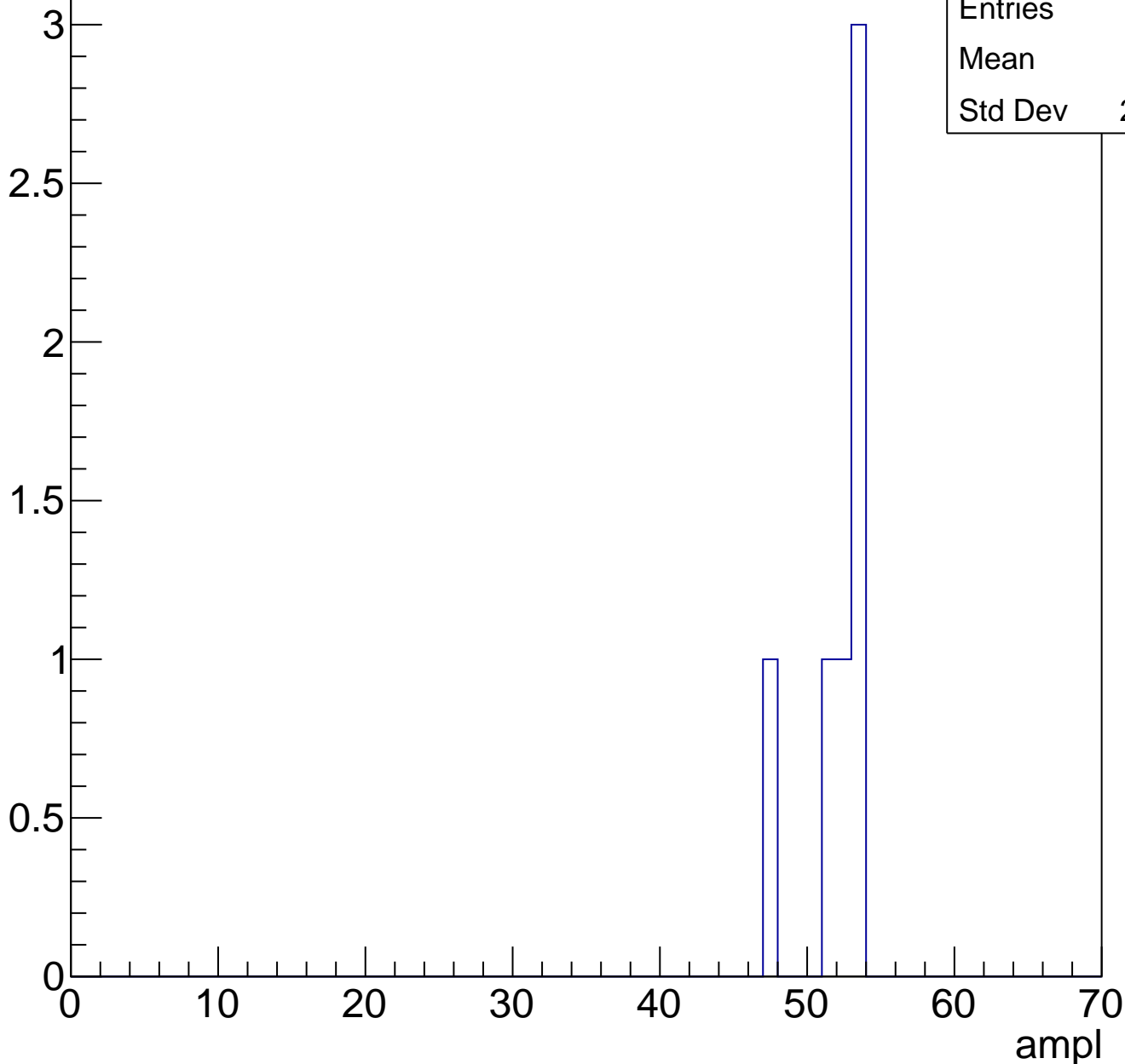
B0L100S, U5-ch85, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch85, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch85, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch85, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch86, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

ampl

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch86, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	47
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
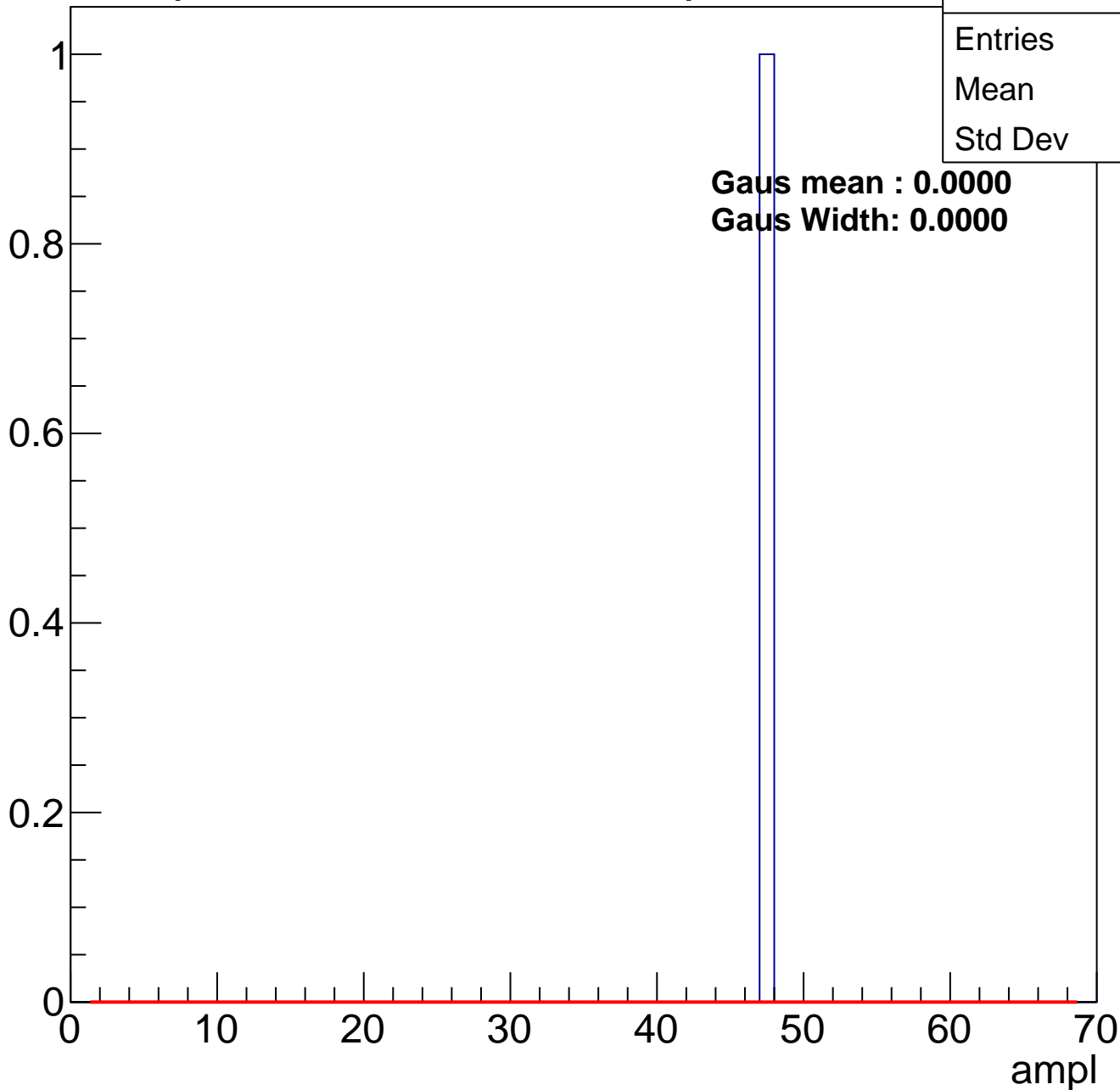
Entry

ampl

B0L100S, U5-ch86, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	50.75
Std Dev	1.785

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
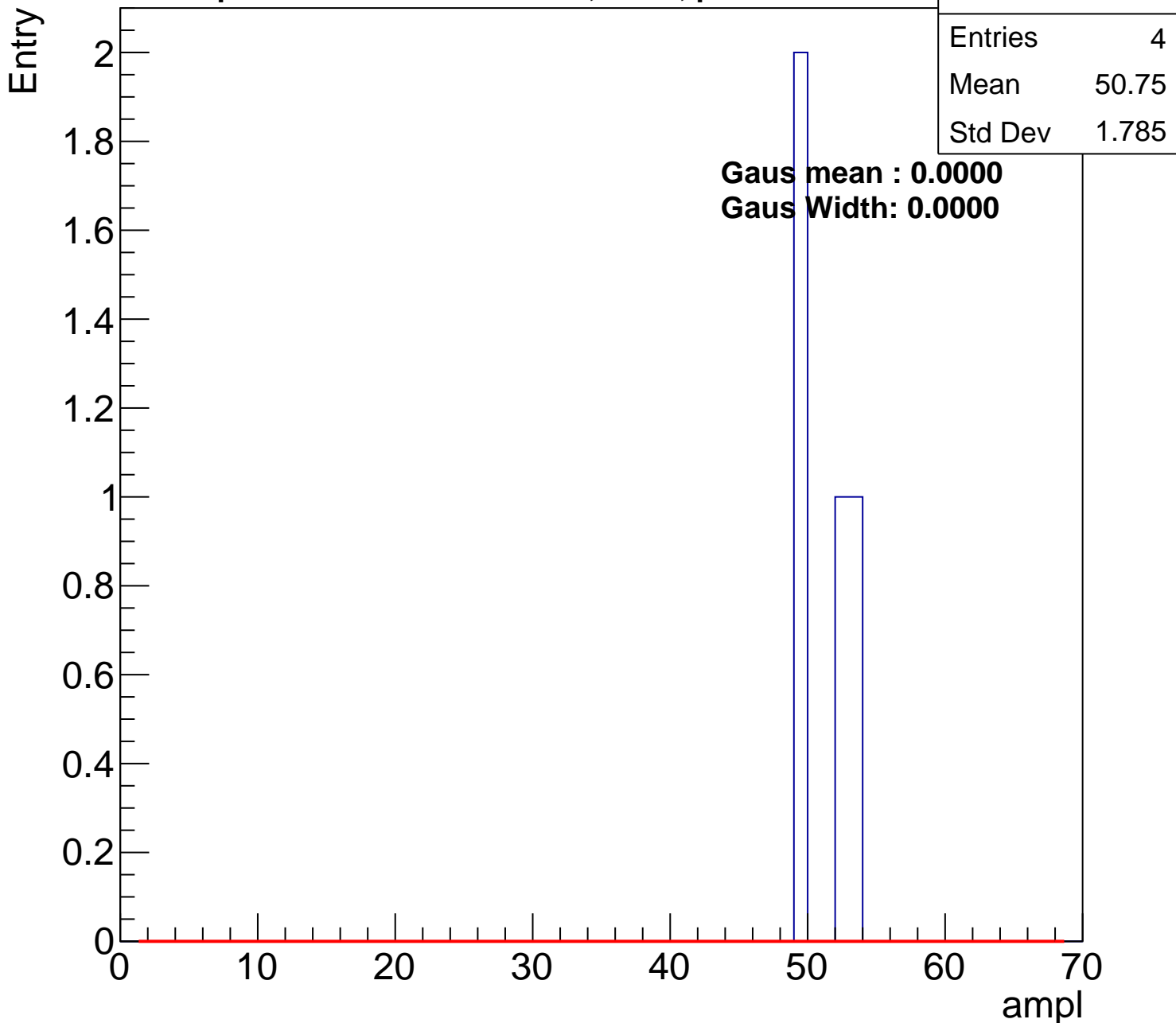
B0L100S, U5-ch86, adc3

calib_packv5_042523_0143.root, FC#6, port A1
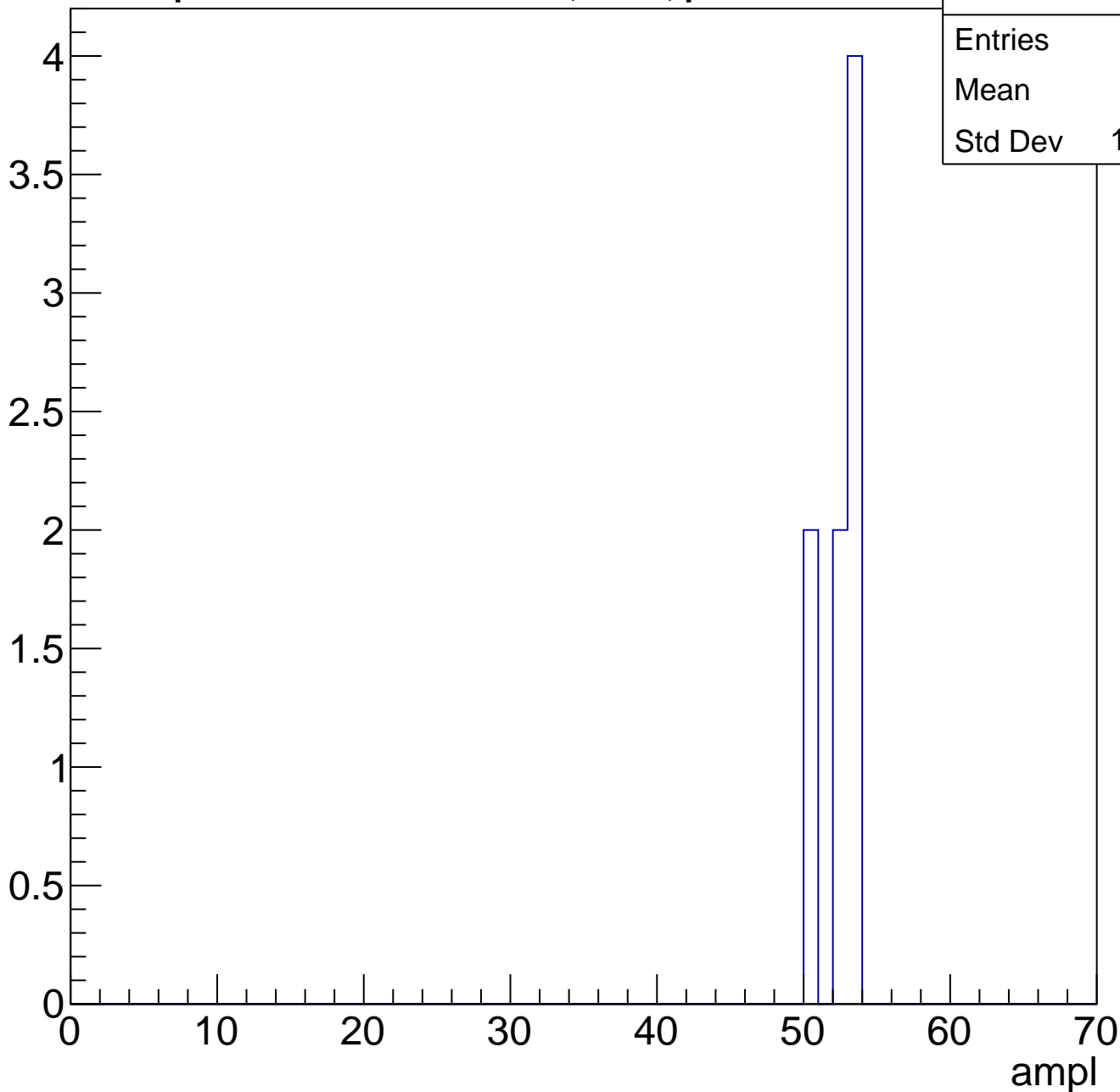
Entries	8
Mean	52
Std Dev	1.225

Entry

ampl

B0L100S, U5-ch86, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch86, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	23
Std Dev	0

Entry

ampl
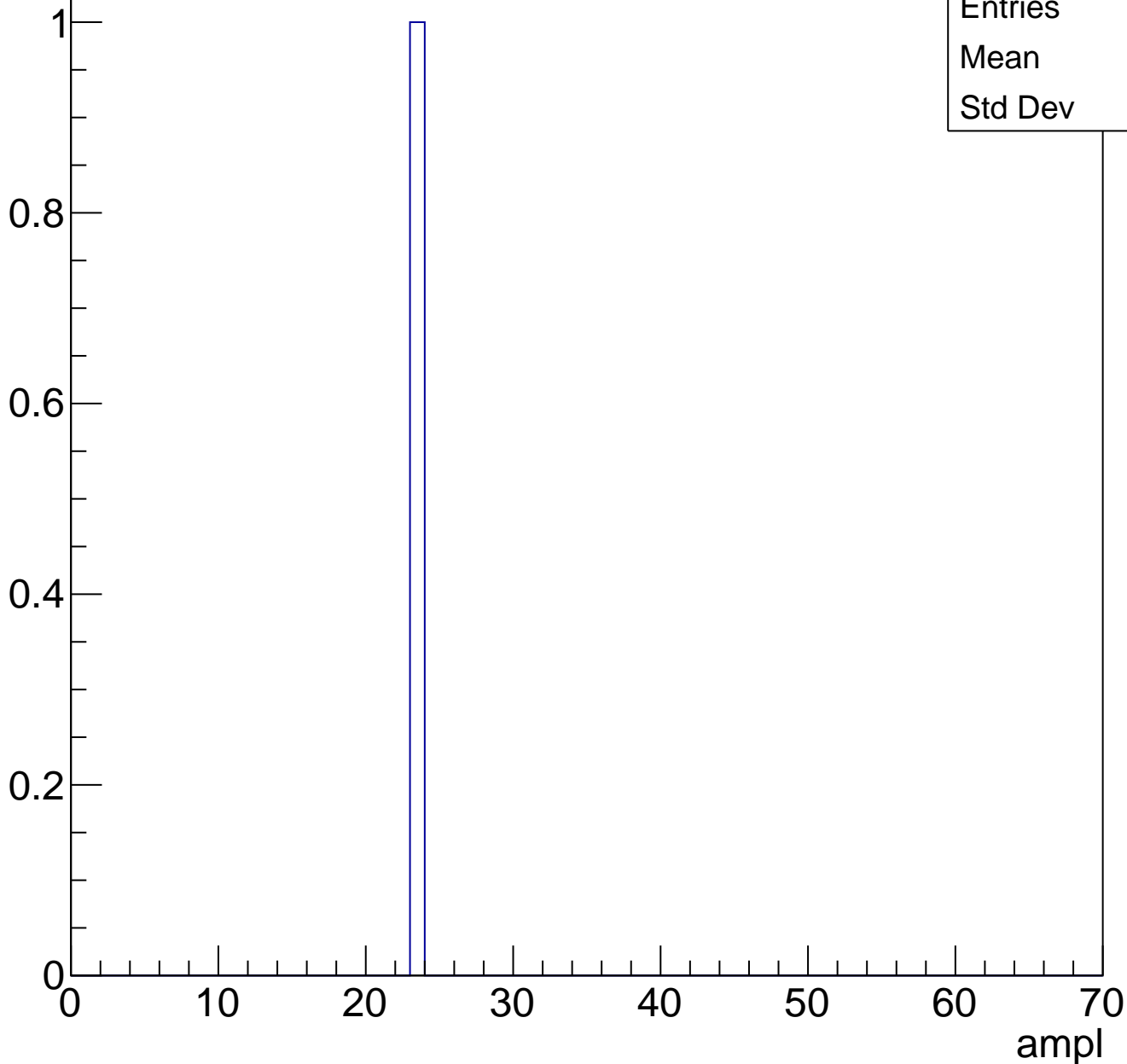
B0L100S, U5-ch86, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch86, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch87, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch87, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch87, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch87, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch87, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch87, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch87, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch87, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch88, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	35.67
Std Dev	5.185

Gaus mean : 0.0000
Gaus Width: 0.0000
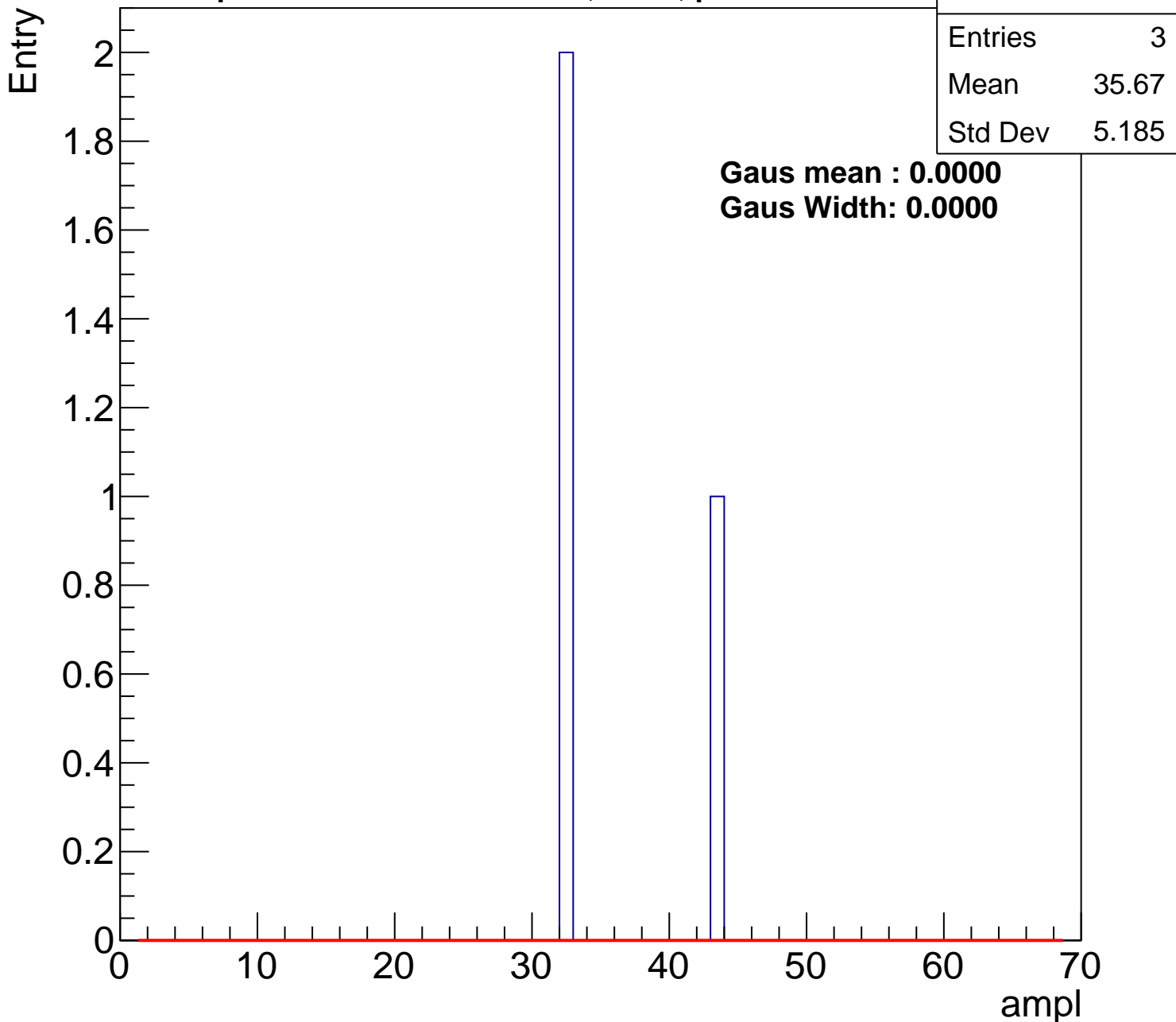
Entry

ampl
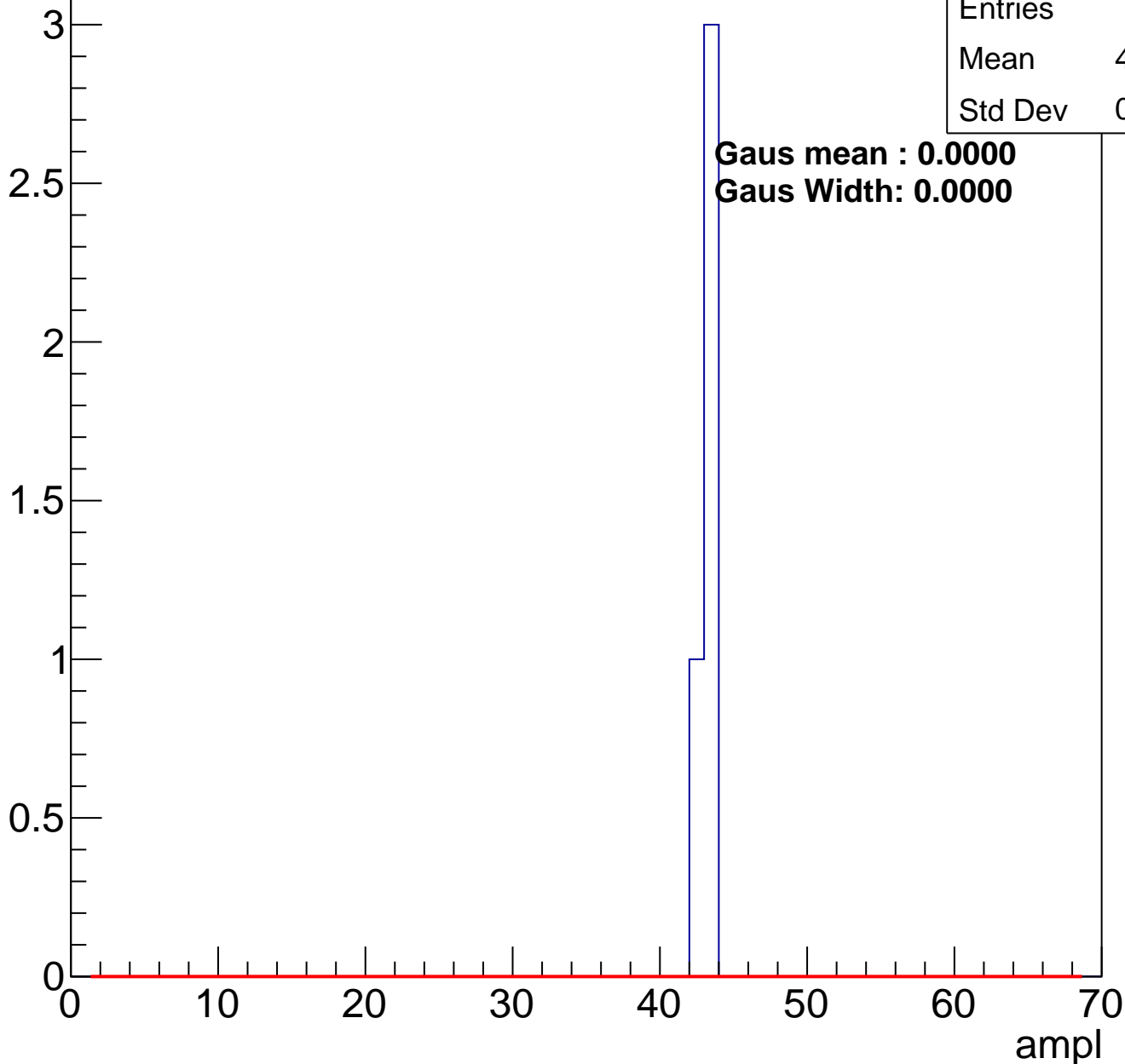
B0L100S, U5-ch88, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	50.86
Std Dev	0.6389

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
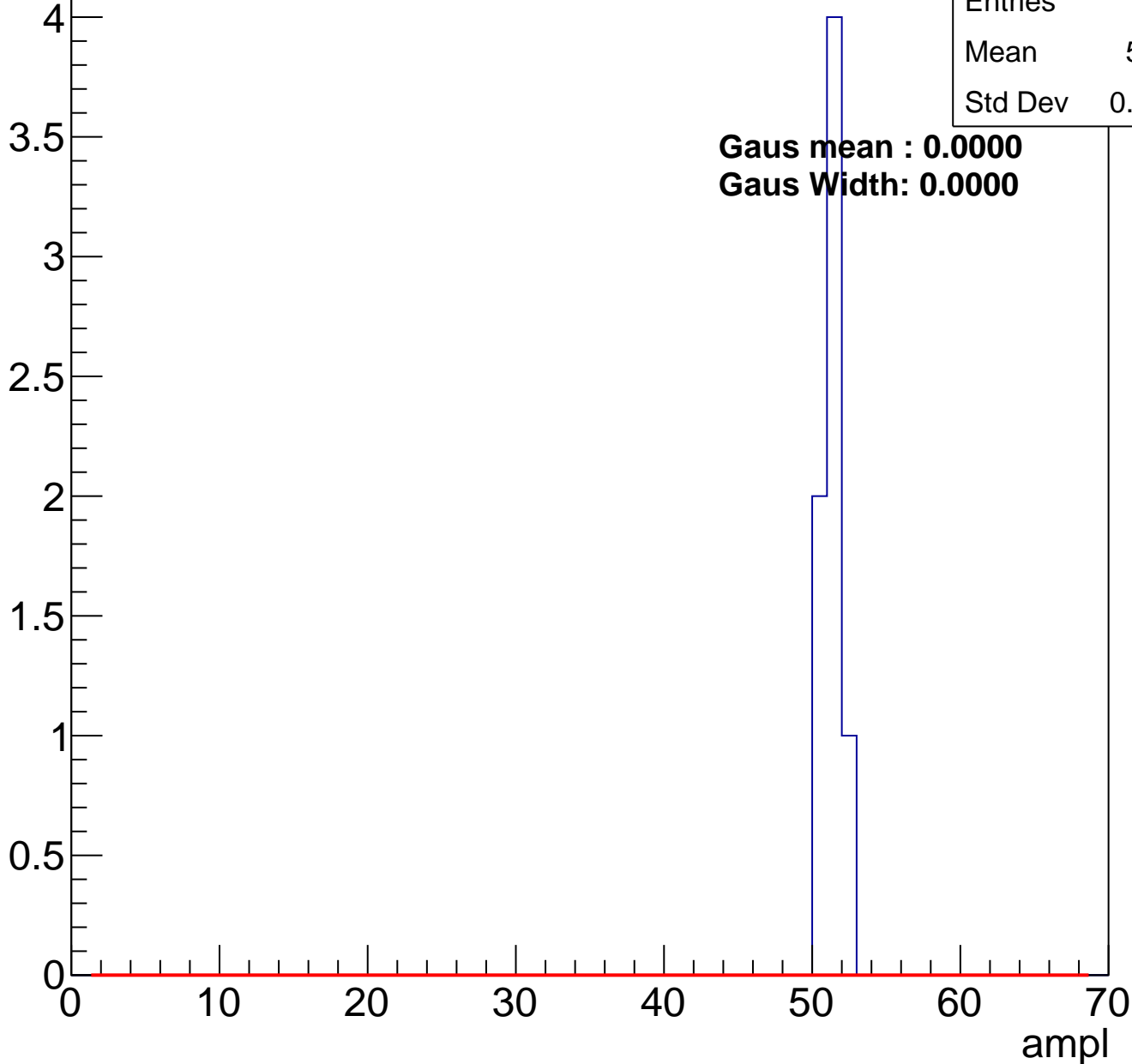
B0L100S, U5-ch88, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	51.4
Std Dev	0.8

ampl
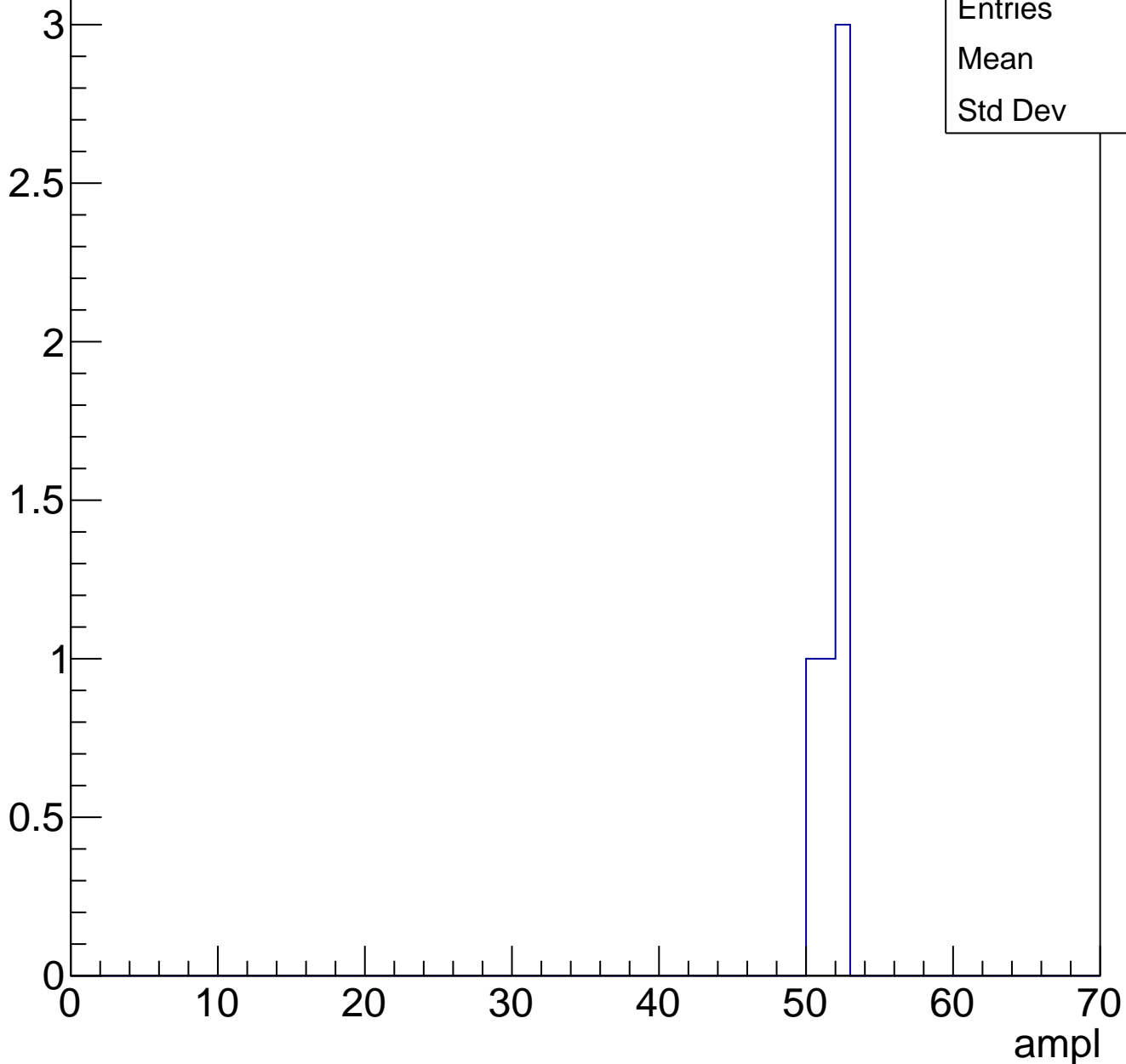
B0L100S, U5-ch88, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	30
Std Dev	0
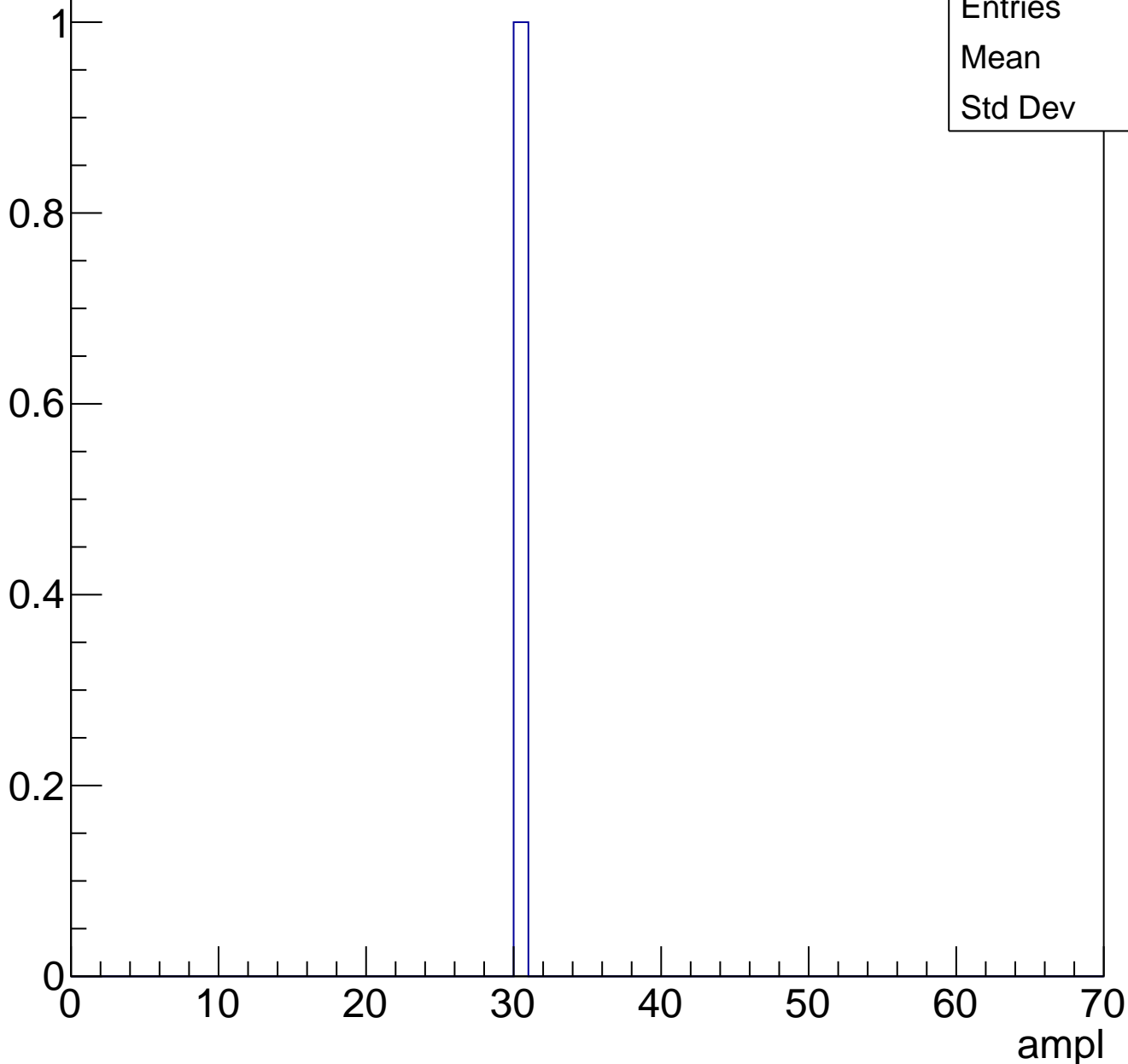
B0L100S, U5-ch88, adc5

calib_packv5_042523_0143.root, FC#6, port A1
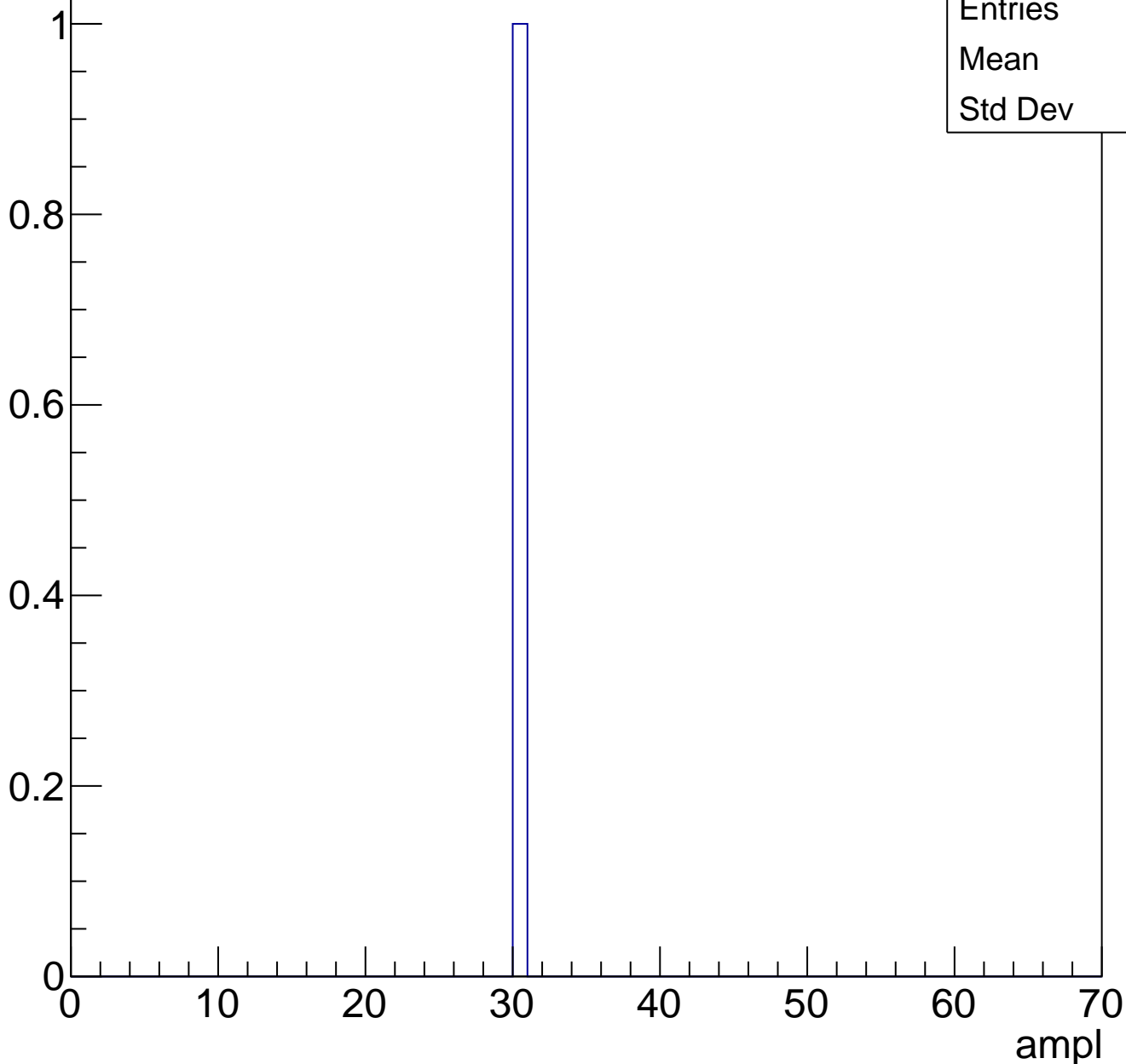
Entries	1
Mean	30
Std Dev	0

B0L100S, U5-ch88, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch88, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch89, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch89, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch89, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	44
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch89, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch89, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch89, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch89, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch89, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch90, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch90, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	13
Mean	45.69
Std Dev	2.197

Gaus mean : 46.0959
Gaus Width: 3.4503
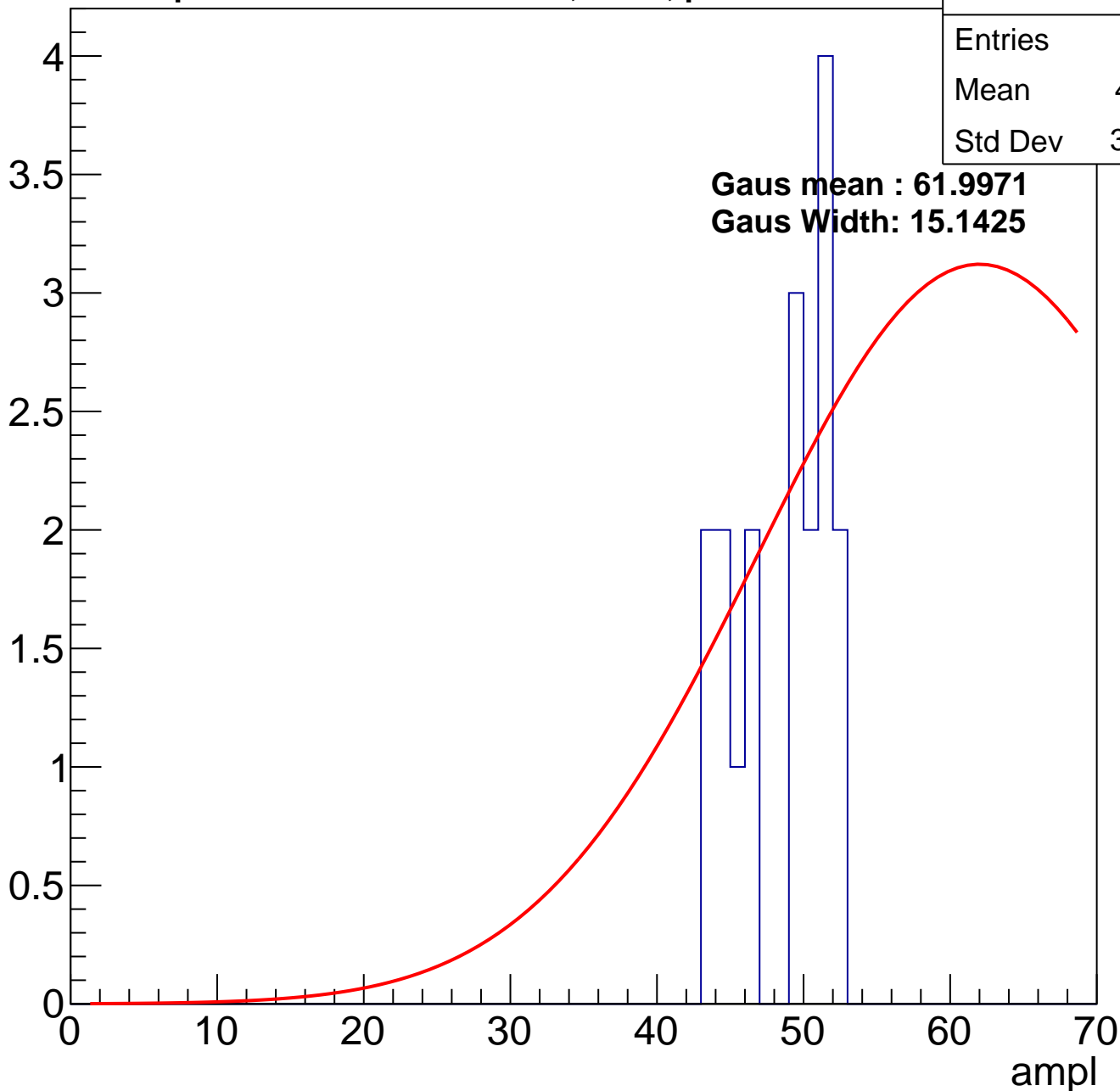
B0L100S, U5-ch90, adc3

calib_packv5_042523_0143.root, FC#6, port A1
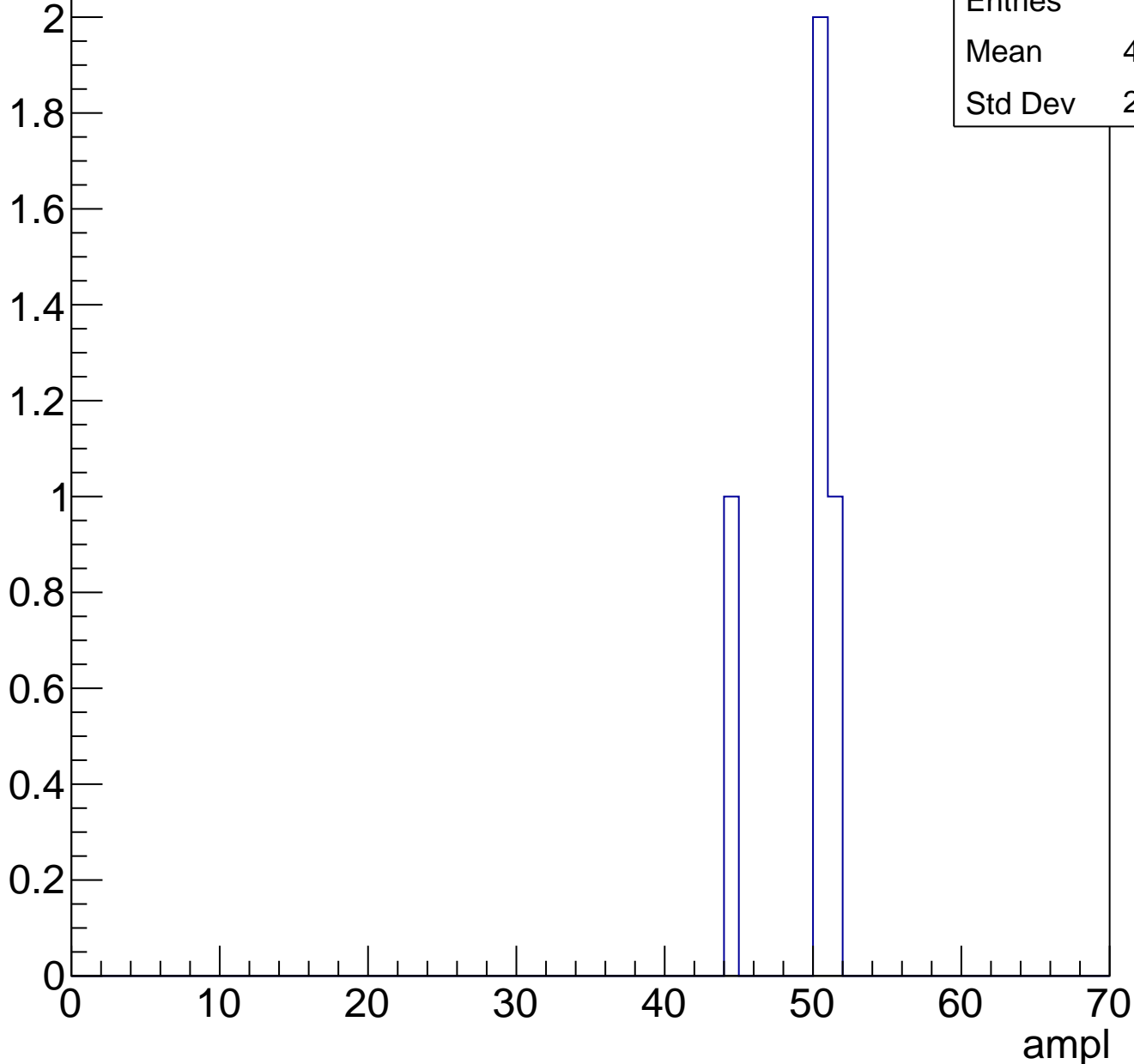
Entries	4
Mean	48.75
Std Dev	2.773

B0L100S, U5-ch90, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch90, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch90, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch90, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch91, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch91, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	47.5
Std Dev	1.5

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch91, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	11
Mean	51.73
Std Dev	2.049

Gaus mean : 51.1920
Gaus Width: 4.3434
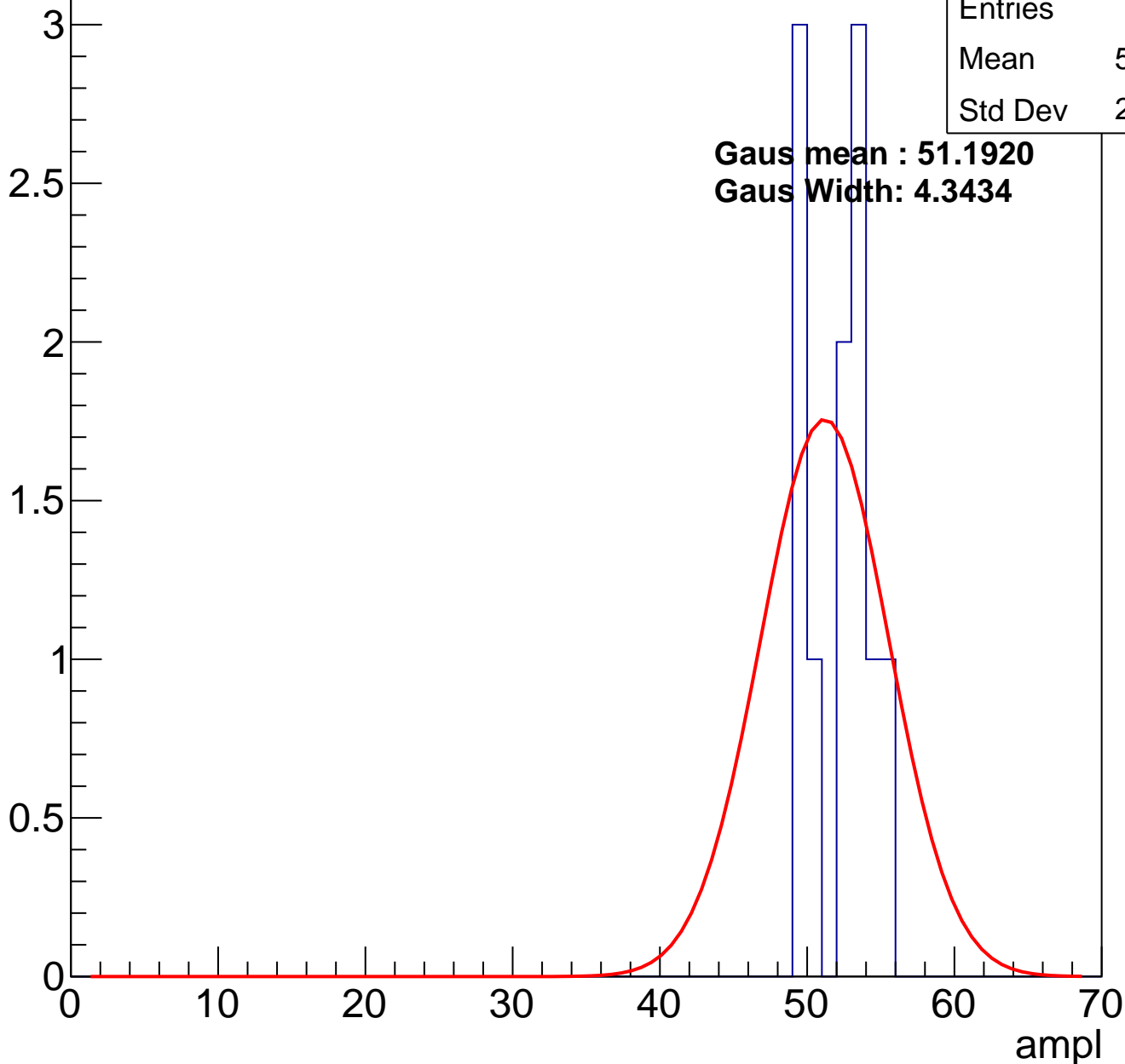
B0L100S, U5-ch91, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	53.78
Std Dev	0.9162

ampl
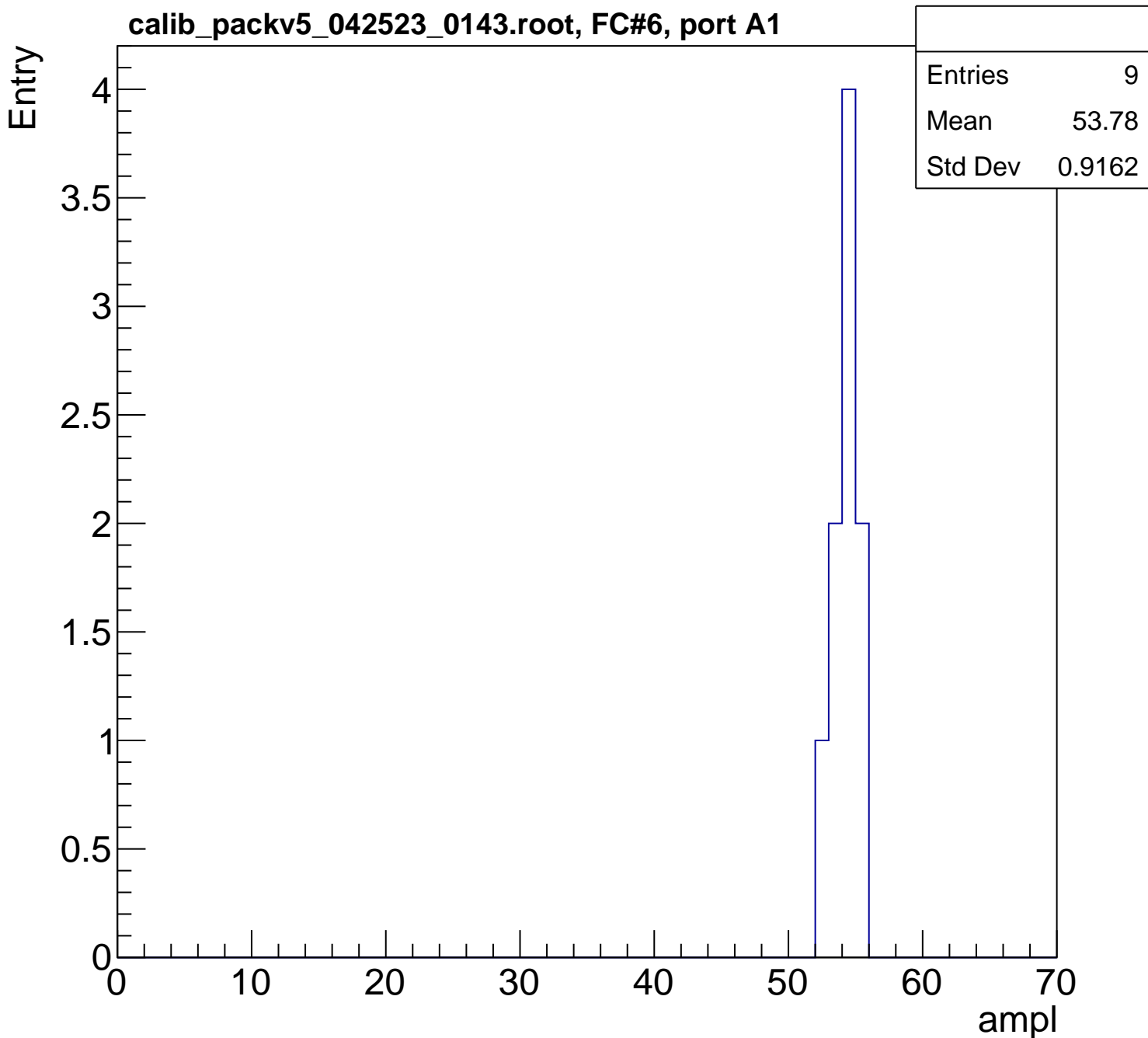
Entry

B0L100S, U5-ch91, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	55
Std Dev	0

Entry

ampl
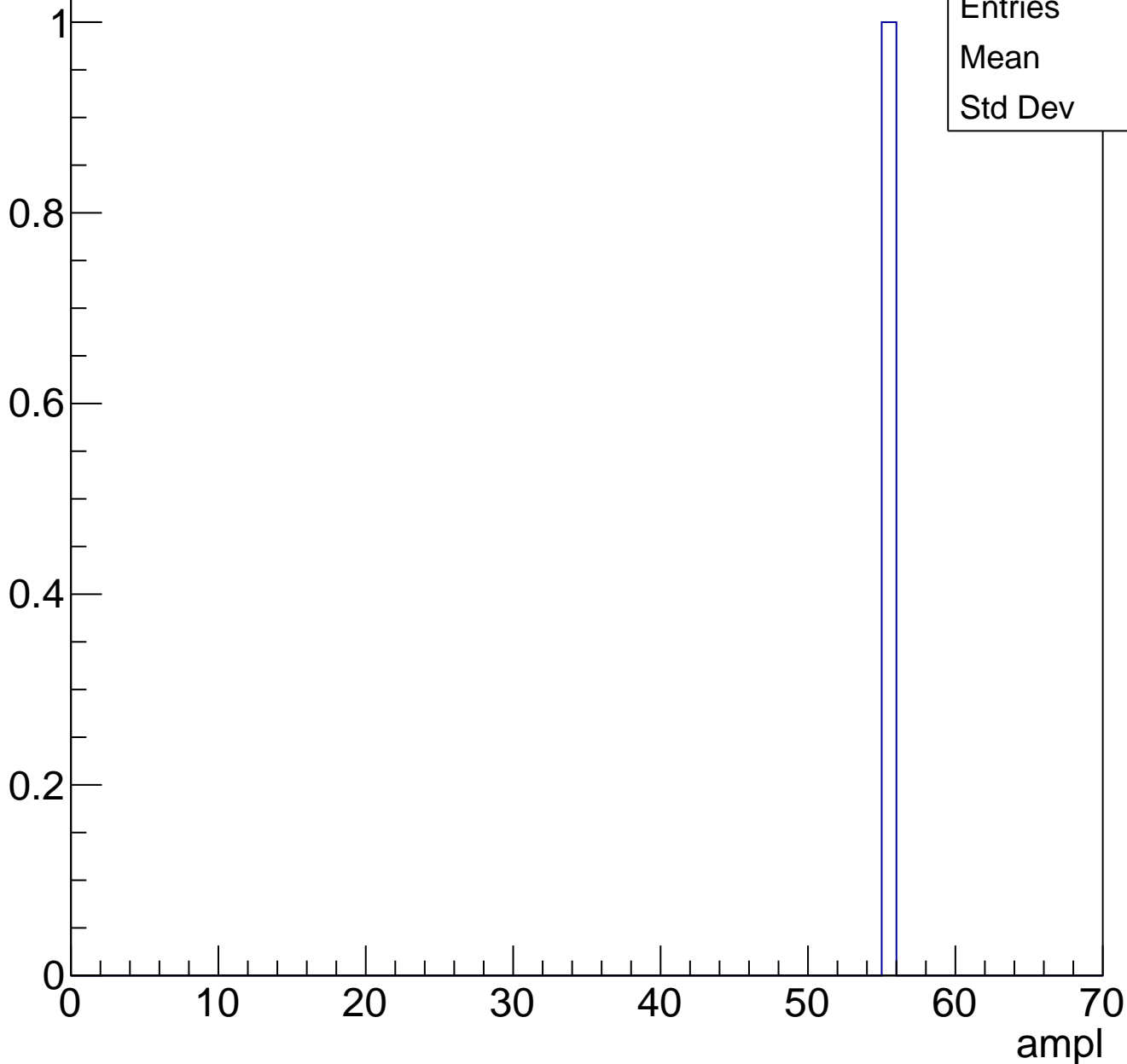
B0L100S, U5-ch91, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch91, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch91, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch92, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch92, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
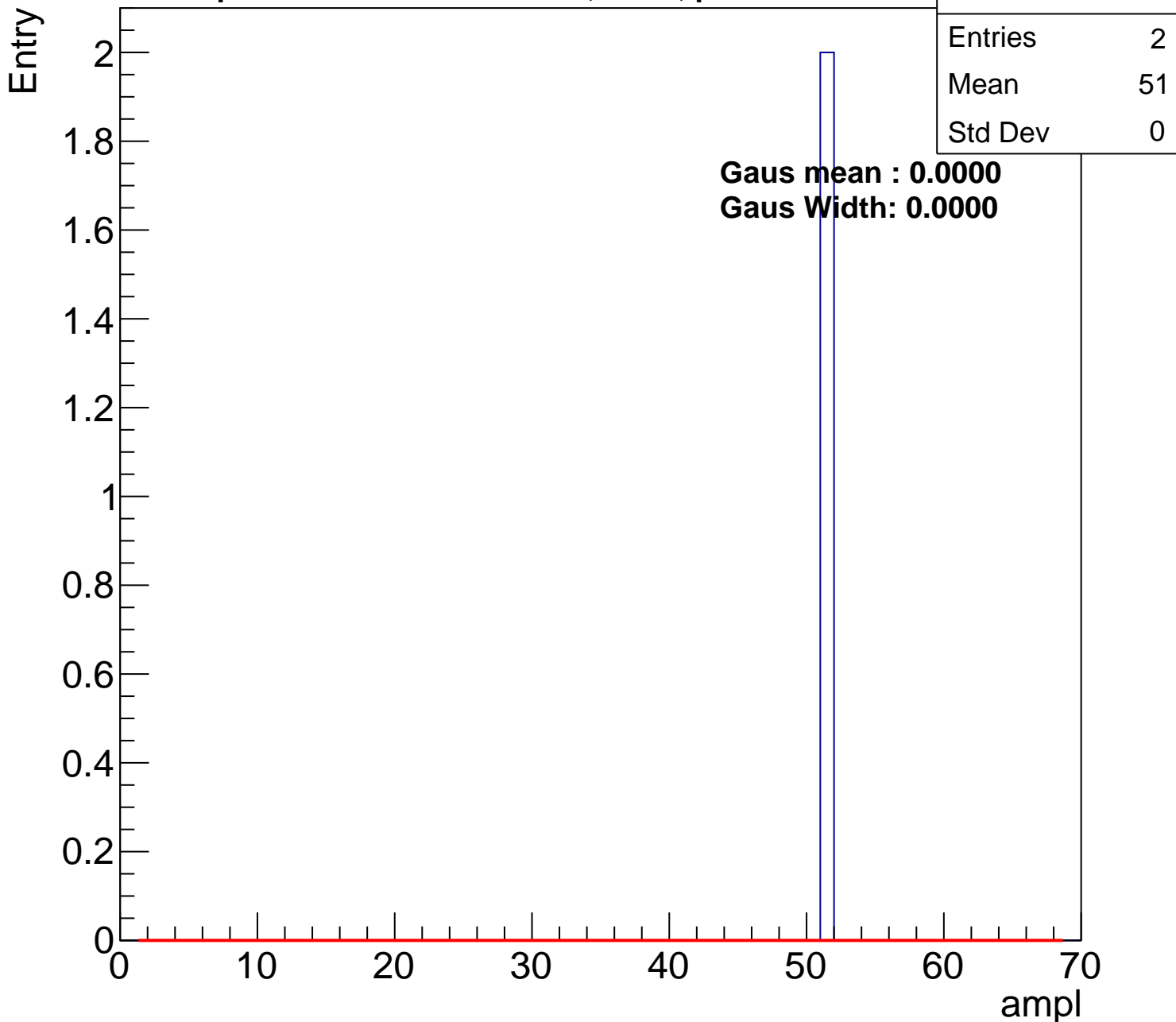
B0L100S, U5-ch92, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	9
Mean	50.22
Std Dev	0.6285

Gaus mean : 0.0000
Gaus Width: 0.0000
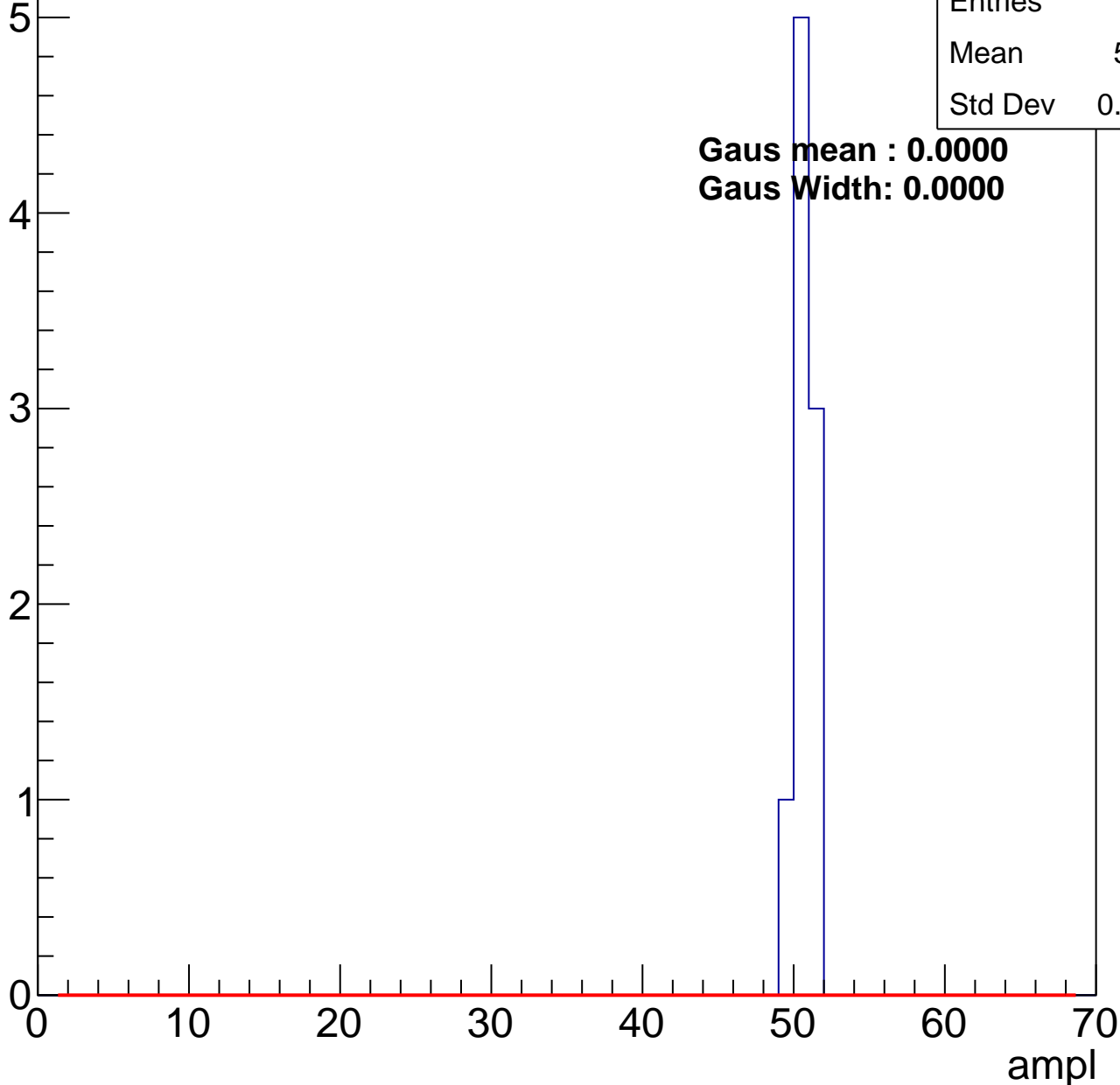
B0L100S, U5-ch92, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

ampl
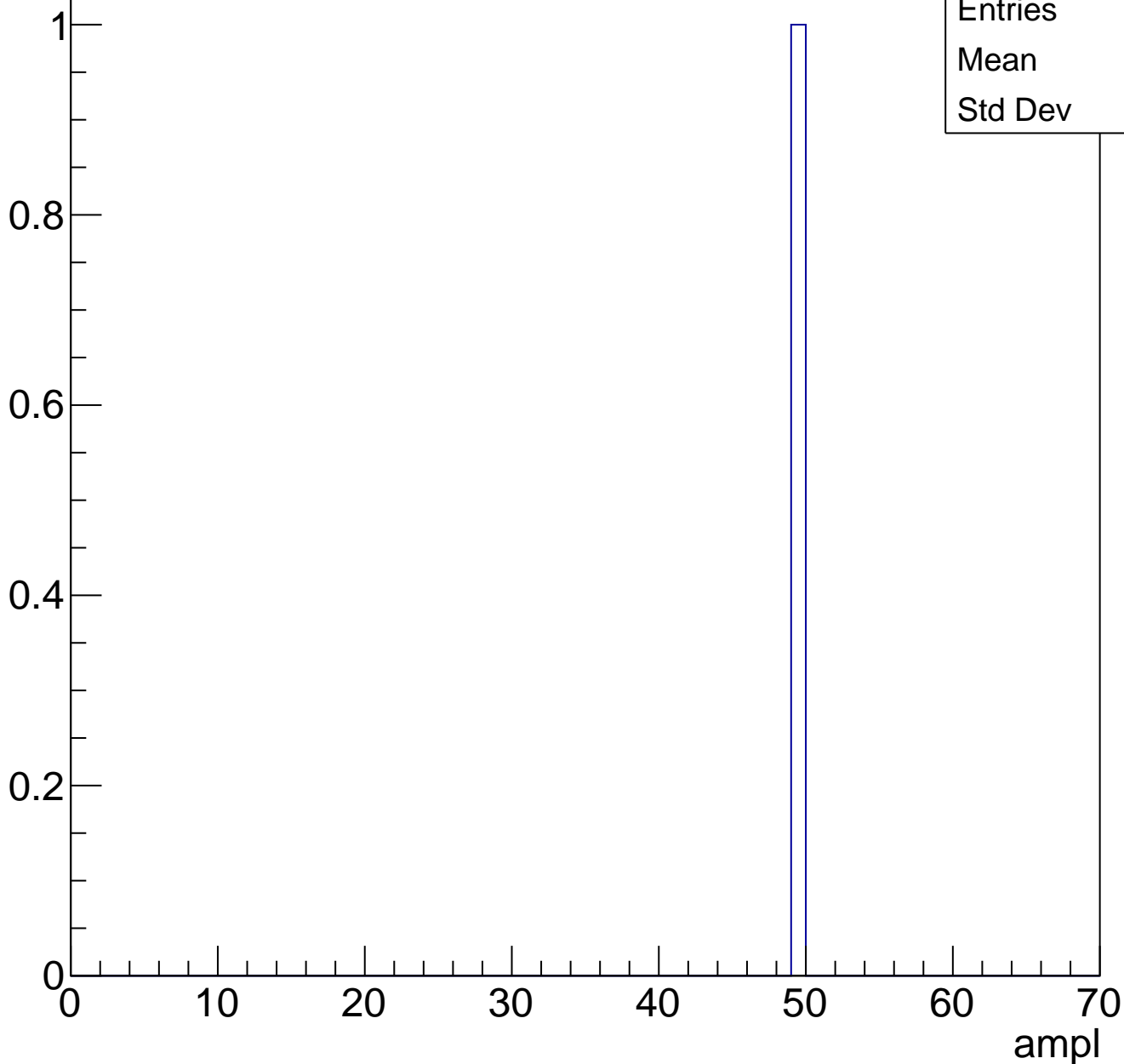
Entry

B0L100S, U5-ch92, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	54
Std Dev	0

ampl
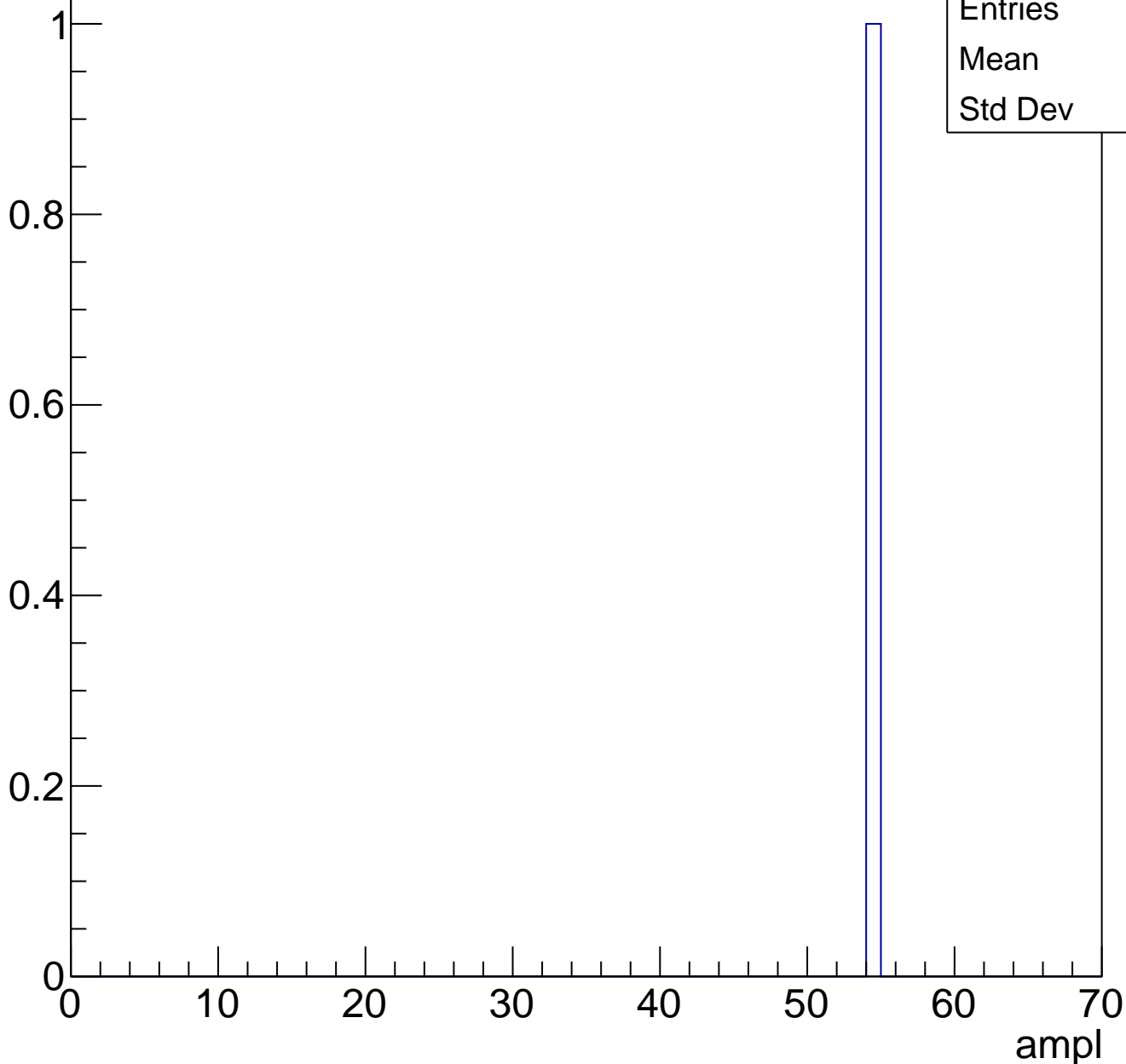
B0L100S, U5-ch92, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch92, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch92, adc7

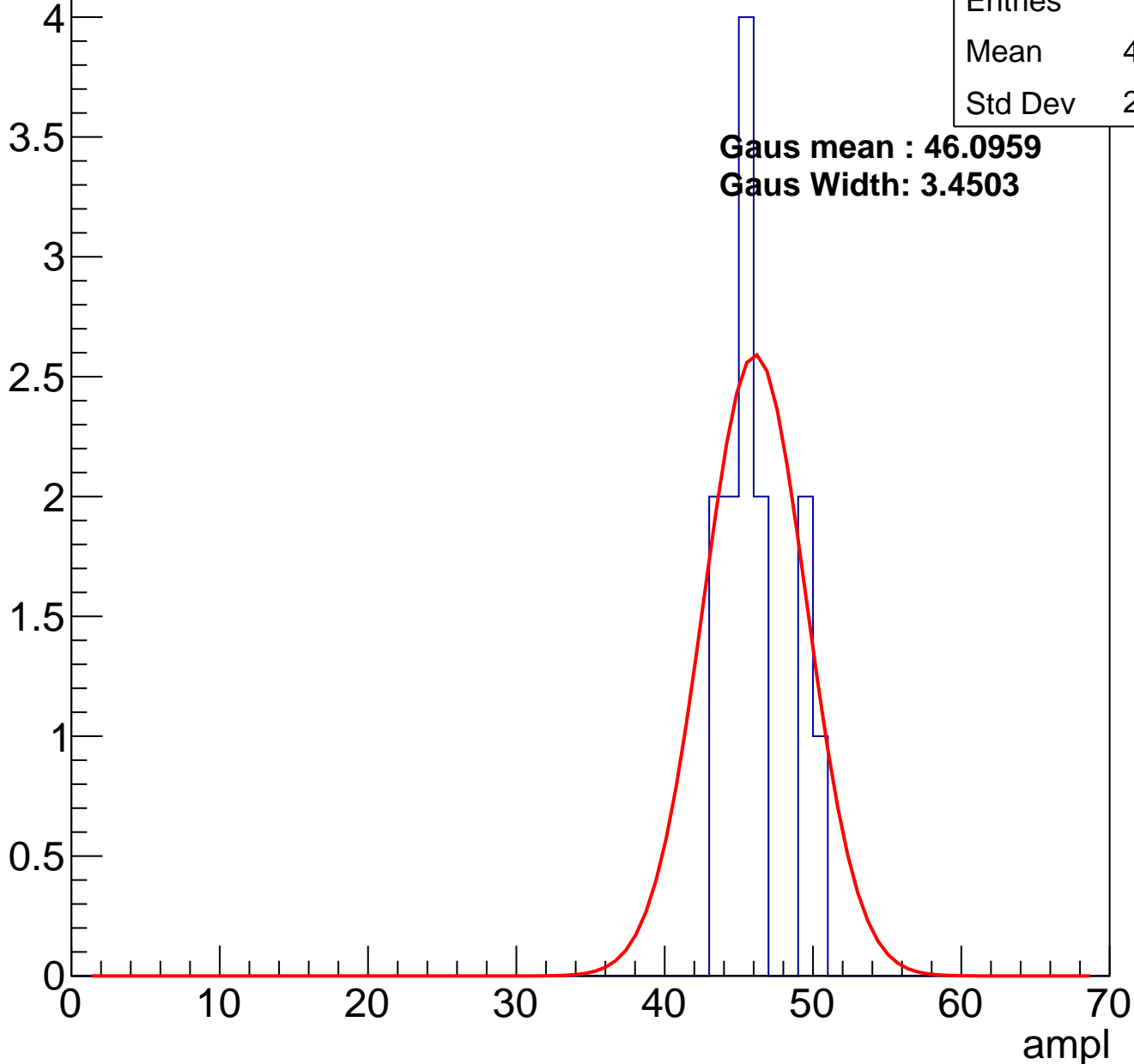**calib_packv5_042523_0143.root, FC#6, port A1**

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch93, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch93, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch93, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	49.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch93, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	52.5
Std Dev	2.5
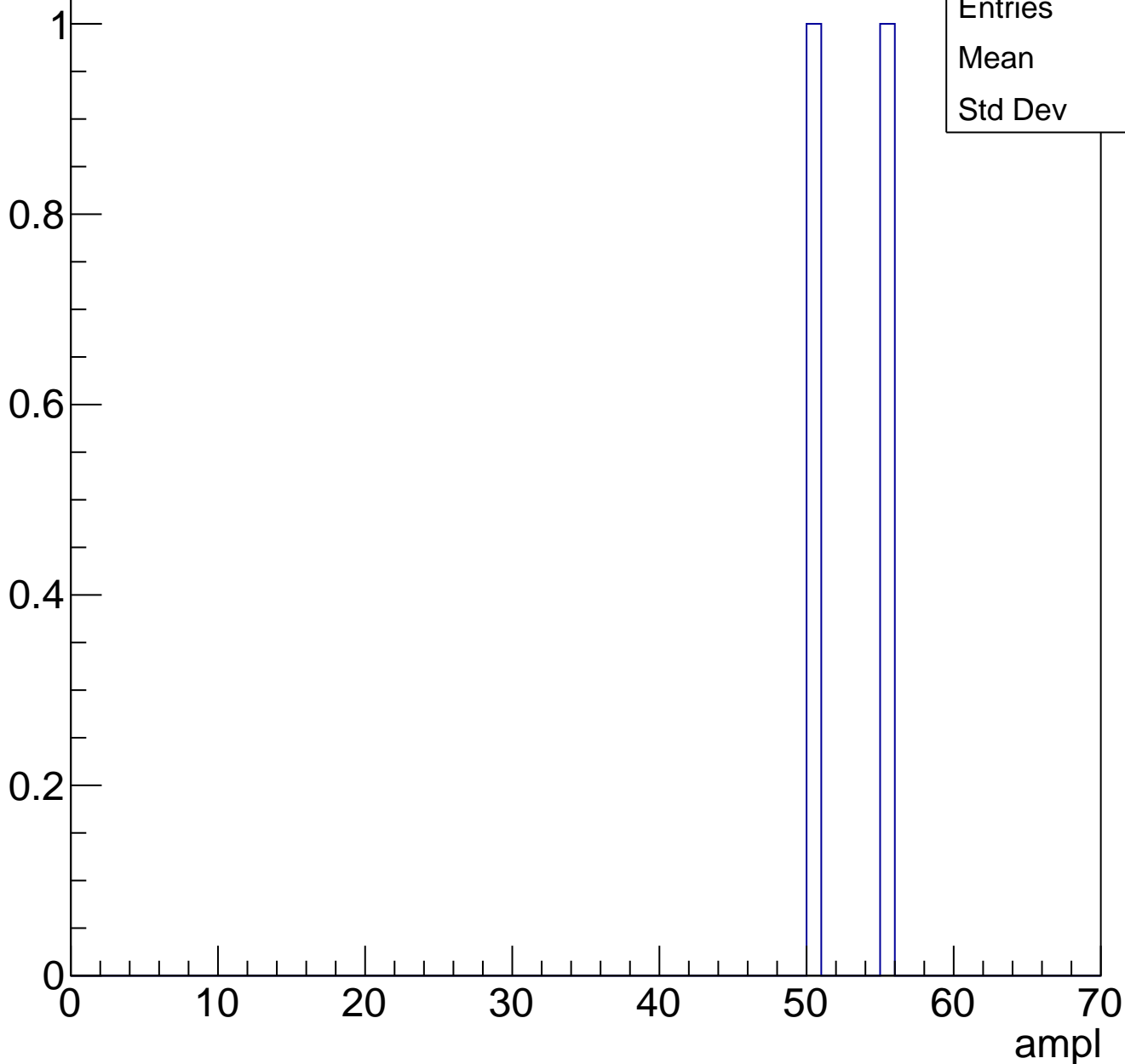
B0L100S, U5-ch93, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch93, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch93, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch93, adc7
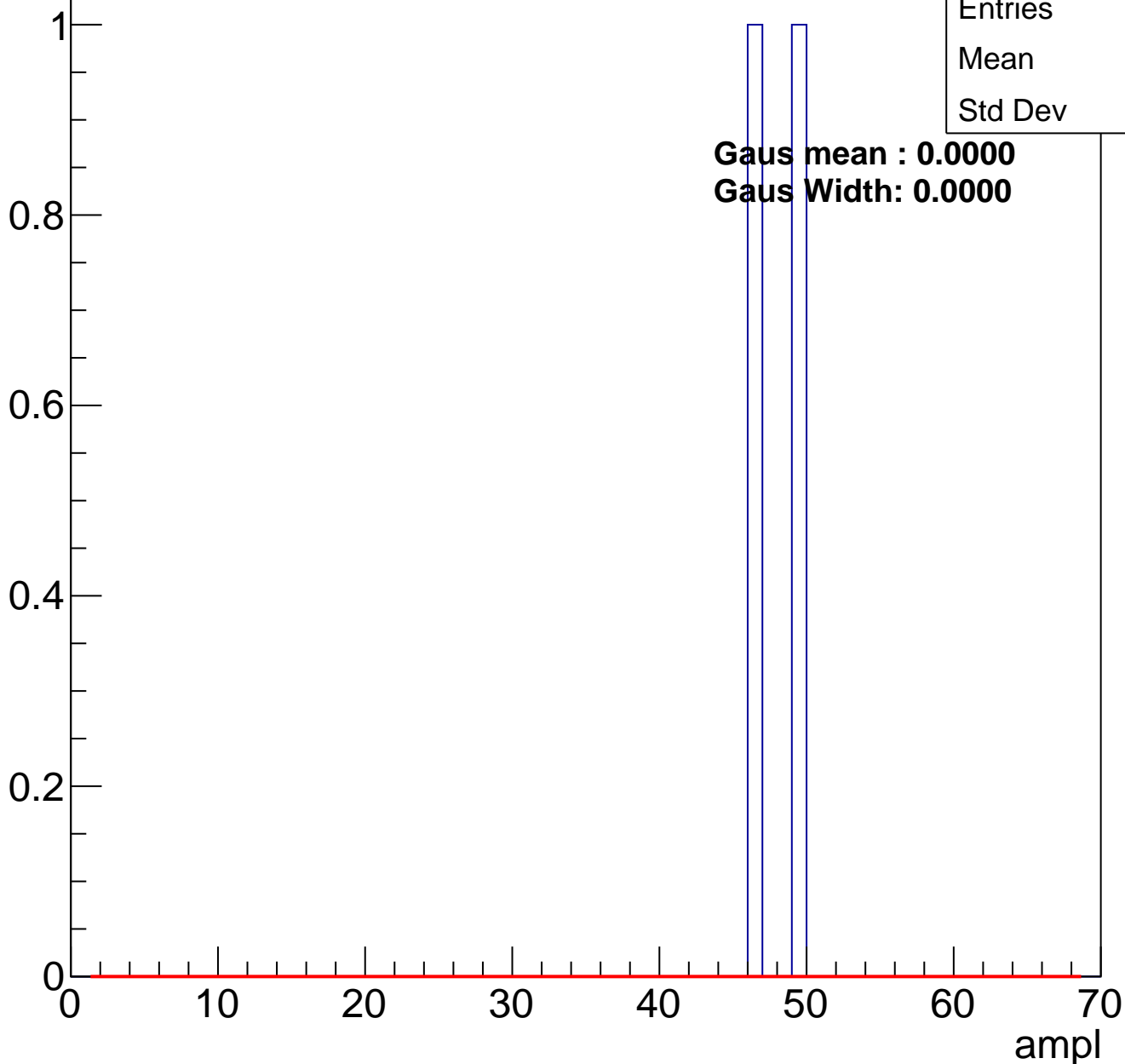
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch94, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	6
Mean	38
Std Dev	17.11

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
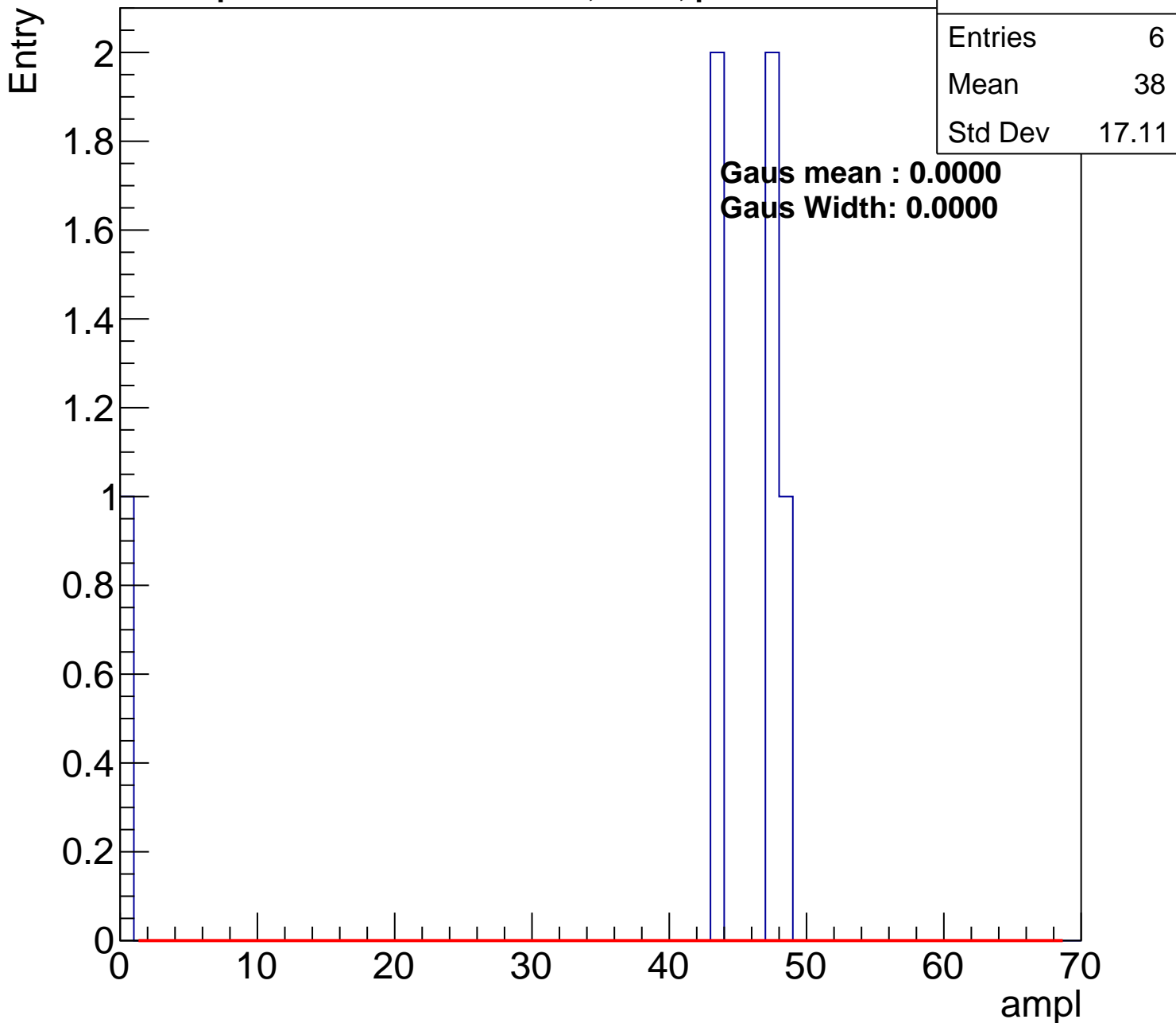
B0L100S, U5-ch94, adc2

calib_packv5_042523_0143.root, FC#6, port A1
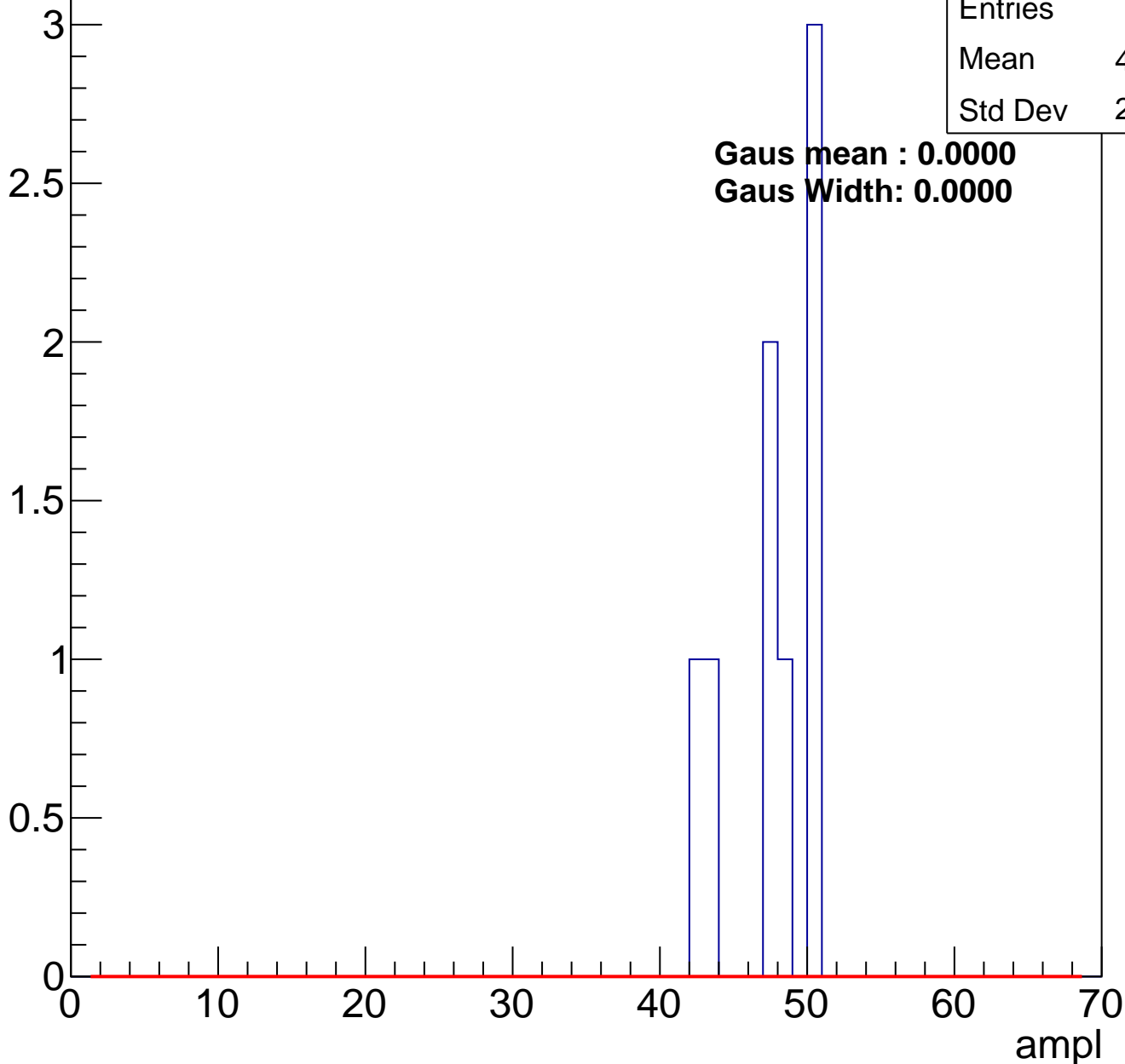
Entries	8
Mean	47.12
Std Dev	2.934

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch94, adc3

calib_packv5_042523_0143.root, FC#6, port A1
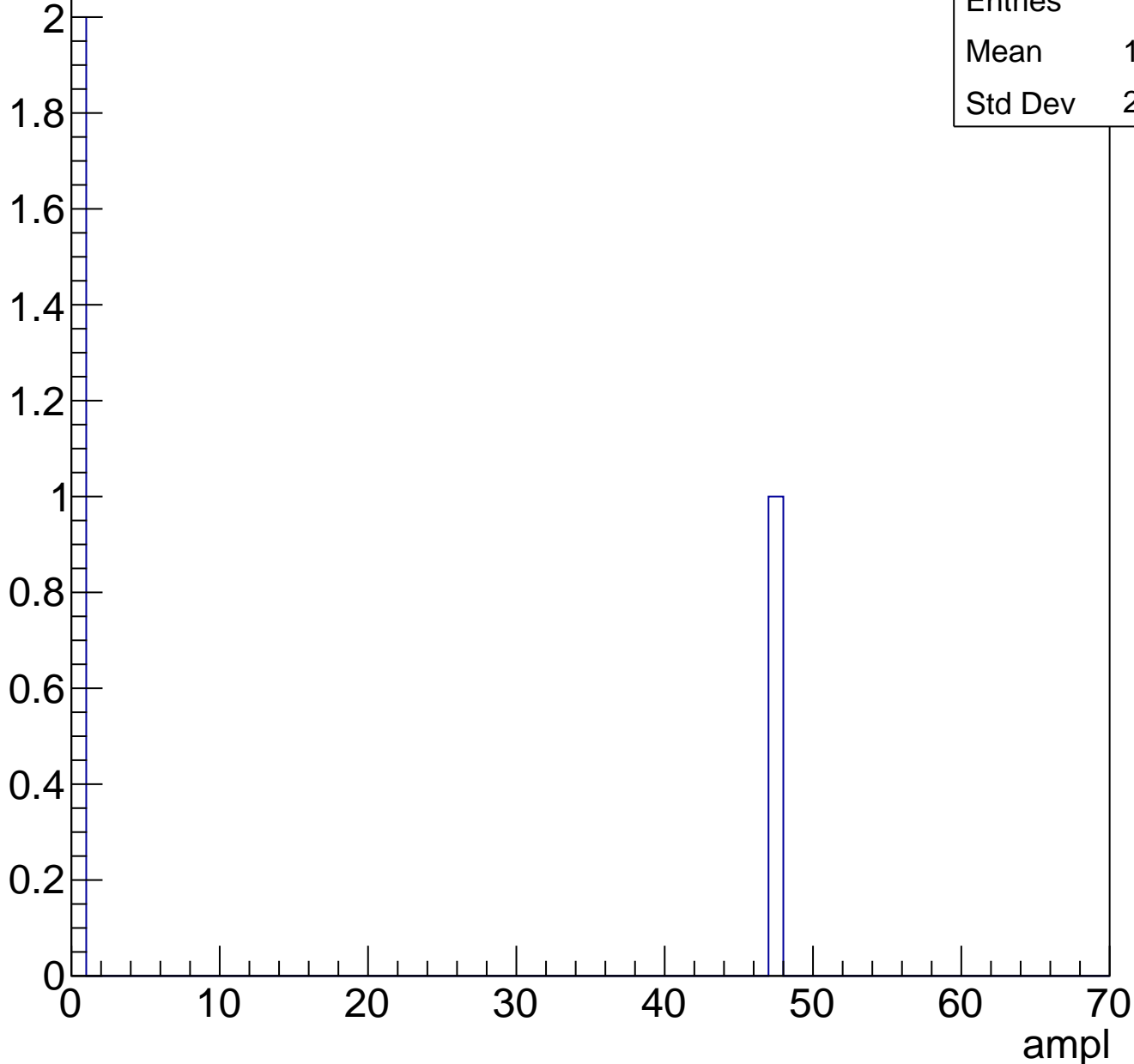
Entries	3
Mean	15.67
Std Dev	22.16

B0L100S, U5-ch94, adc4

calib_packv5_042523_0143.root, FC#6, port A1
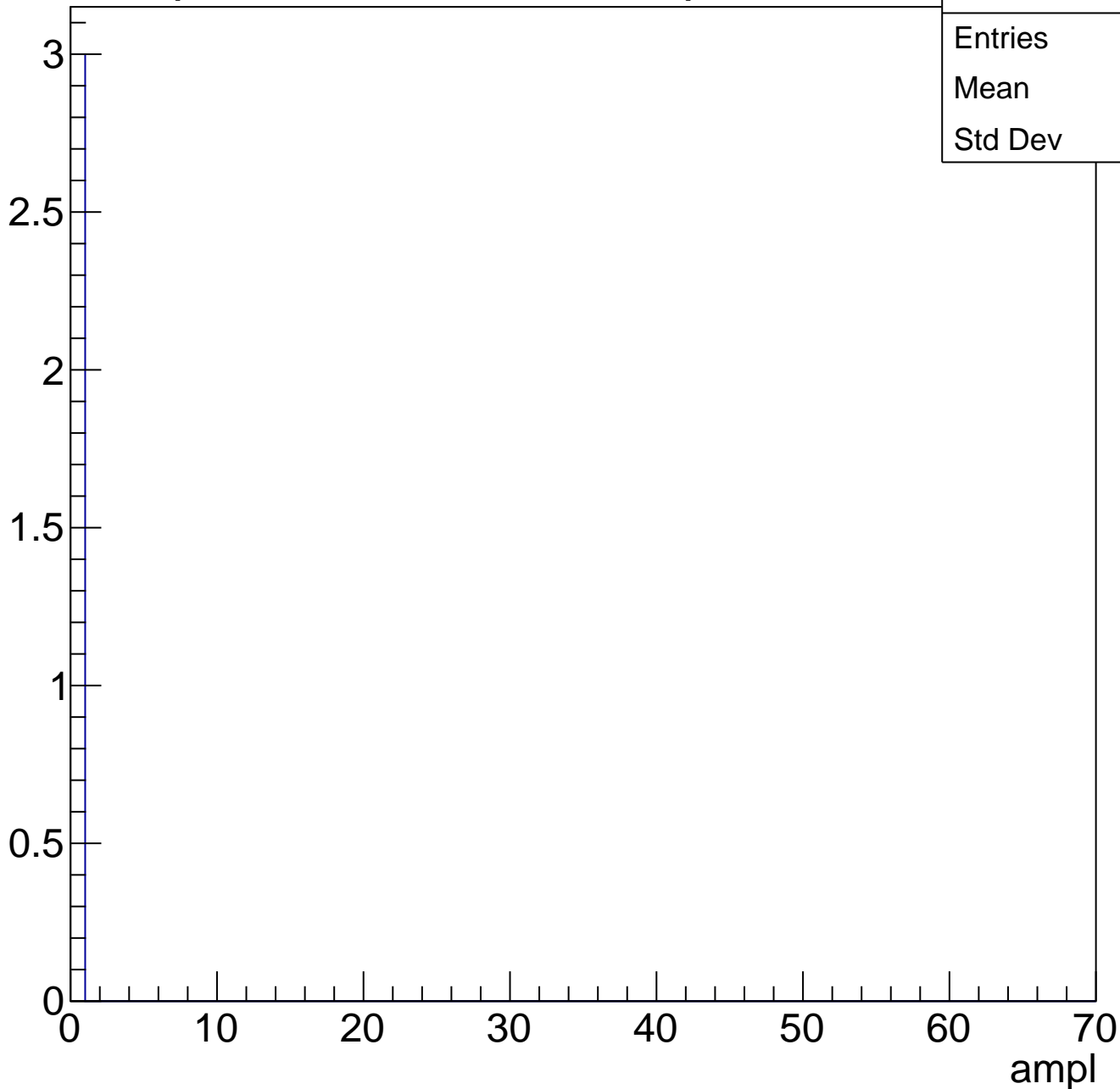
Entries	3
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch94, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Entry

ampl
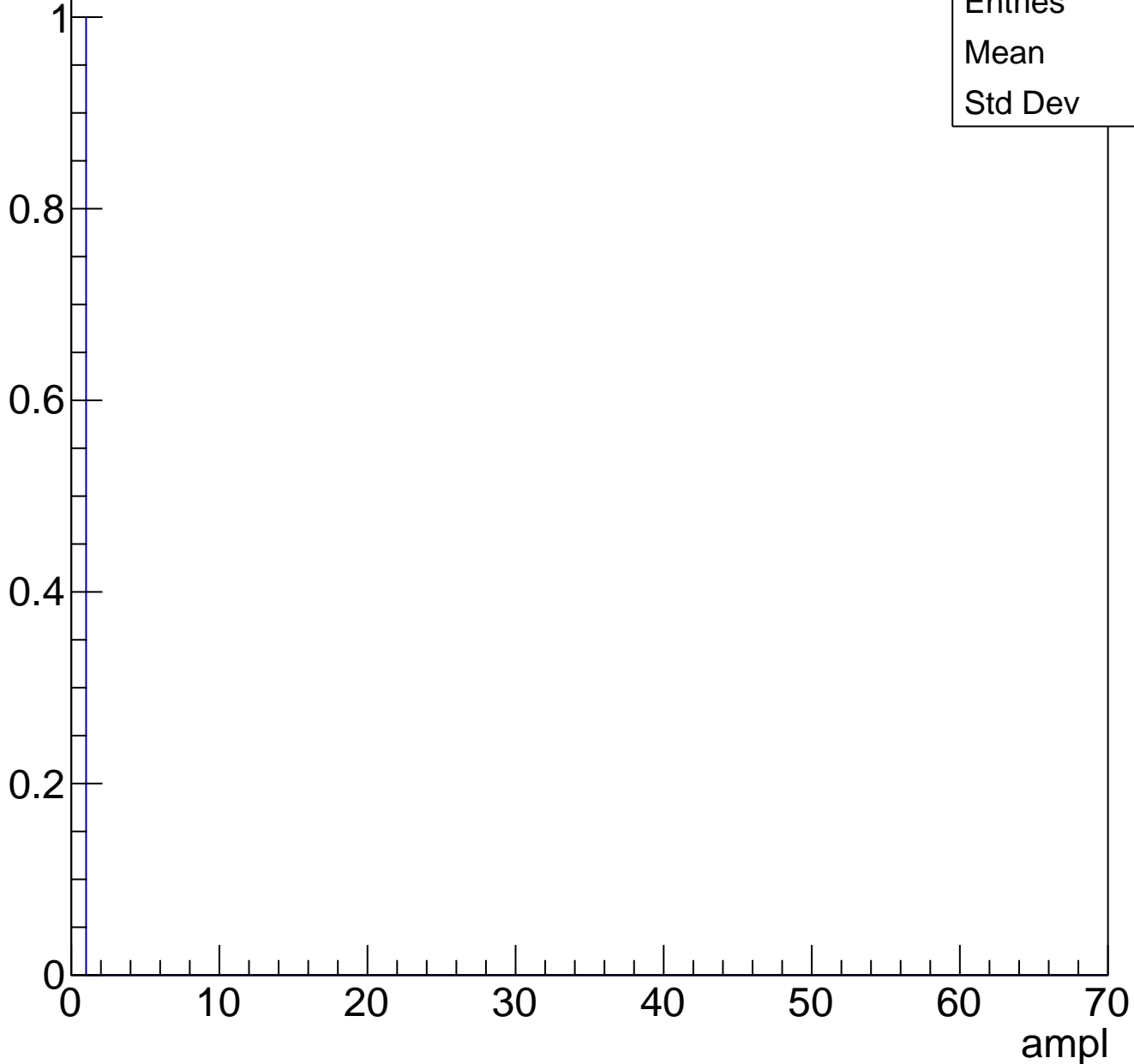
B0L100S, U5-ch94, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch94, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	0
Std Dev	0

Entry

ampl
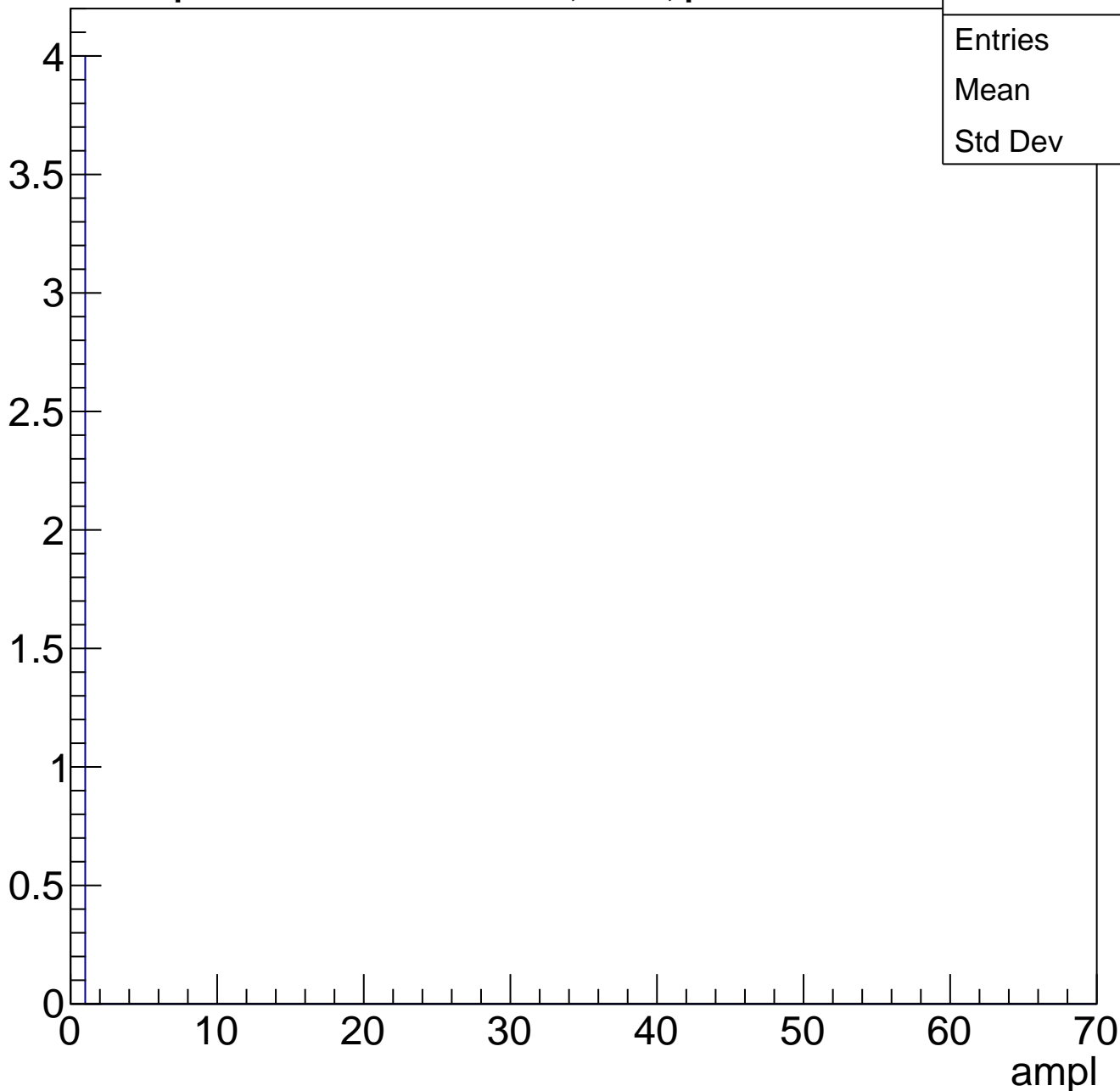
B0L100S, U5-ch95, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch95, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : -3.9748
Gaus Width: 13.3208
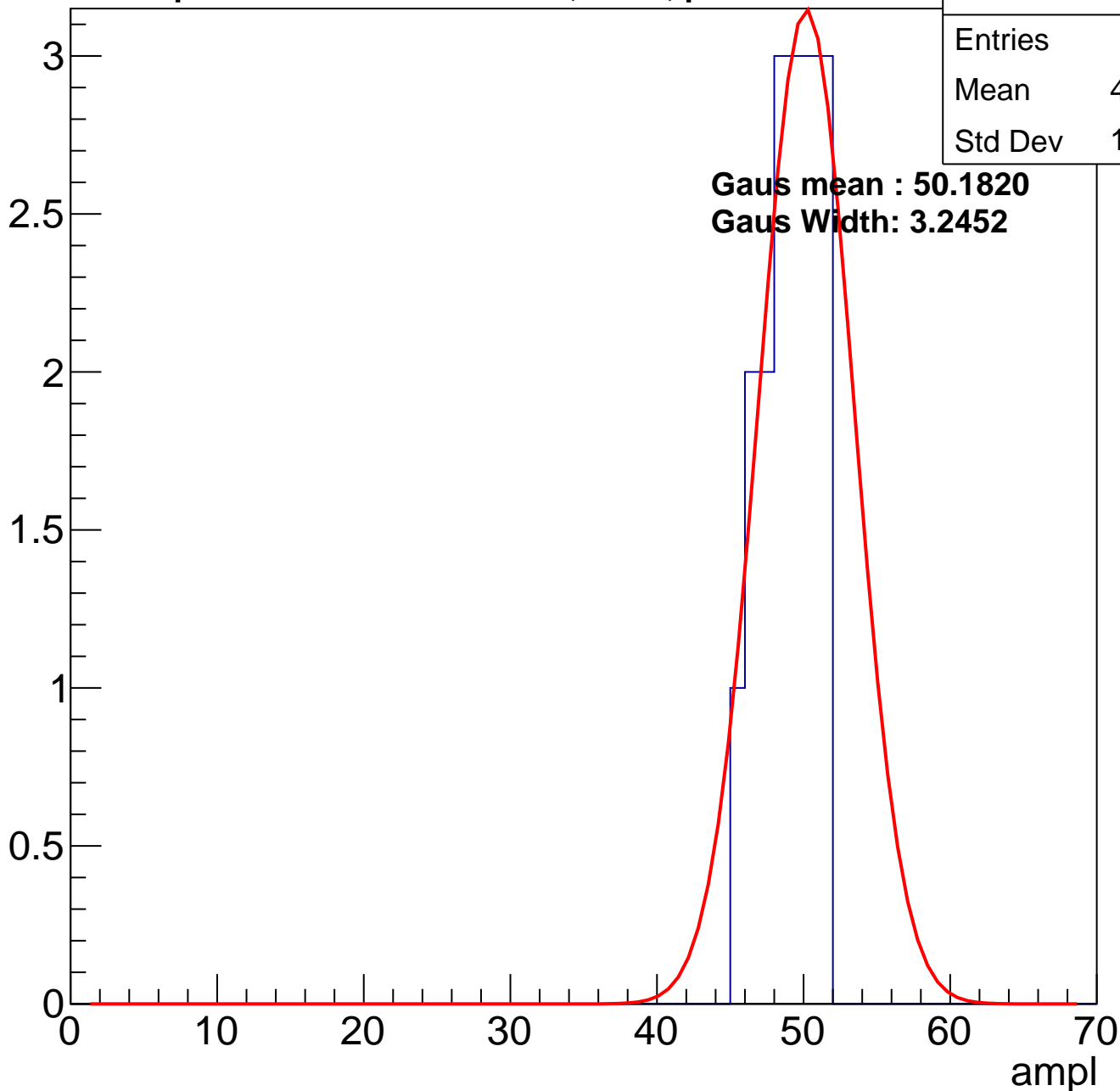
Entries	14
Mean	46.79
Std Dev	1.612

B0L100S, U5-ch95, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	49.67
Std Dev	1.247
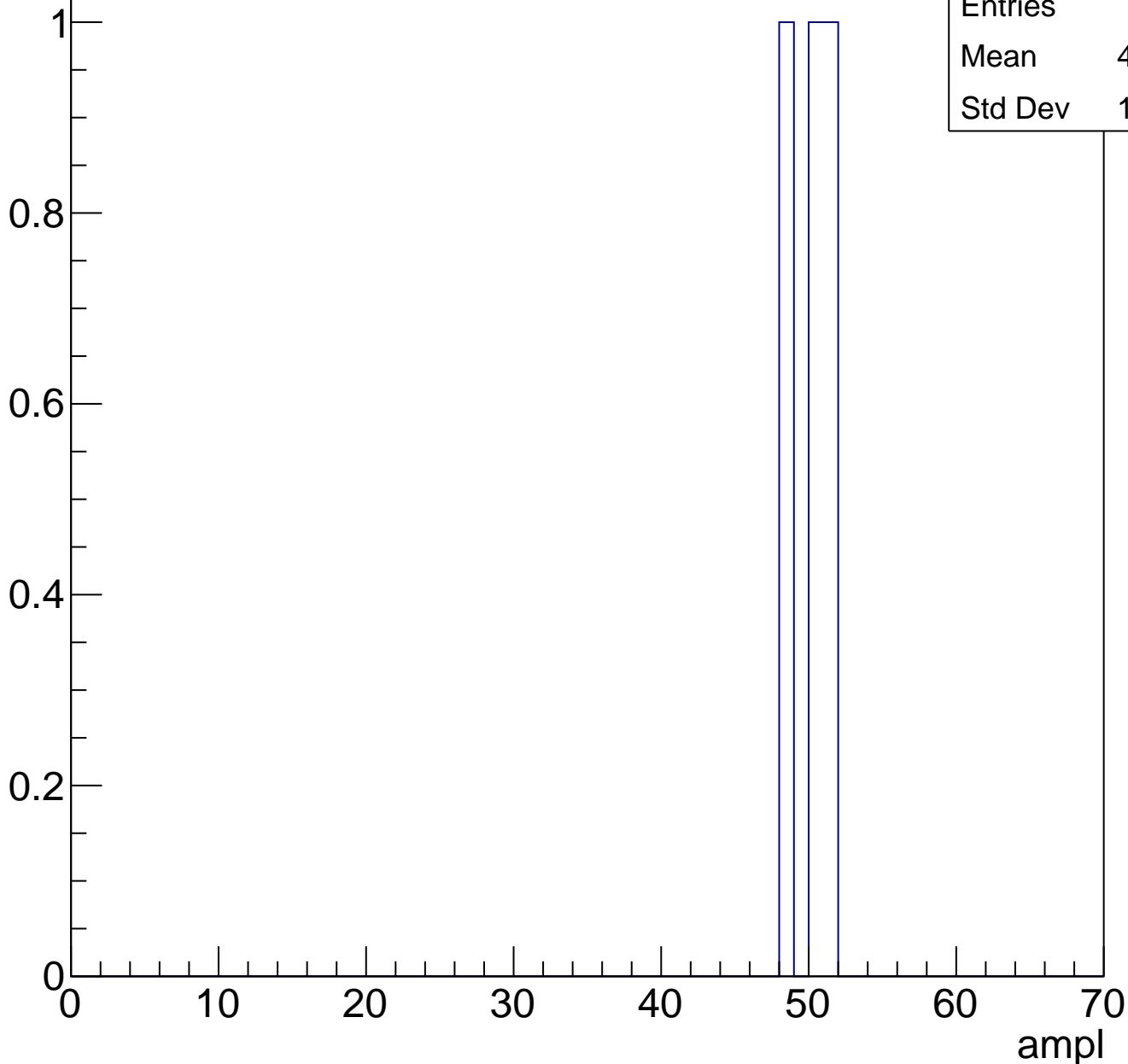
B0L100S, U5-ch95, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch95, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch95, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch95, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch96, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch96, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	43.33
Std Dev	0.4714

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch96, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	53
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
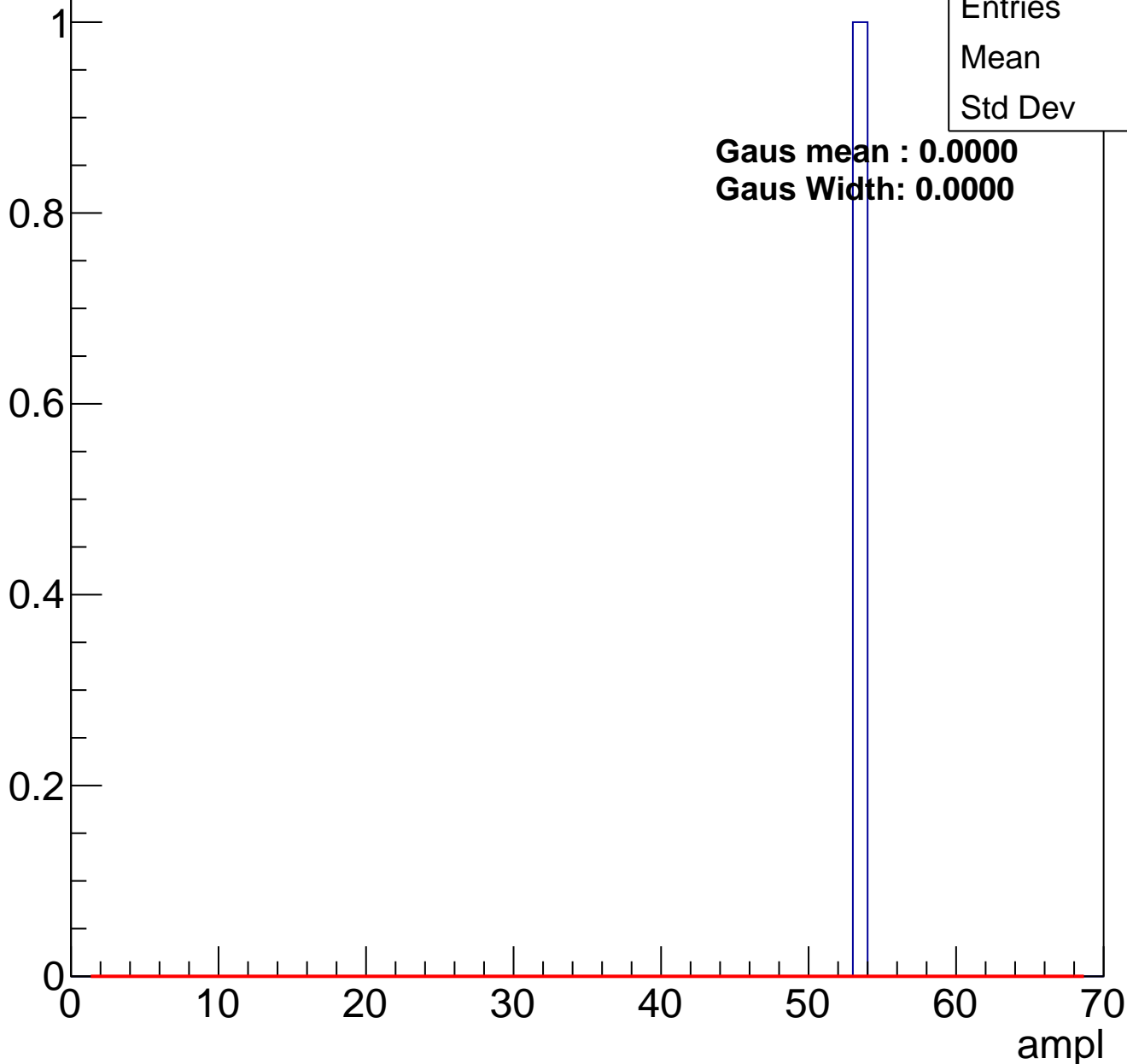
B0L100S, U5-ch96, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	53
Std Dev	0

Entry

ampl
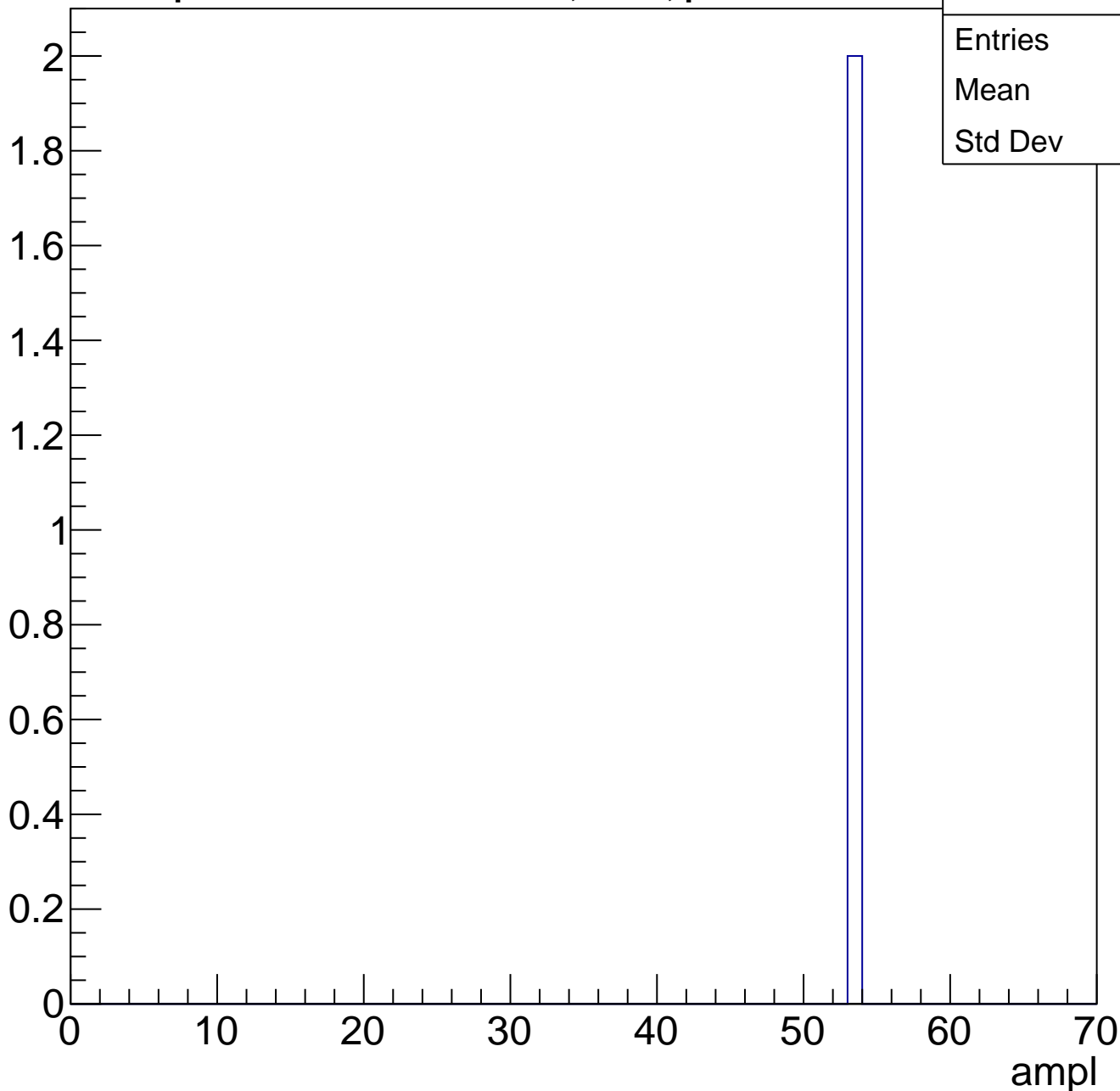
B0L100S, U5-ch96, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch96, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch96, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch96, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch97, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch97, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch97, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
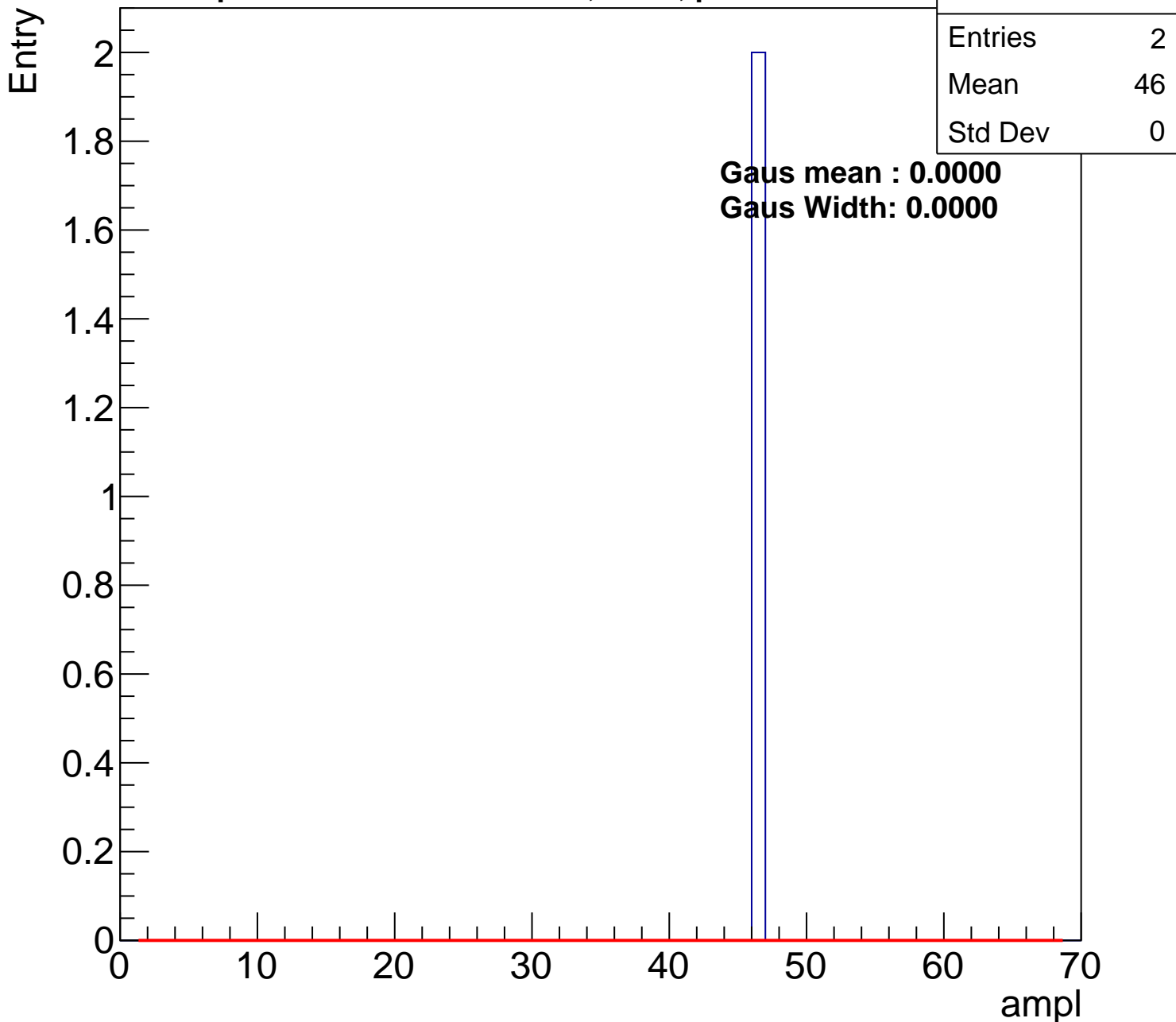
B0L100S, U5-ch97, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	53
Std Dev	0
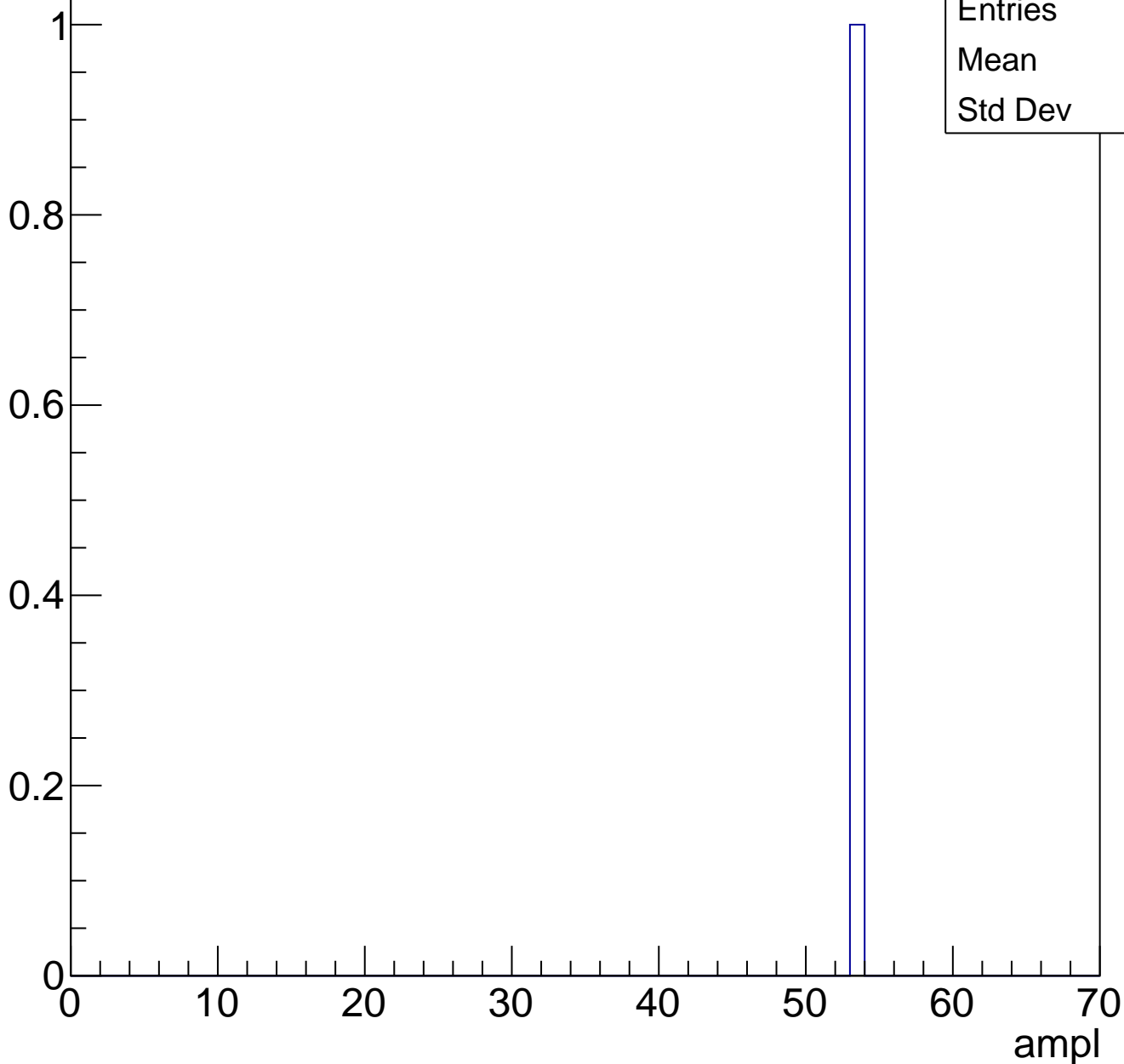
B0L100S, U5-ch97, adc4
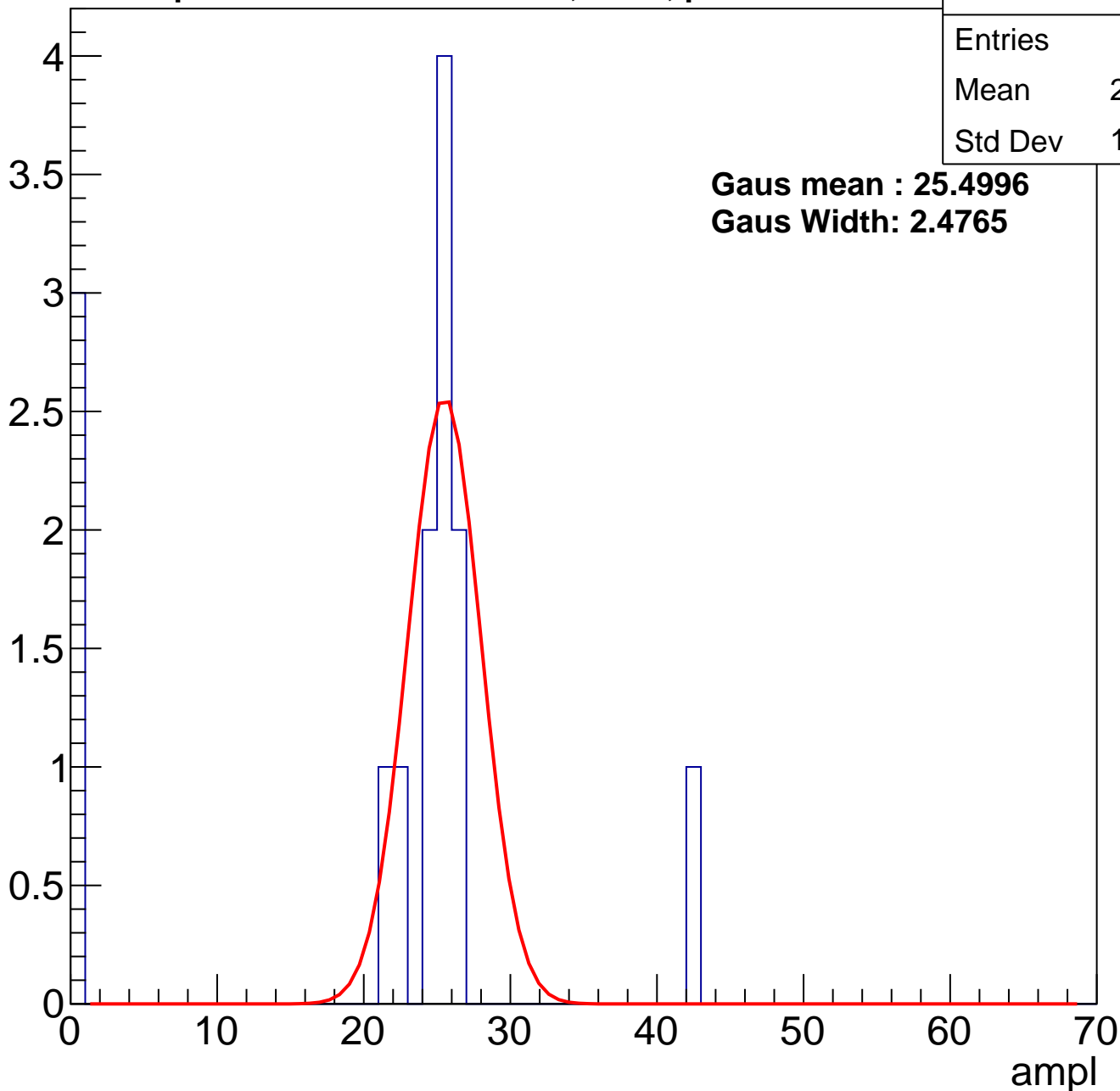
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch97, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch97, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch97, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch98, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch98, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch98, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch98, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch98, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch98, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch98, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch98, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch99, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch99, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch99, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	3
Mean	51.33
Std Dev	1.886

Entry

ampl
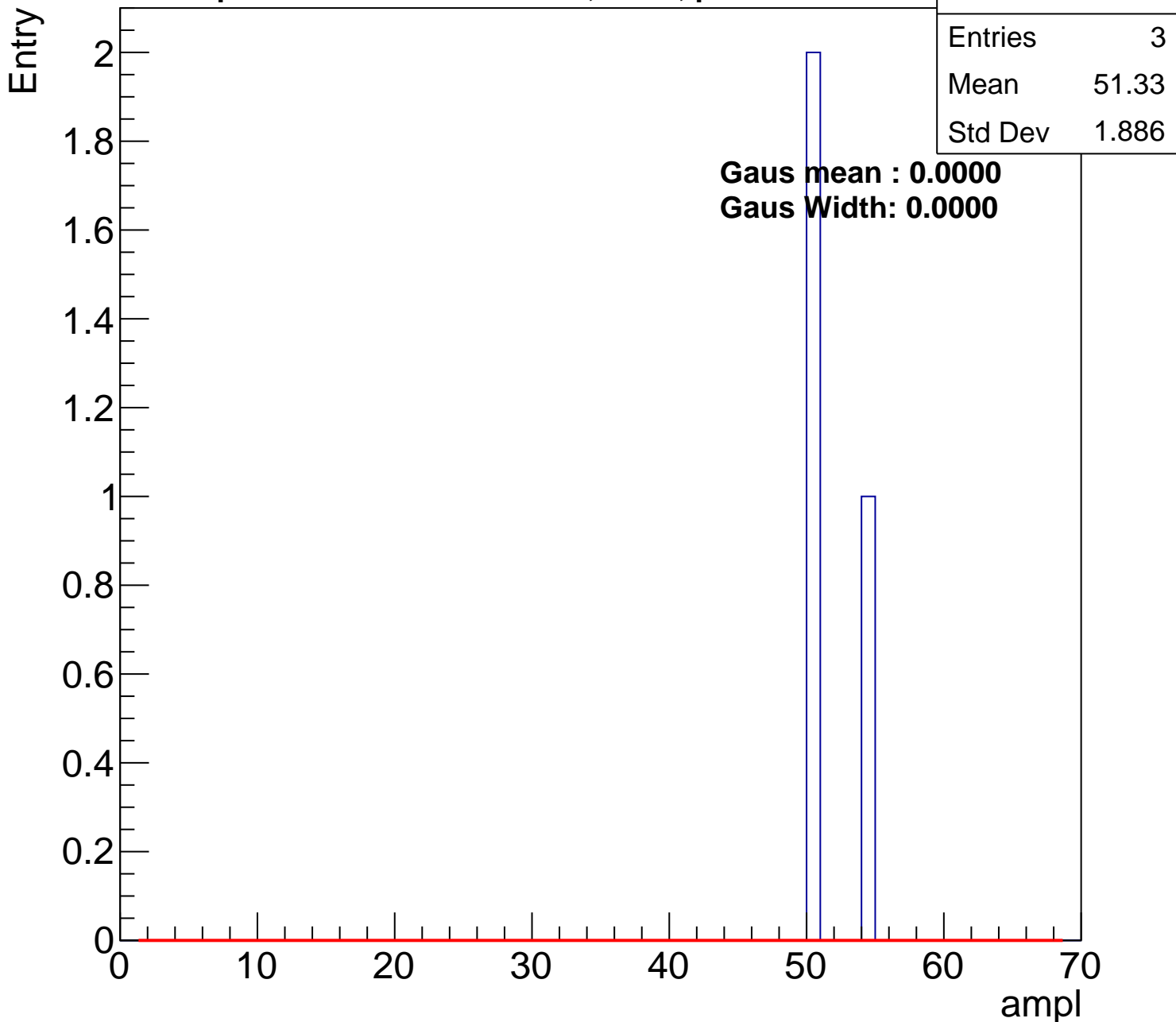
B0L100S, U5-ch99, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	4
Mean	53
Std Dev	0

Entry

ampl
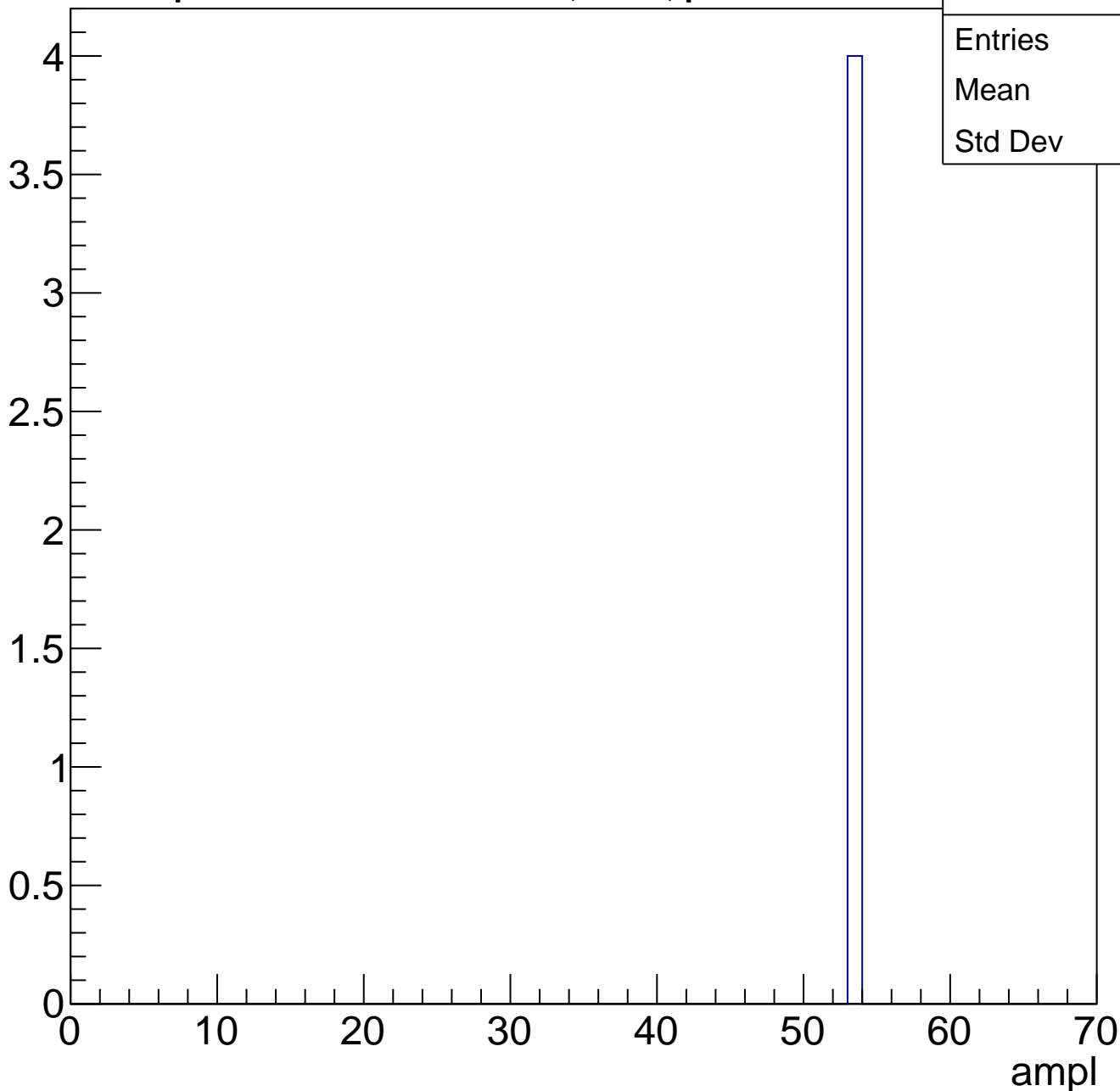
B0L100S, U5-ch99, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch99, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch99, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch99, adc7

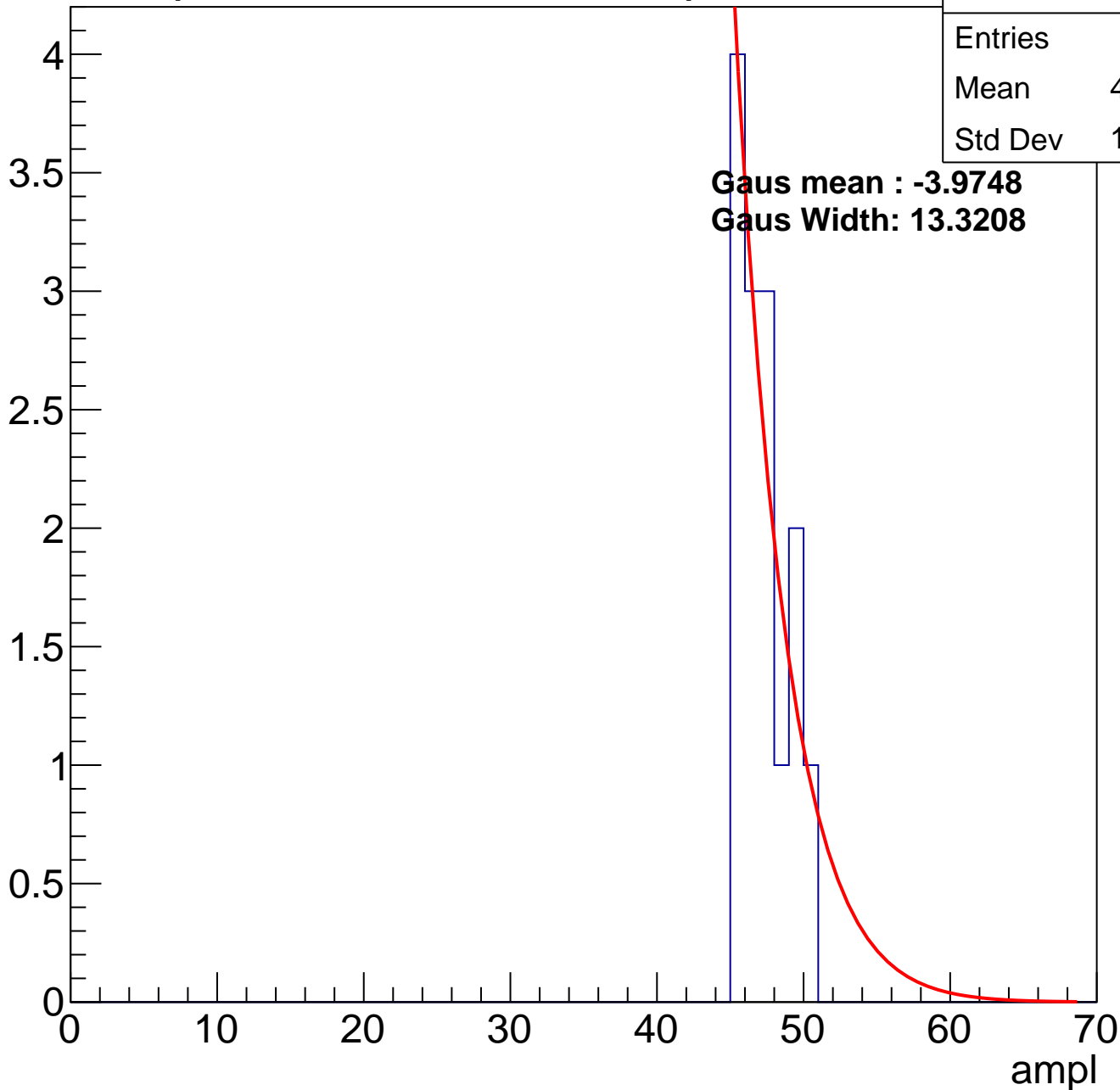**calib_packv5_042523_0143.root, FC#6, port A1**

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch100, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch100, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch100, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch100, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch100, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch100, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch100, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch100, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch101, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch101, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch101, adc2

calib_packv5_042523_0143.root, FC#6, port A1
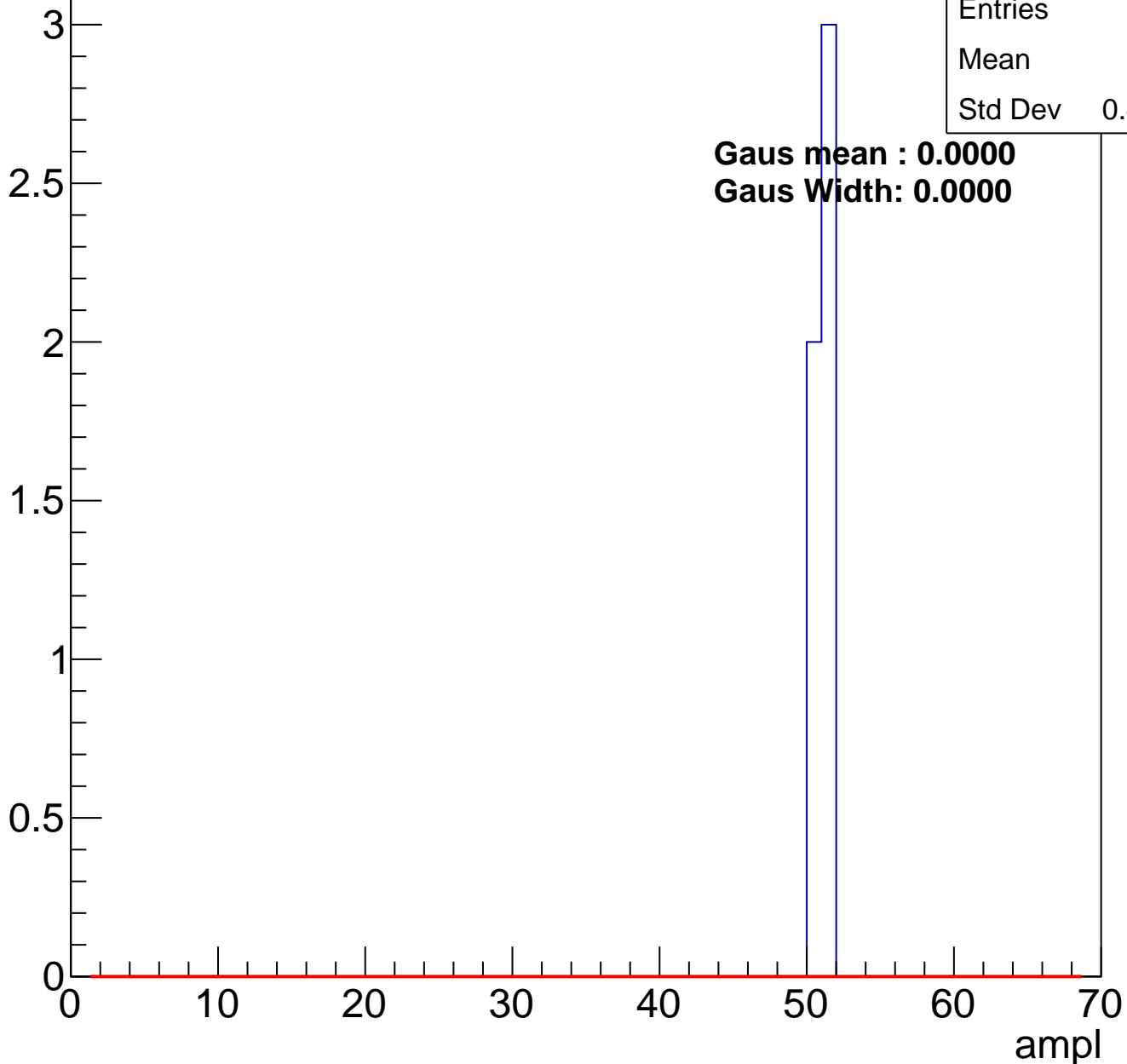
|	
Entries	5
Mean	50.6
Std Dev	0.4899

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch101, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch101, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch101, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch101, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch101, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch102, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch102, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch102, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch102, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch102, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch102, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch102, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch102, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch103, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch103, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000
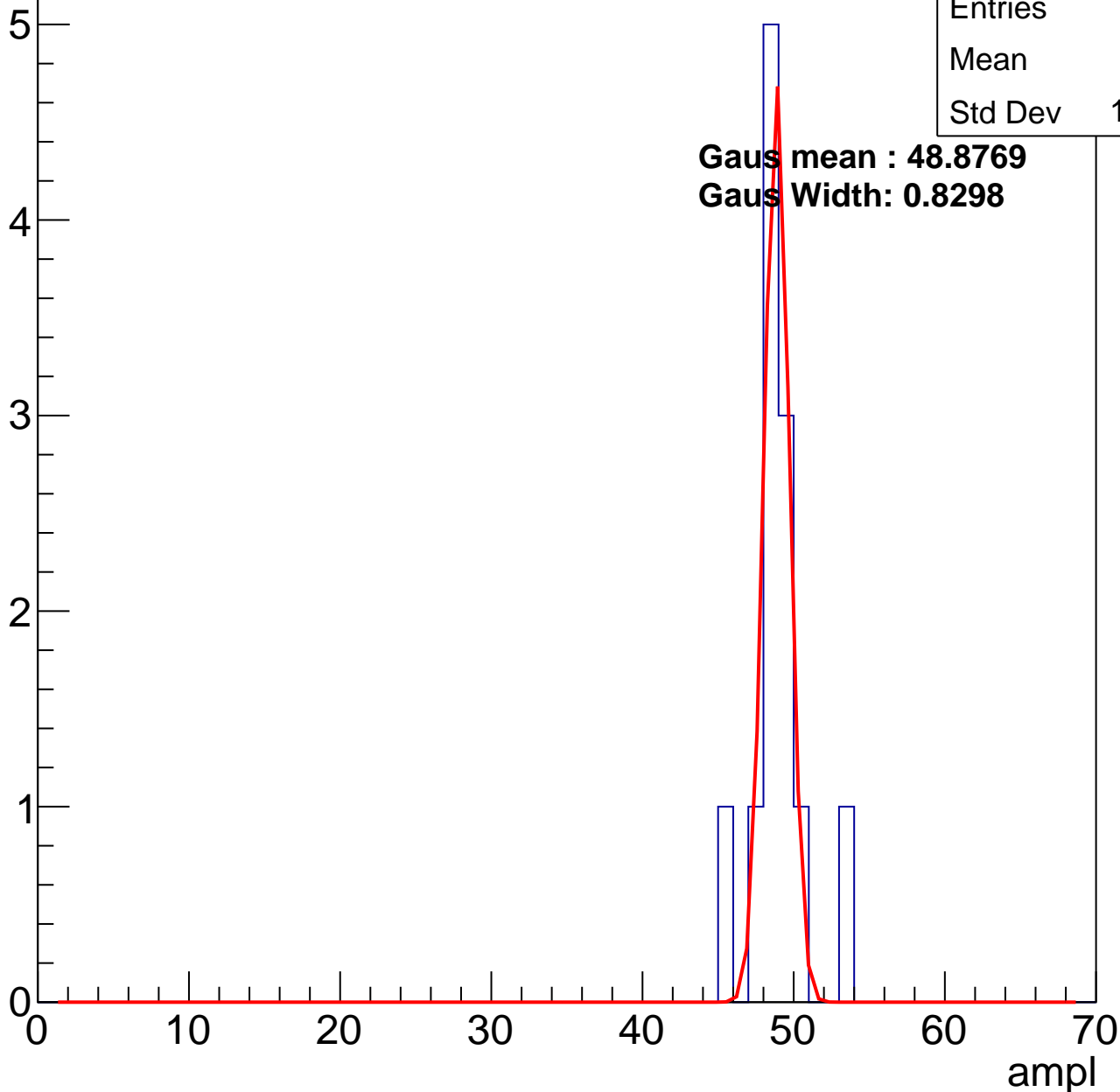
B0L100S, U5-ch103, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	7
Mean	50.86
Std Dev	1.641

Entry

ampl
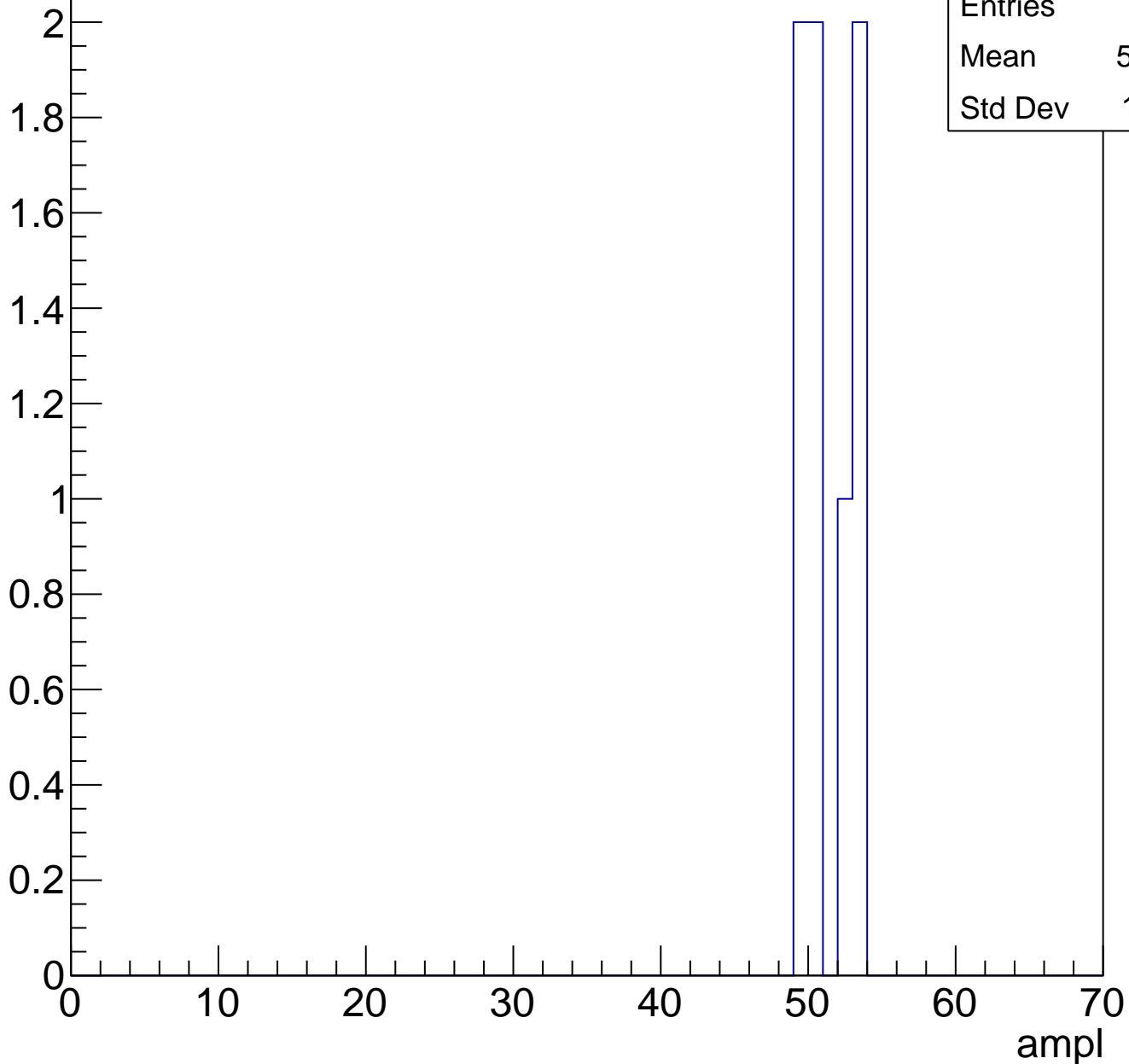
B0L100S, U5-ch103, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch103, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch103, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch103, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch104, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch104, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch104, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch104, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Entry

ampl

B0L100S, U5-ch104, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch104, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch104, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch104, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch105, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch105, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch105, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch105, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch105, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch105, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch105, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch105, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch106, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch106, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	49.67
Std Dev	0.9428

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch106, adc2

calib_packv5_042523_0143.root, FC#6, port A1
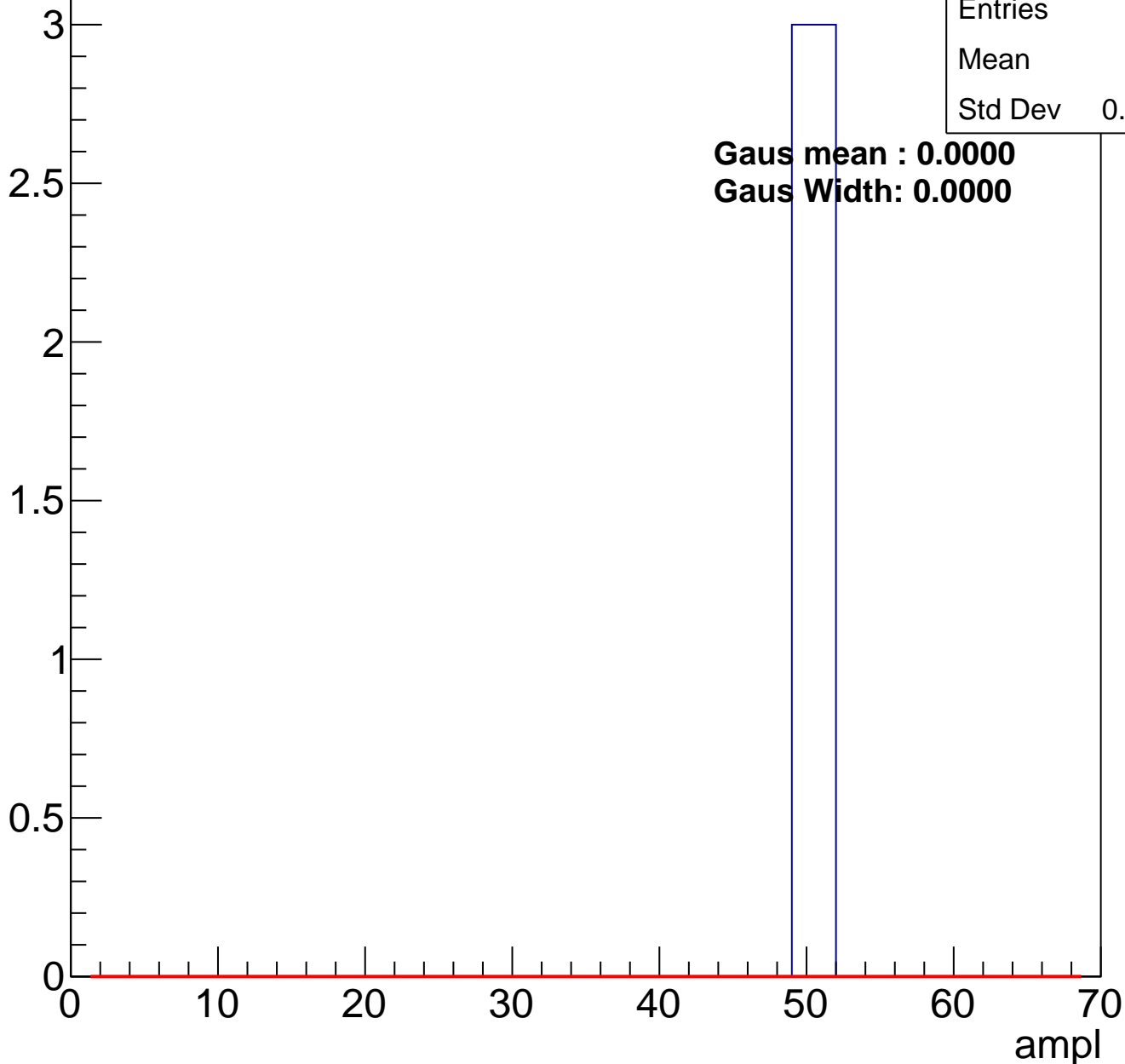
Entries	9
Mean	50
Std Dev	0.8165

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch106, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.6
Std Dev	2.8

Entry

ampl
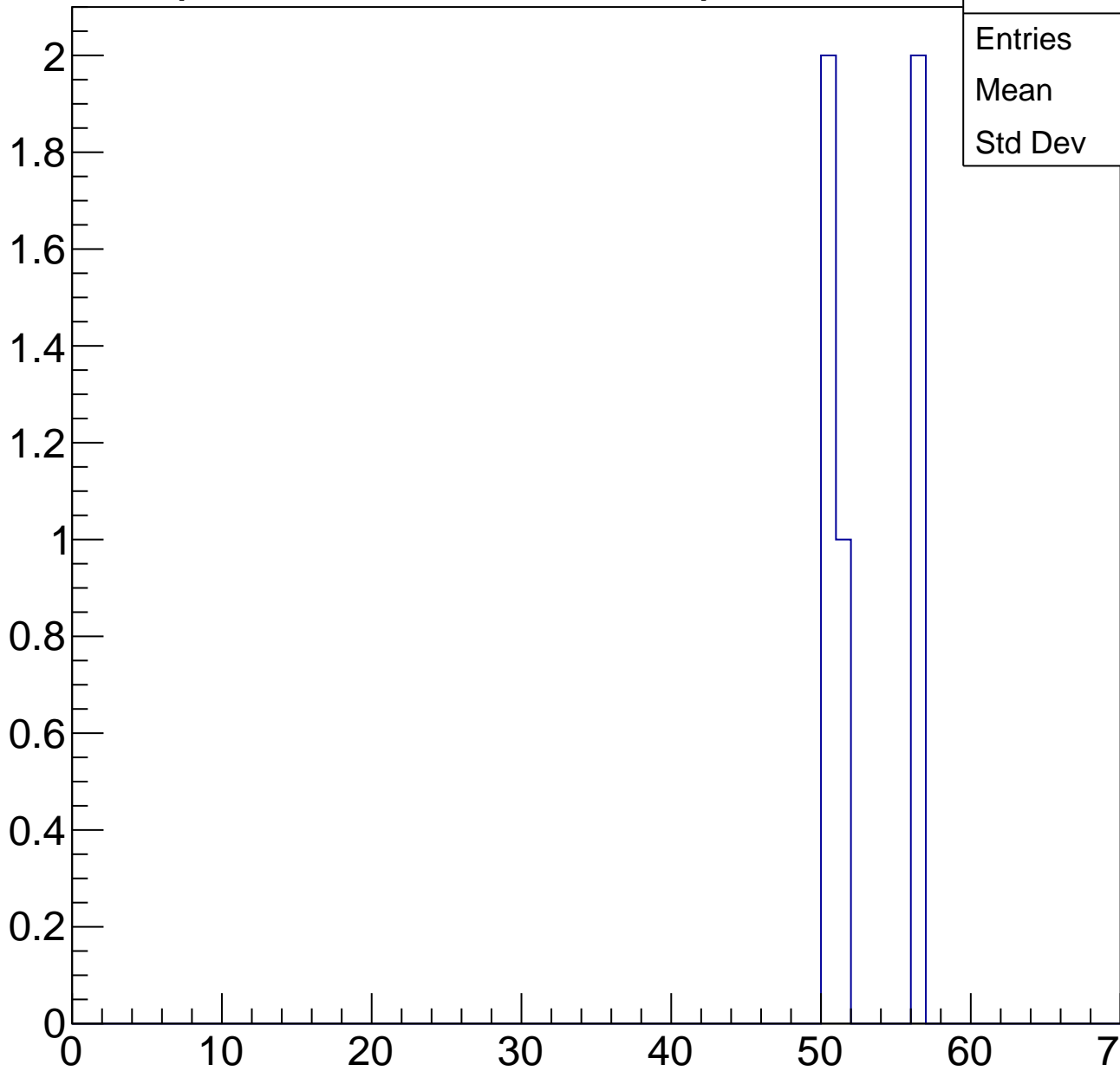
B0L100S, U5-ch106, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch106, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch106, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch106, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch107, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch107, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch107, adc2

calib_packv5_042523_0143.root, FC#6, port A1
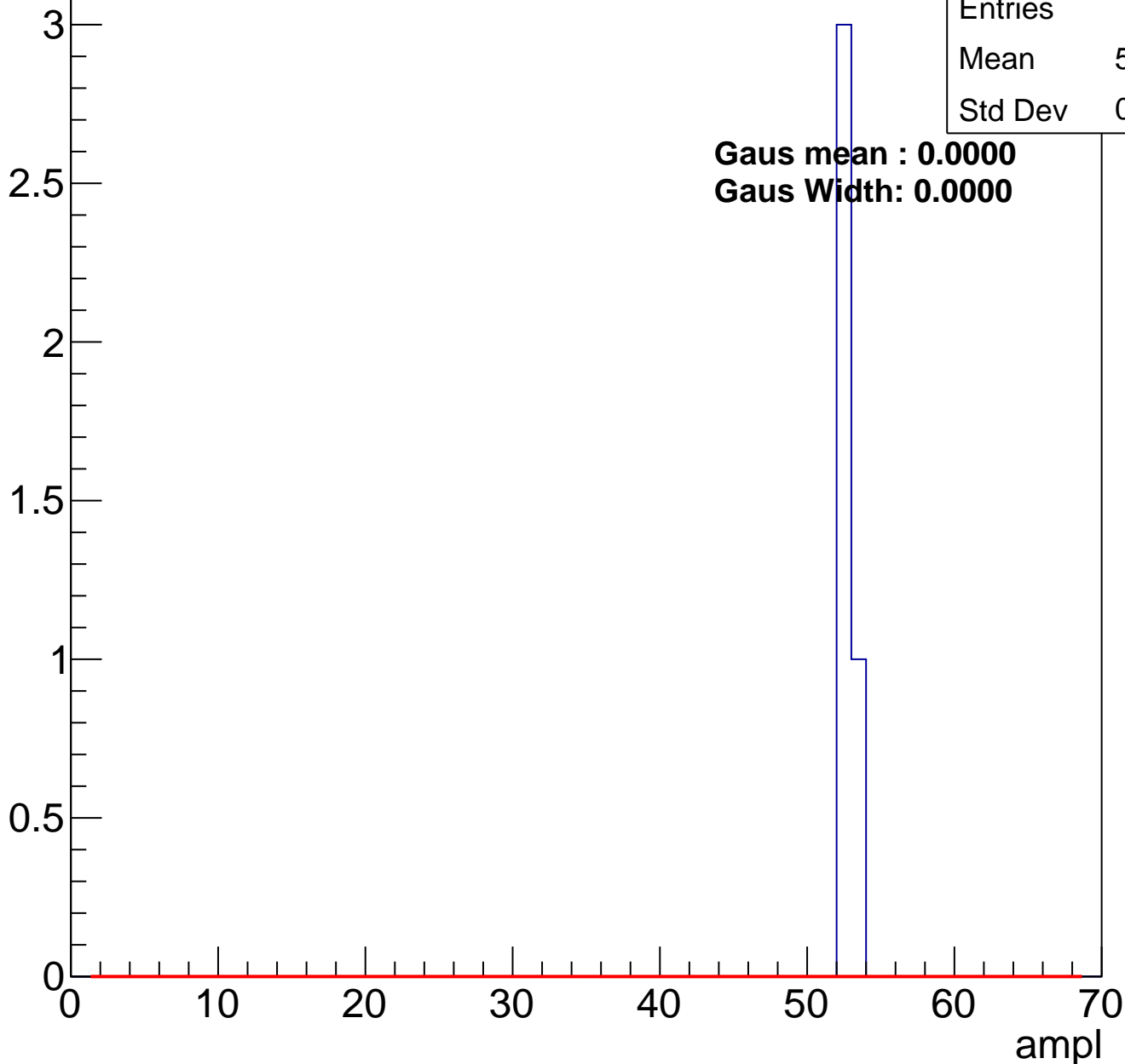
Entries	4
Mean	52.25
Std Dev	0.433

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch107, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	53
Std Dev	0

Entry

ampl

B0L100S, U5-ch107, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch107, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch107, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch107, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch108, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch108, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch108, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch108, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch108, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch108, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch108, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch108, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch109, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch109, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch109, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch109, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch109, adc4
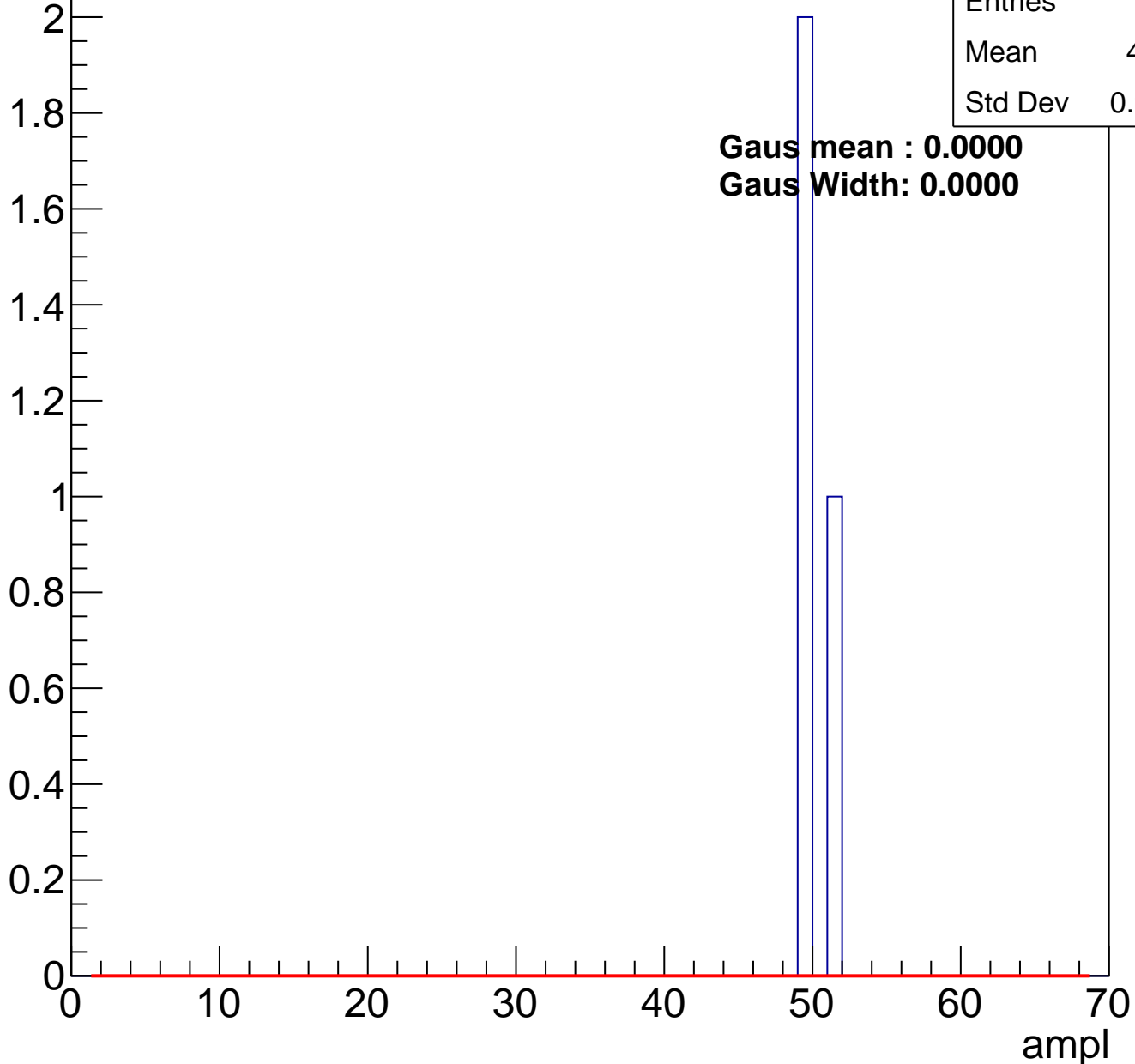
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch109, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch109, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch109, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch110, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch110, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch110, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch110, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch110, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch110, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch110, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch110, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch111, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch111, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch111, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch111, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch111, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch111, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch111, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch111, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch112, adc0
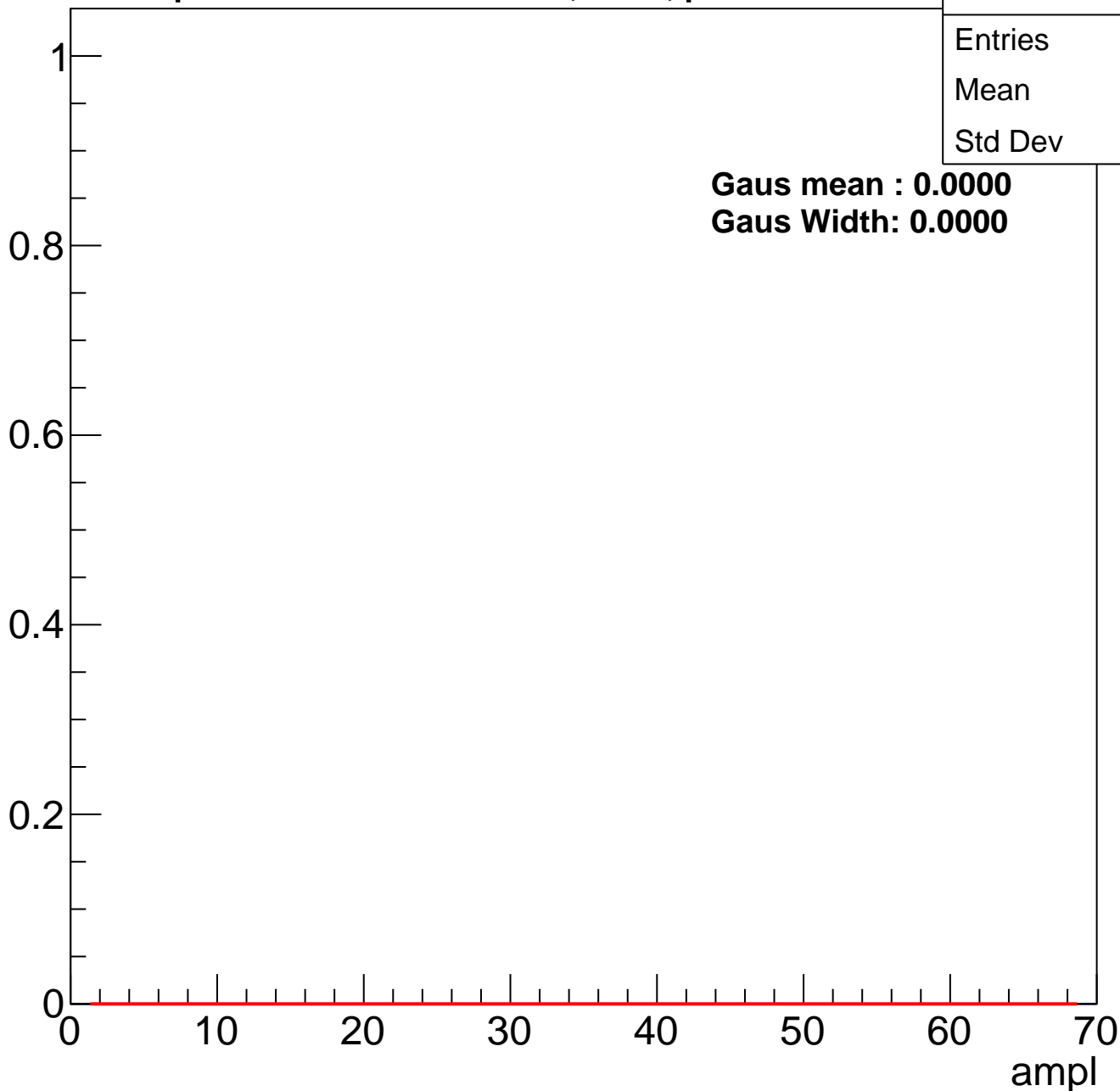
calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch112, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch112, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch112, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch112, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch112, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch112, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch112, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch113, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch113, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch113, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch113, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch113, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch113, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch113, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch113, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch114, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch114, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch114, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch114, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch114, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch114, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch114, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch114, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch115, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch115, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch115, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch115, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch115, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch115, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch115, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch115, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch116, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch116, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch116, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch116, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch116, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	0
Std Dev	0

Entry

ampl
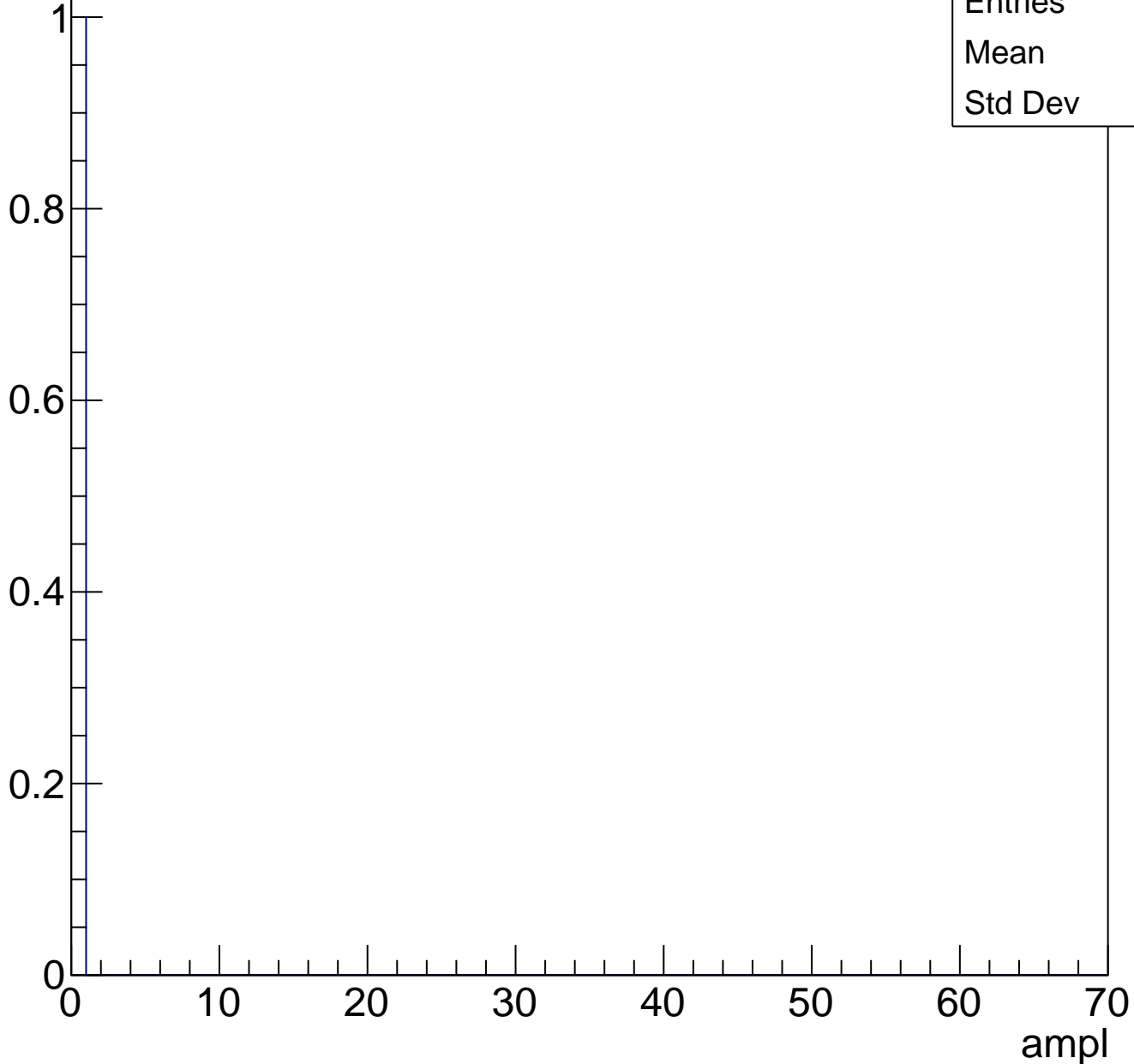
B0L100S, U5-ch116, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch116, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries		0
Mean		0
Std Dev		0

B0L100S, U5-ch116, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch117, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch117, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch117, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch117, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch117, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch117, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch117, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch117, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch118, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	1
Mean	45
Std Dev	0

ampl
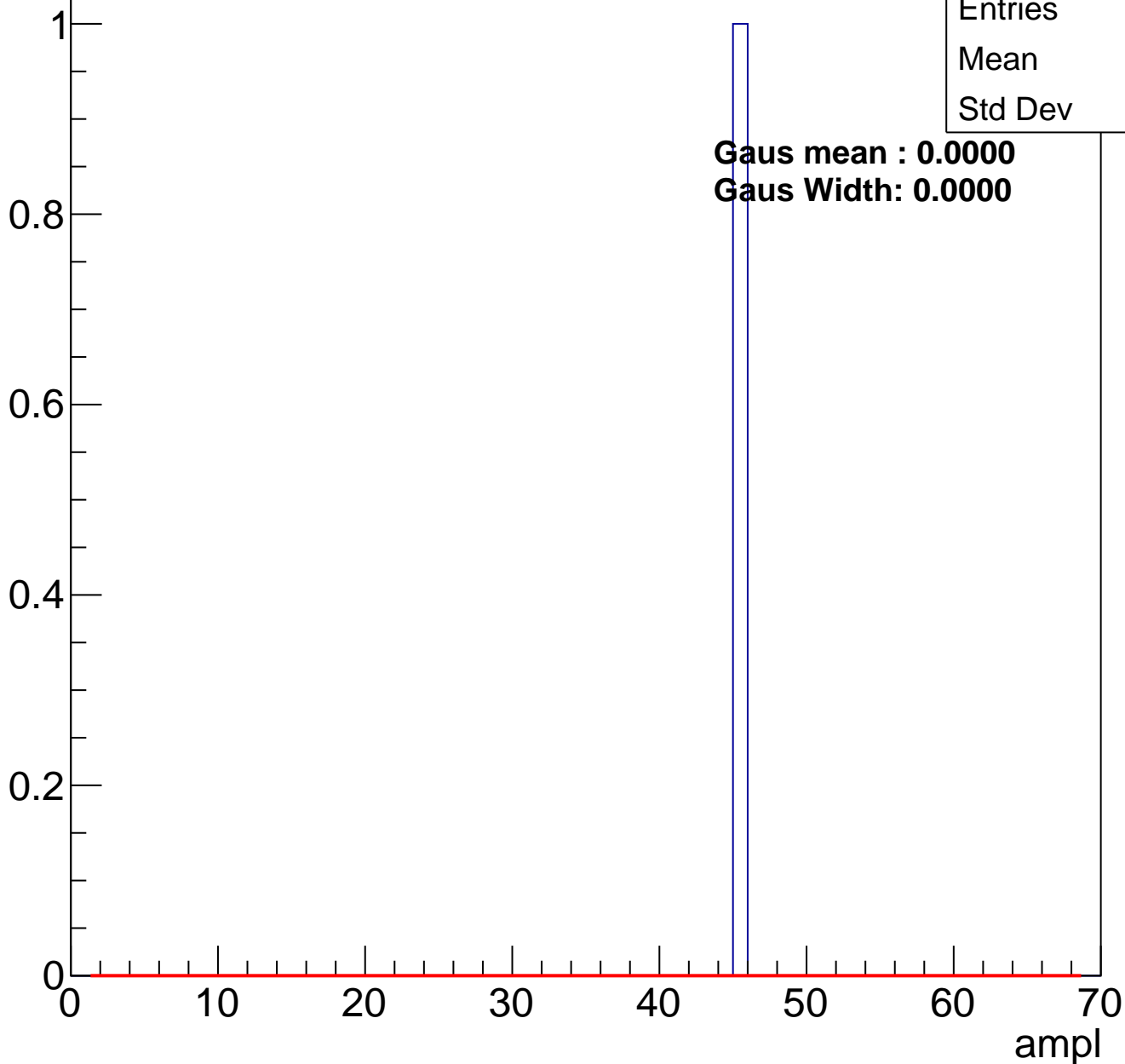
B0L100S, U5-ch118, adc1

calib_packv5_042523_0143.root, FC#6, port A1
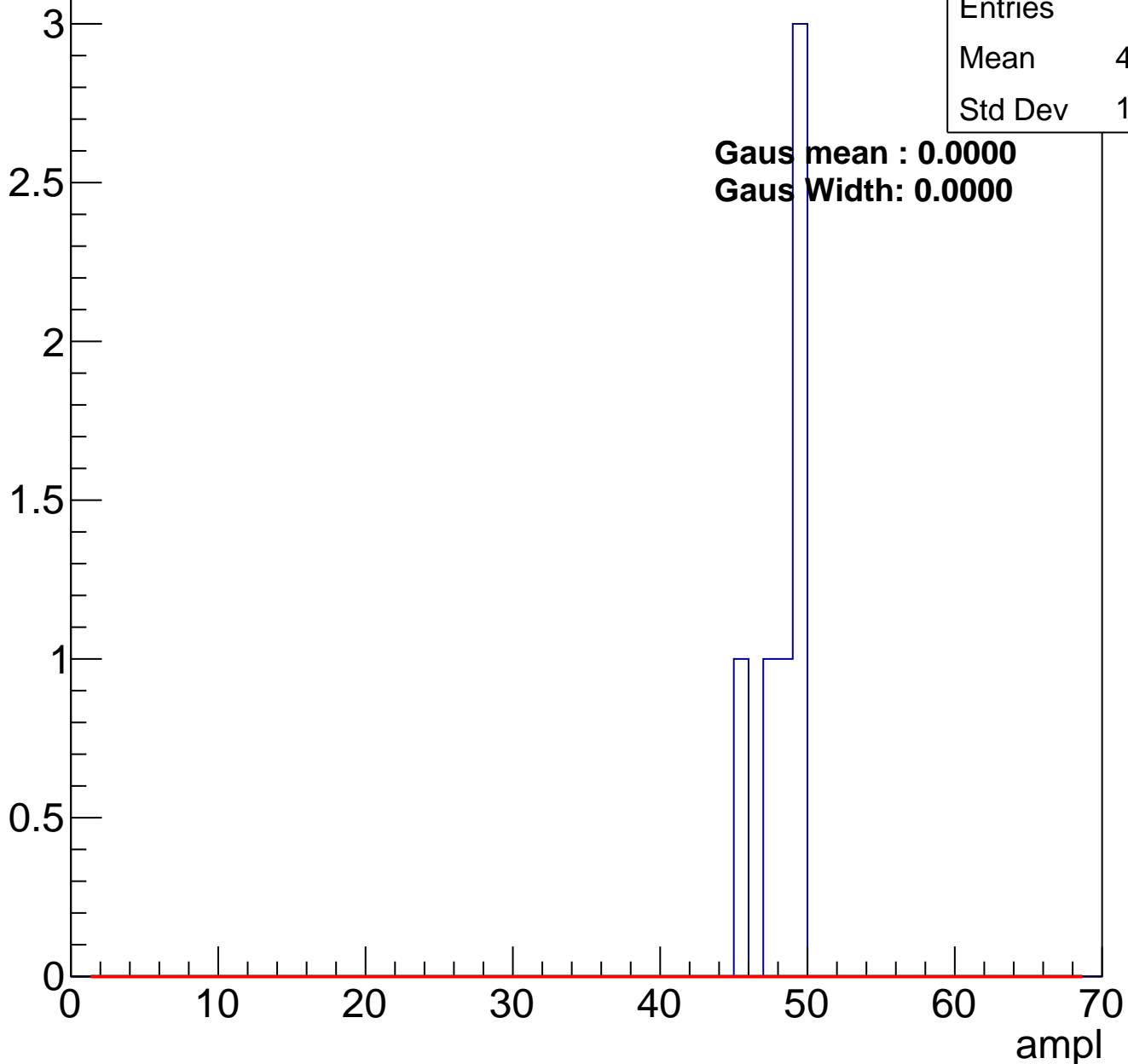
Entries	6
Mean	47.83
Std Dev	1.462

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch118, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 46.9211
Gaus Width: 1.5500

Entries	17
Mean	47.94
Std Dev	2.071
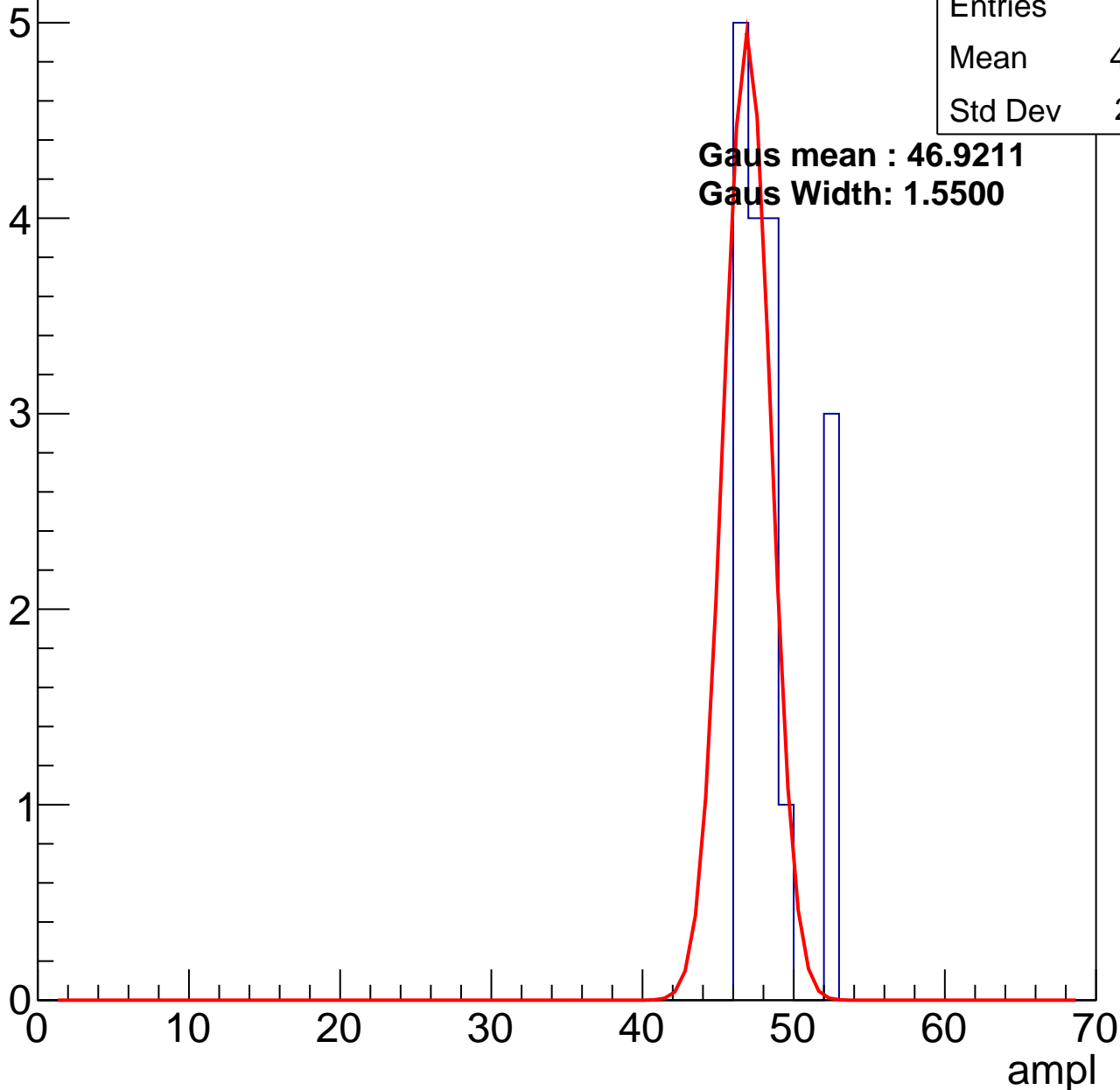
B0L100S, U5-ch118, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	5
Mean	52.6
Std Dev	0.4899
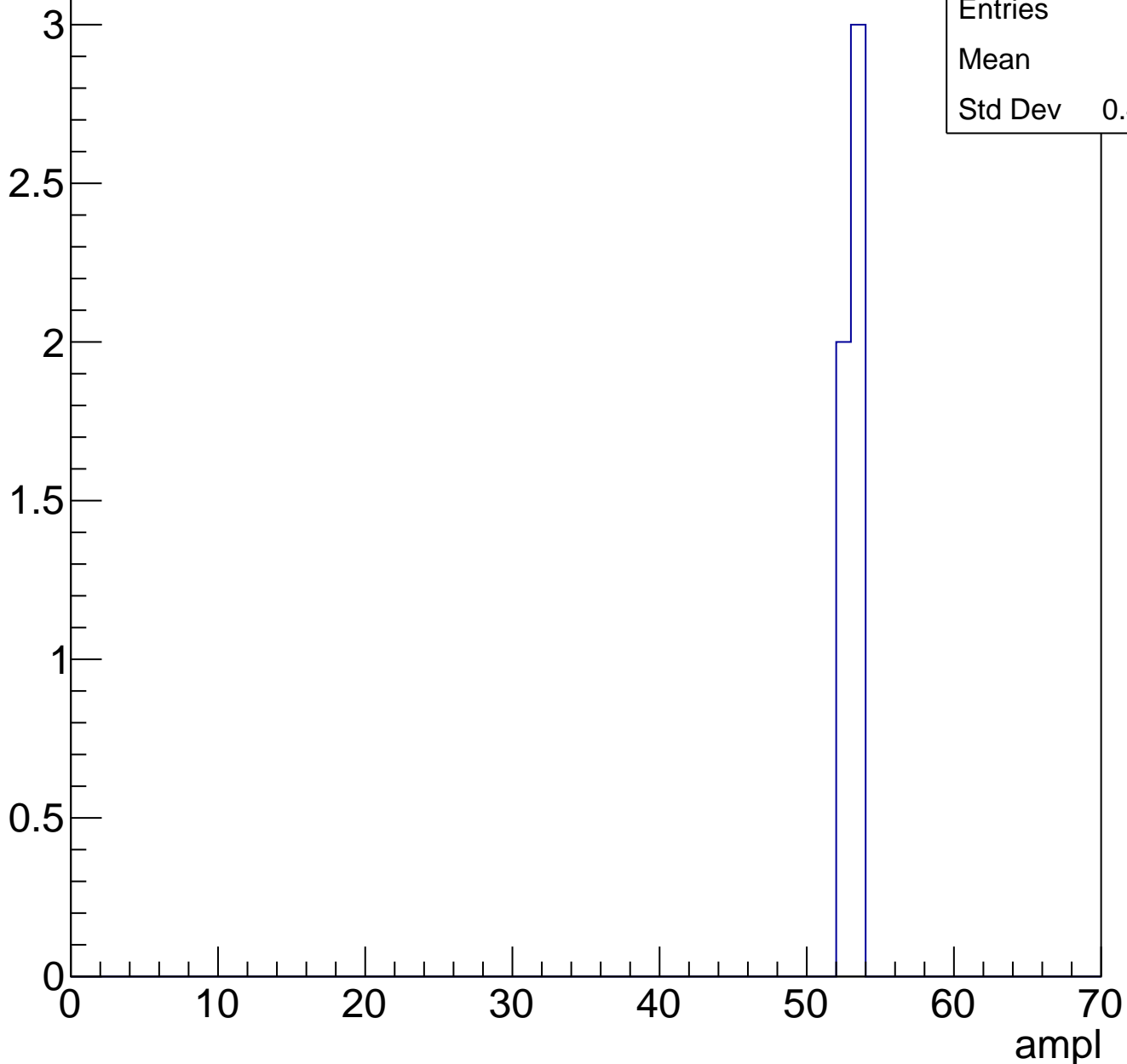
B0L100S, U5-ch118, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch118, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch118, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch118, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch119, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch119, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	48.5
Std Dev	0.5

Gaus mean : 0.0000
Gaus Width: 0.0000
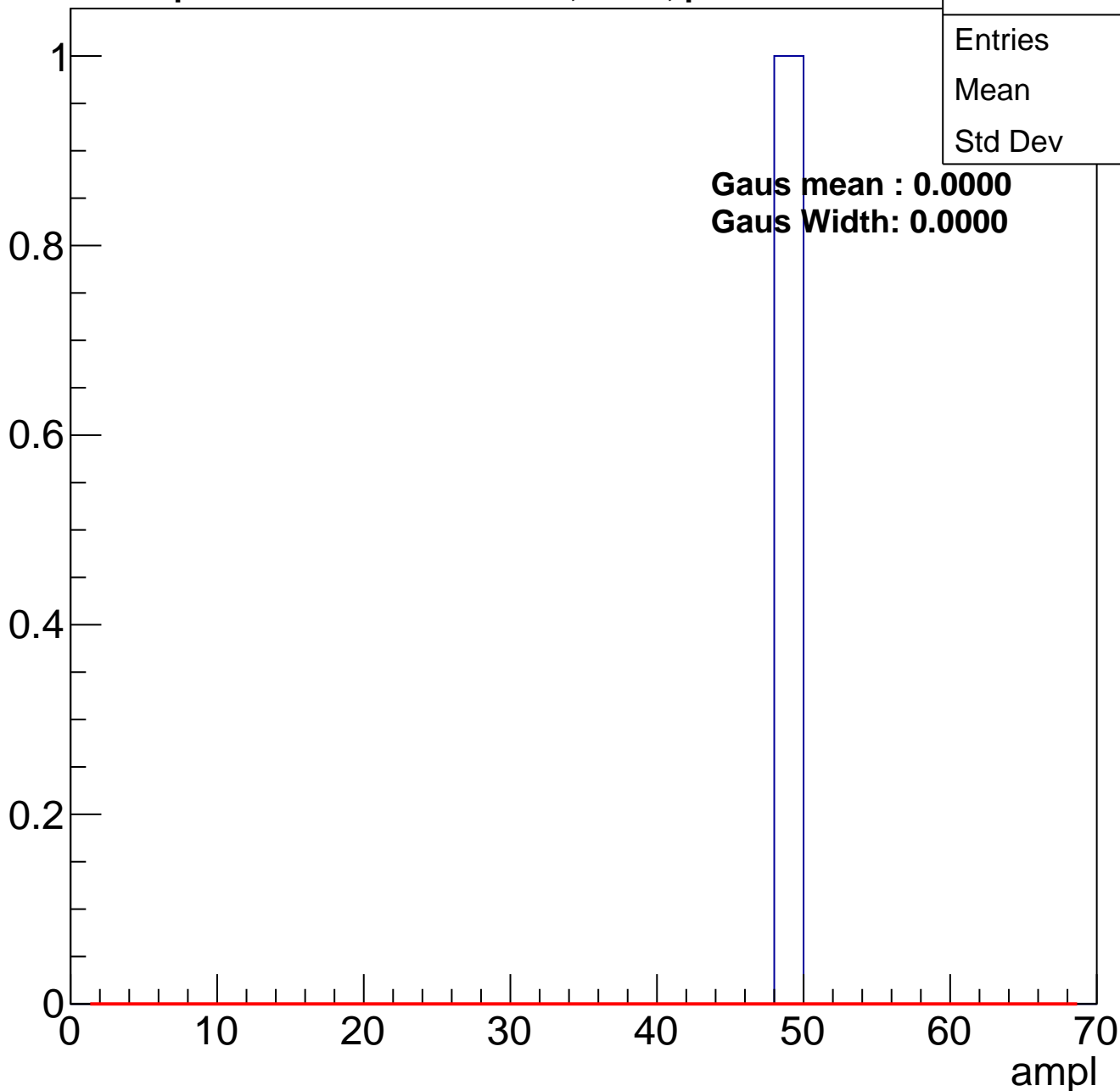
B0L100S, U5-ch119, adc2

calib_packv5_042523_0143.root, FC#6, port A1
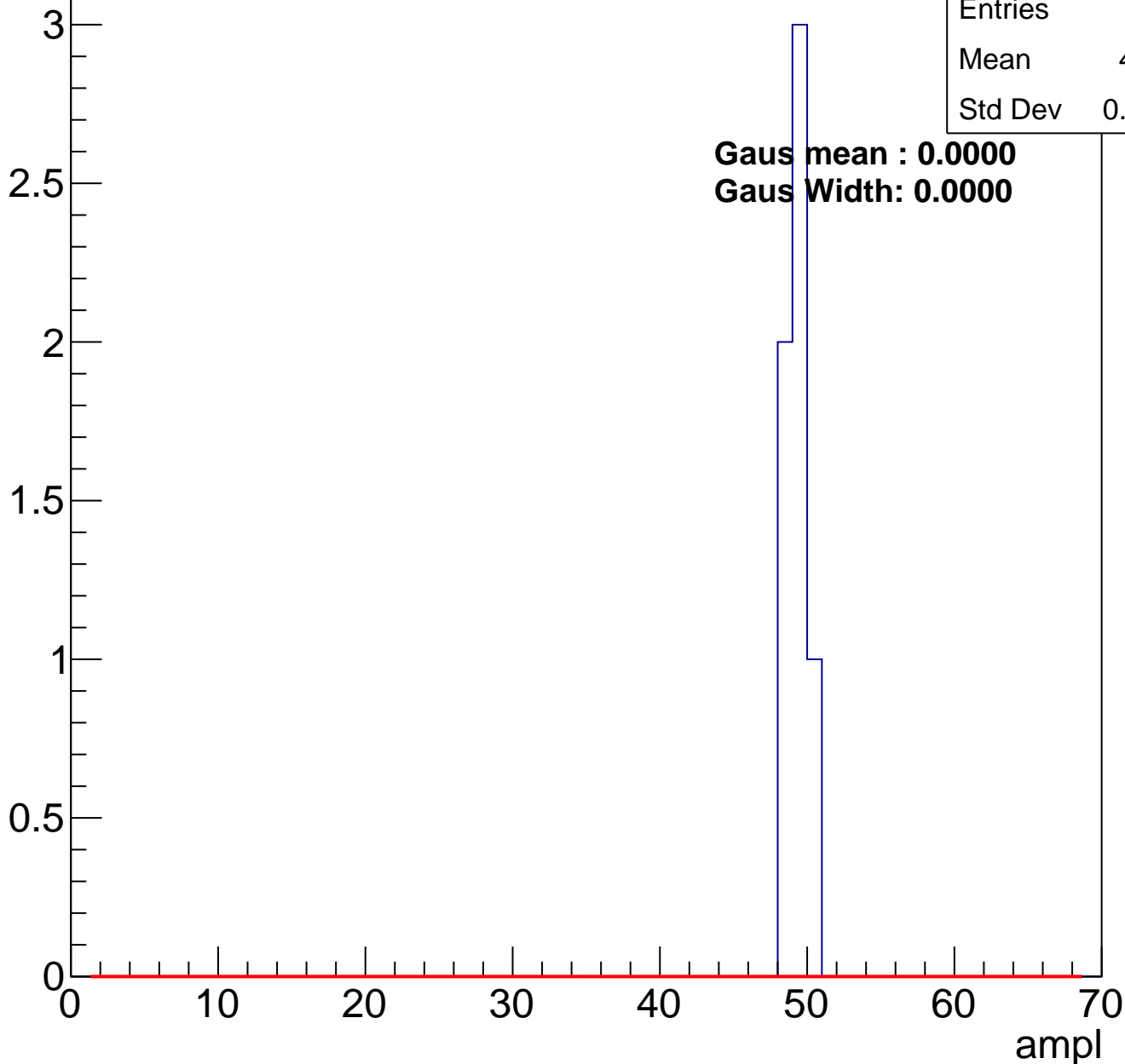
Entries	6
Mean	48.83
Std Dev	0.6872

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch119, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	2
Mean	49.5
Std Dev	0.5

ampl
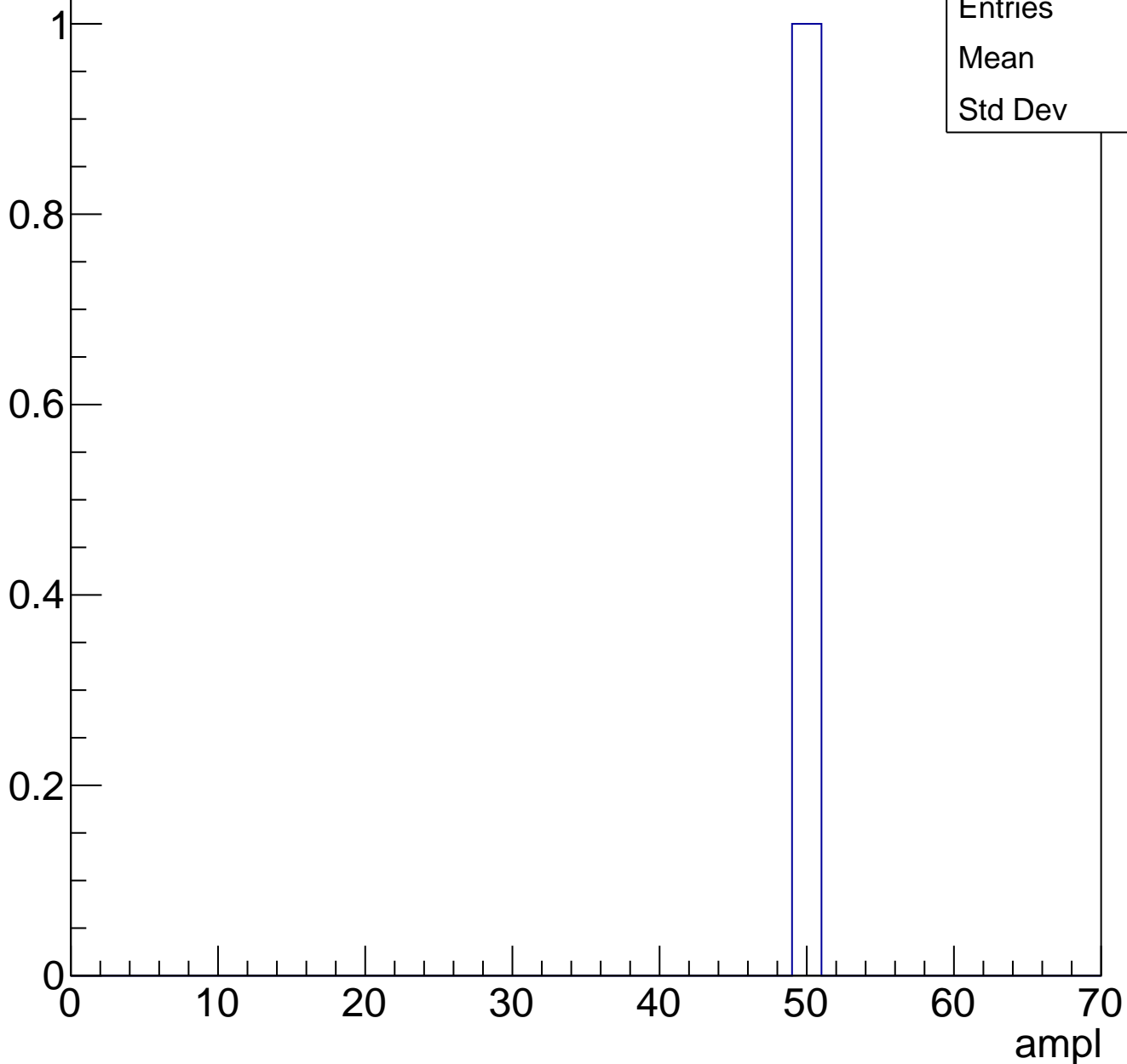
B0L100S, U5-ch119, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch119, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch119, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch119, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch120, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch120, adc1

calib_packv5_042523_0143.root, FC#6, port A1
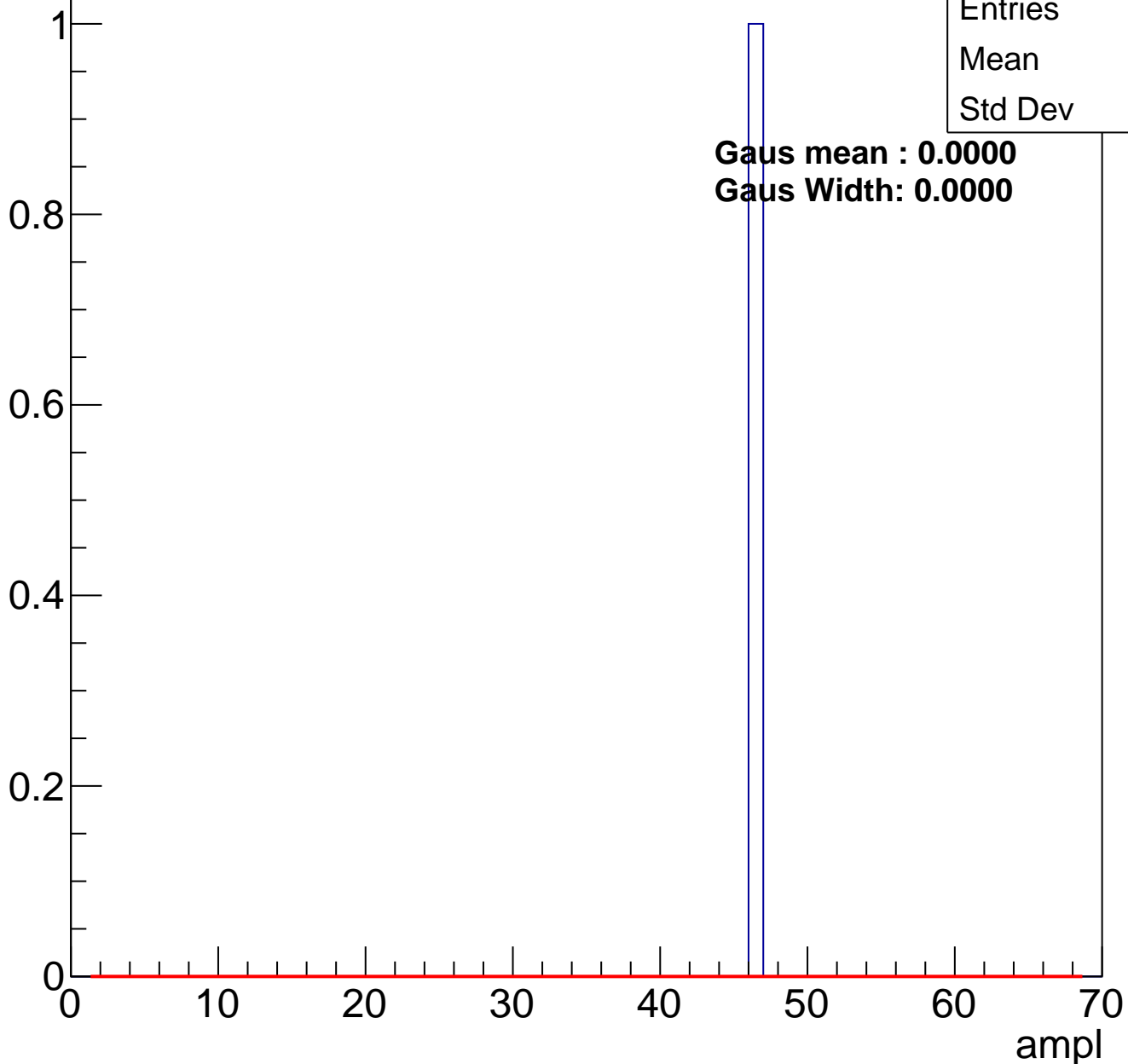
Entries	1
Mean	46
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch120, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Gaus mean : 0.0000
Gaus Width: 0.0000

Entries	3
Mean	47.33
Std Dev	1.247
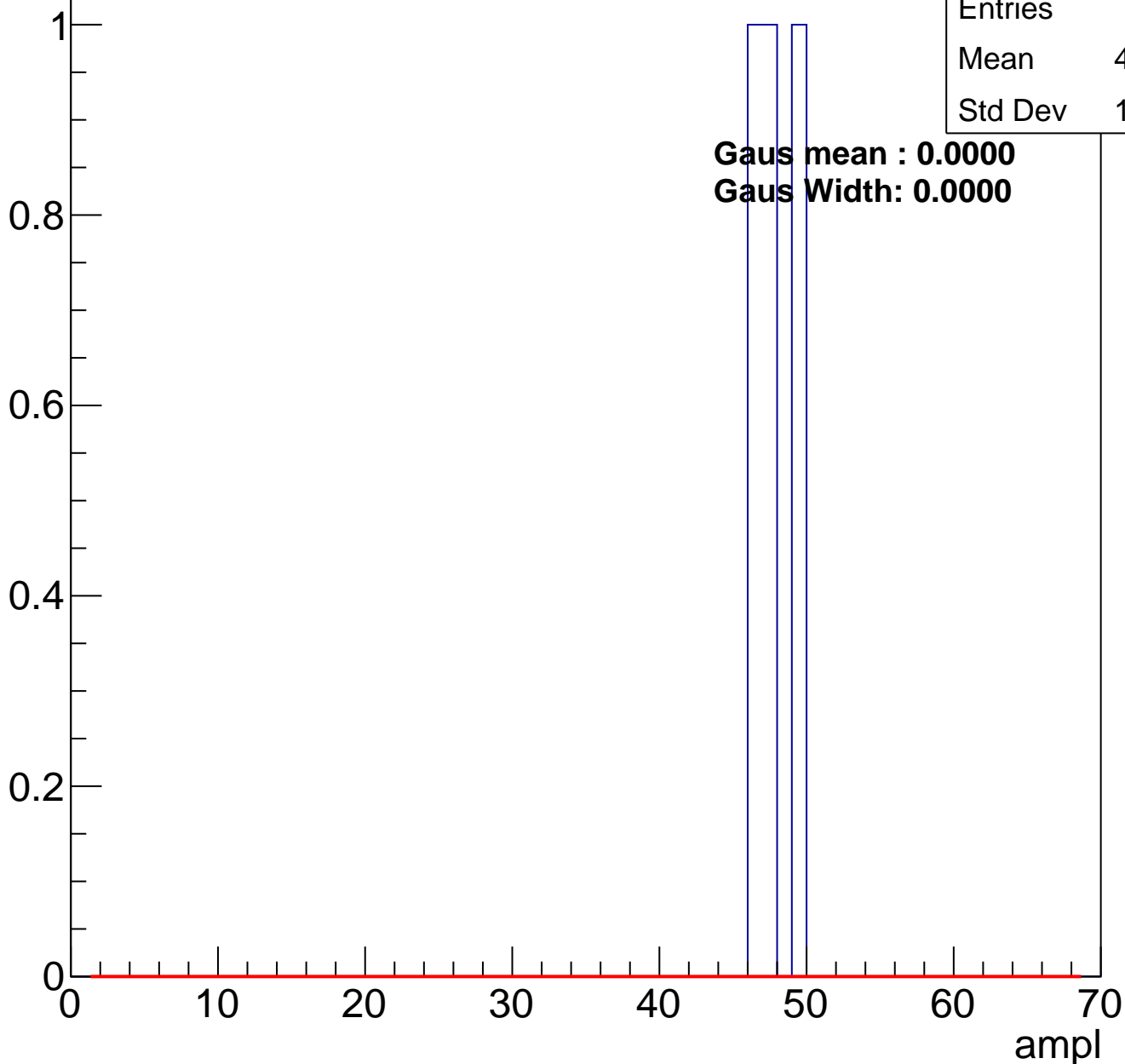
B0L100S, U5-ch120, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch120, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch120, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

Entry

B0L100S, U5-ch120, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch120, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch121, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch121, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	49
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
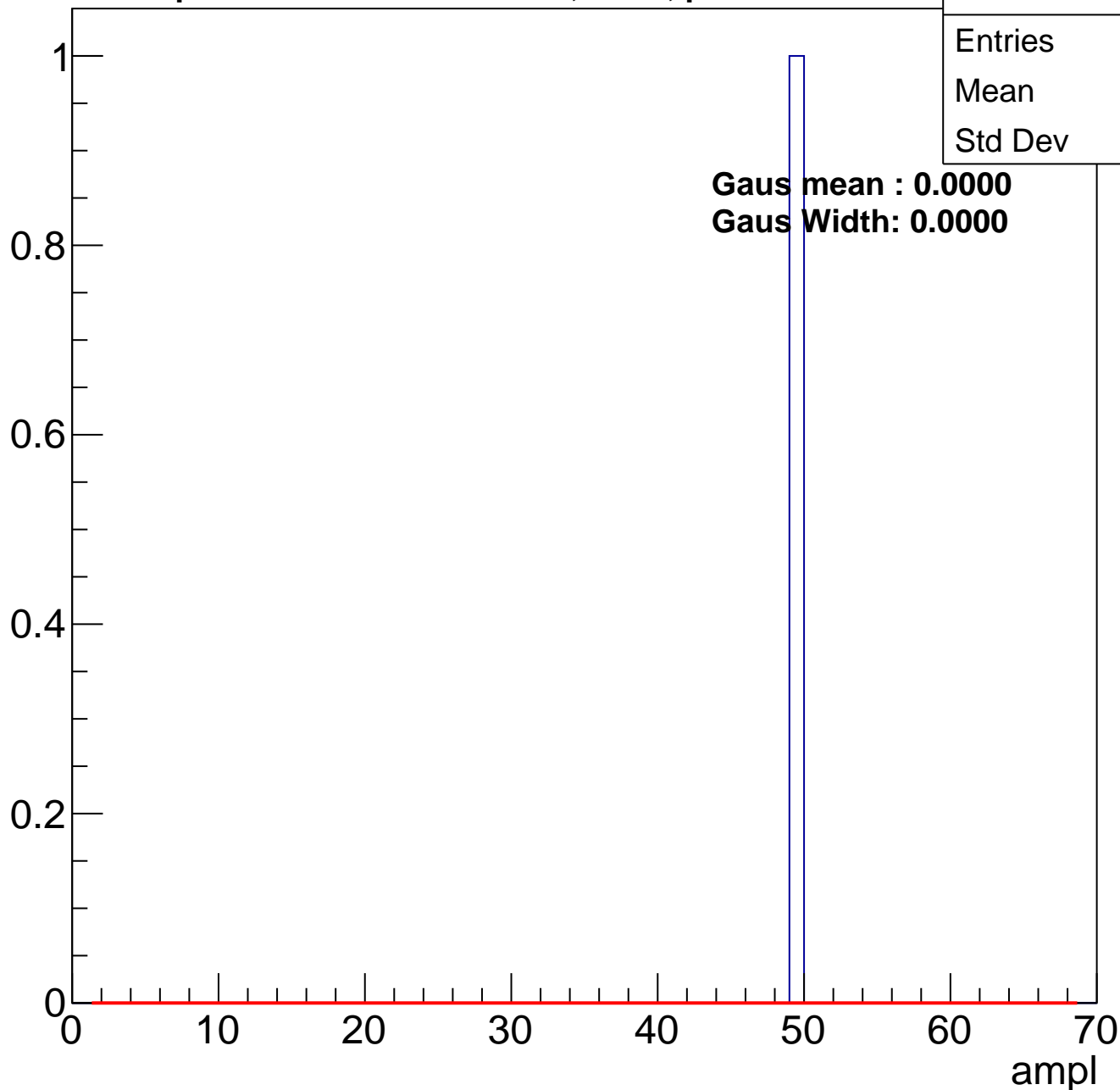
B0L100S, U5-ch121, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch121, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch121, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch121, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch121, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch121, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch122, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch122, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch122, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch122, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch122, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl
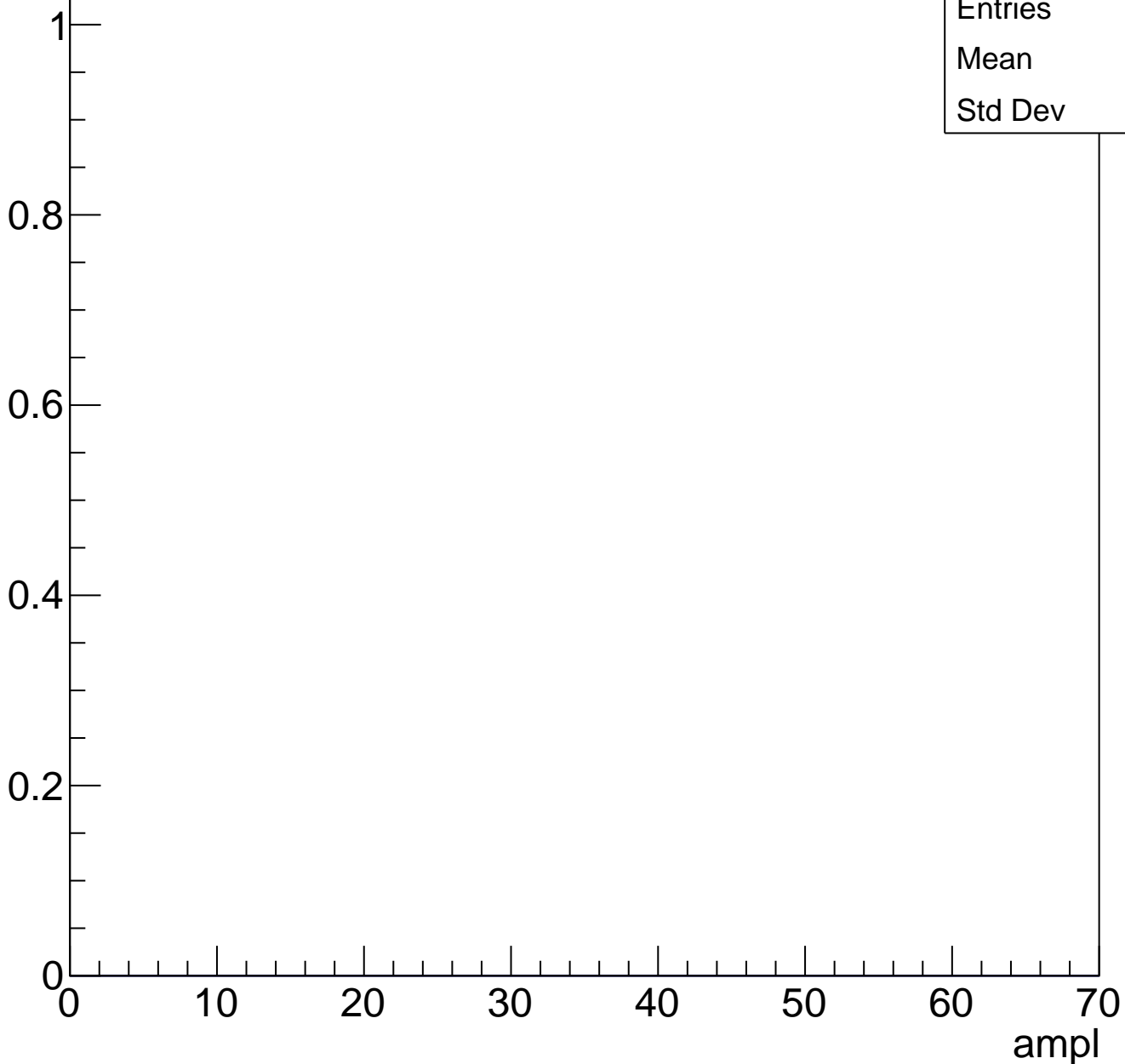
B0L100S, U5-ch122, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch122, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch122, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch123, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch123, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch123, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch123, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch123, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch123, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch123, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch123, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch124, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch124, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch124, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch124, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch124, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	30
Std Dev	0

Entry

ampl
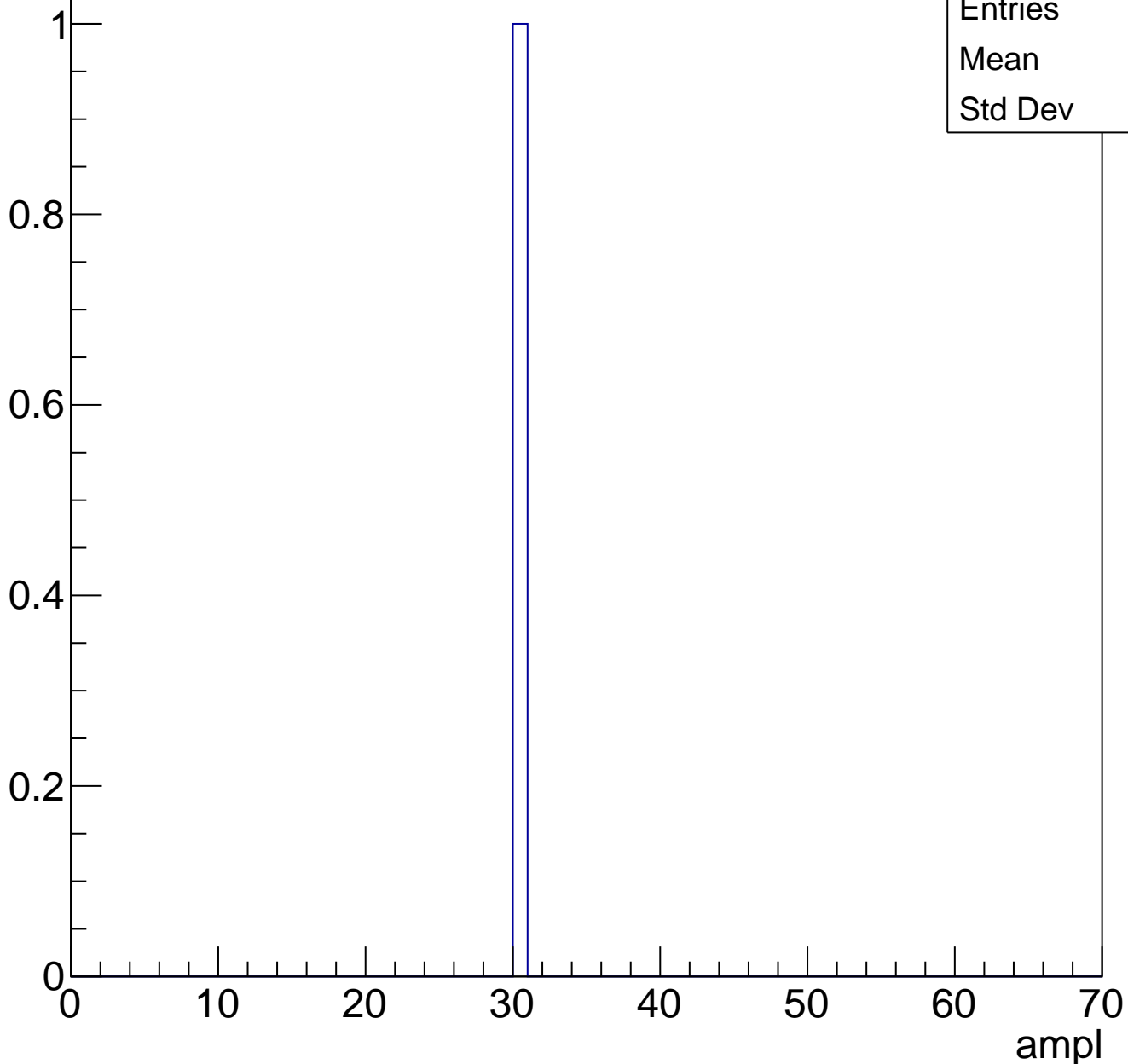
B0L100S, U5-ch124, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	30
Std Dev	0

Entry

ampl
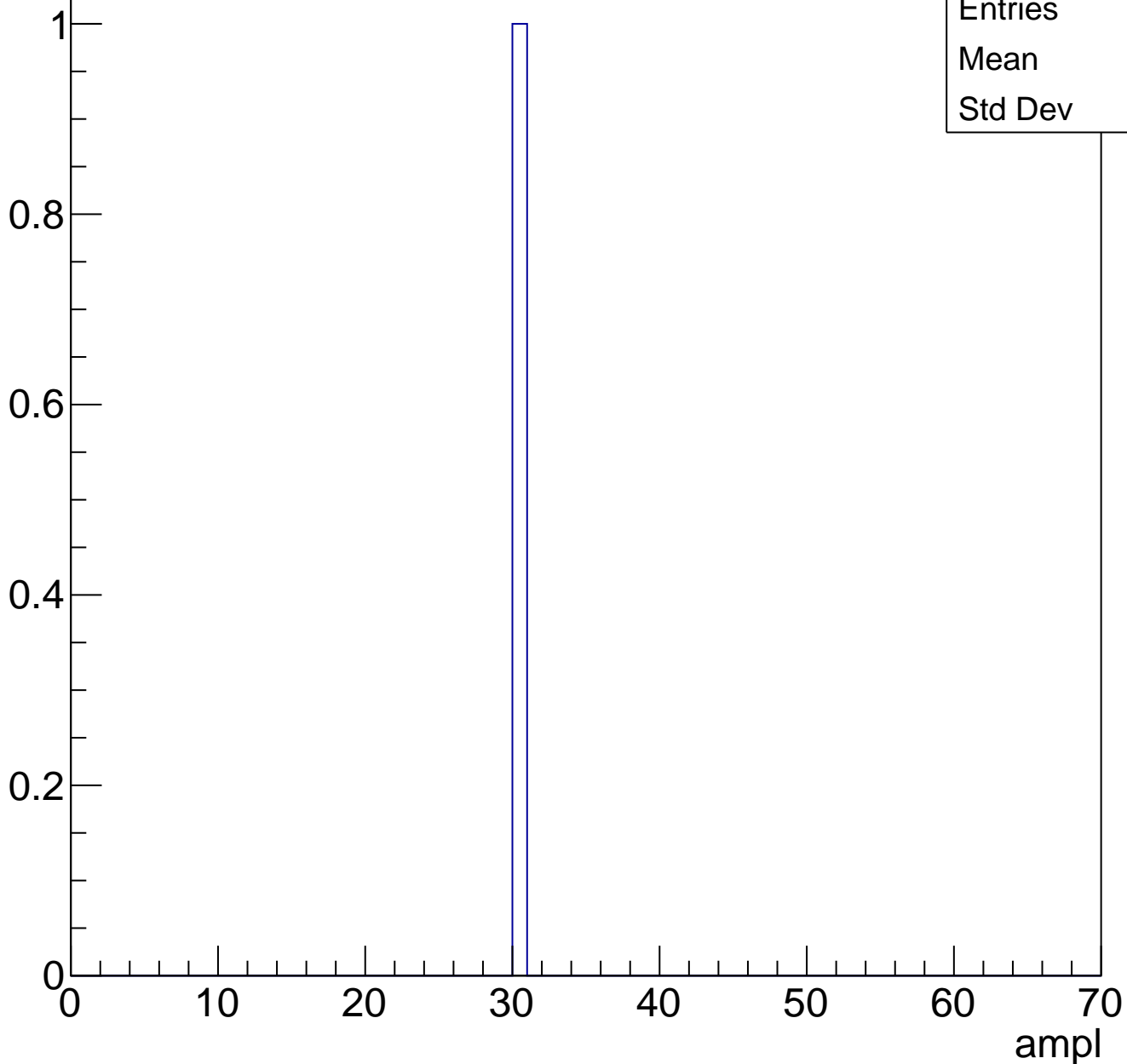
B0L100S, U5-ch124, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch124, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch125, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entry

Entries	1
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

ampl

B0L100S, U5-ch125, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch125, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch125, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch125, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch125, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

ampl

B0L100S, U5-ch125, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch125, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch126, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch126, adc1

calib_packv5_042523_0143.root, FC#6, port A1

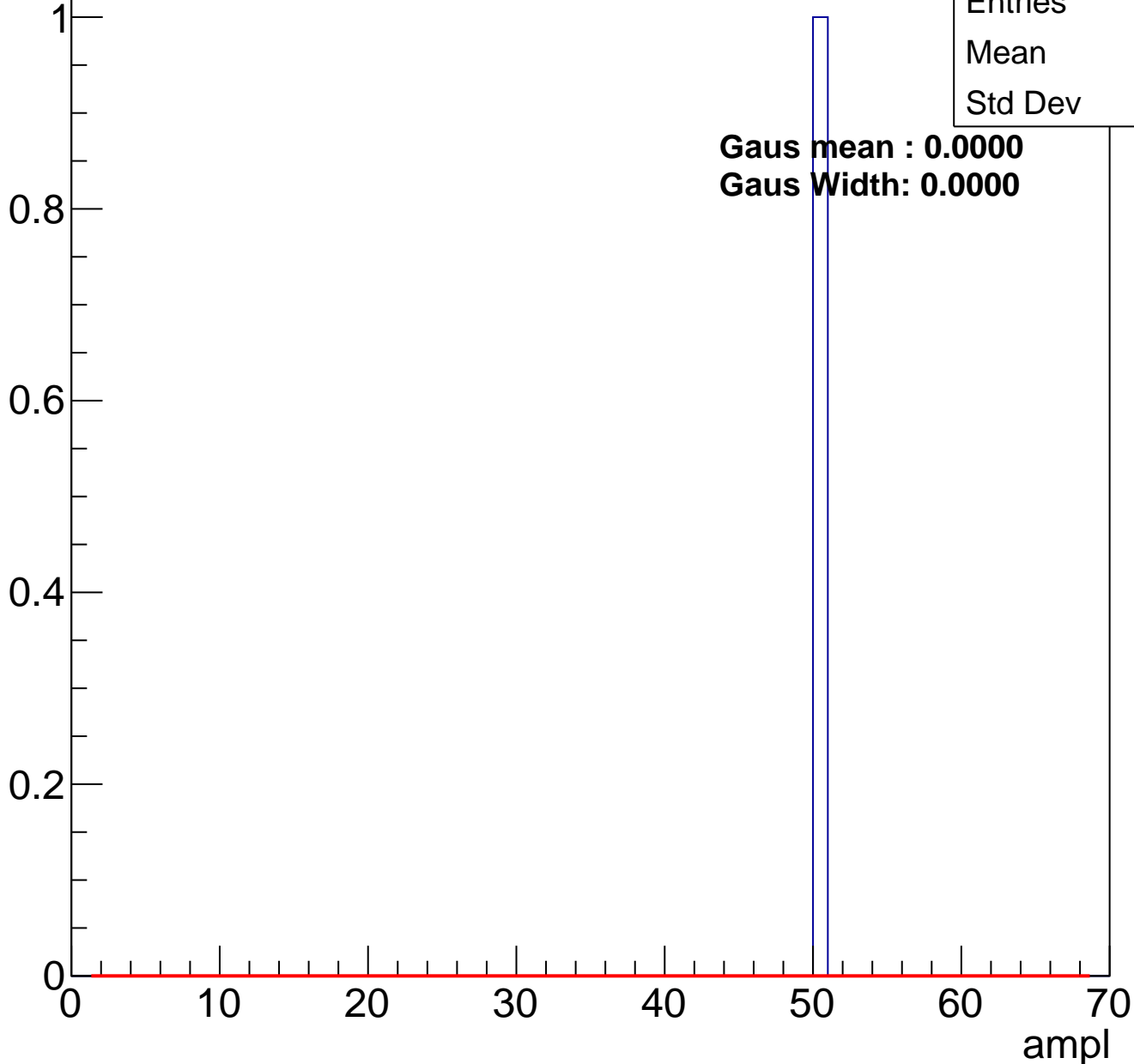|	
Entries	1
Mean	51
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
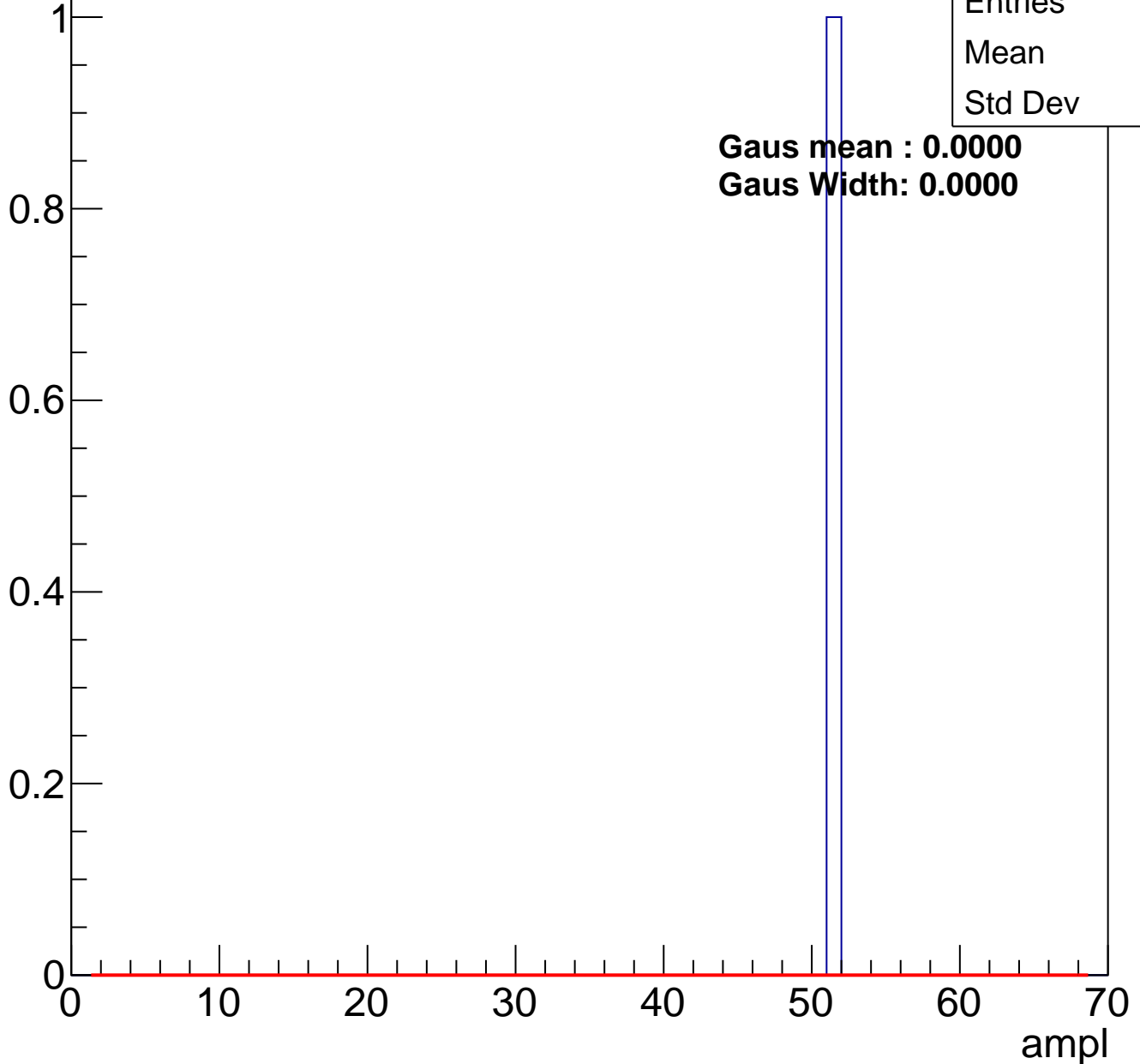
B0L100S, U5-ch126, adc2

calib_packv5_042523_0143.root, FC#6, port A1
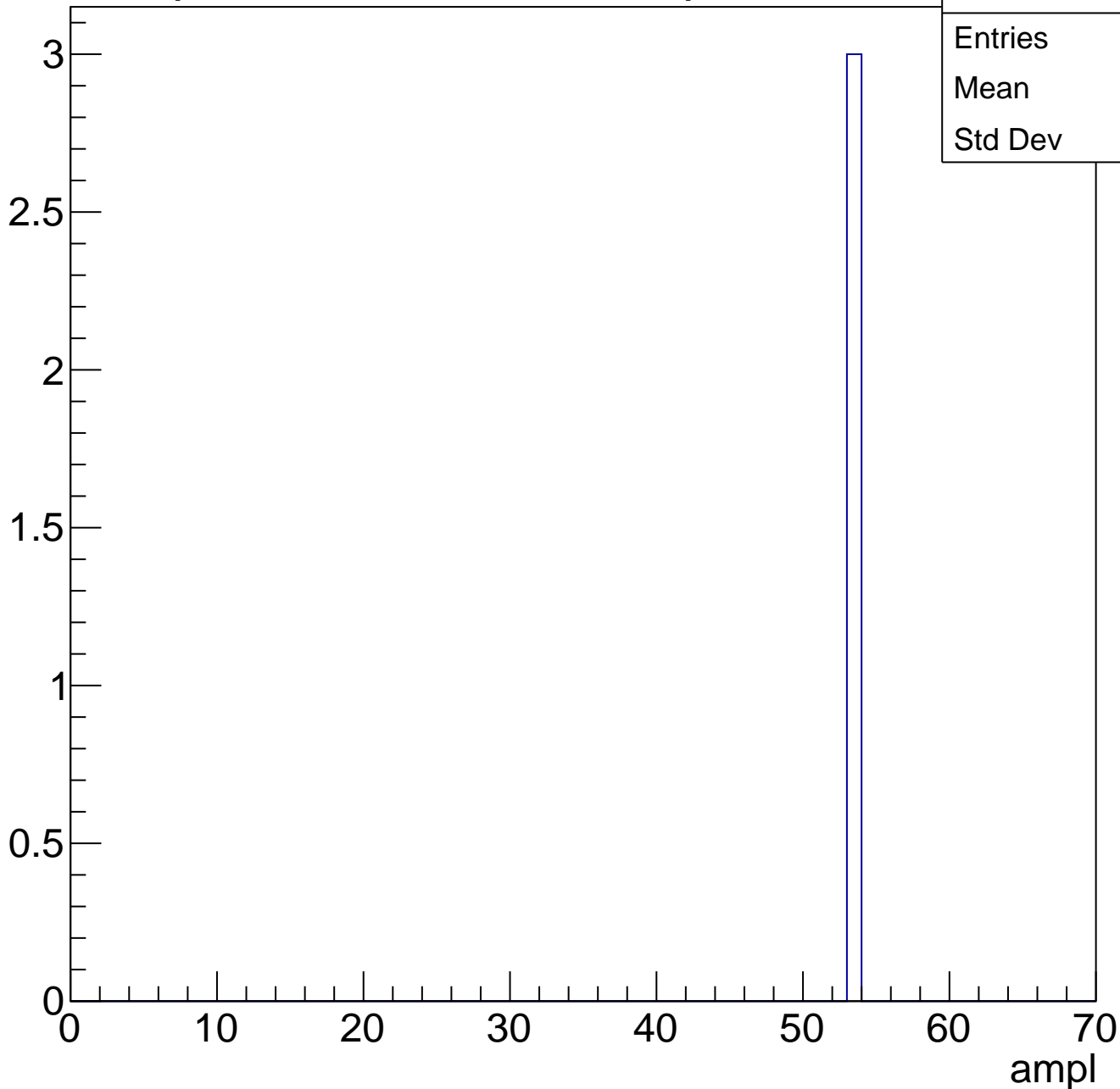
Entries	1
Mean	50
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

B0L100S, U5-ch126, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	3
Mean	53
Std Dev	0

Entry

ampl

B0L100S, U5-ch126, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch126, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch126, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch126, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch127, adc0

calib_packv5_042523_0143.root, FC#6, port A1

Entries	1
Mean	60
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl
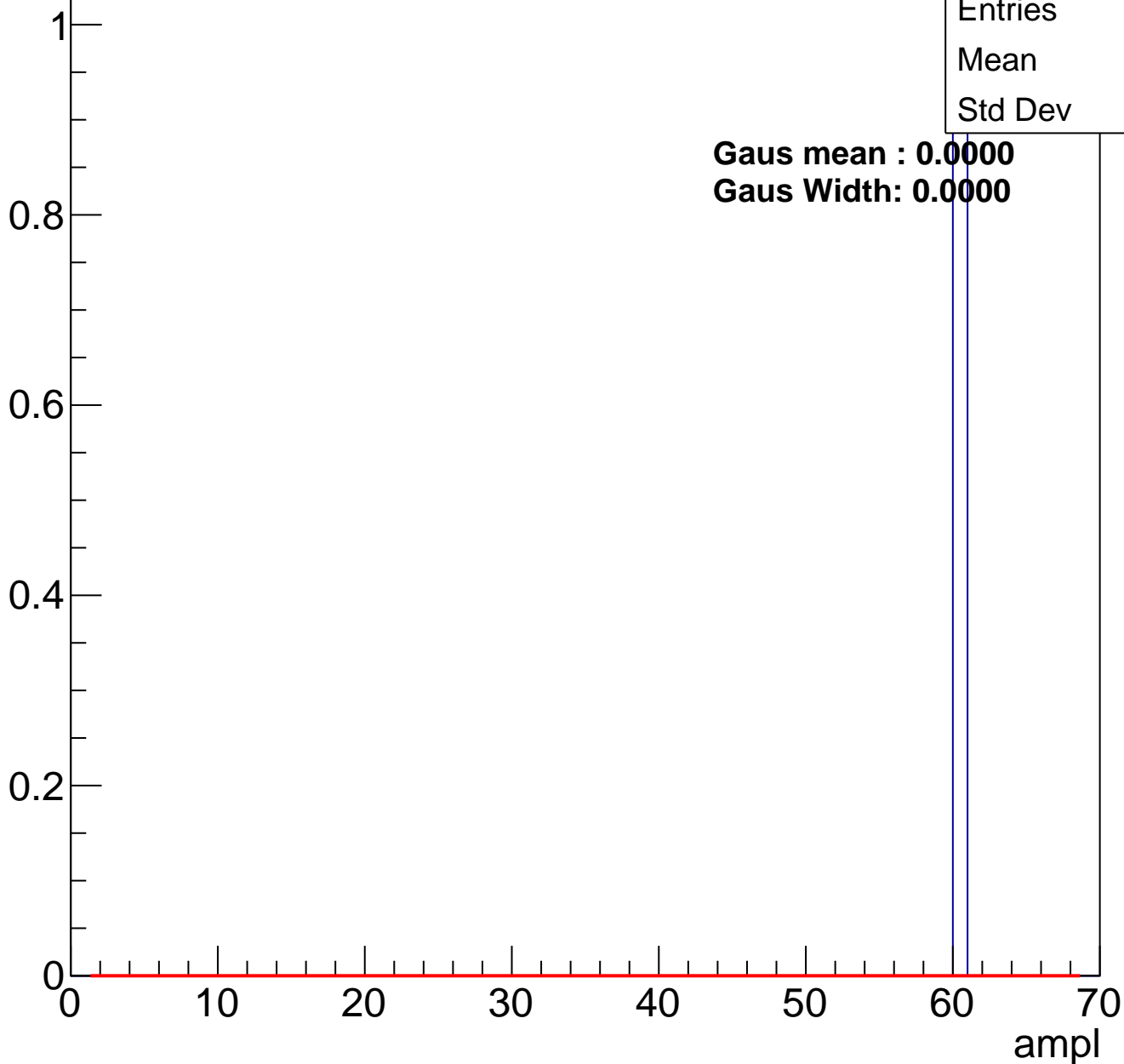
B0L100S, U5-ch127, adc1

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch127, adc2

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Gaus mean : 0.0000
Gaus Width: 0.0000

Entry

ampl

B0L100S, U5-ch127, adc3

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch127, adc4

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch127, adc5

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch127, adc6

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl

B0L100S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

B0L100S, U5-ch127, adc7

calib_packv5_042523_0143.root, FC#6, port A1

Entries	0
Mean	0
Std Dev	0

Entry

ampl