



# B1L003S, U13-ch0

calib\_packv5\_042523\_0143.root, FC#13, port D2

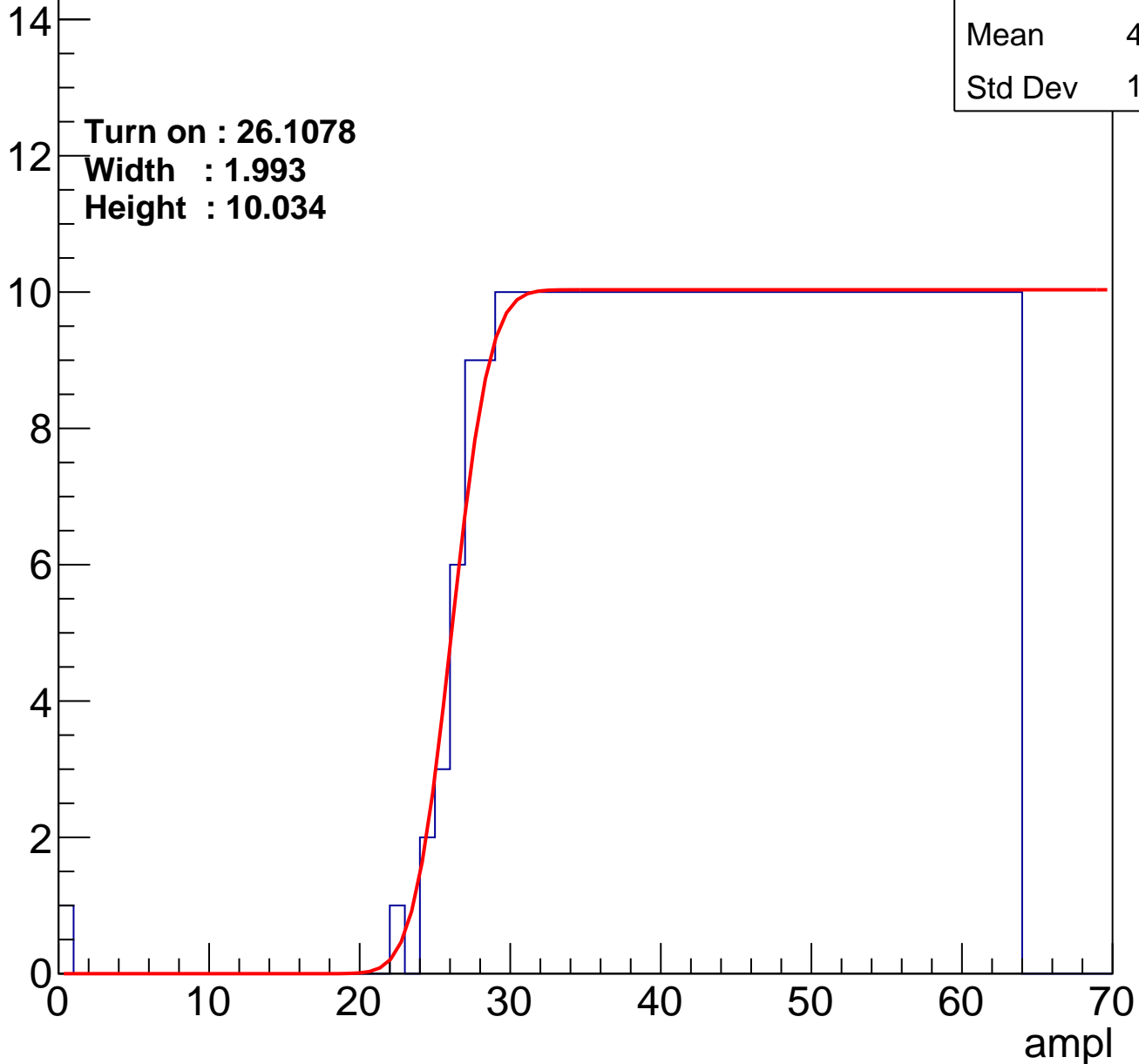
|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 44.35 |
| Std Dev | 11.25 |

**Turn on : 26.1078**

**Width : 1.993**

**Height : 10.034**

Entry



# B1L003S, U13-ch1

calib\_packv5\_042523\_0143.root, FC#13, port D2

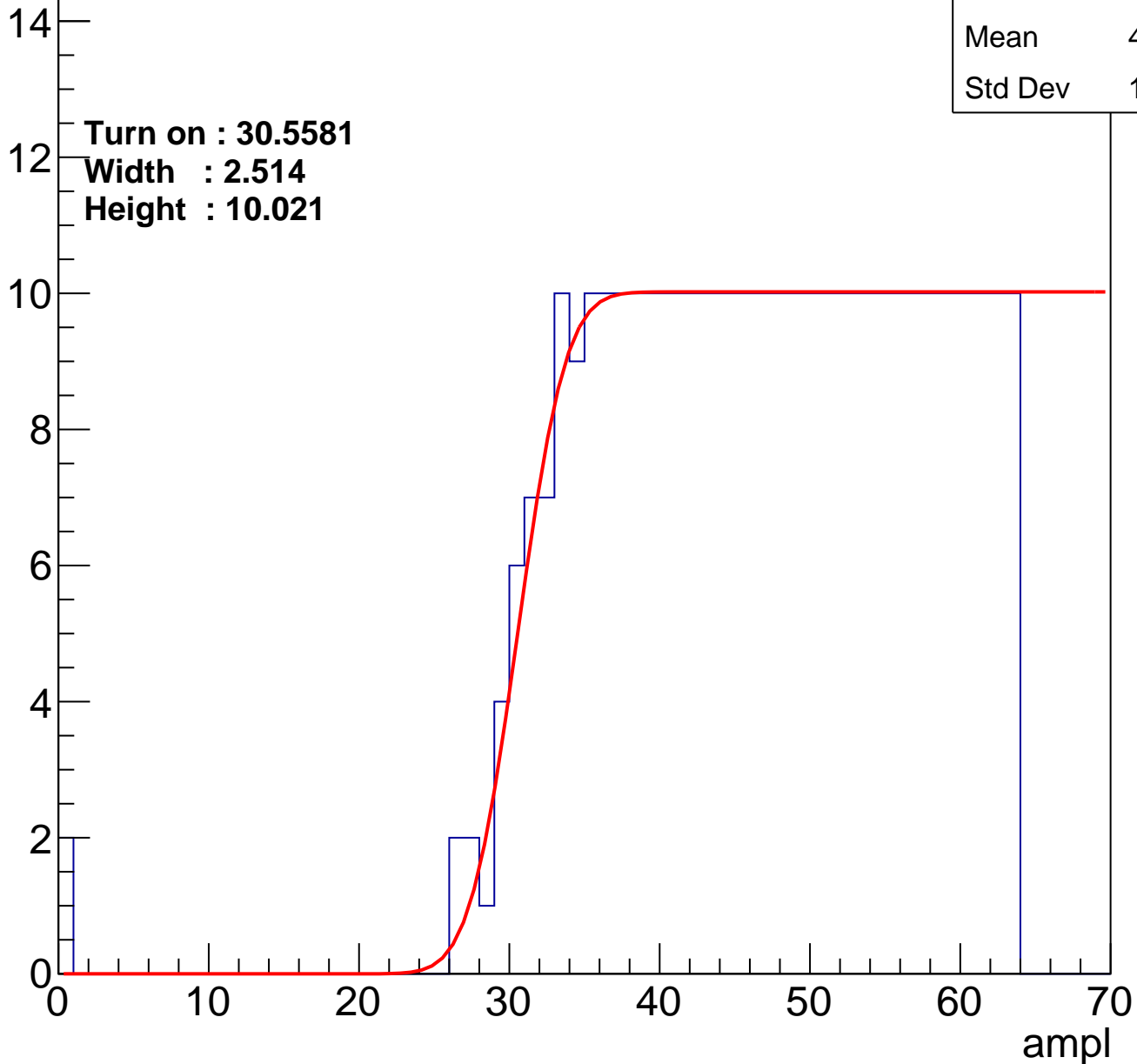
|         |       |
|---------|-------|
| Entries | 340   |
| Mean    | 46.23 |
| Std Dev | 10.52 |

Turn on : 30.5581

Width : 2.514

Height : 10.021

Entry



# B1L003S, U13-ch2

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.31 |
| Std Dev | 10.91 |

**Turn on : 28.0126**

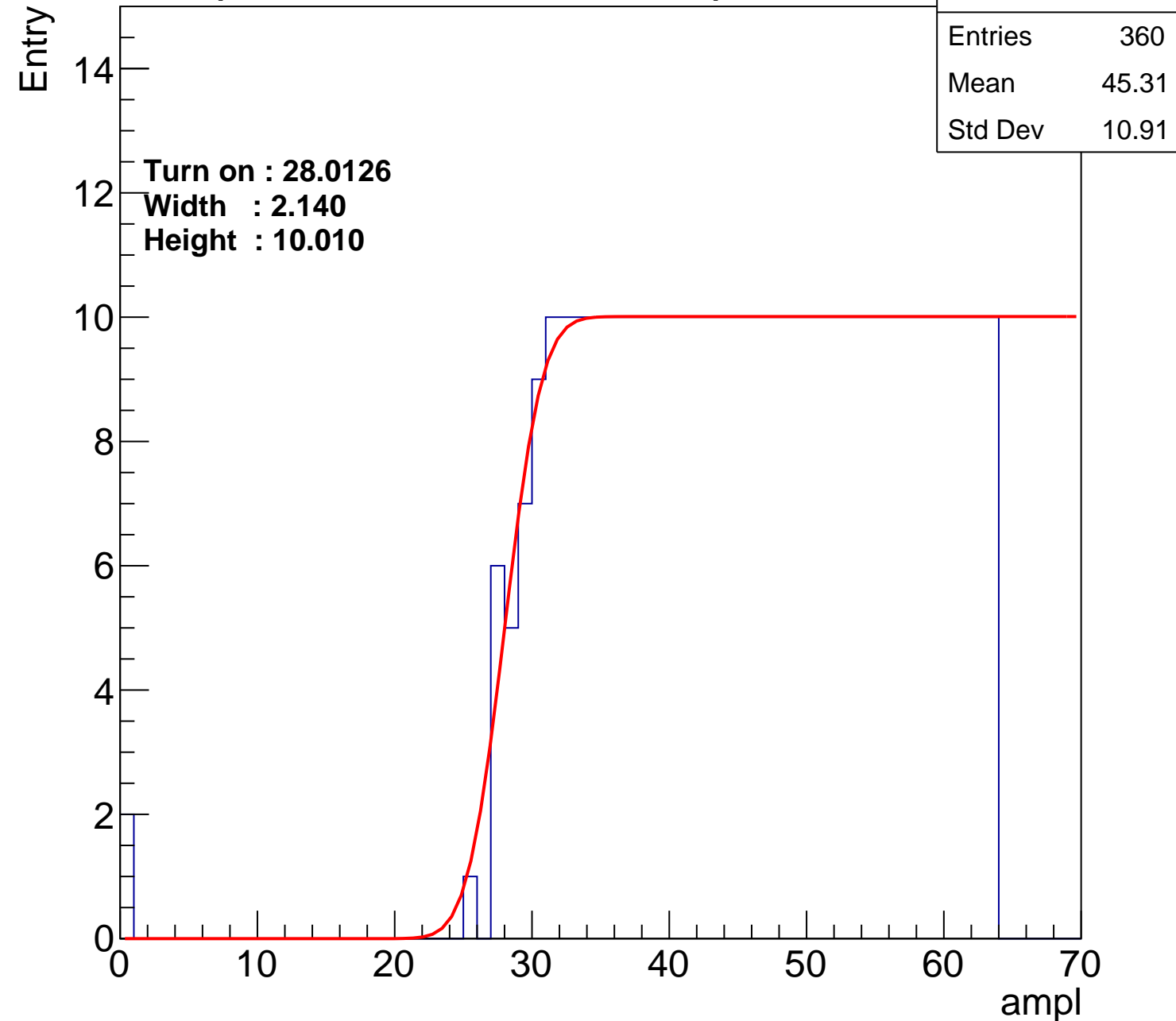
**Width : 2.140**

**Height : 10.010**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch3

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.81 |
| Std Dev | 10.53 |

Turn on : 29.6156

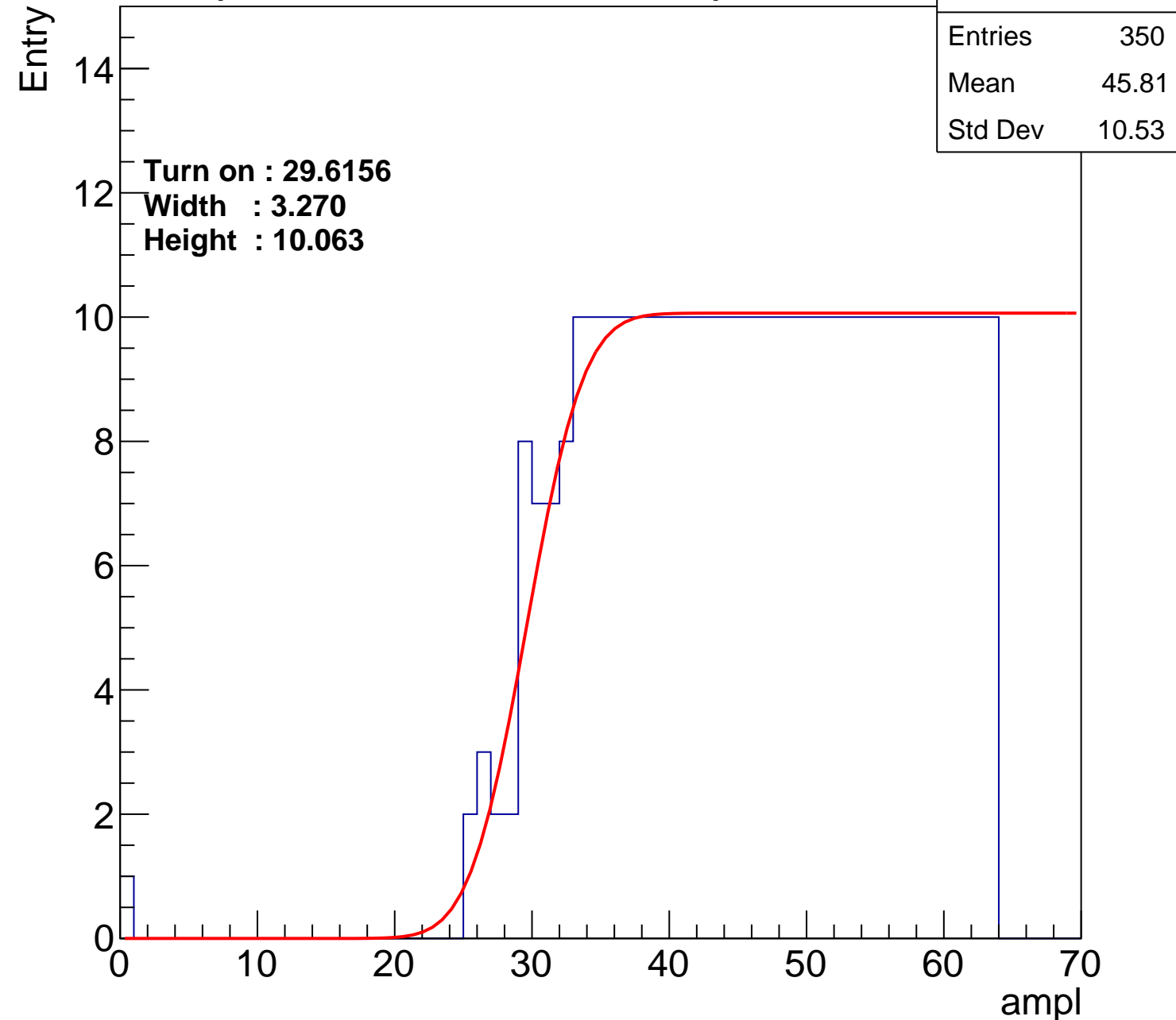
Width : 3.270

Height : 10.063

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch4

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 356   |
| Mean    | 45.22 |
| Std Dev | 11.54 |

Turn on : 28.9671

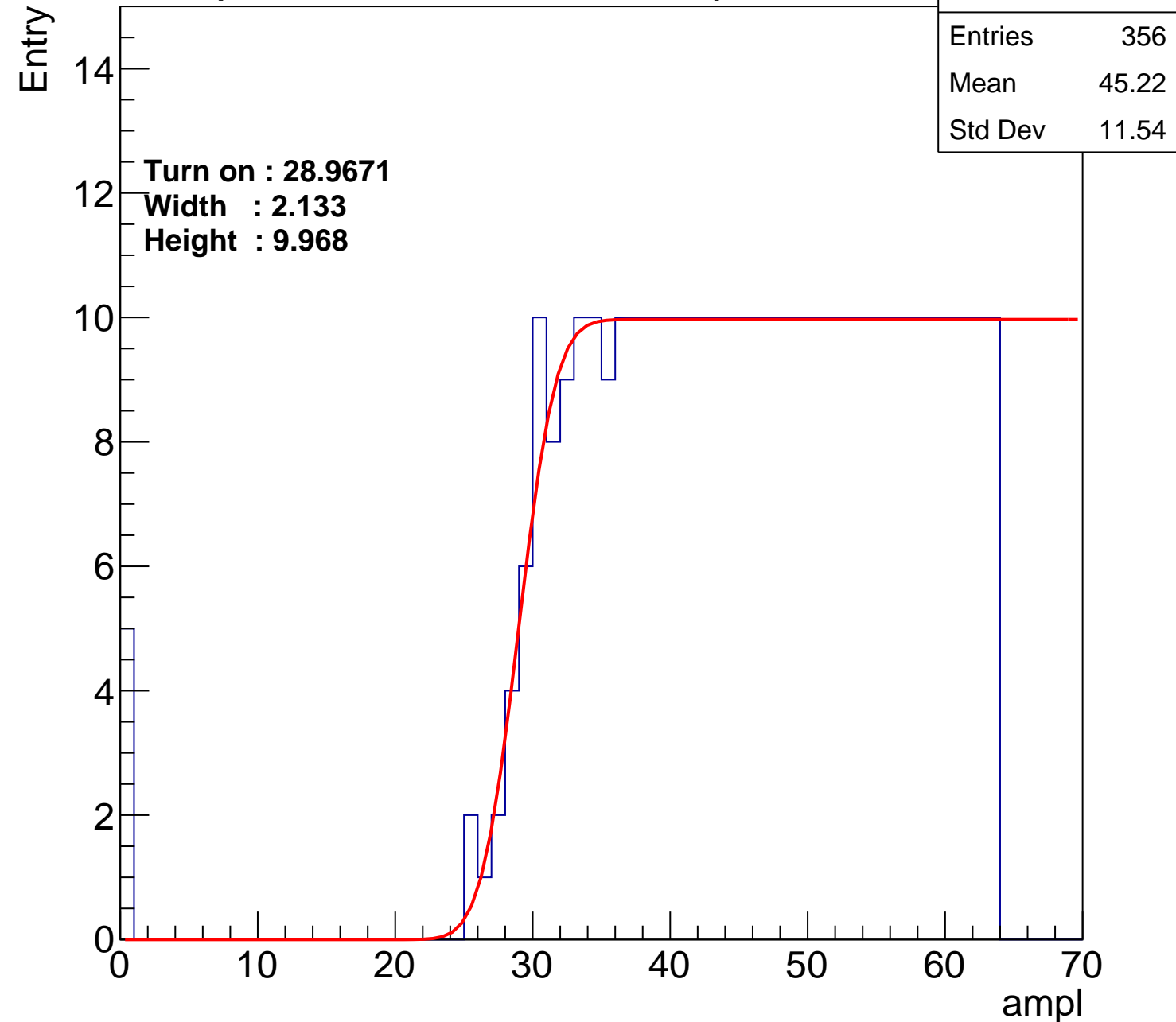
Width : 2.133

Height : 9.968

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch5

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 45.05 |
| Std Dev | 10.91 |

**Turn on : 27.5198**

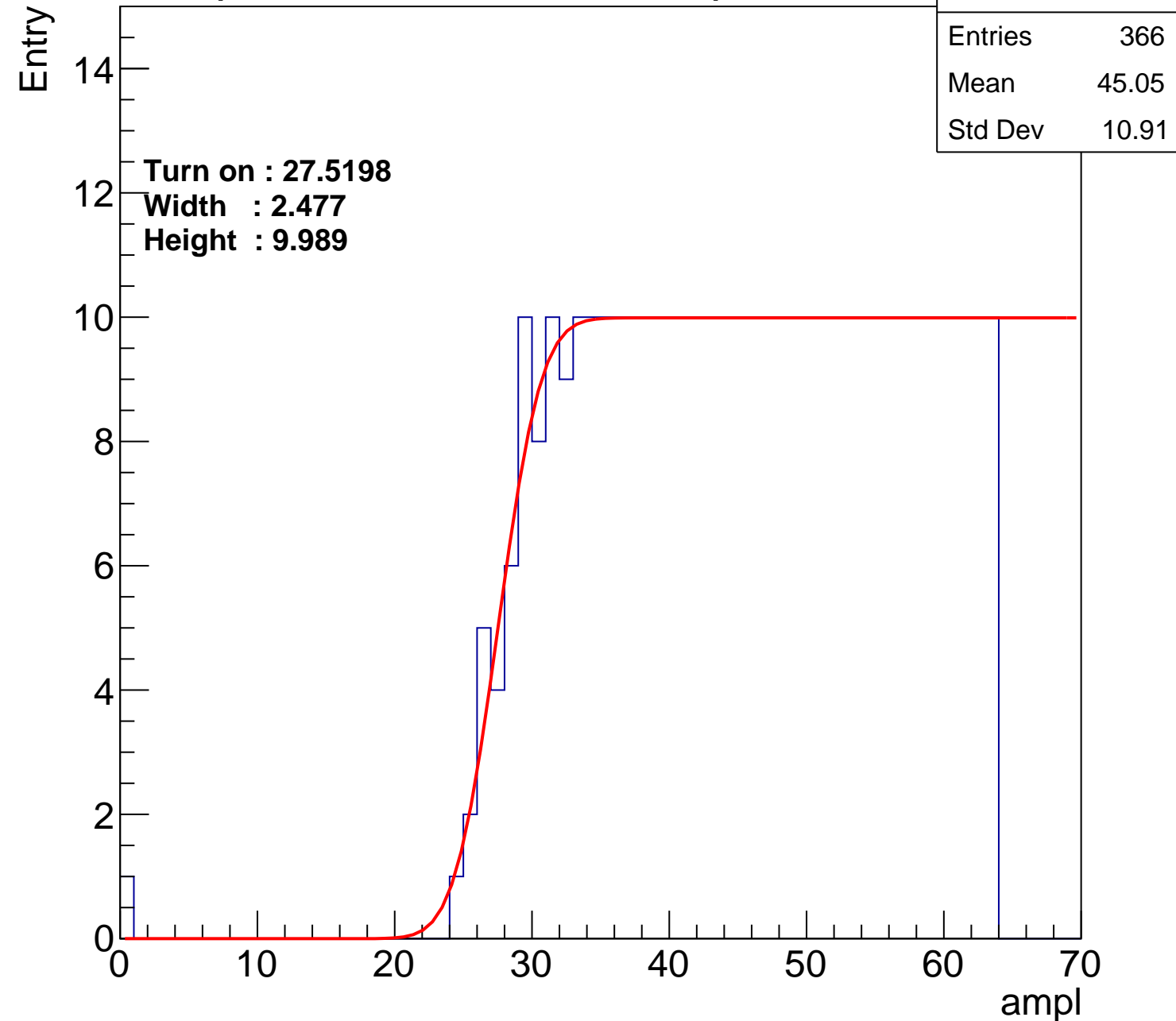
**Width : 2.477**

**Height : 9.989**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**B1L003S, U13-ch6**

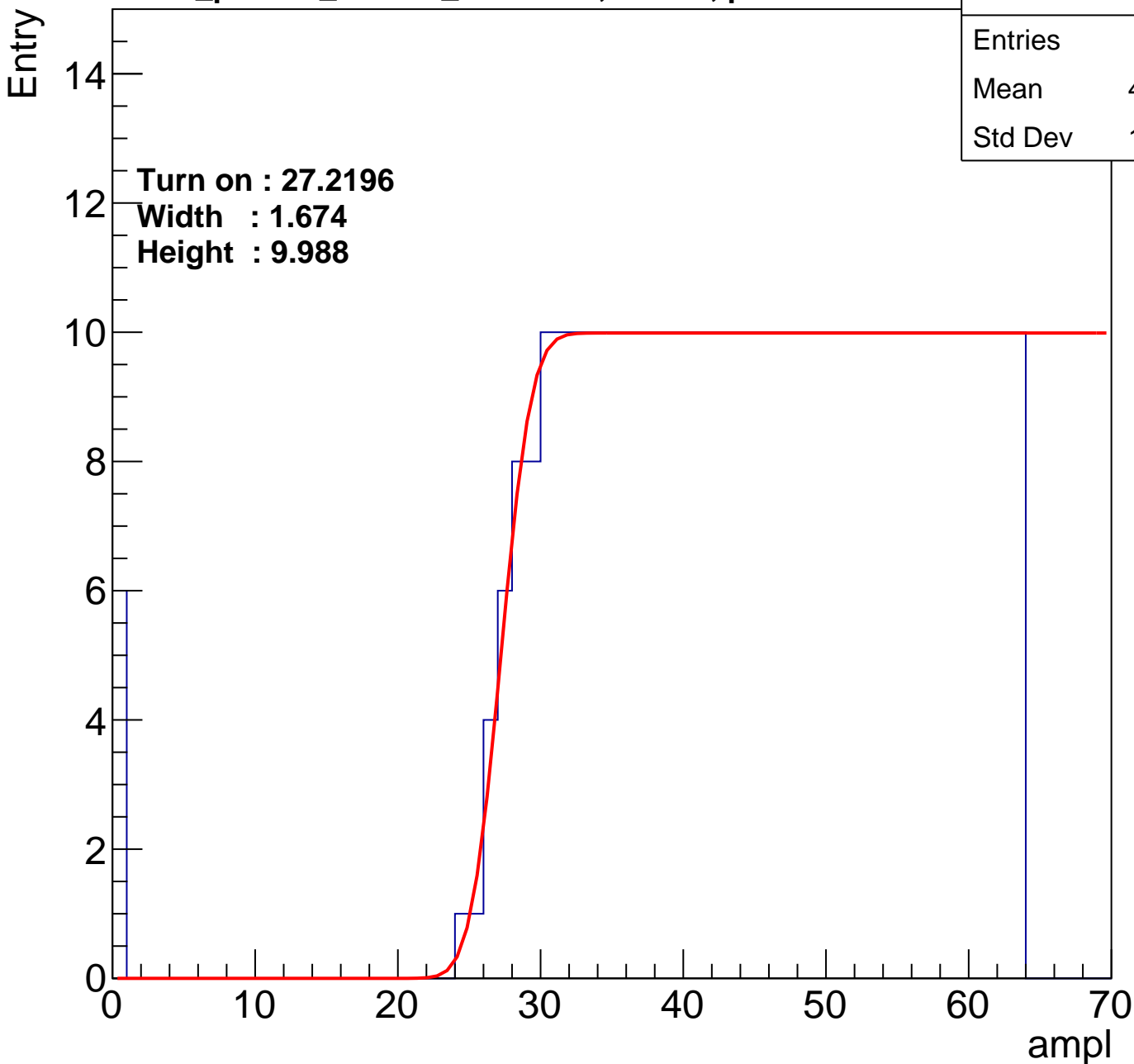
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.33 |
| Std Dev | 12.02 |

**Turn on : 27.2196**

**Width : 1.674**

**Height : 9.988**





# B1L003S, U13-ch7

calib\_packv5\_042523\_0143.root, FC#13, port D2

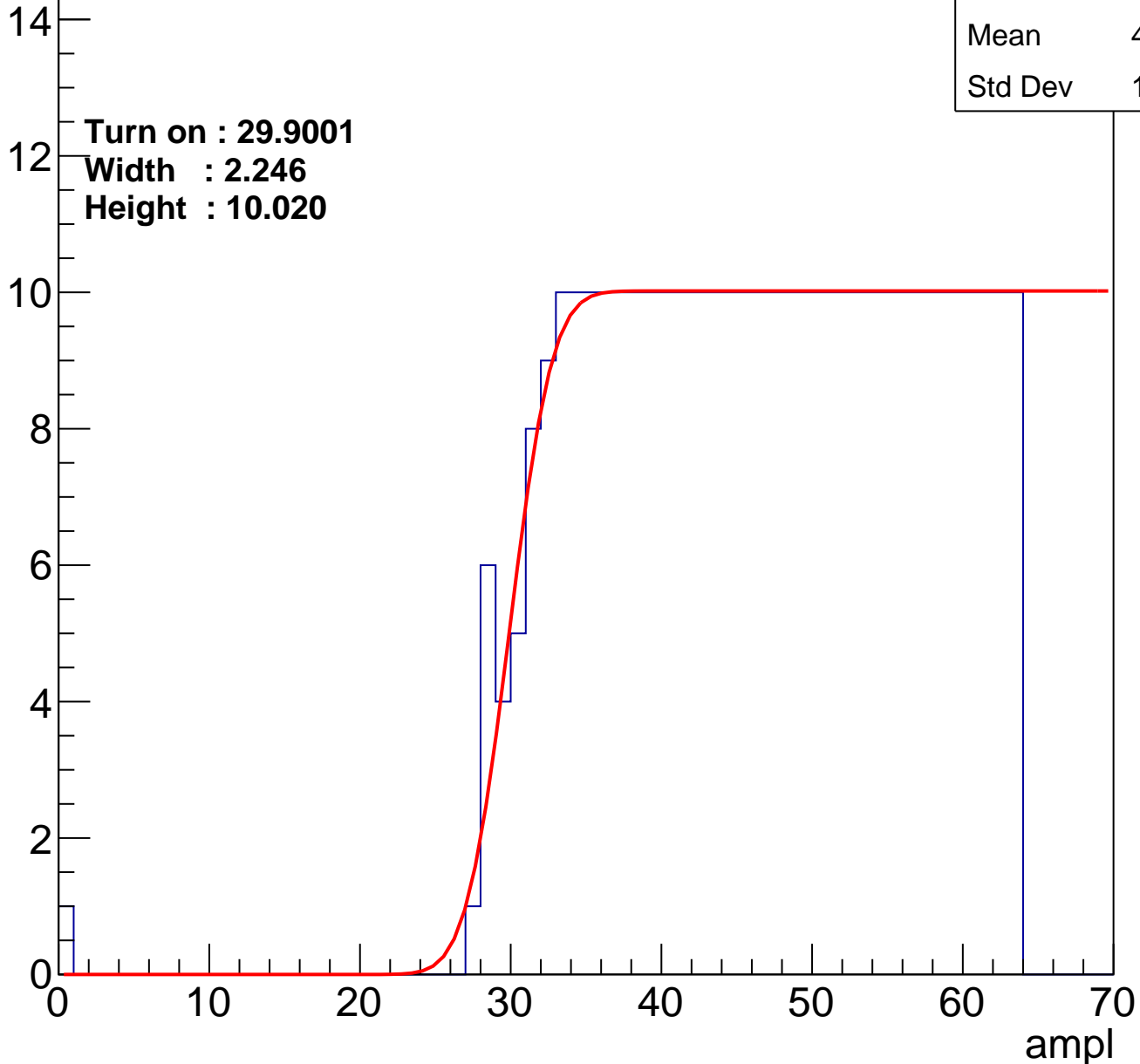
|         |       |
|---------|-------|
| Entries | 344   |
| Mean    | 46.15 |
| Std Dev | 10.29 |

Turn on : 29.9001

Width : 2.246

Height : 10.020

Entry



# B1L003S, U13-ch8

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.19 |
| Std Dev | 11.04 |

Turn on : 28.3611

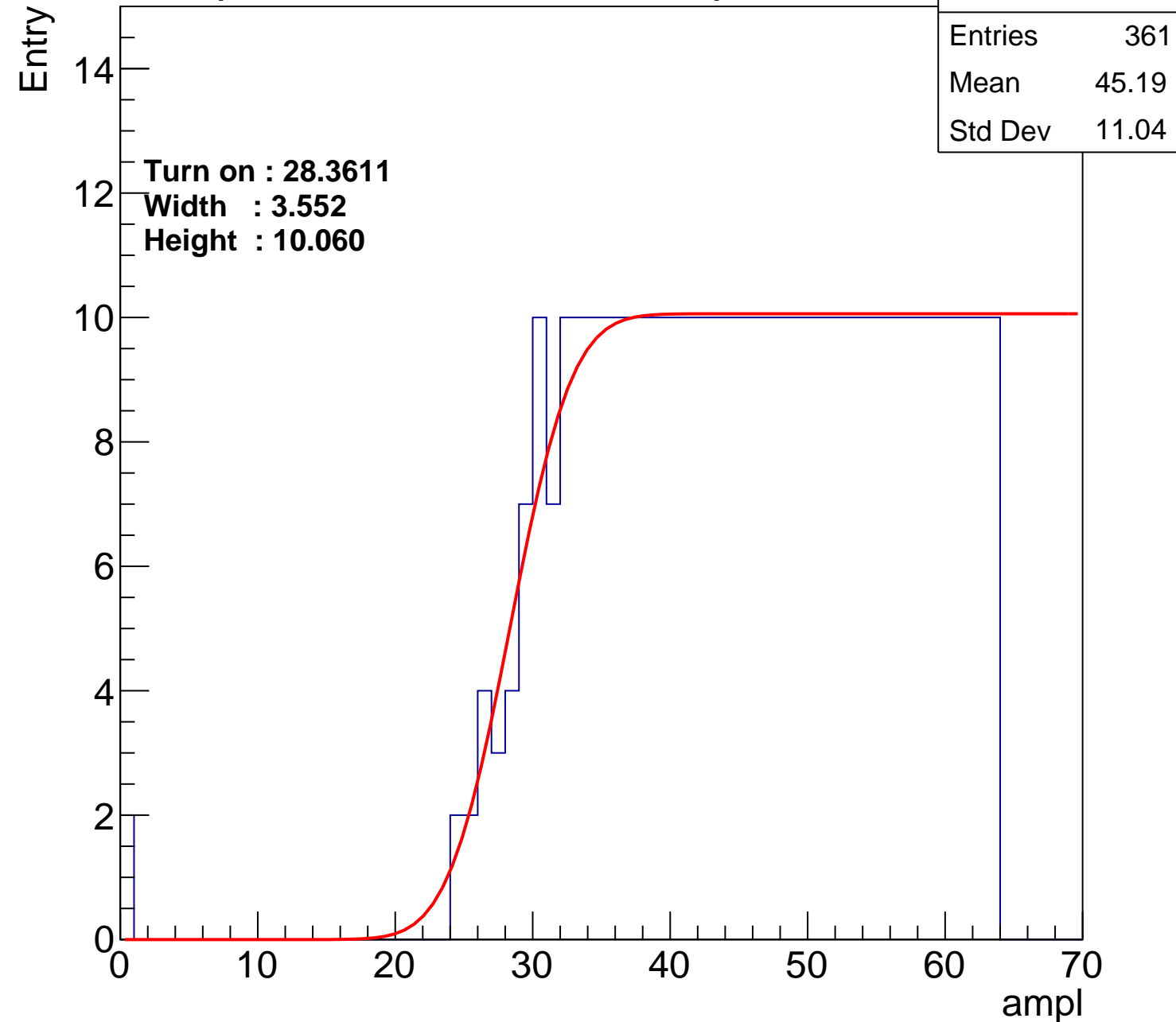
Width : 3.552

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch9

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.38 |
| Std Dev | 11.61 |

**Turn on : 26.8010**

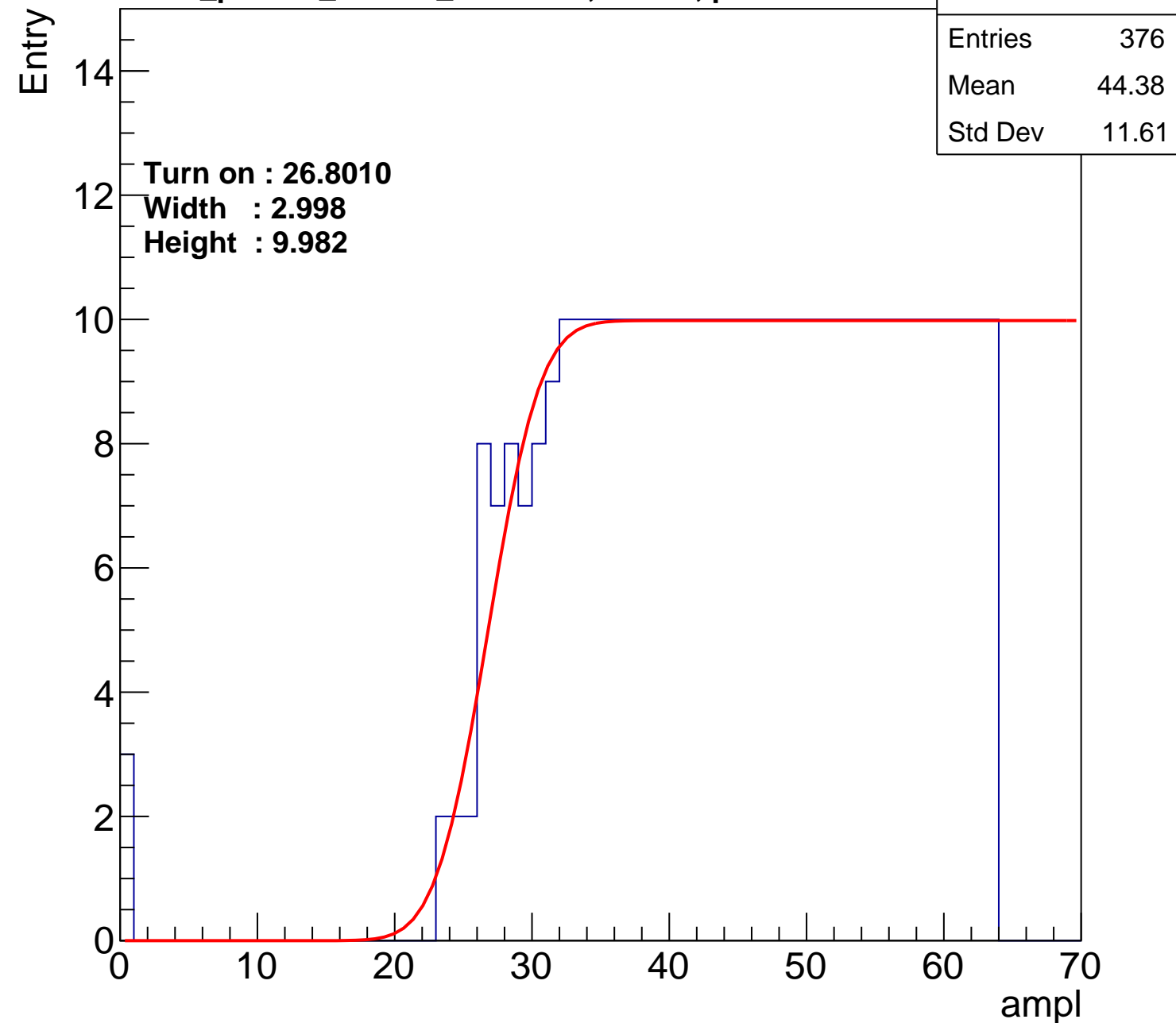
**Width : 2.998**

**Height : 9.982**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch10

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.52 |
| Std Dev | 11.55 |

Turn on : 26.7456

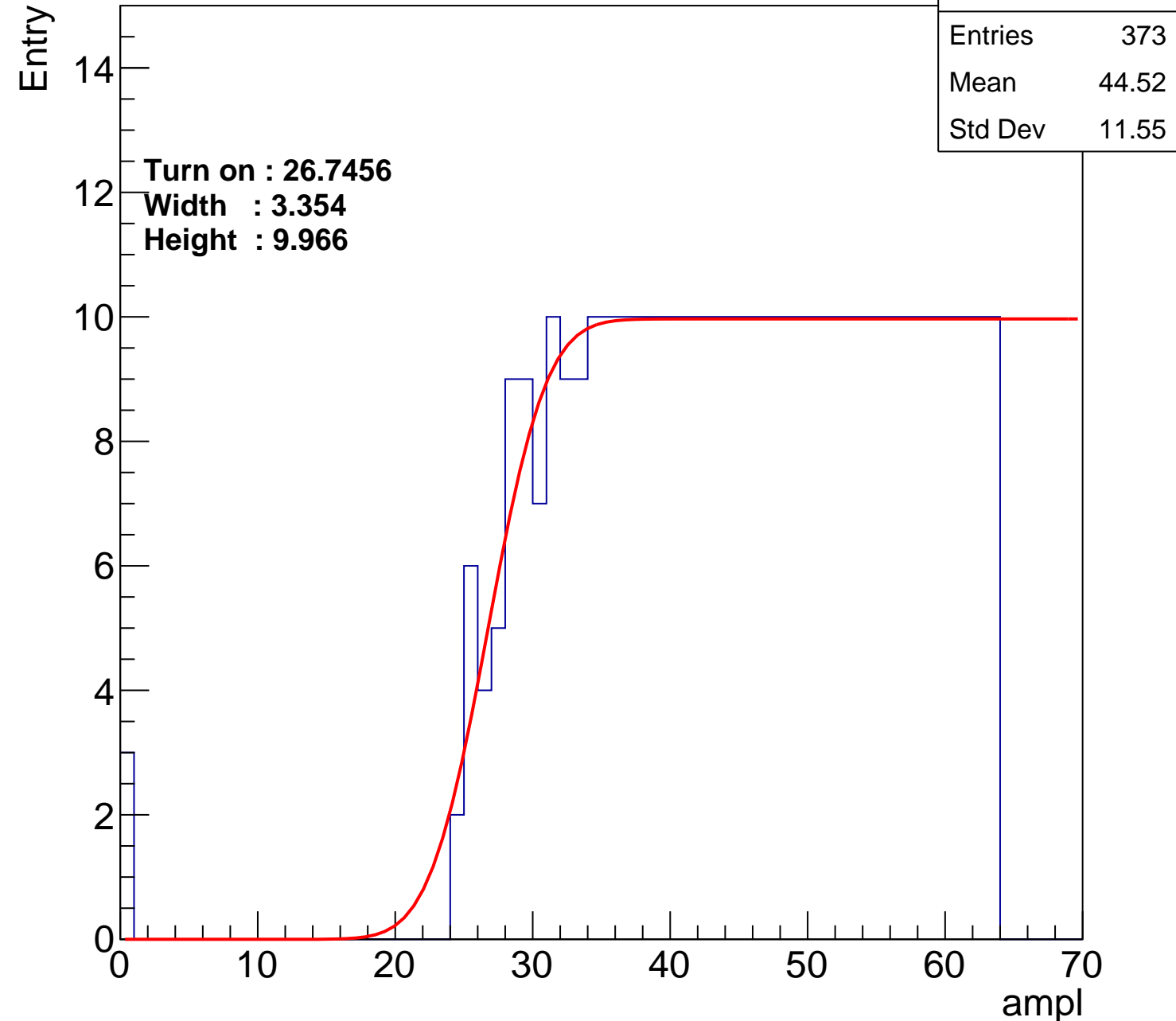
Width : 3.354

Height : 9.966

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch11

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.67 |
| Std Dev | 11.29 |

Turn on : 26.6241

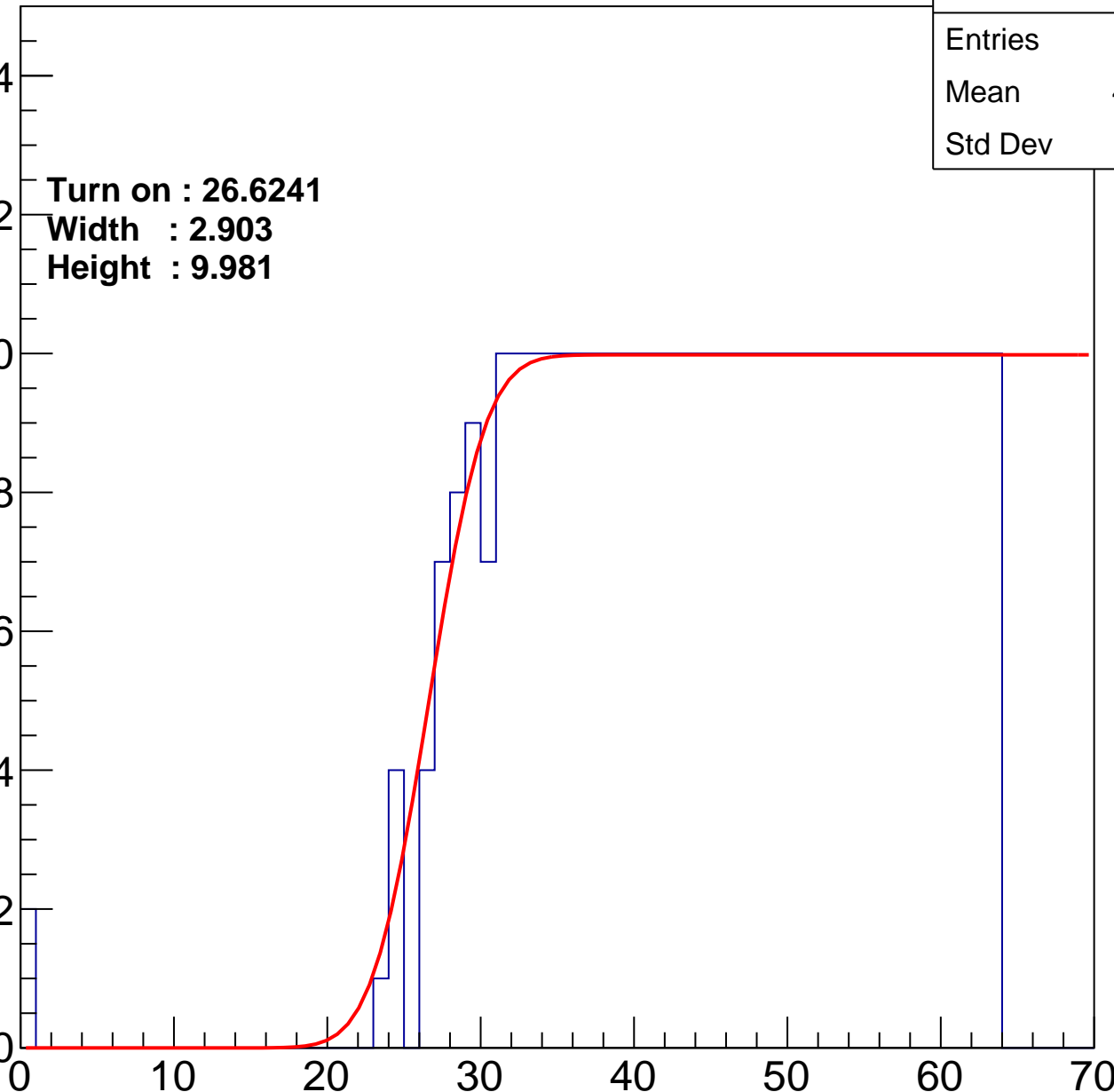
Width : 2.903

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch12

calib\_packv5\_042523\_0143.root, FC#13, port D2

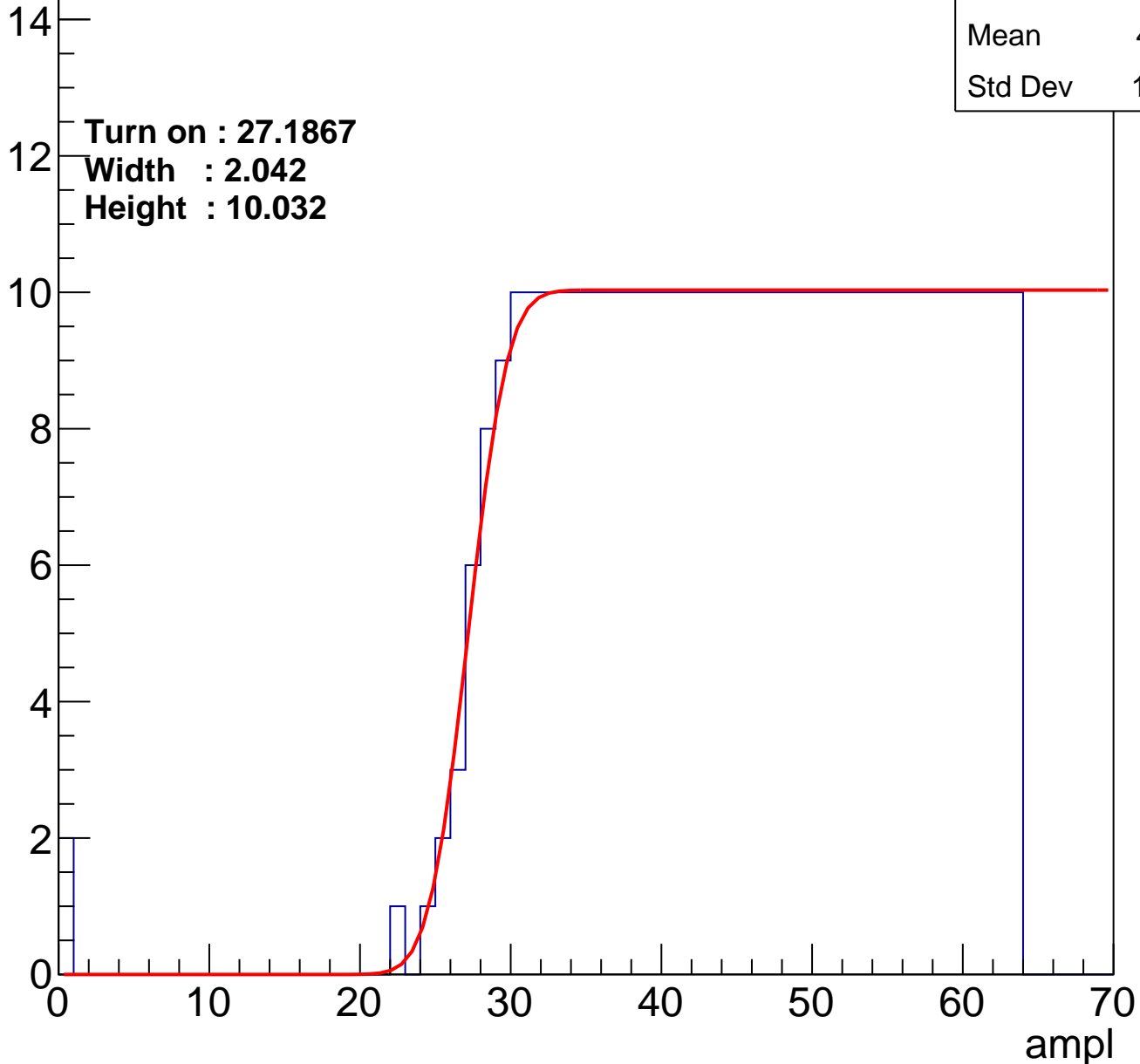
|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.71 |
| Std Dev | 11.23 |

Turn on : 27.1867

Width : 2.042

Height : 10.032

Entry



# B1L003S, U13-ch13

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.66 |
| Std Dev | 11.24 |

Turn on : 26.5976

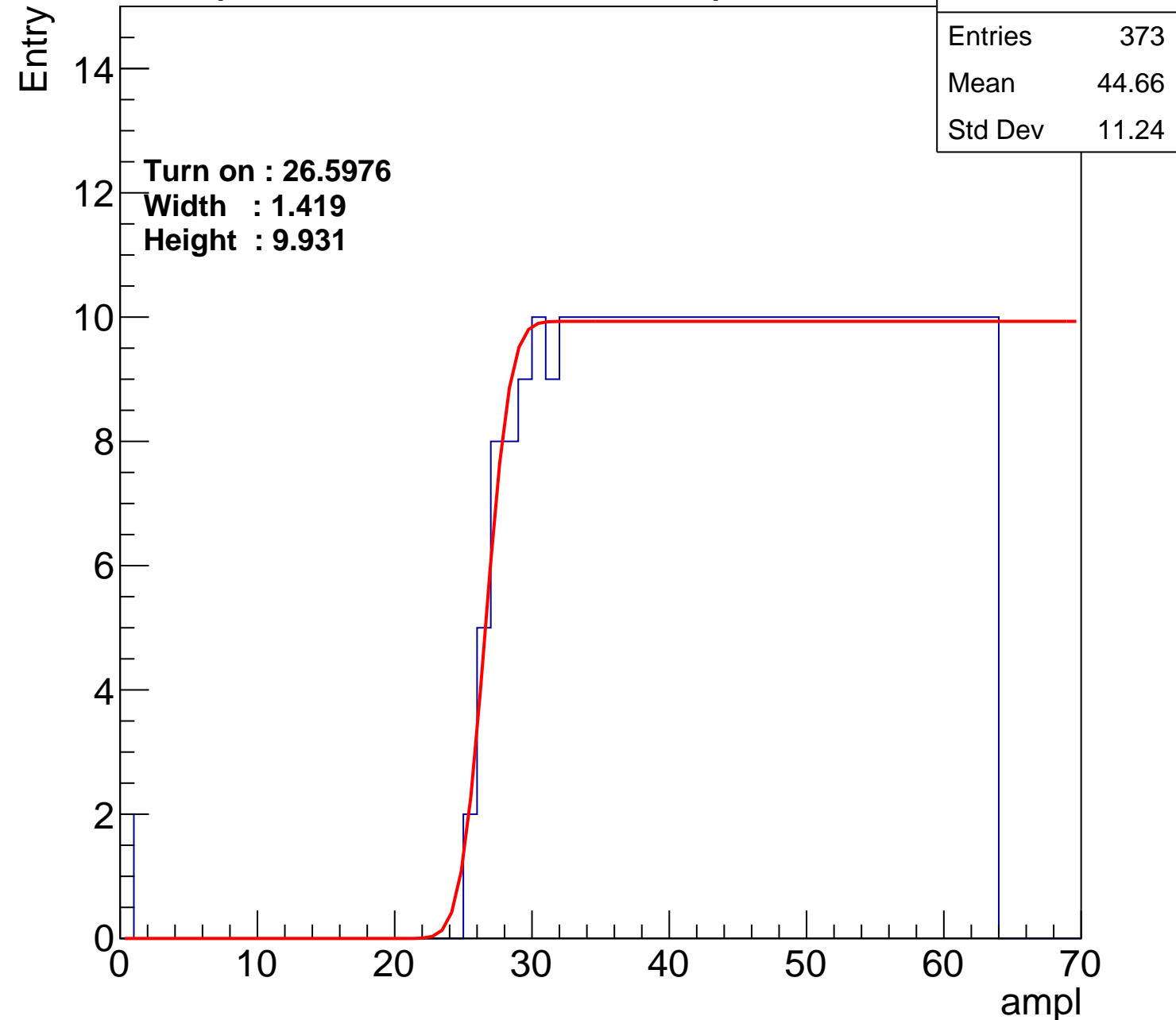
Width : 1.419

Height : 9.931

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch14

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 363   |
| Mean    | 45.17 |
| Std Dev | 10.86 |

Turn on : 27.5800

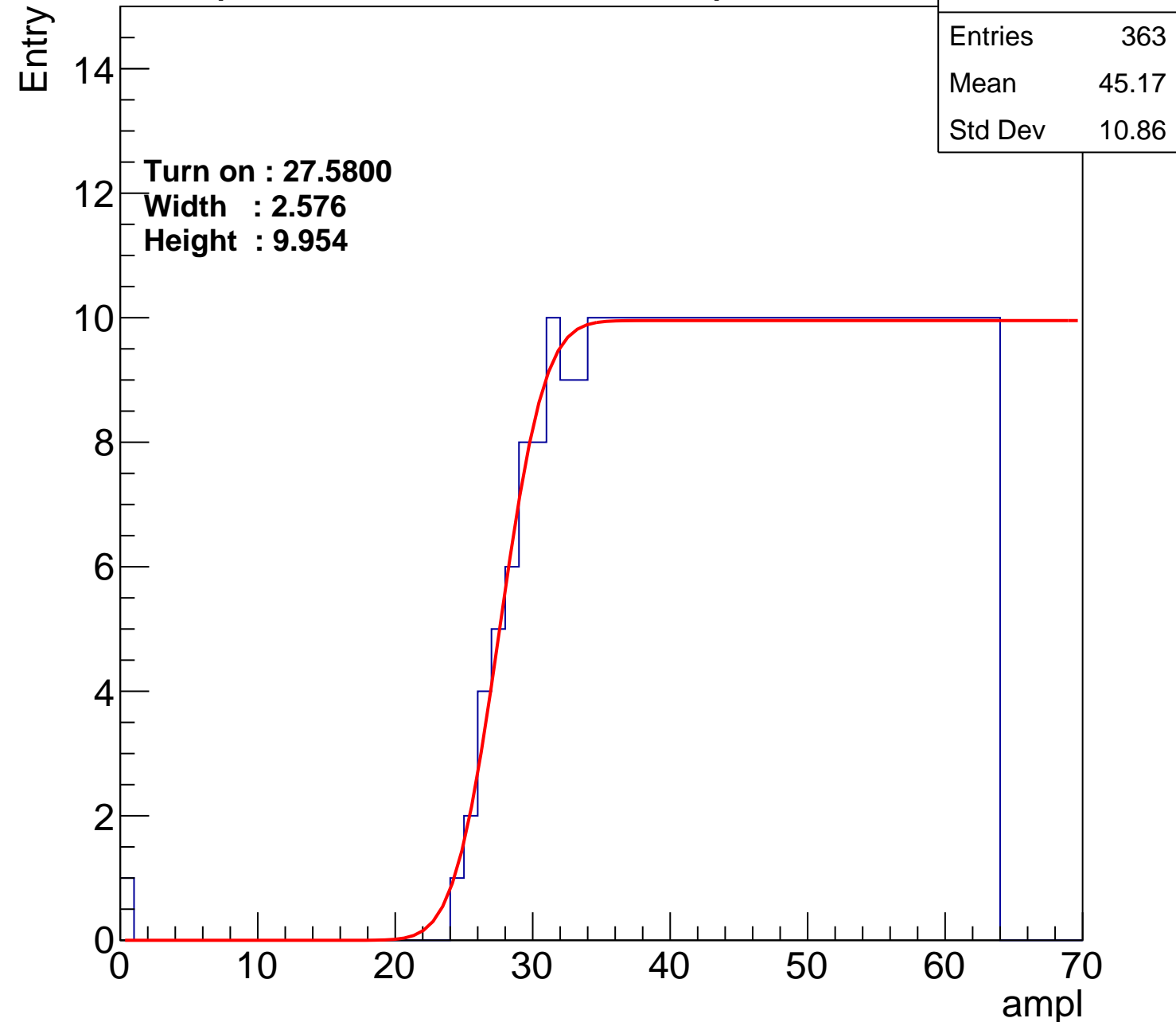
Width : 2.576

Height : 9.954

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch15

calib\_packv5\_042523\_0143.root, FC#13, port D2

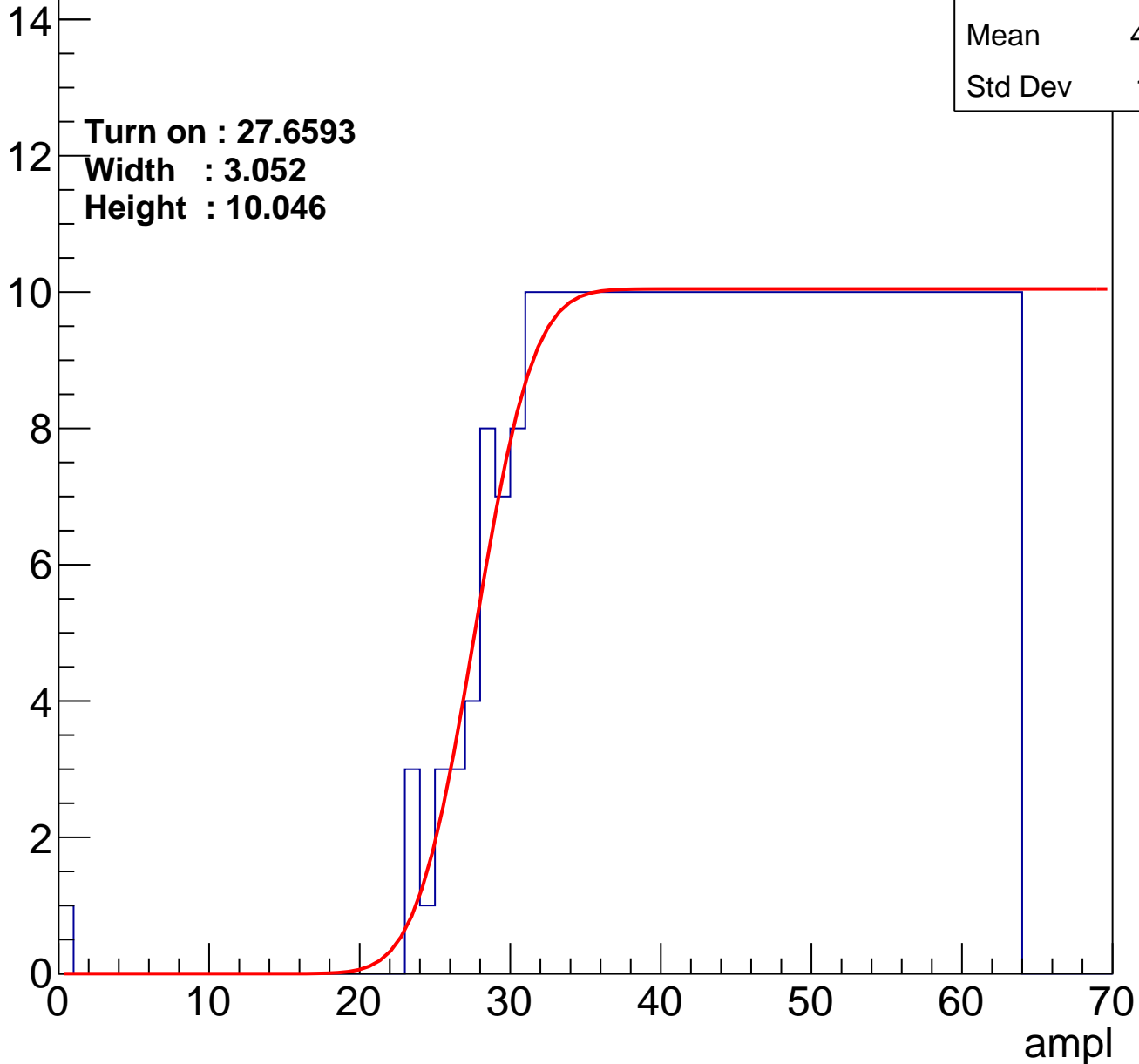
|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.92 |
| Std Dev | 11.01 |

Turn on : 27.6593

Width : 3.052

Height : 10.046

Entry



# B1L003S, U13-ch16

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 44.18 |
| Std Dev | 11.67 |

Turn on : 26.2771

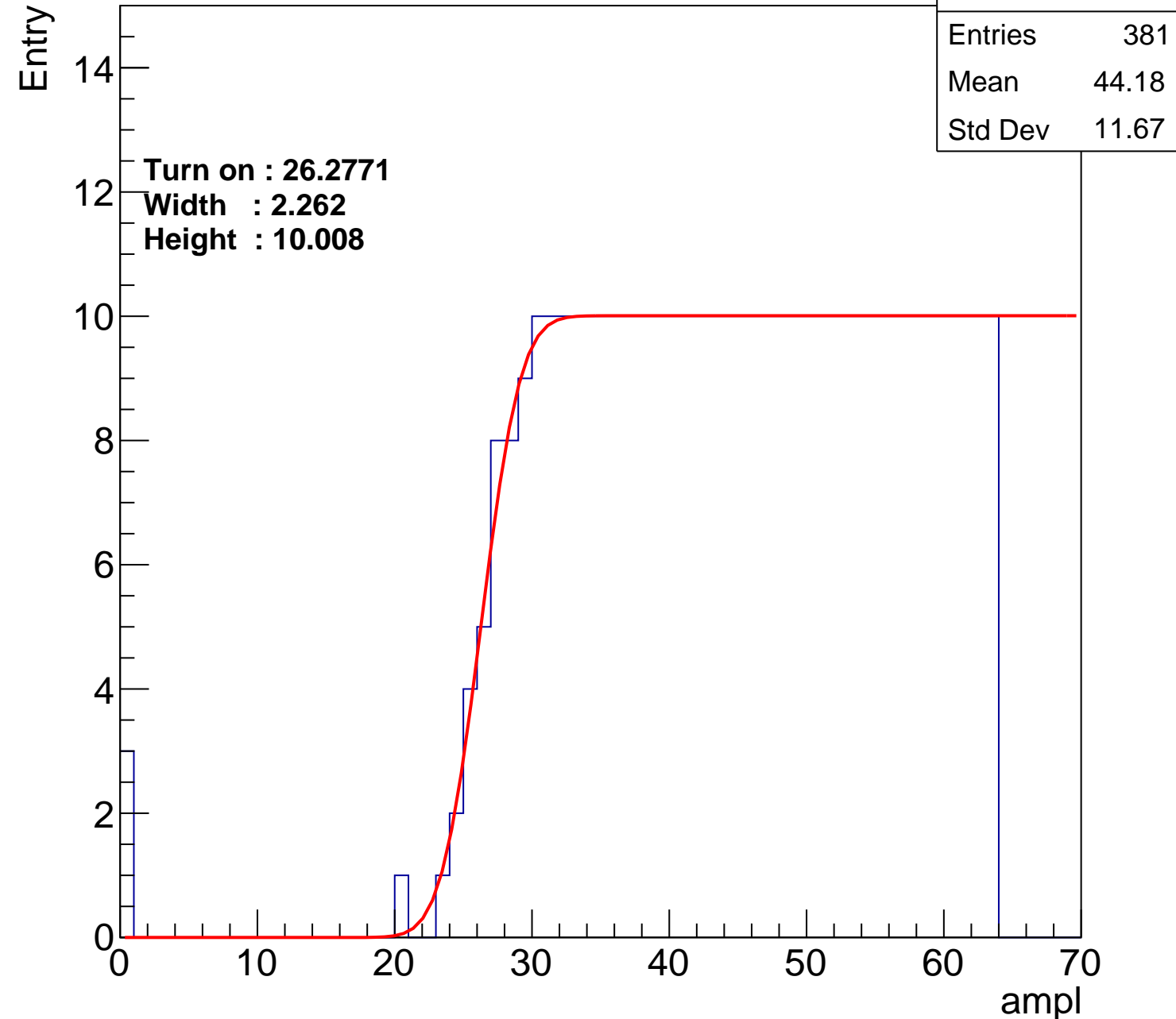
Width : 2.262

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch17

calib\_packv5\_042523\_0143.root, FC#13, port D2

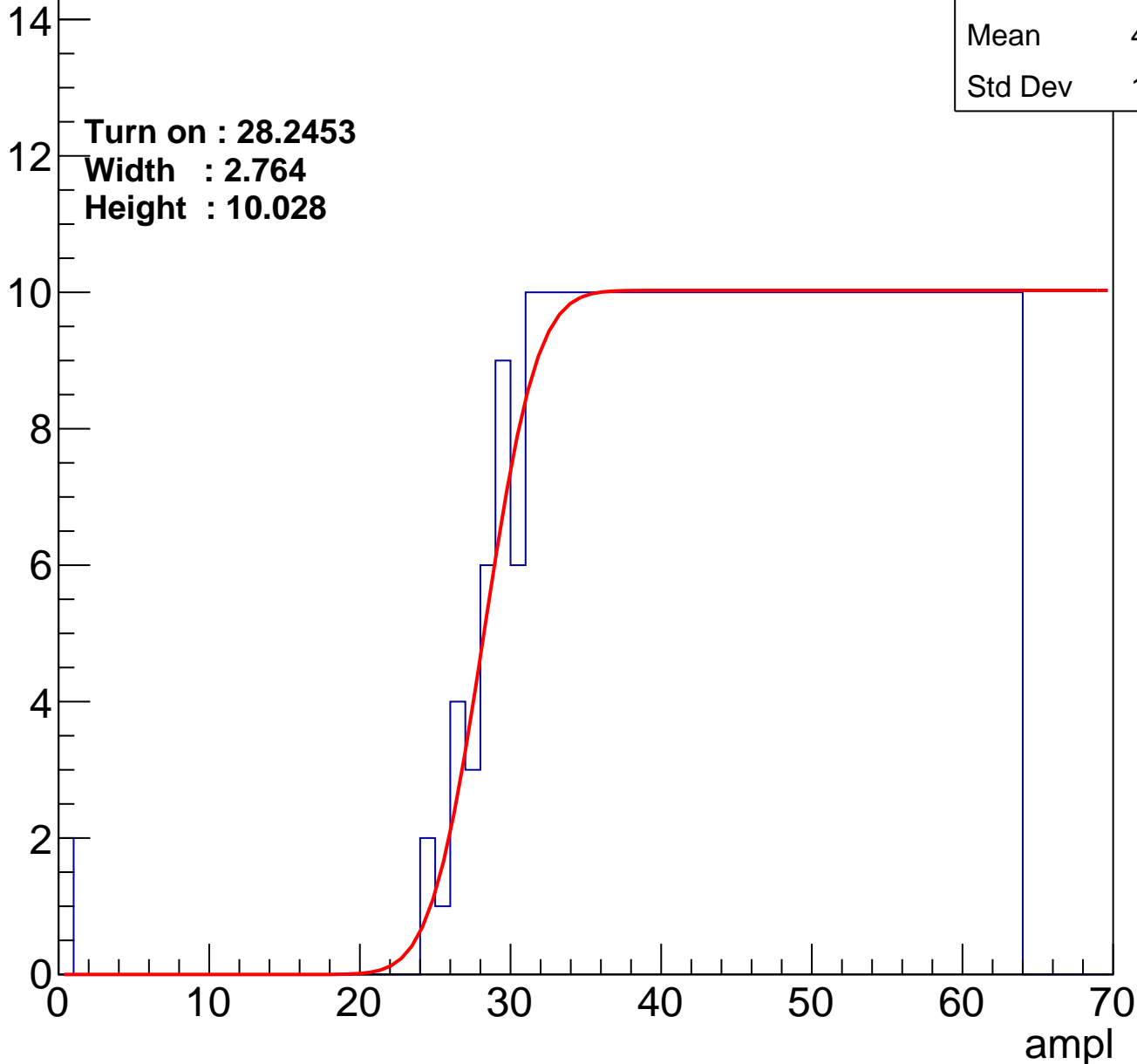
|         |       |
|---------|-------|
| Entries | 363   |
| Mean    | 45.12 |
| Std Dev | 11.06 |

Turn on : 28.2453

Width : 2.764

Height : 10.028

Entry



# B1L003S, U13-ch18

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.82 |
| Std Dev | 11.35 |

Turn on : 27.7267

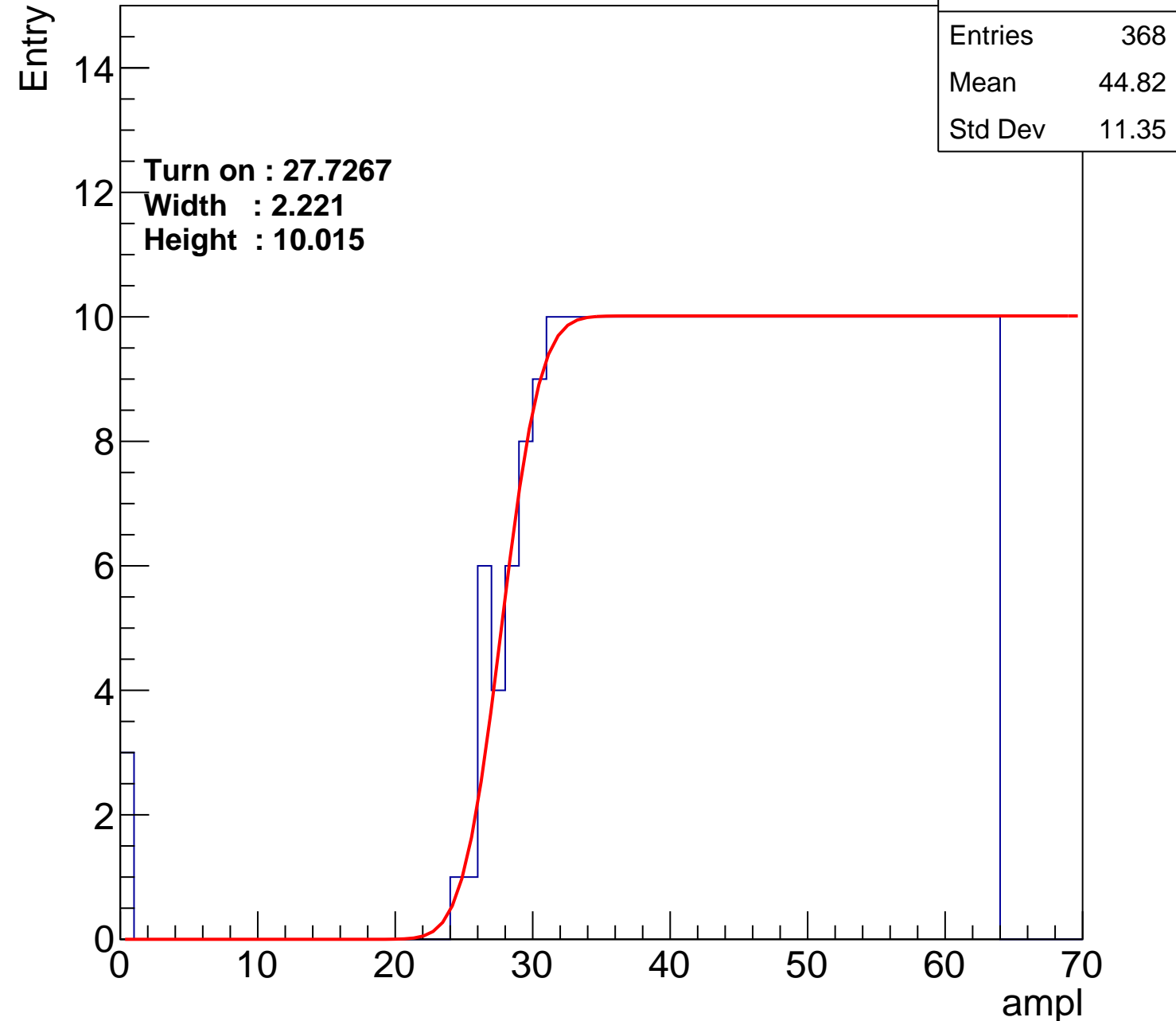
Width : 2.221

Height : 10.015

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch19

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.27 |
| Std Dev | 10.98 |

Turn on : 28.2431

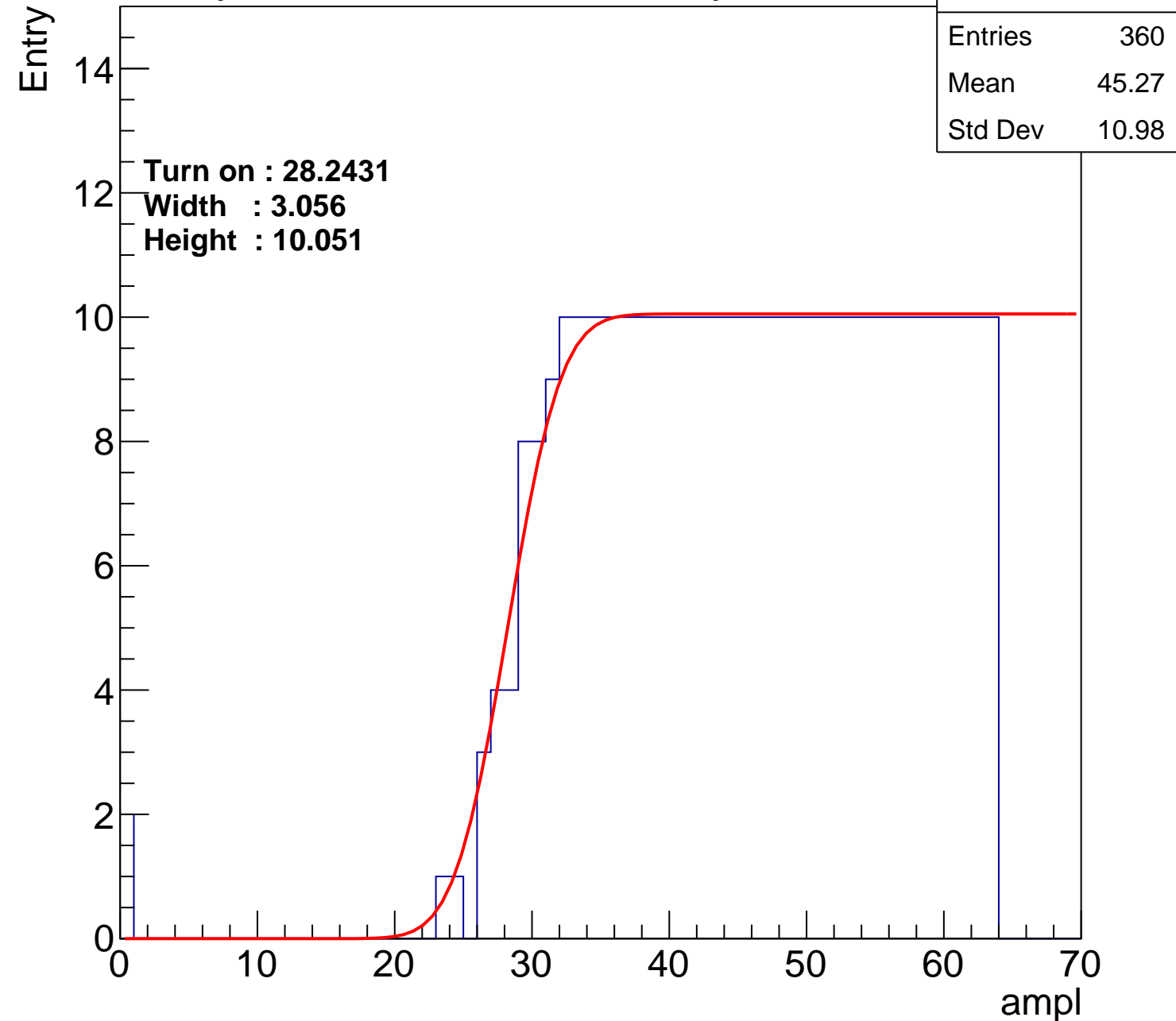
Width : 3.056

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch20

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.67 |
| Std Dev | 11.42 |

Turn on : 27.6280

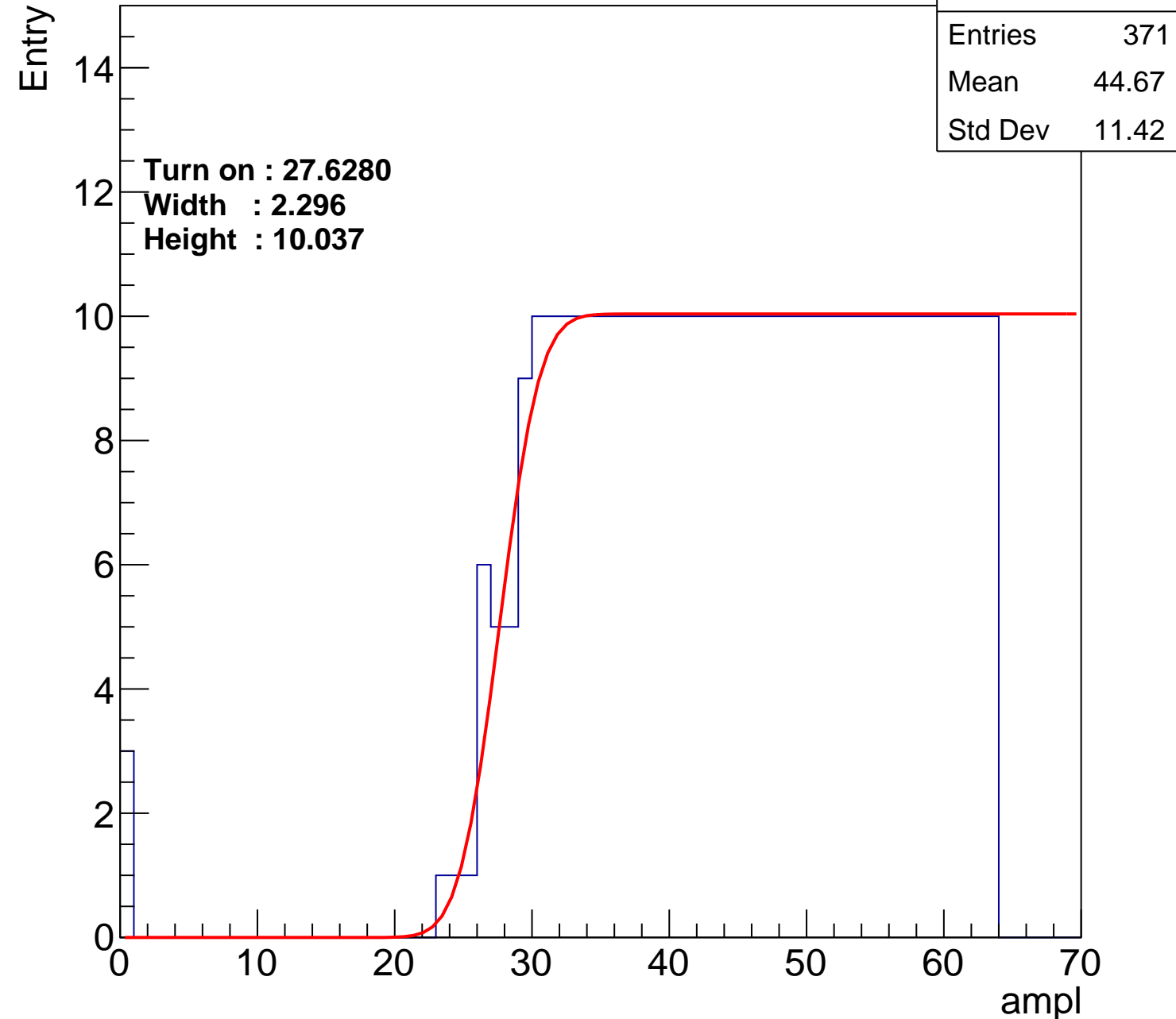
Width : 2.296

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch21

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.25 |
| Std Dev | 11.2  |

Turn on : 28.4027

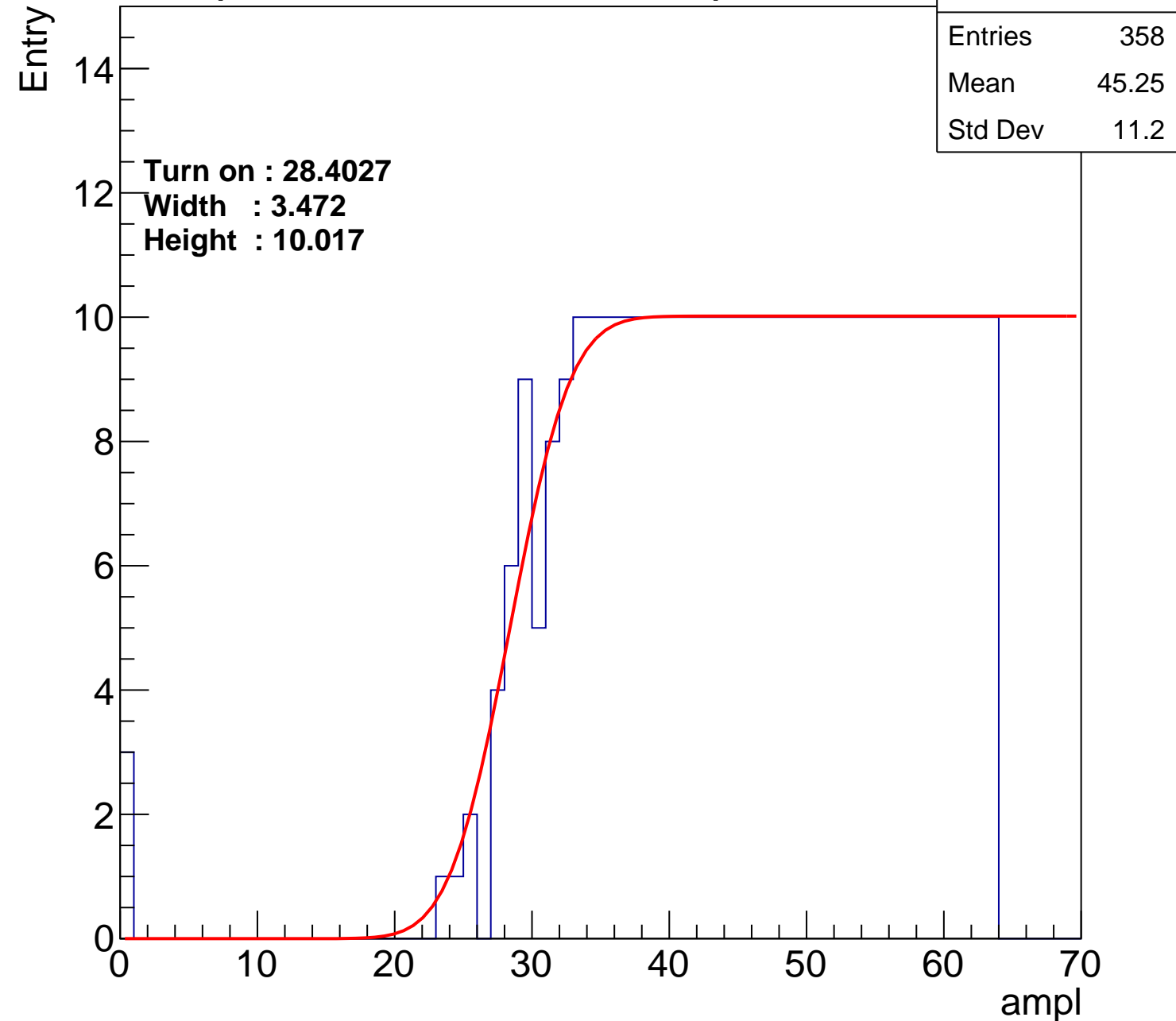
Width : 3.472

Height : 10.017

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch22

calib\_packv5\_042523\_0143.root, FC#13, port D2

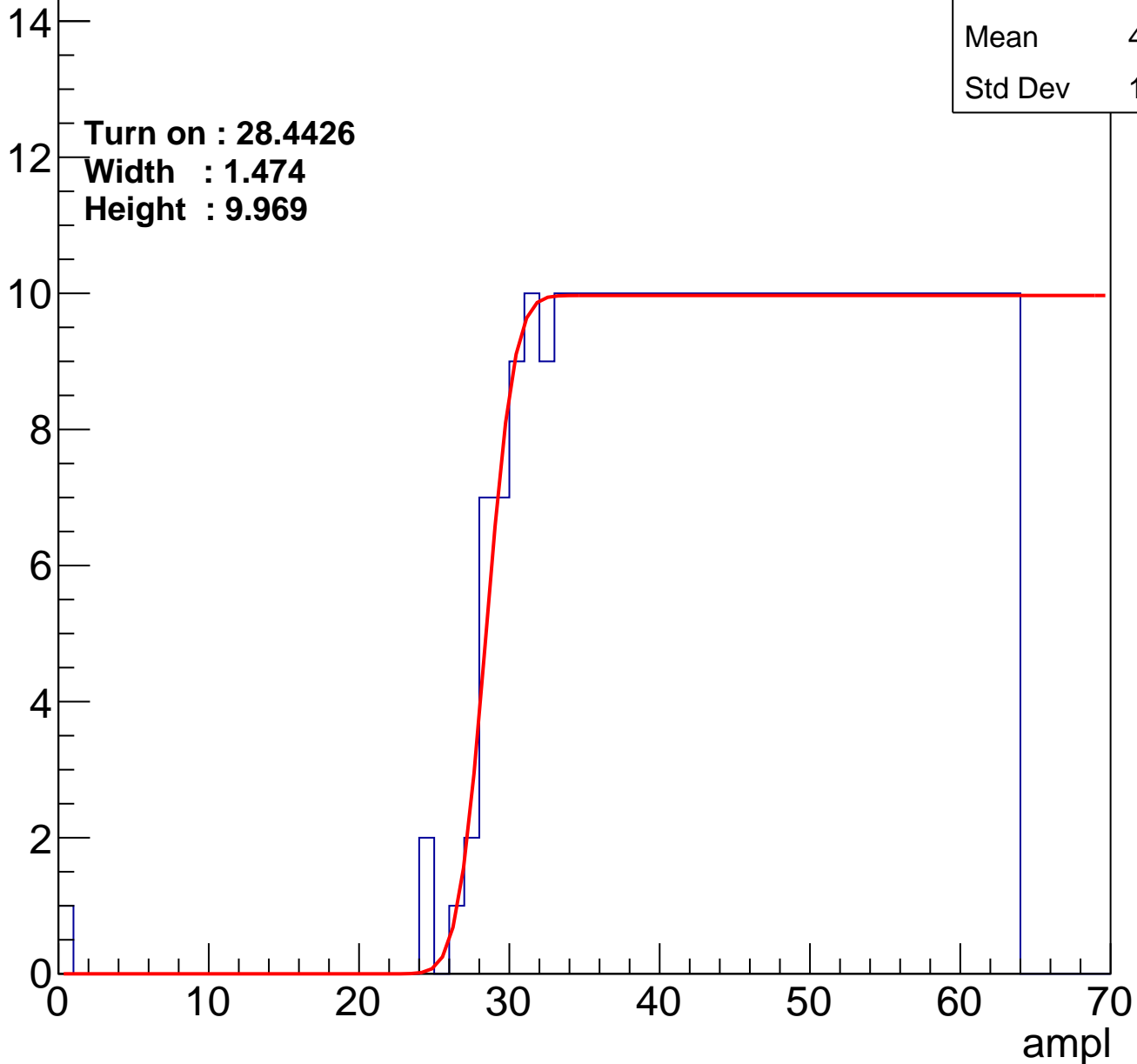
Entry

|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.46 |
| Std Dev | 10.67 |

Turn on : 28.4426

Width : 1.474

Height : 9.969





# B1L003S, U13-ch23

calib\_packv5\_042523\_0143.root, FC#13, port D2

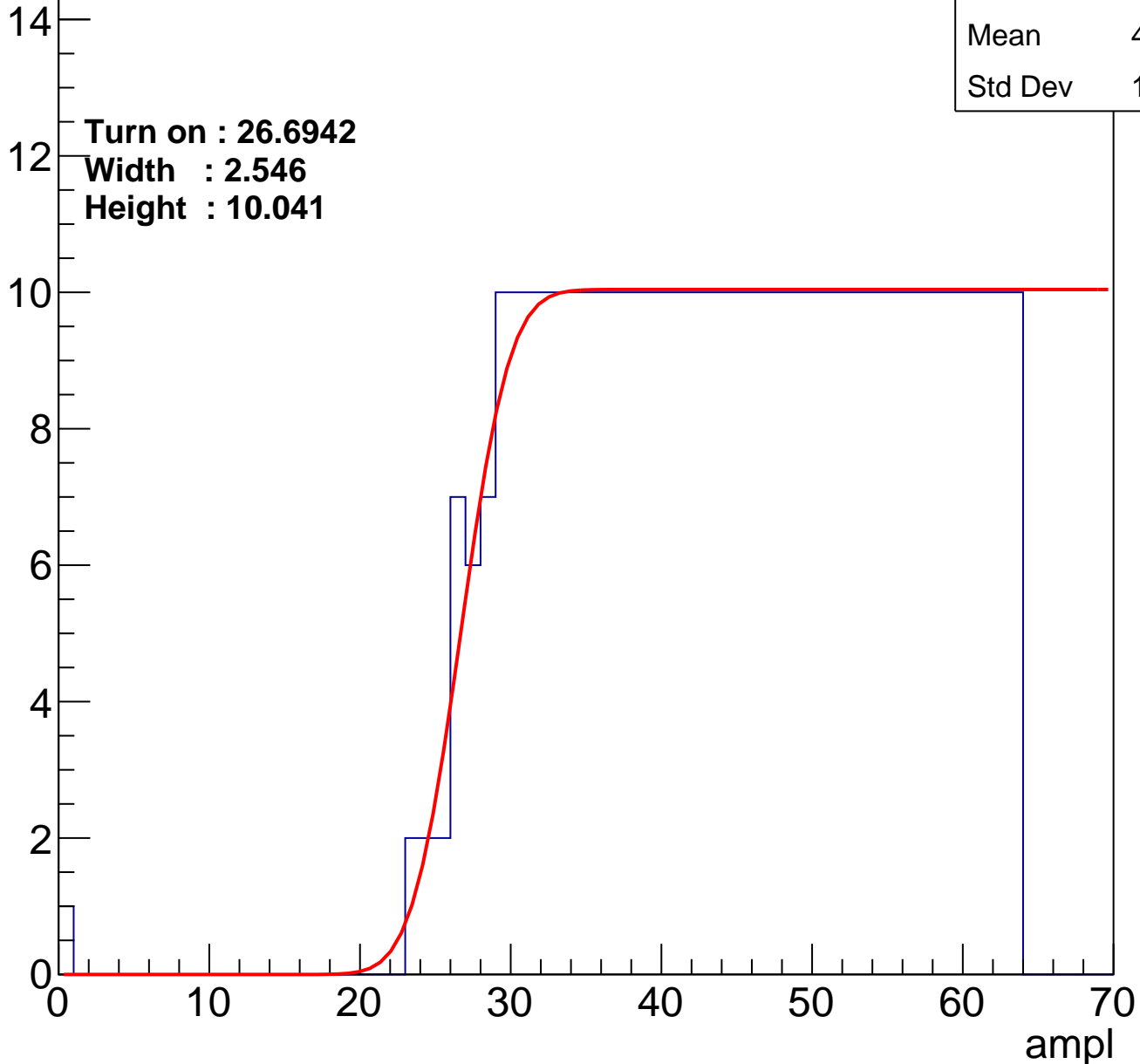
|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.52 |
| Std Dev | 11.18 |

Turn on : 26.6942

Width : 2.546

Height : 10.041

Entry



# B1L003S, U13-ch24

calib\_packv5\_042523\_0143.root, FC#13, port D2

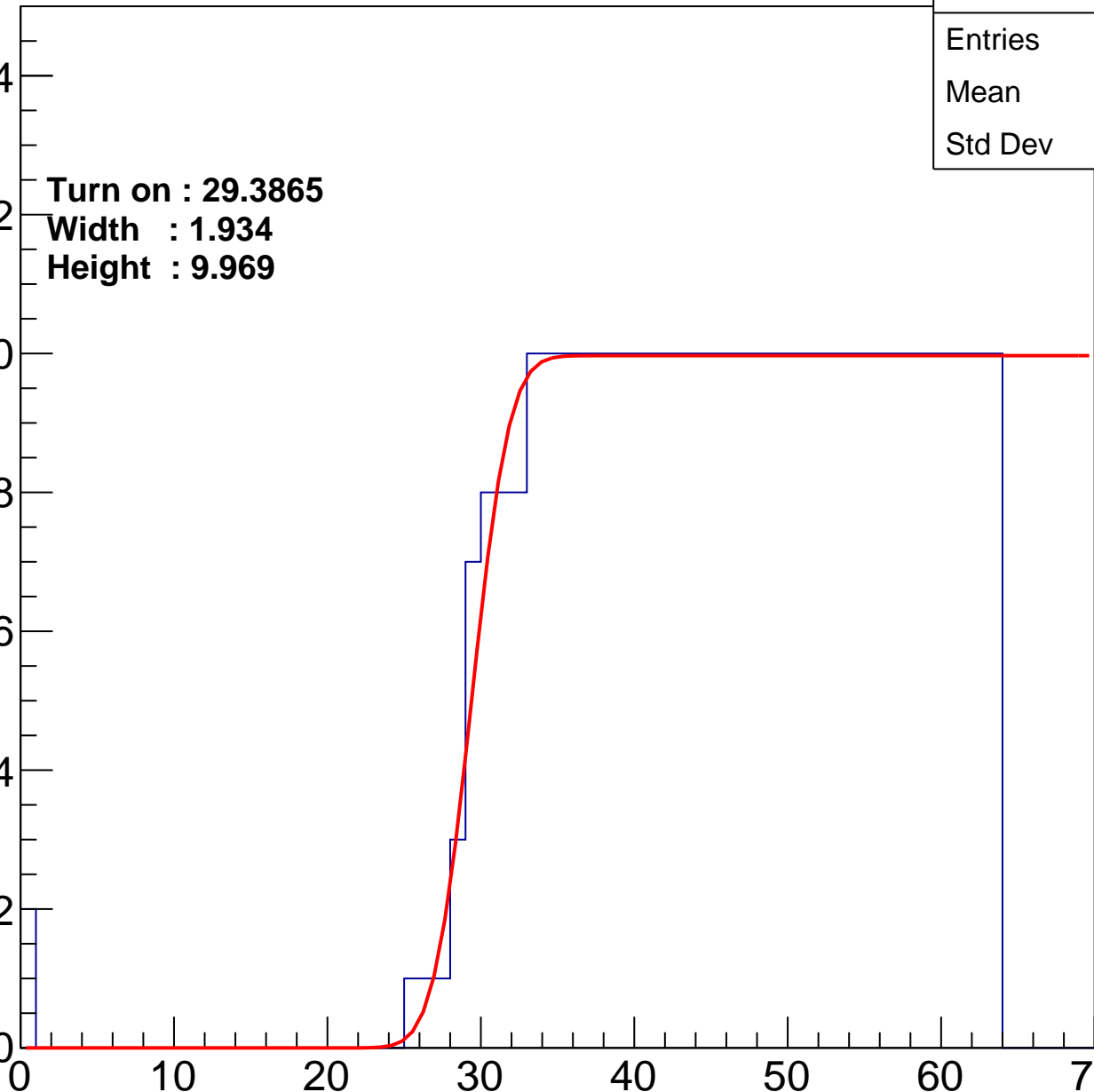
Entry

14  
12  
10  
8  
6  
4  
2  
0

**Turn on : 29.3865**  
**Width : 1.934**  
**Height : 9.969**

|         |       |
|---------|-------|
| Entries | 349   |
| Mean    | 45.81 |
| Std Dev | 10.69 |

ampl



# B1L003S, U13-ch25

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.08 |
| Std Dev | 11.26 |

Turn on : 27.9980

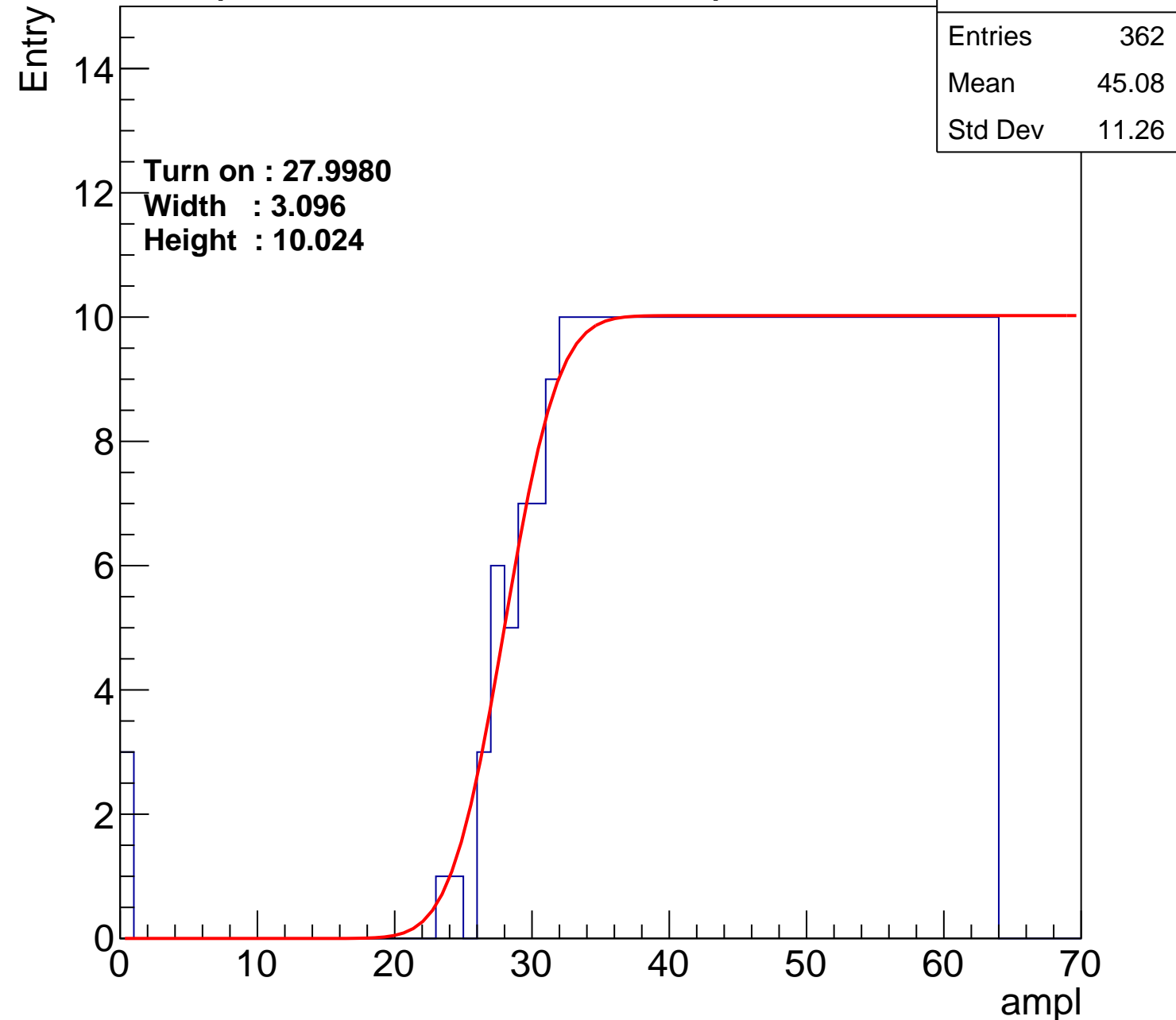
Width : 3.096

Height : 10.024

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch26

calib\_packv5\_042523\_0143.root, FC#13, port D2

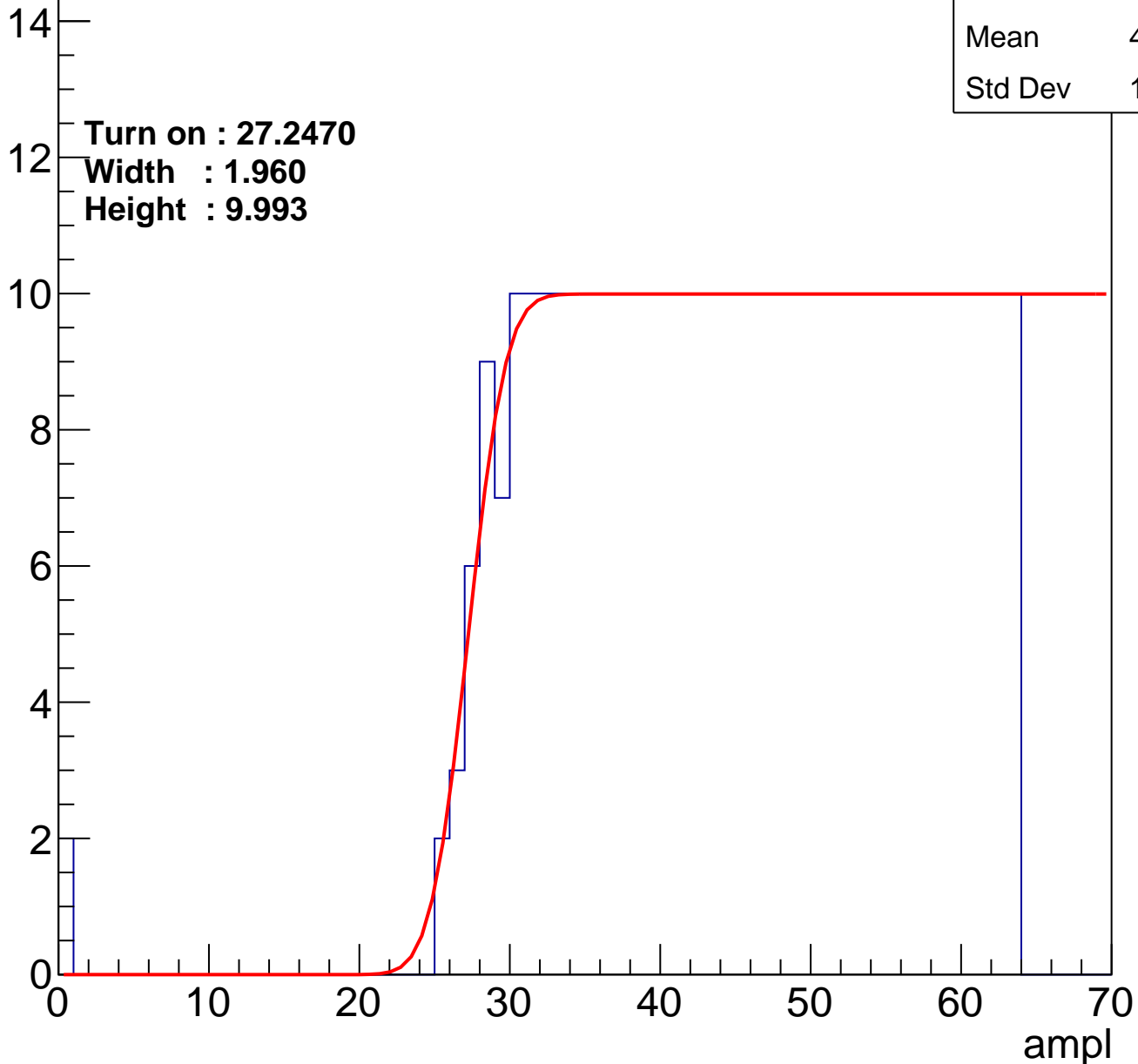
|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.86 |
| Std Dev | 11.14 |

**Turn on : 27.2470**

**Width : 1.960**

**Height : 9.993**

Entry



# B1L003S, U13-ch27

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 45.07 |
| Std Dev | 10.93 |

Turn on : 27.9828

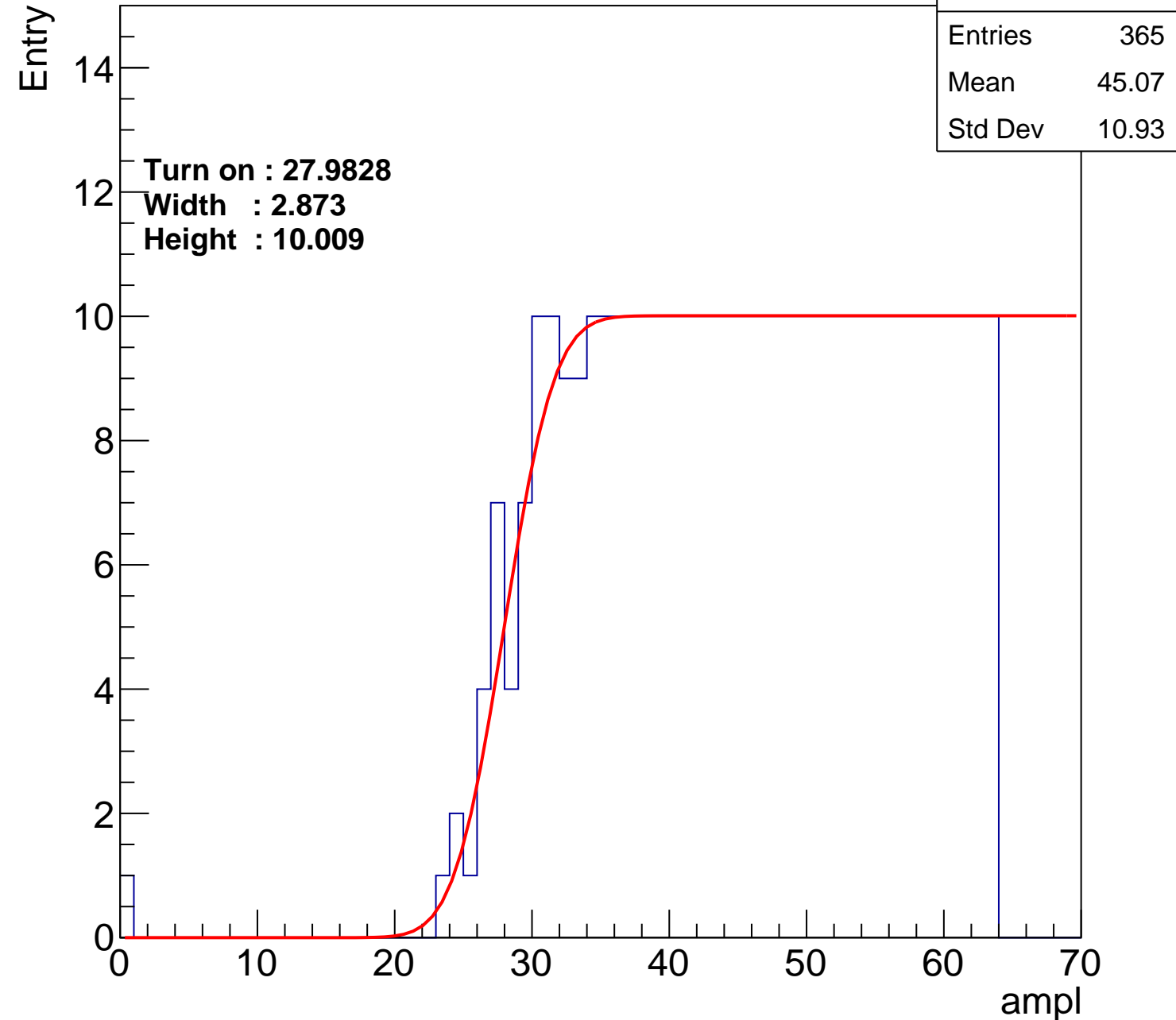
Width : 2.873

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch28

calib\_packv5\_042523\_0143.root, FC#13, port D2

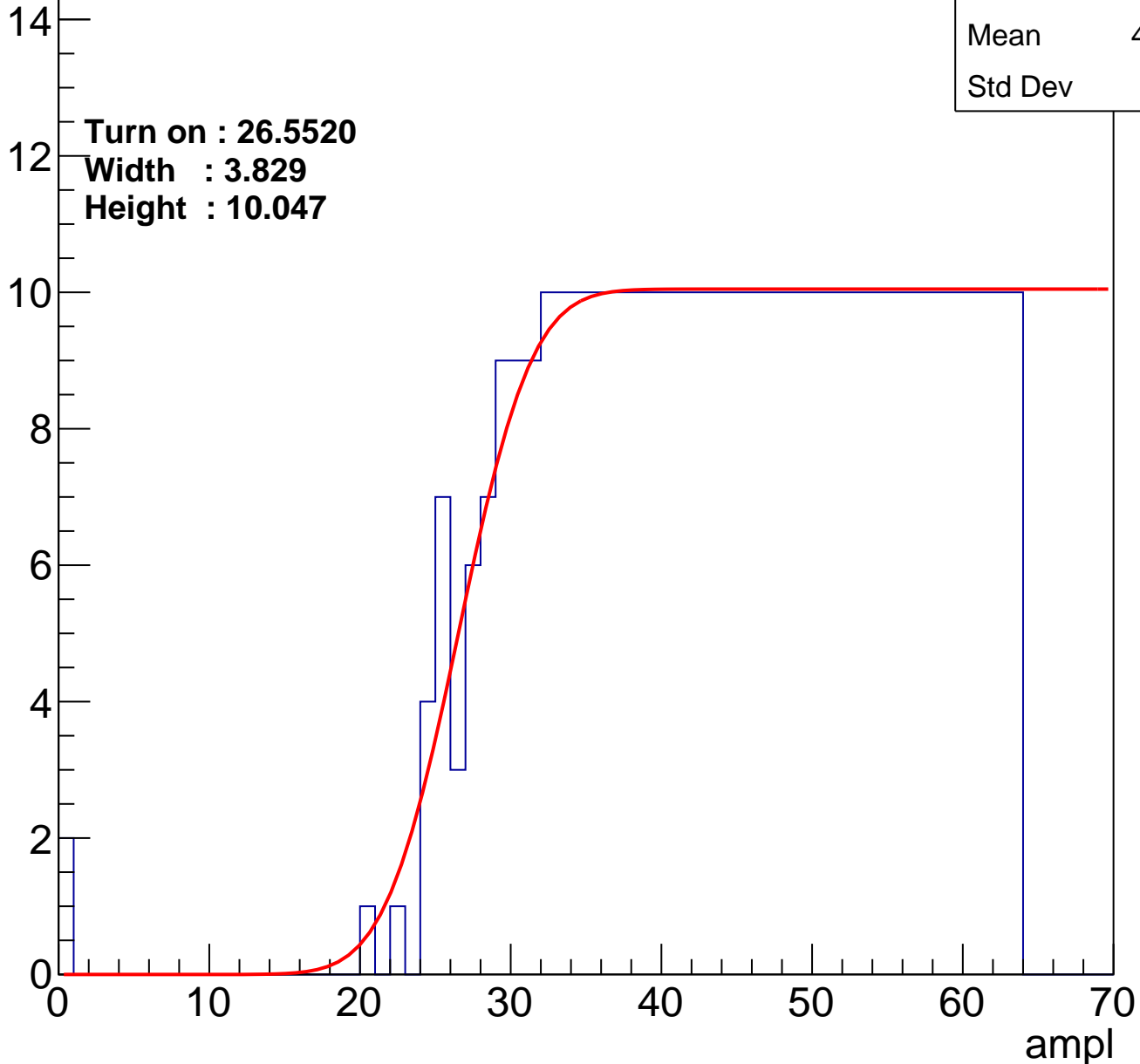
|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.34 |
| Std Dev | 11.5  |

Turn on : 26.5520

Width : 3.829

Height : 10.047

Entry



# B1L003S, U13-ch29

calib\_packv5\_042523\_0143.root, FC#13, port D2

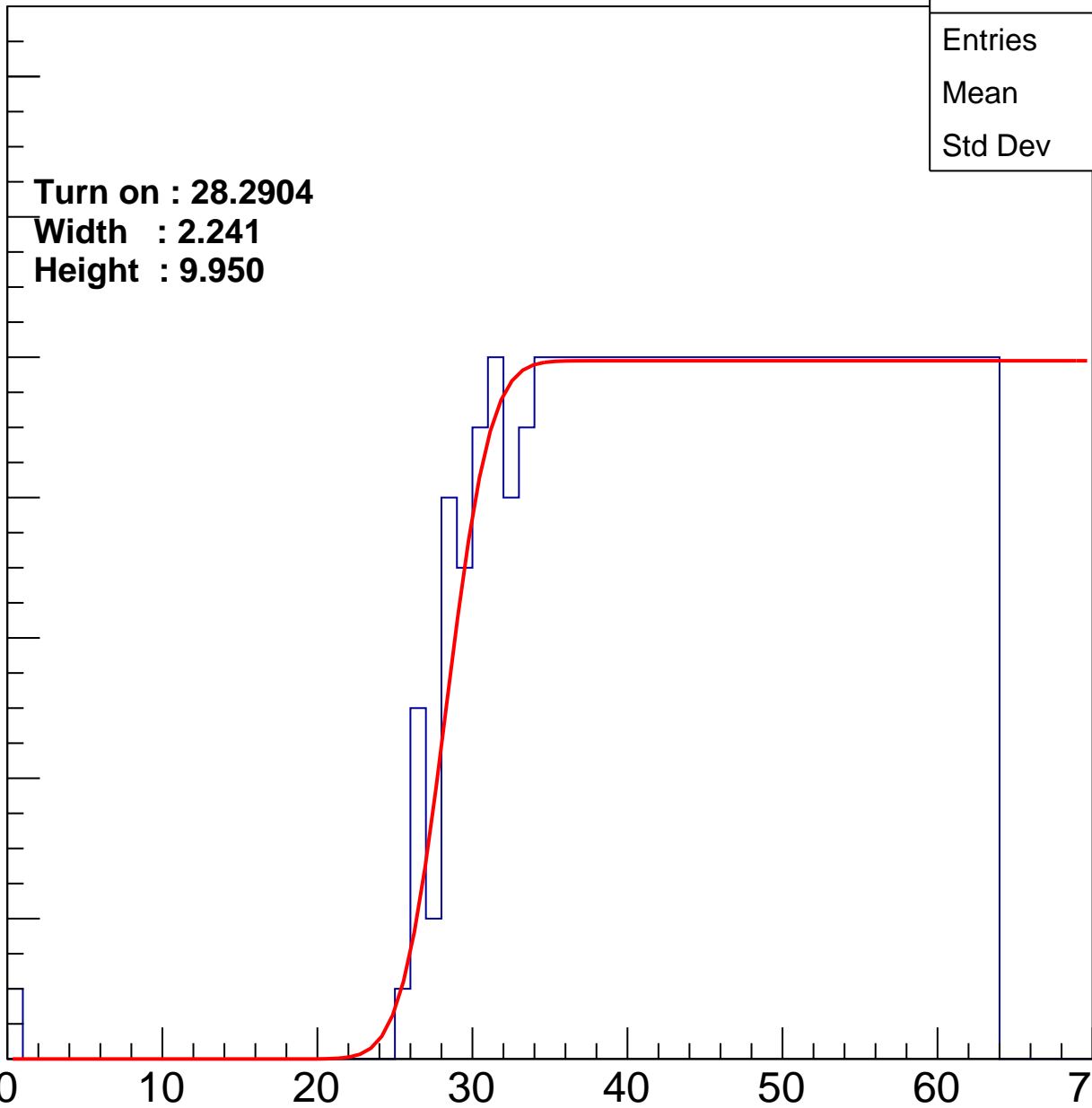
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 28.2904  
Width : 2.241  
Height : 9.950

|         |       |
|---------|-------|
| Entries | 360   |
| Mean    | 45.33 |
| Std Dev | 10.77 |

ampl



# B1L003S, U13-ch30

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.24 |
| Std Dev | 11.23 |

Turn on : 28.9703

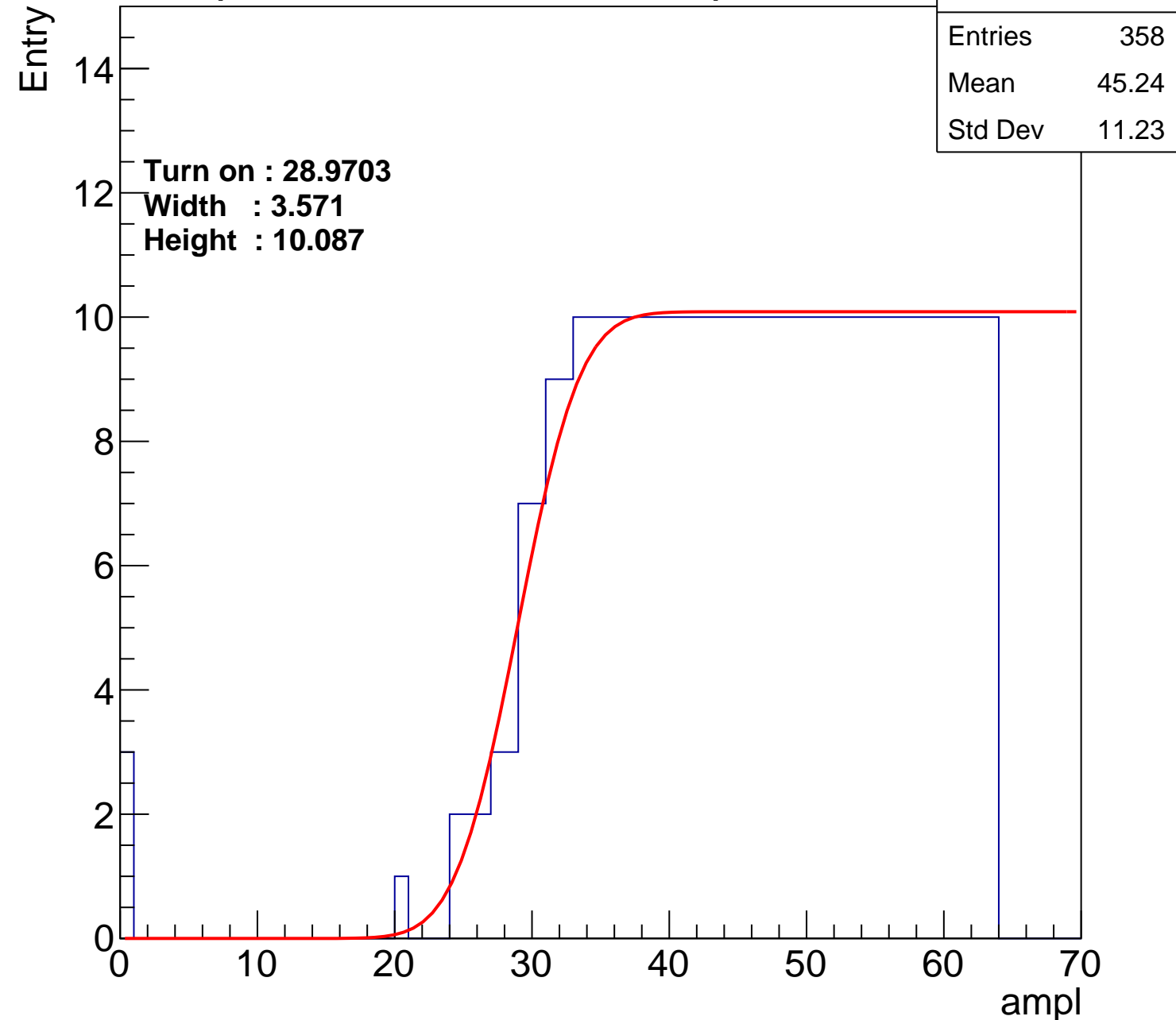
Width : 3.571

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch31

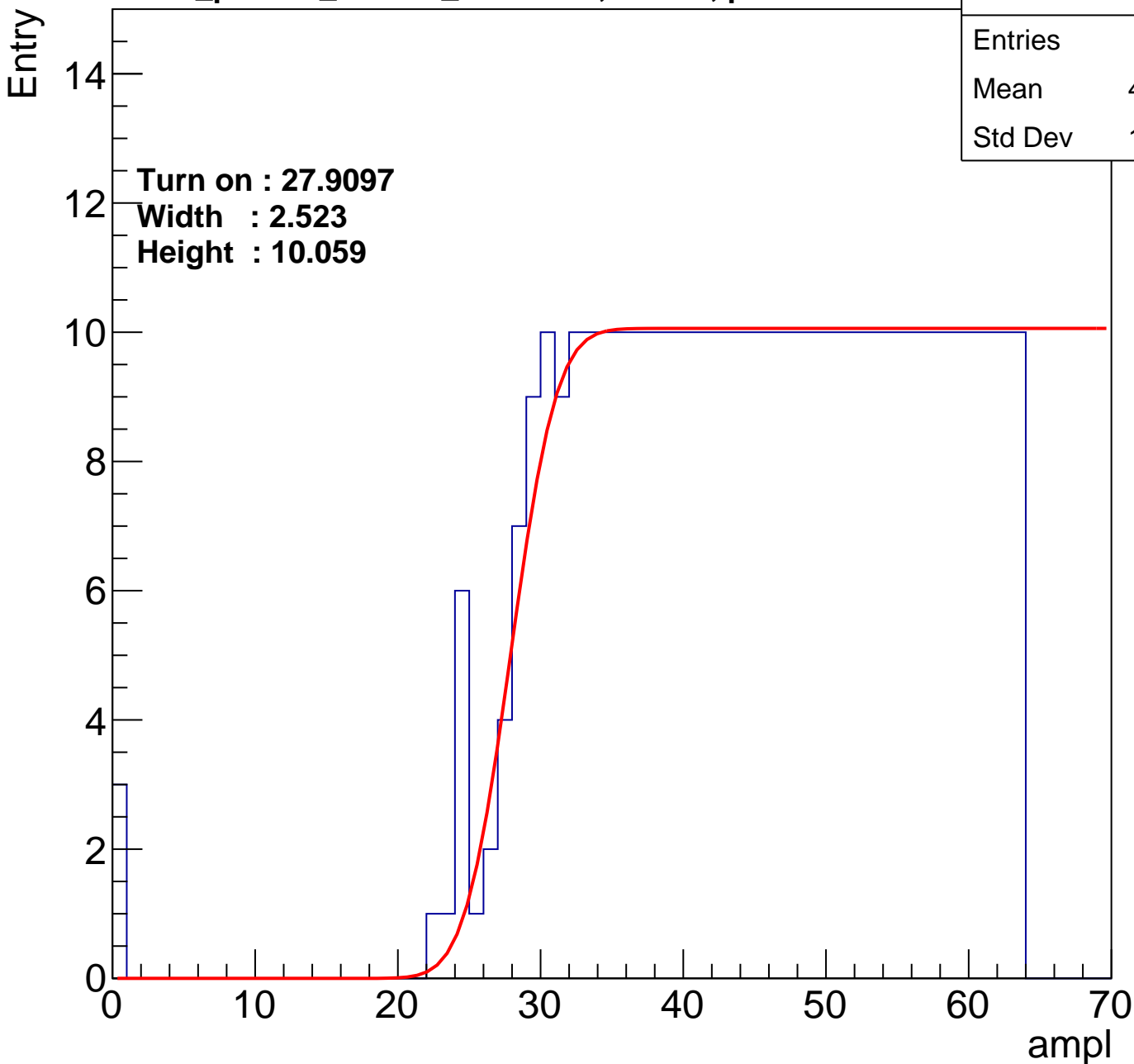
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

**Turn on : 27.9097**

**Width : 2.523**

**Height : 10.059**

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.53 |
| Std Dev | 11.54 |



# B1L003S, U13-ch32

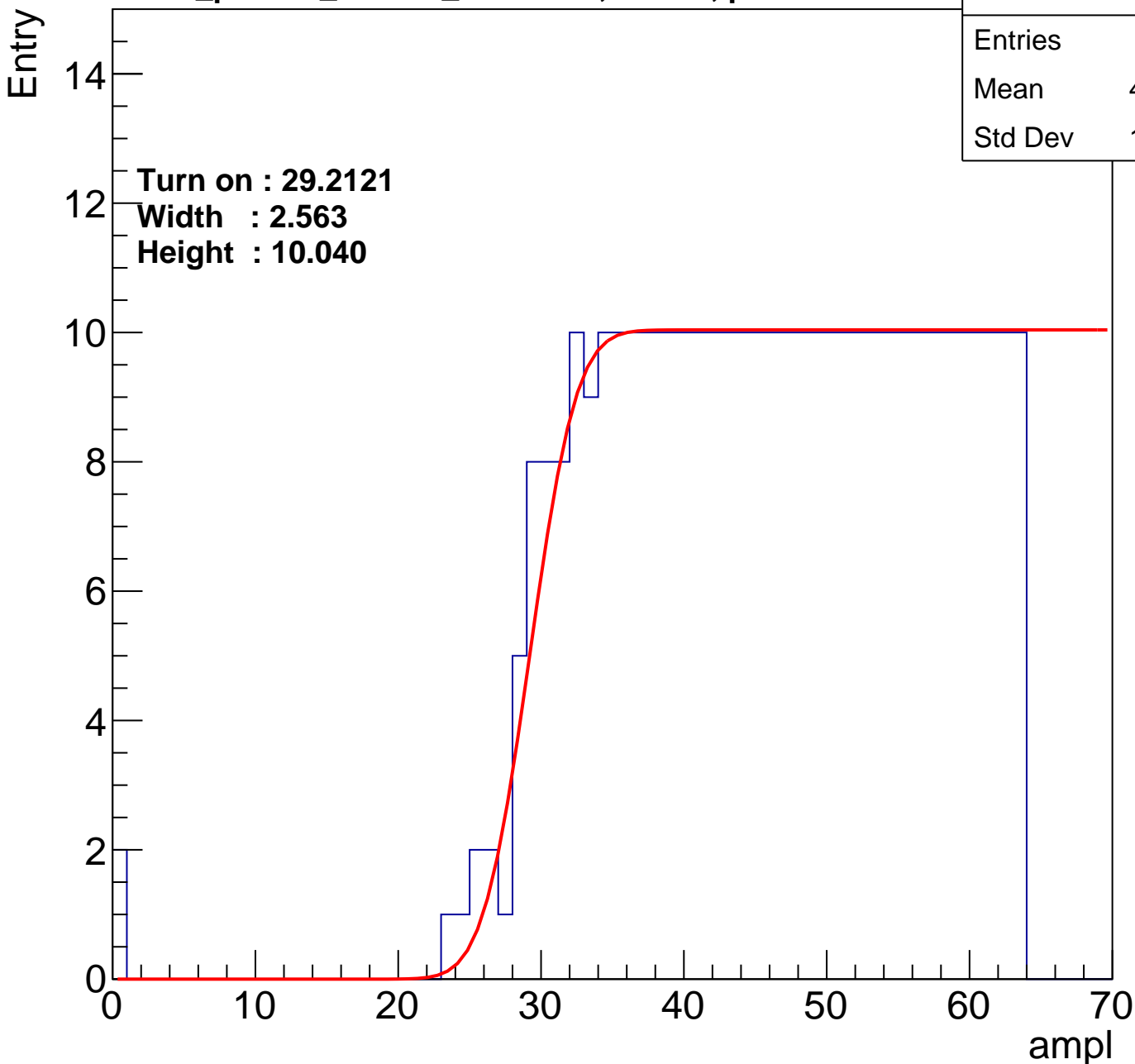
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 357   |
| Mean    | 45.39 |
| Std Dev | 10.94 |

**Turn on : 29.2121**

**Width : 2.563**

**Height : 10.040**



# B1L003S, U13-ch33

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.72 |
| Std Dev | 10.78 |

Turn on : 29.6165

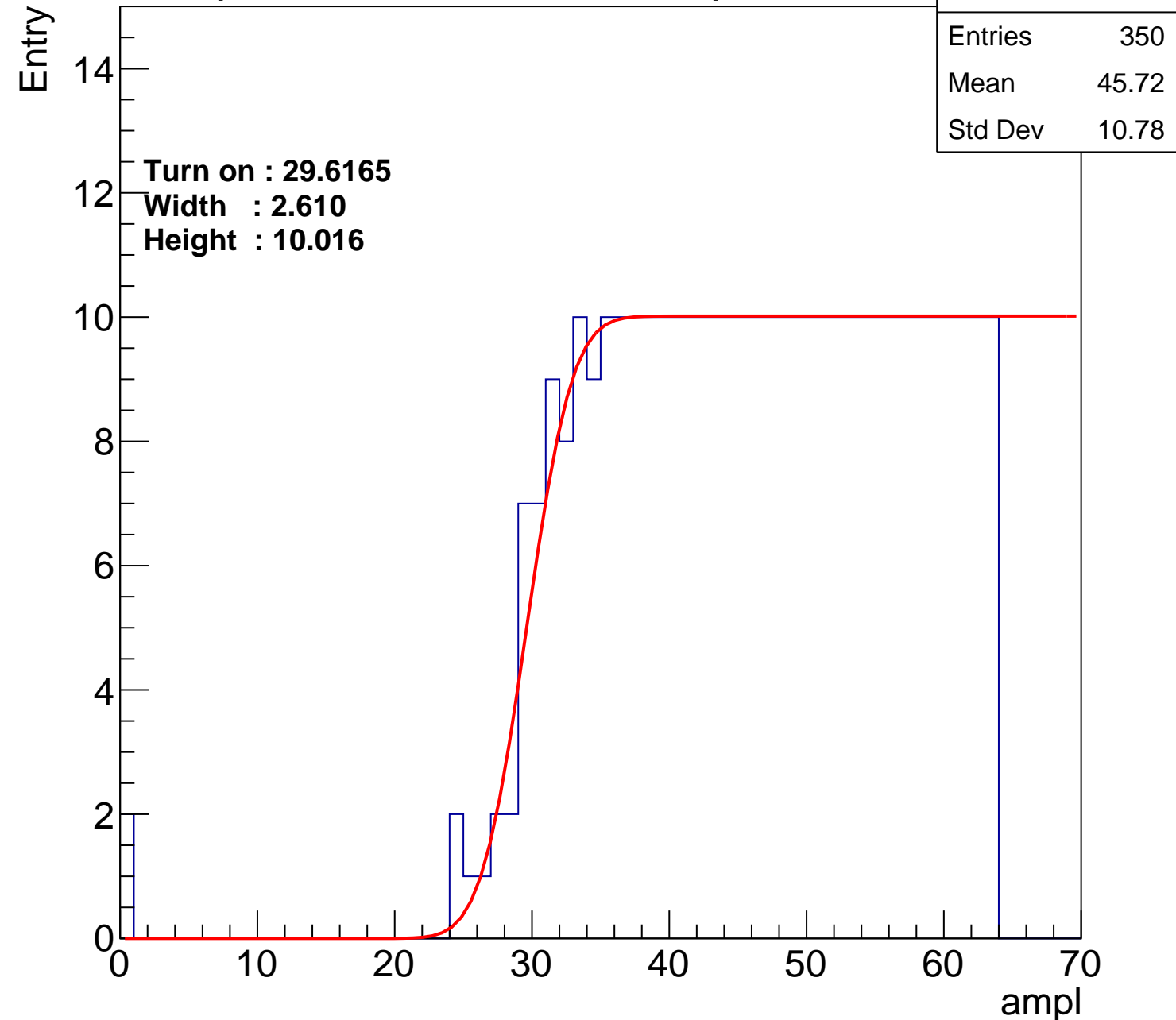
Width : 2.610

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch34

calib\_packv5\_042523\_0143.root, FC#13, port D2

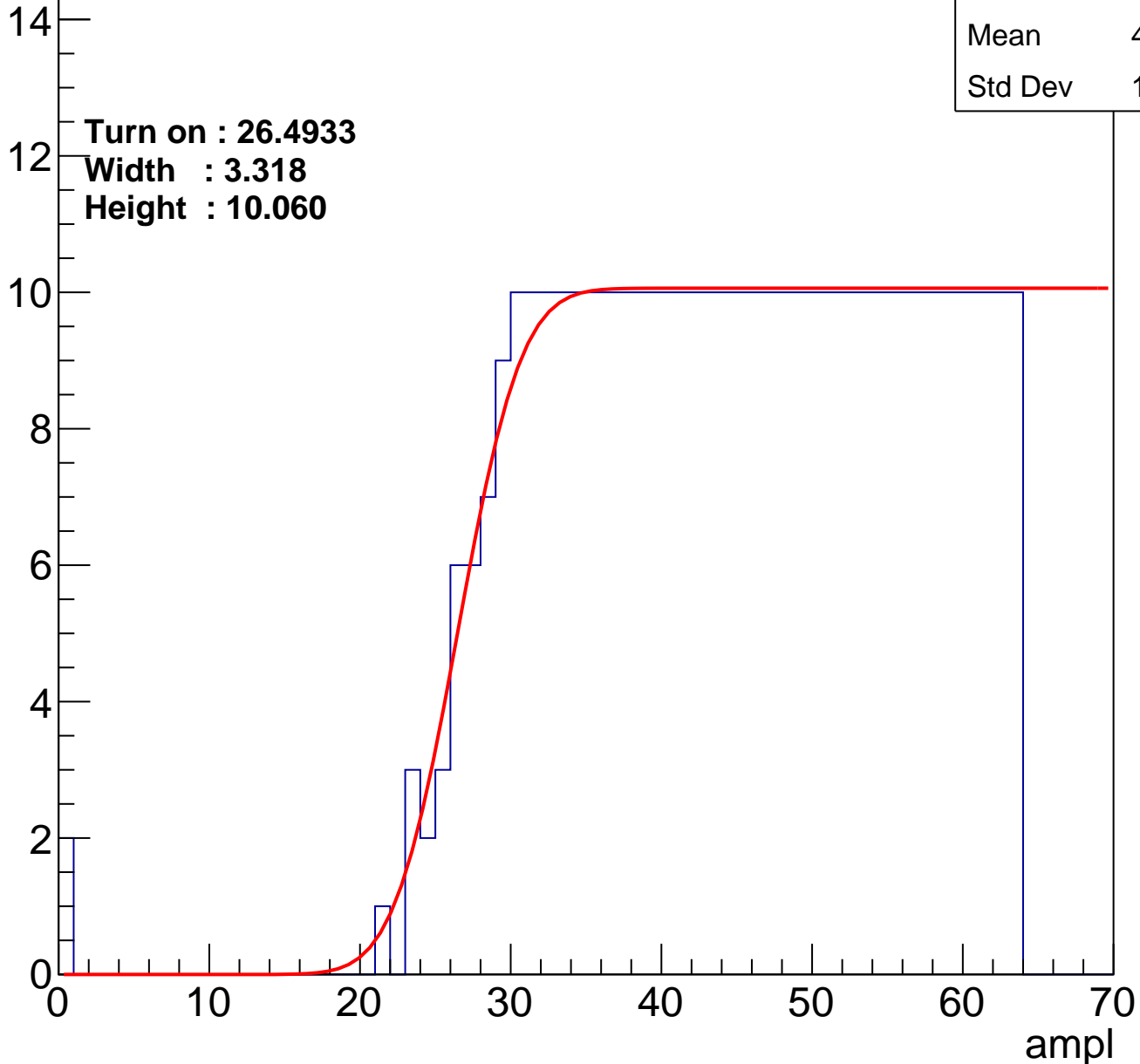
|         |       |
|---------|-------|
| Entries | 379   |
| Mean    | 44.32 |
| Std Dev | 11.48 |

Turn on : 26.4933

Width : 3.318

Height : 10.060

Entry



# B1L003S, U13-ch35

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.76 |
| Std Dev | 10.72 |

Turn on : 29.1249

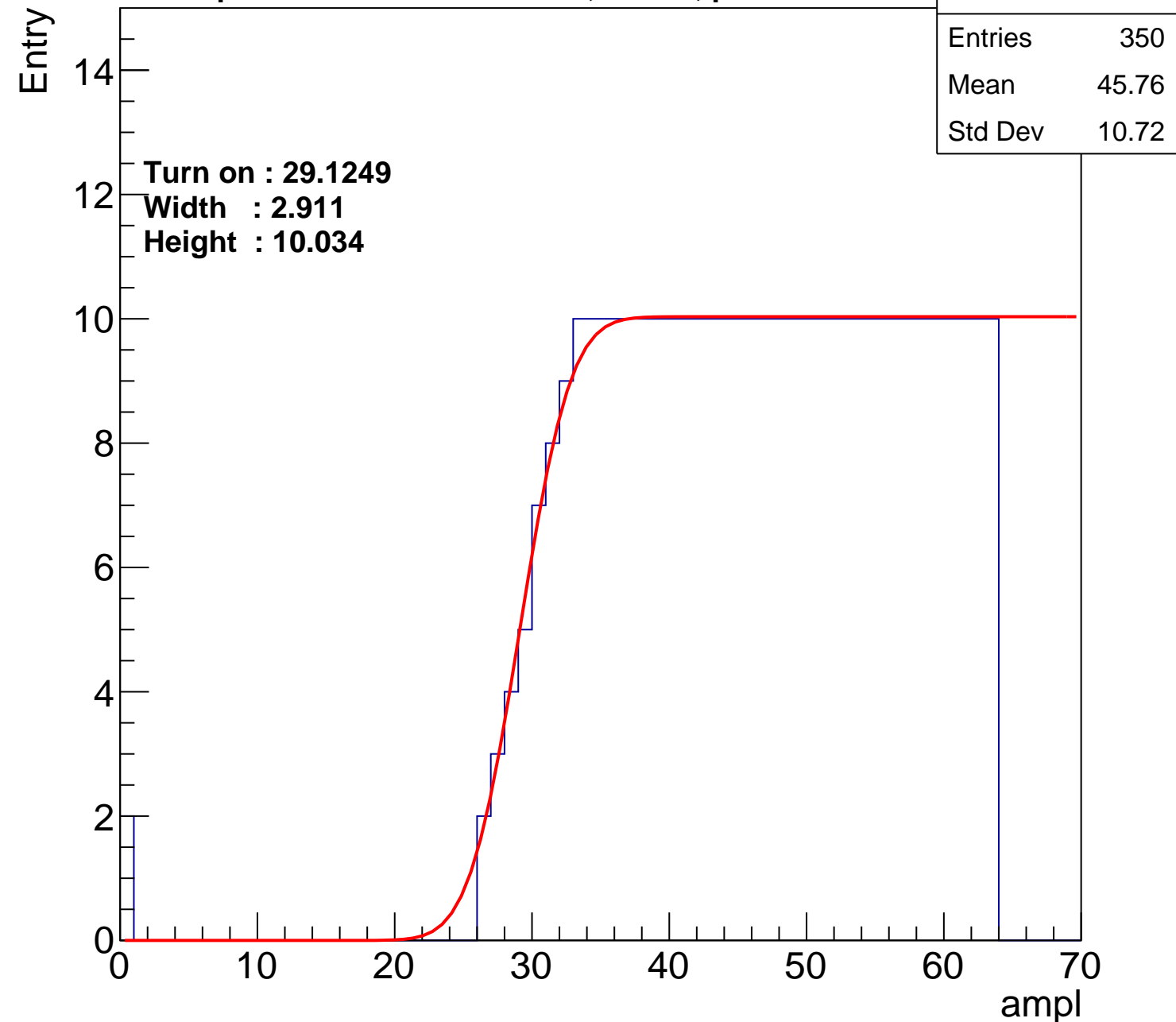
Width : 2.911

Height : 10.034

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch36

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.34 |
| Std Dev | 11.32 |

**Turn on : 26.5284**

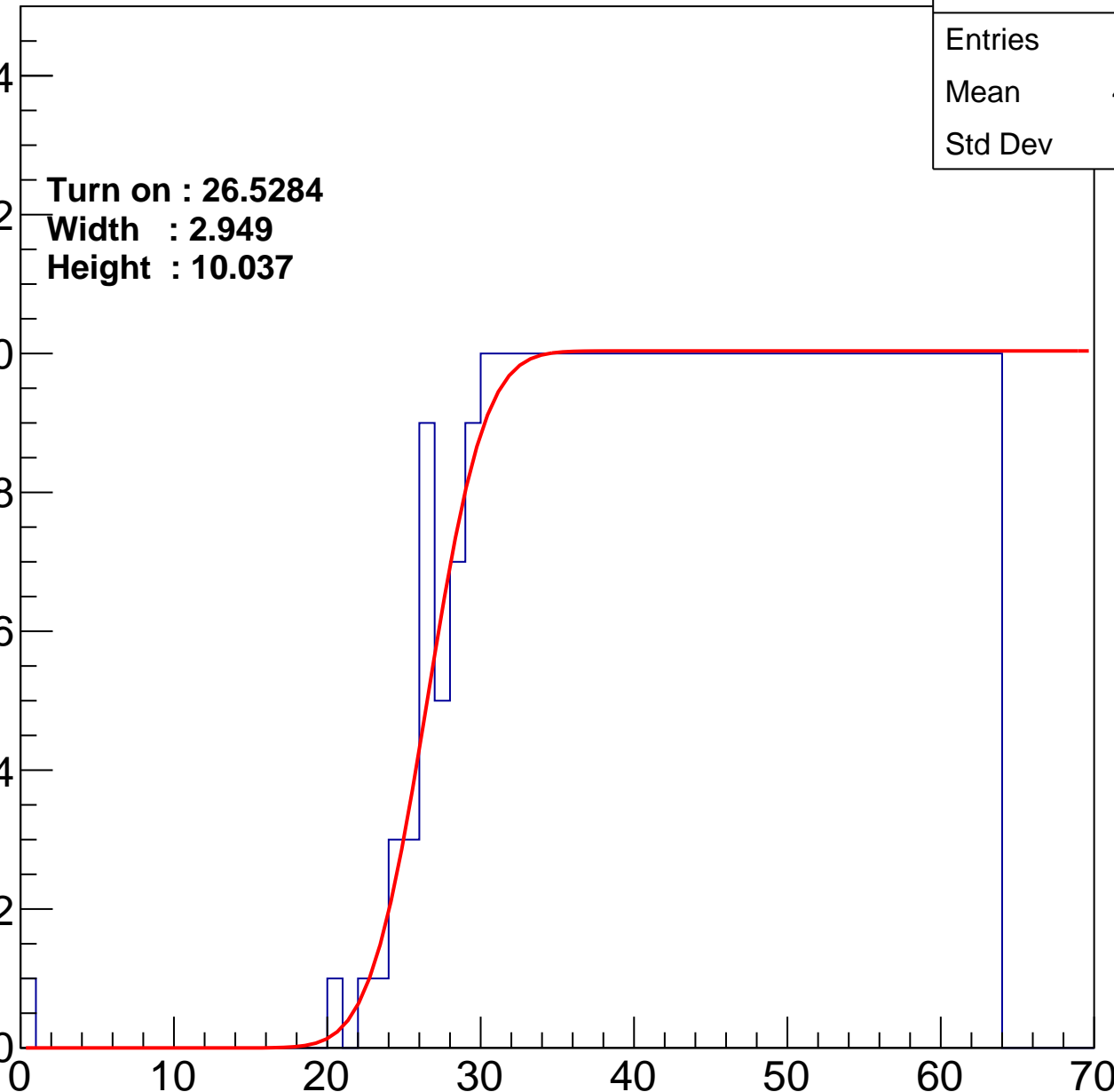
**Width : 2.949**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch37

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 352   |
| Mean    | 45.76 |
| Std Dev | 10.5  |

Turn on : 28.8307

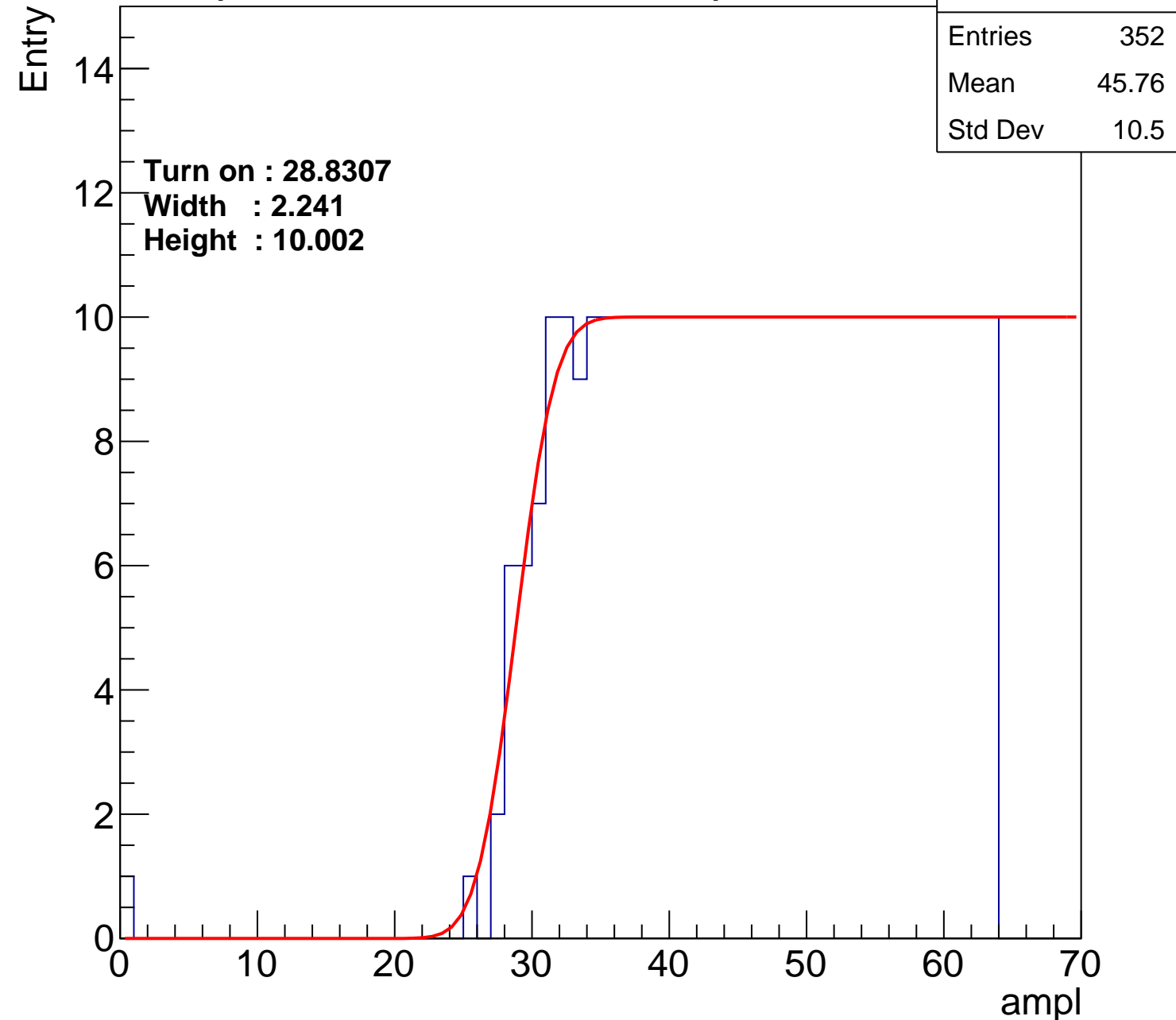
Width : 2.241

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch38

calib\_packv5\_042523\_0143.root, FC#13, port D2

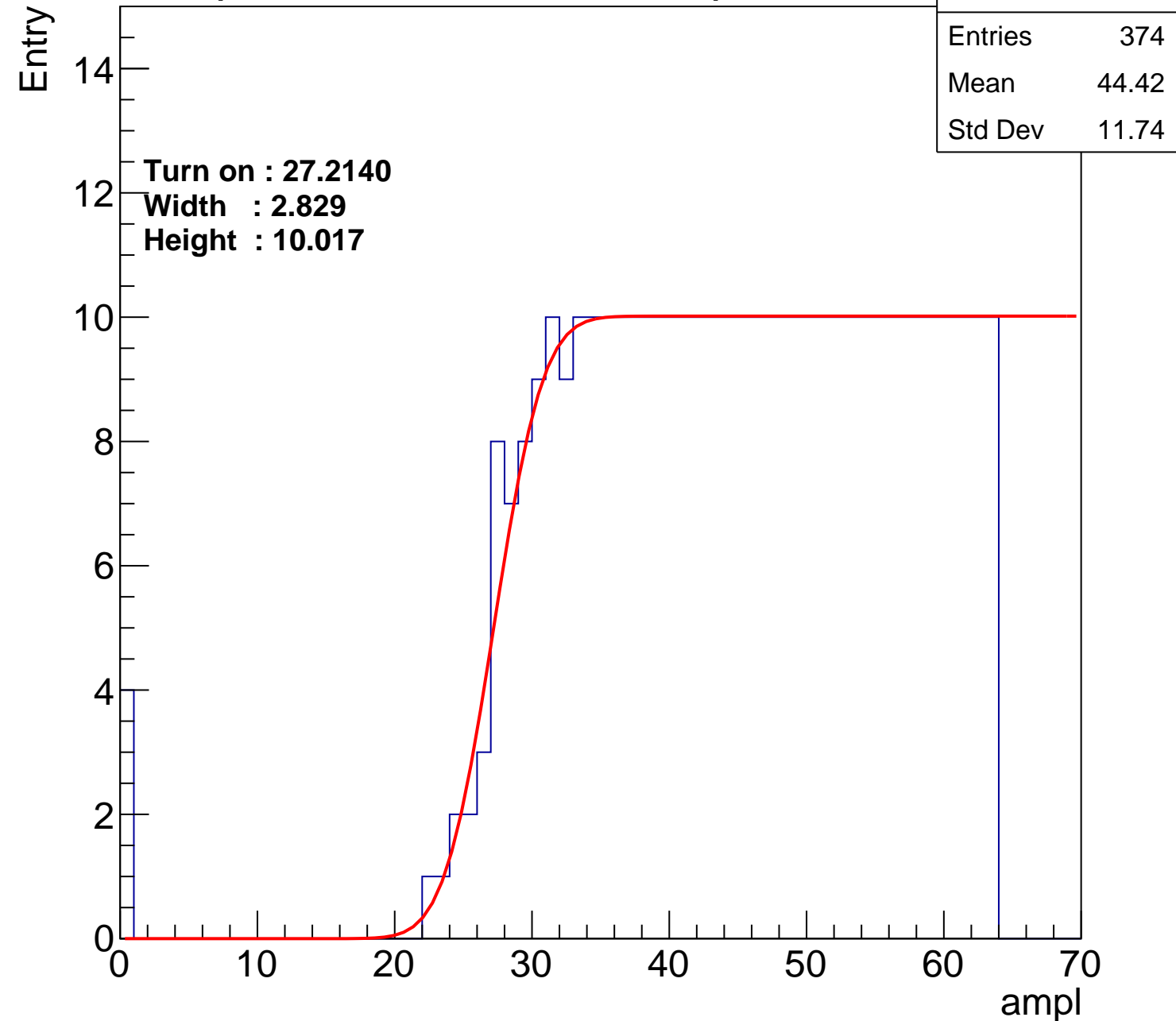
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.2140  
Width : 2.829  
Height : 10.017

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.42 |
| Std Dev | 11.74 |

ampl





# B1L003S, U13-ch39

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.79 |
| Std Dev | 11.25 |

Turn on : 27.5655

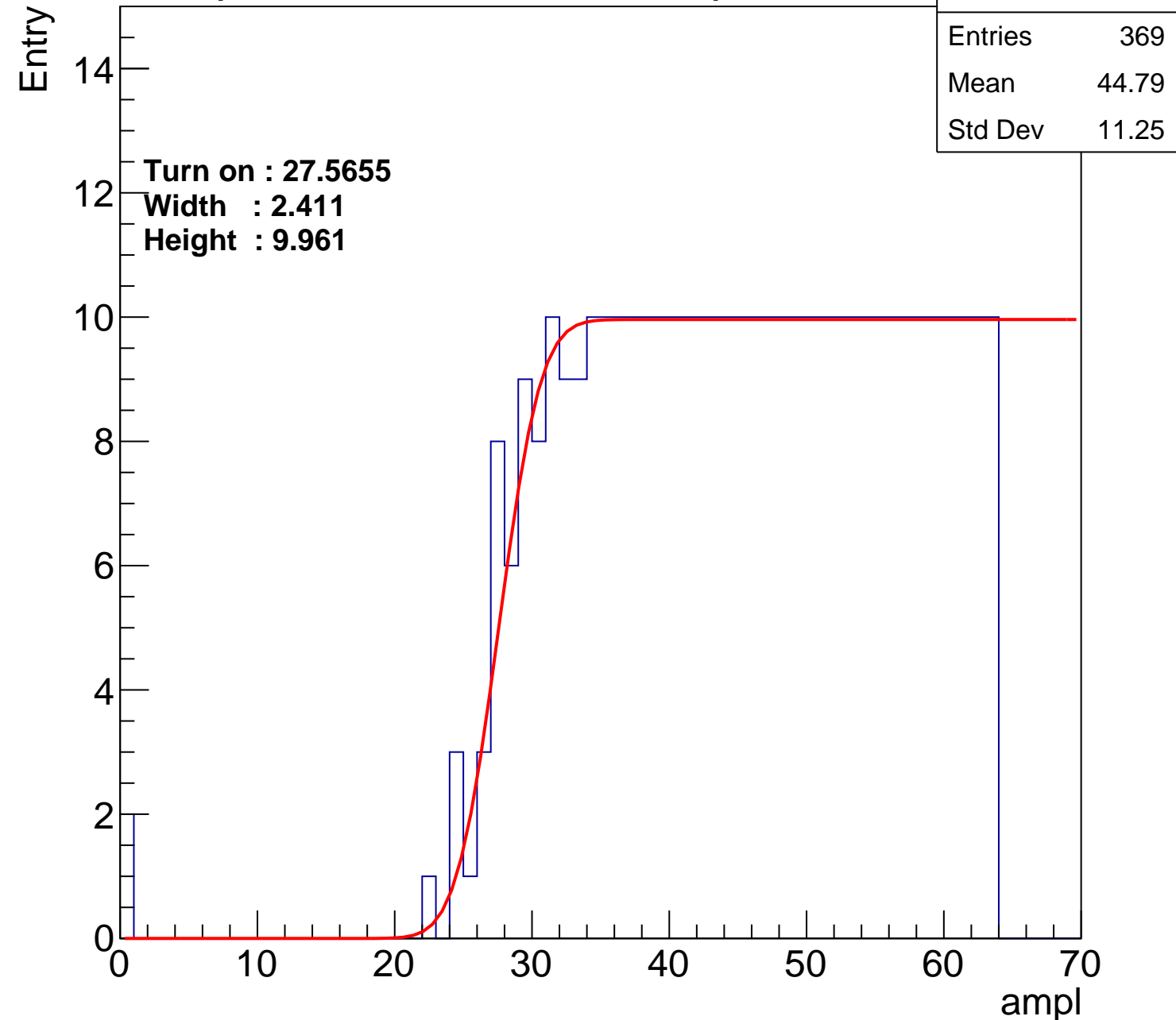
Width : 2.411

Height : 9.961

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch40

calib\_packv5\_042523\_0143.root, FC#13, port D2

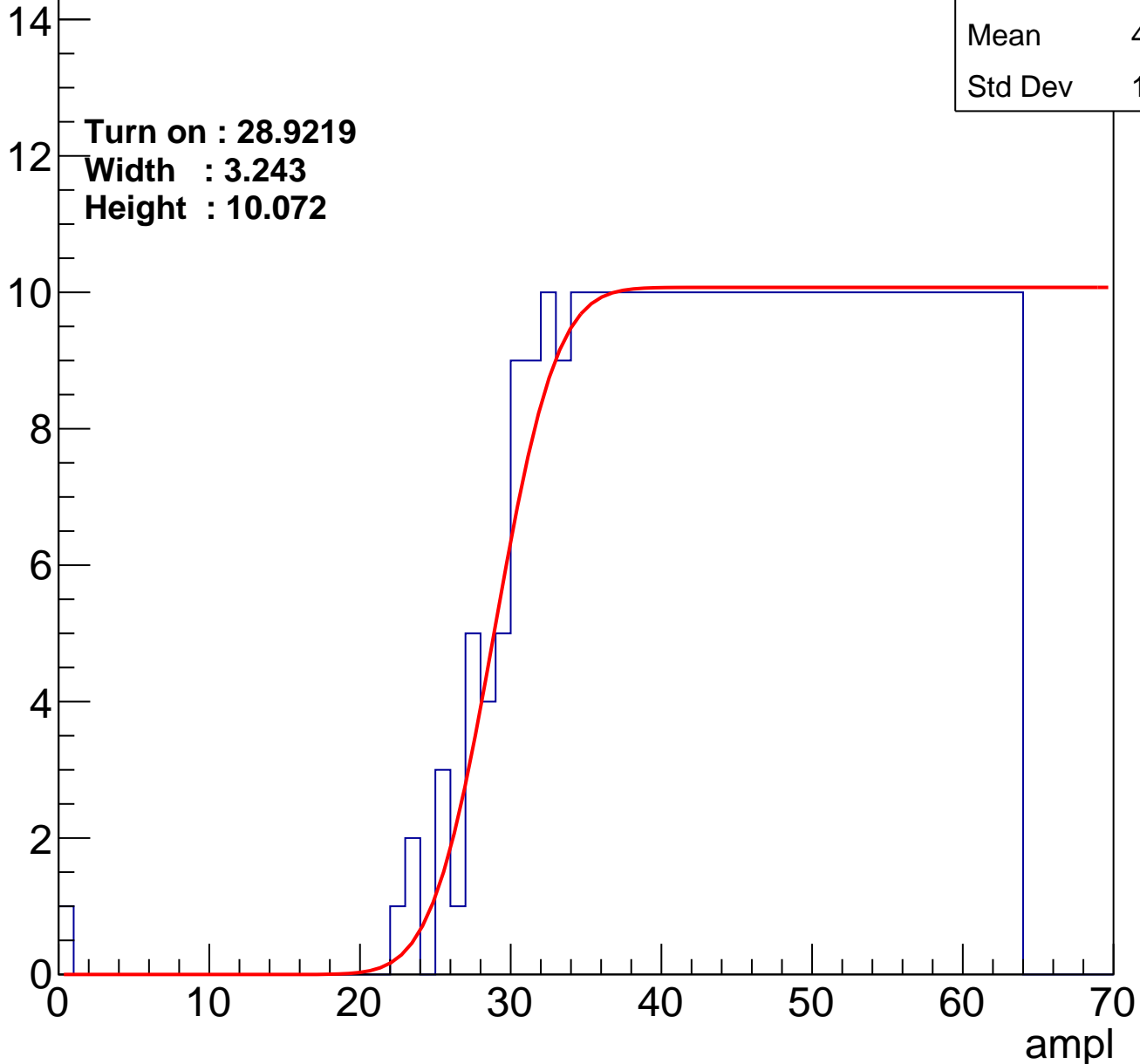
|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.34 |
| Std Dev | 10.82 |

Turn on : 28.9219

Width : 3.243

Height : 10.072

Entry



# B1L003S, U13-ch41

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 44.92 |
| Std Dev | 11.35 |

**Turn on : 27.7427**

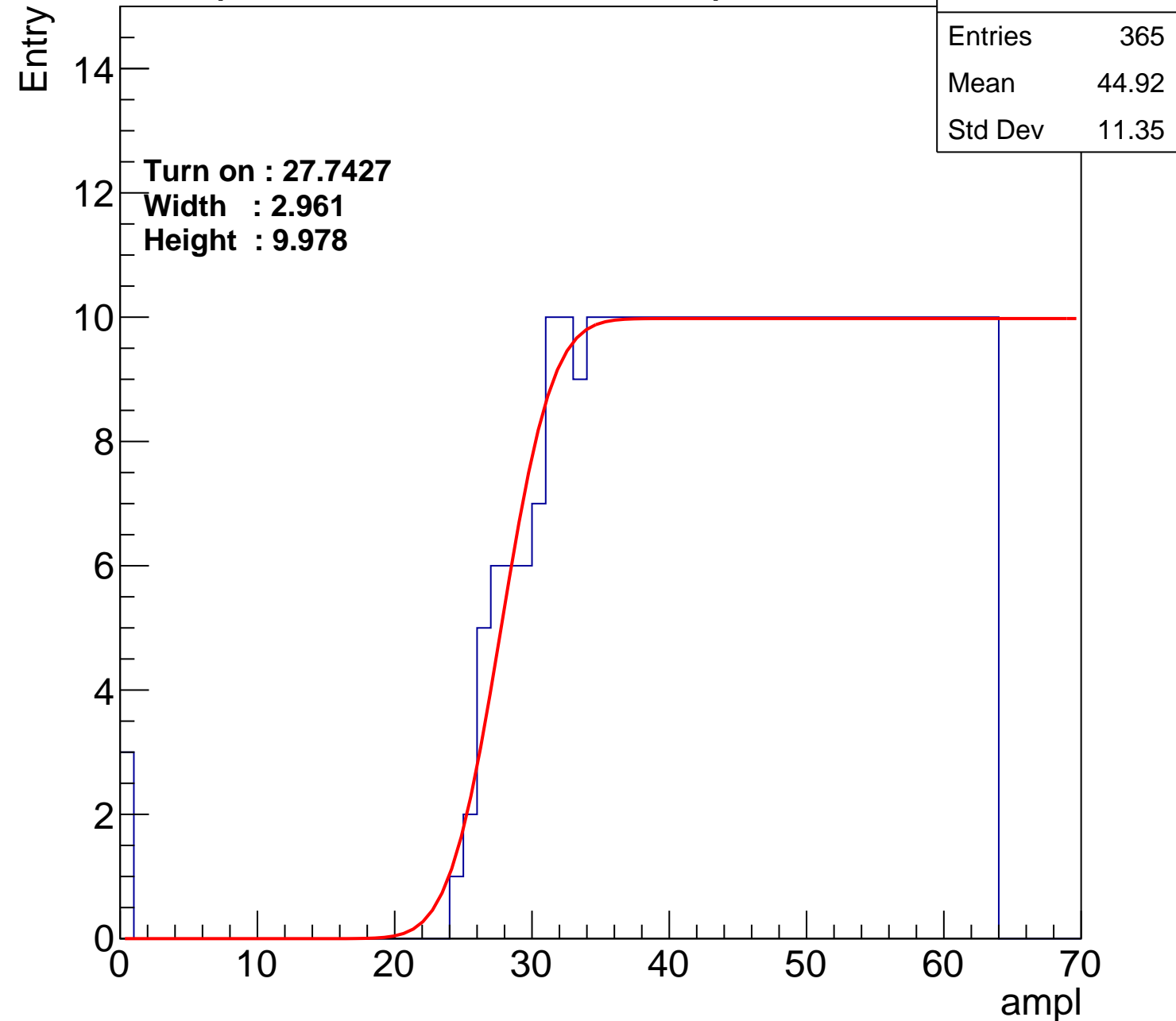
**Width : 2.961**

**Height : 9.978**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch42

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 43.99 |
| Std Dev | 11.63 |

**Turn on : 25.8858**

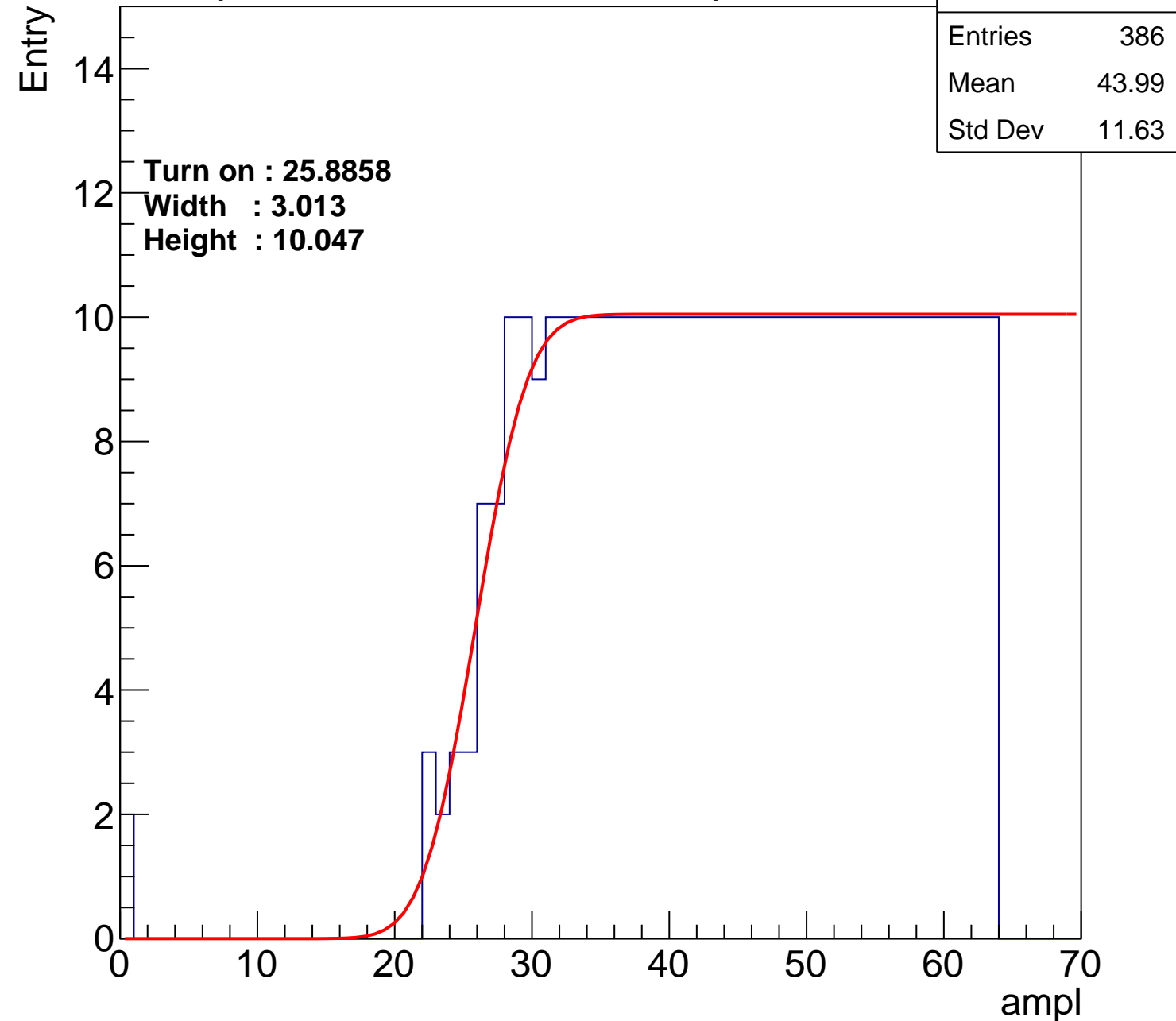
**Width : 3.013**

**Height : 10.047**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch43

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.34 |
| Std Dev | 10.95 |

Turn on : 27.5977

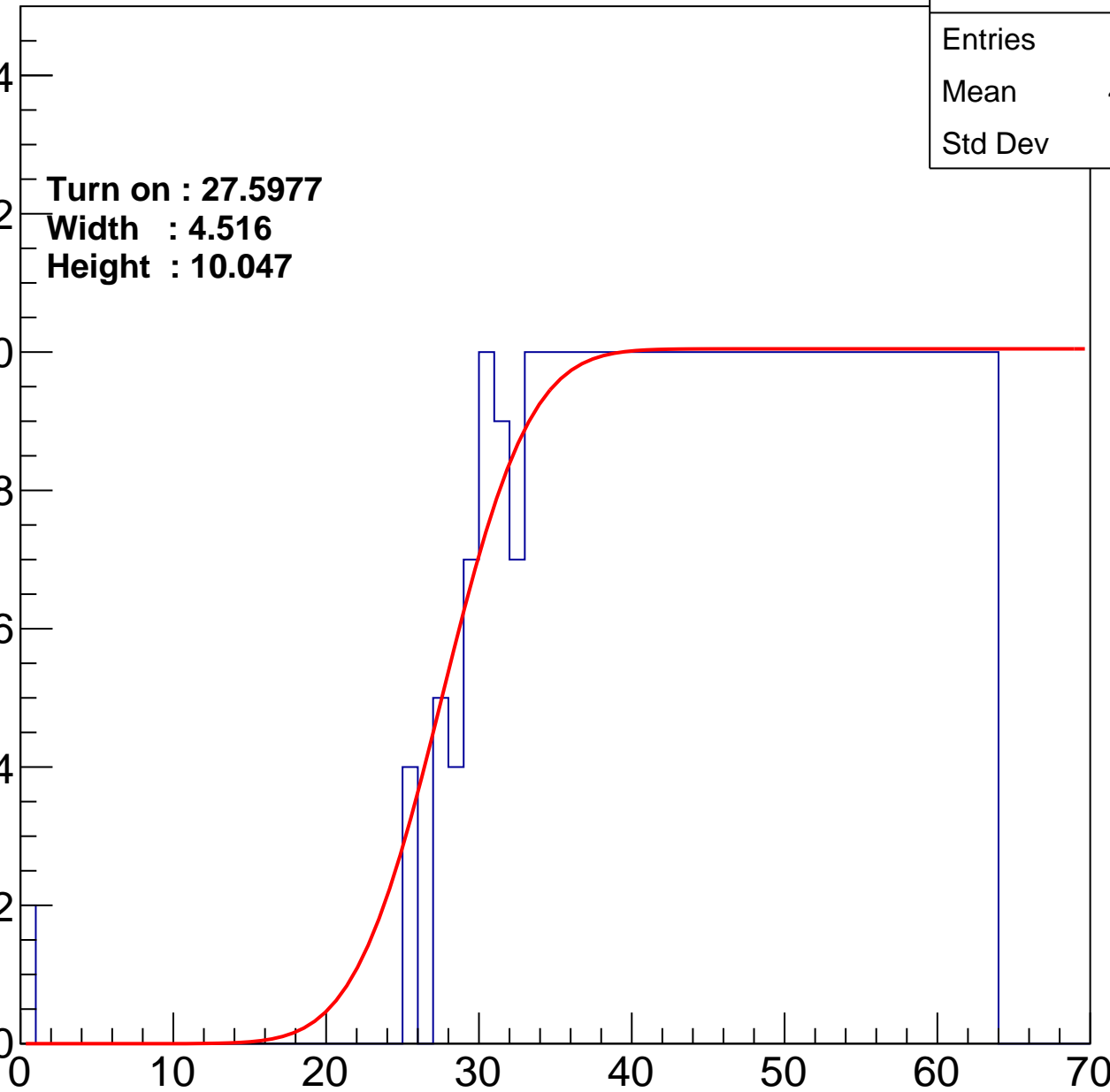
Width : 4.516

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch44

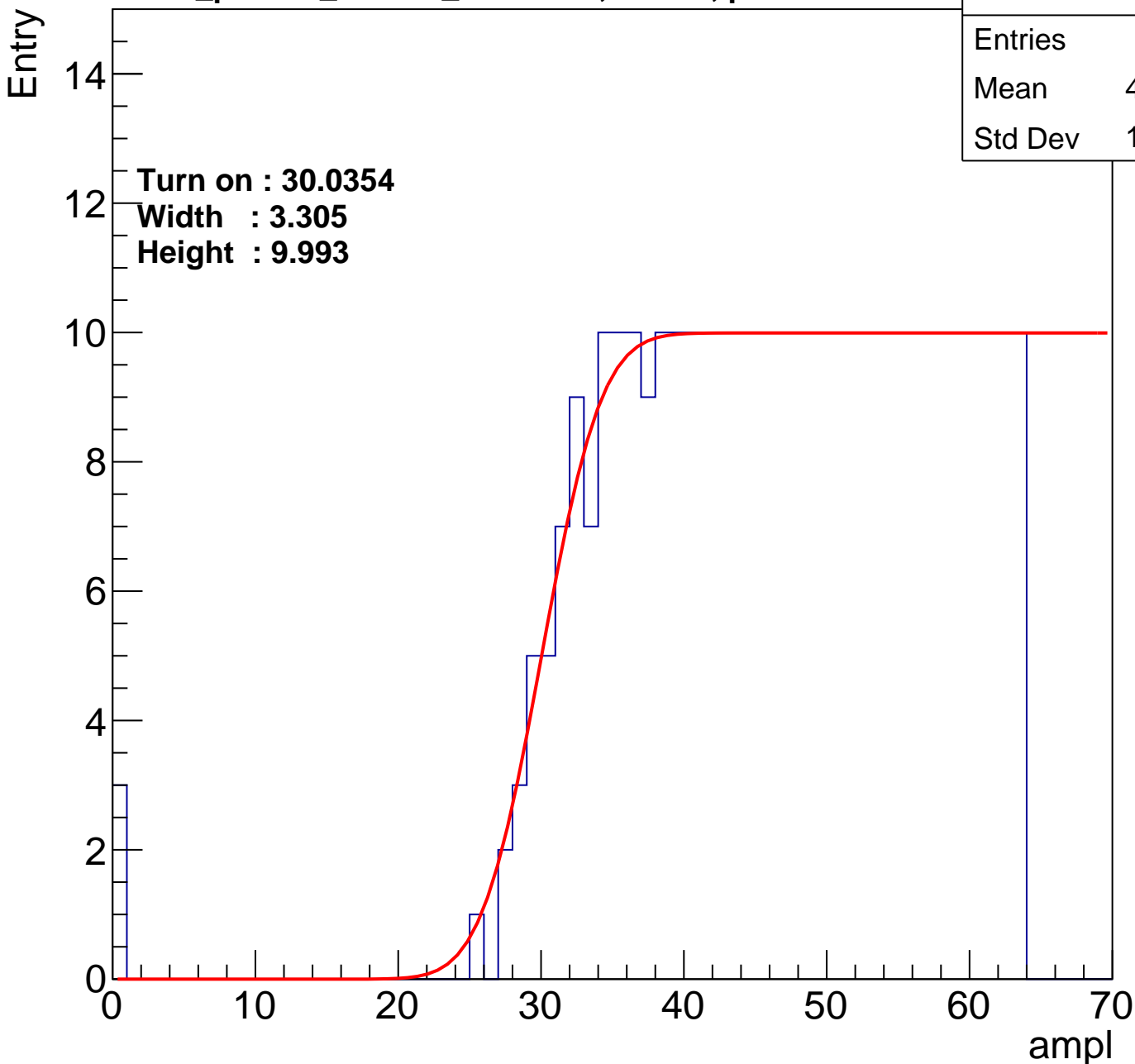
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 341   |
| Mean    | 46.06 |
| Std Dev | 10.83 |

**Turn on : 30.0354**

**Width : 3.305**

**Height : 9.993**



# B1L003S, U13-ch45

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.37 |
| Std Dev | 11.88 |

Turn on : 27.0555

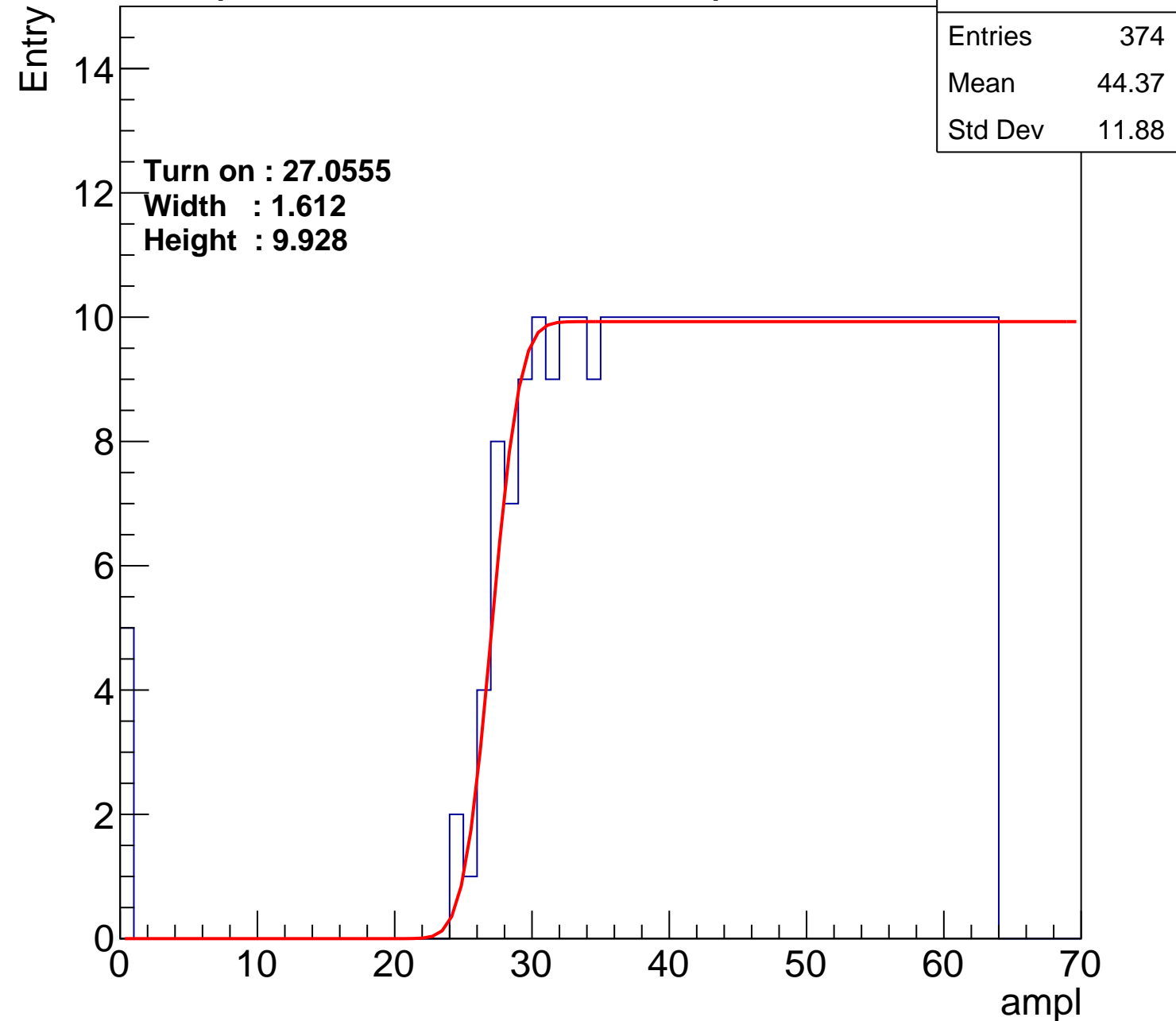
Width : 1.612

Height : 9.928

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch46

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.19 |
| Std Dev | 11.04 |

Turn on : 28.4984

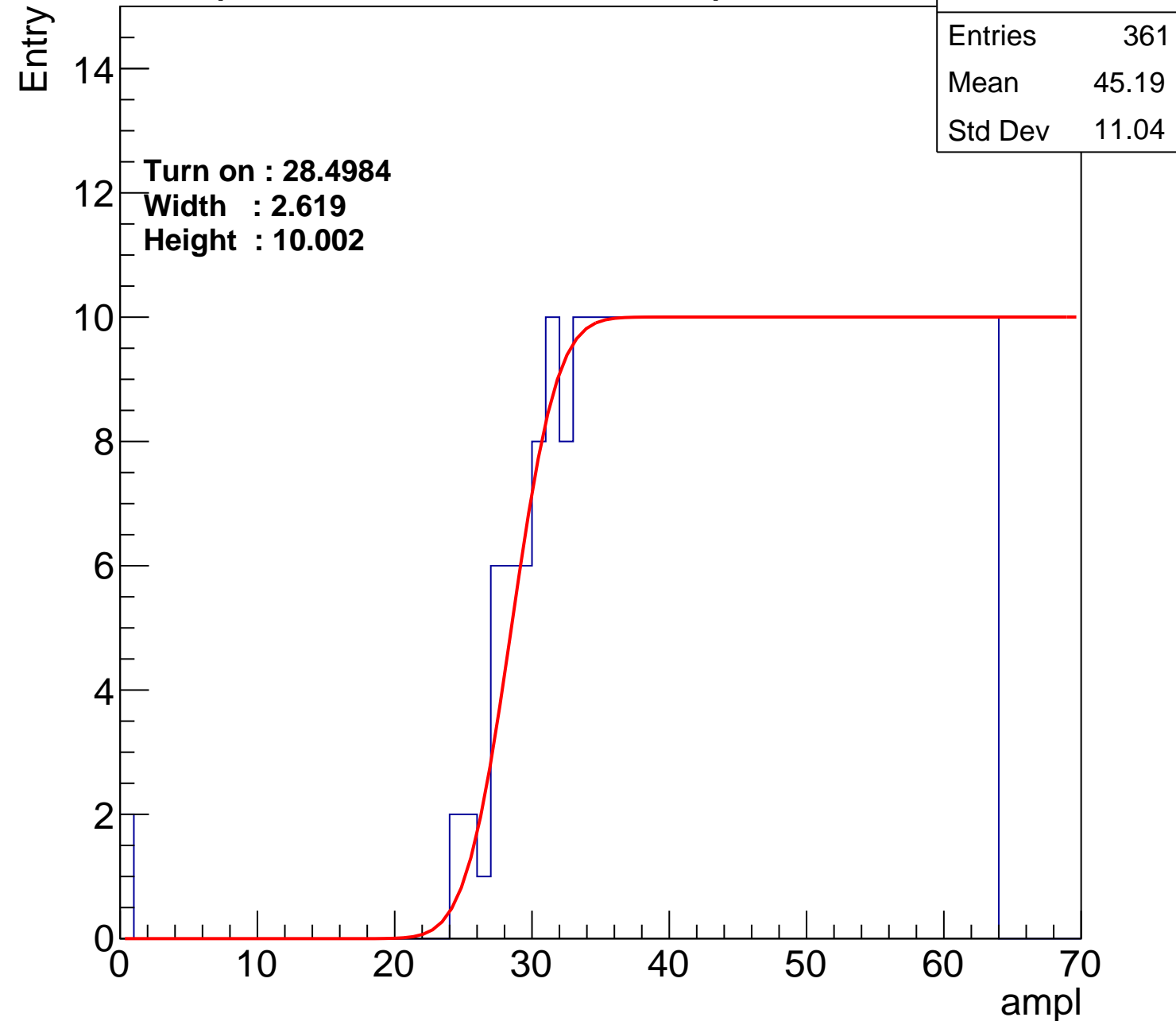
Width : 2.619

Height : 10.002

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch47

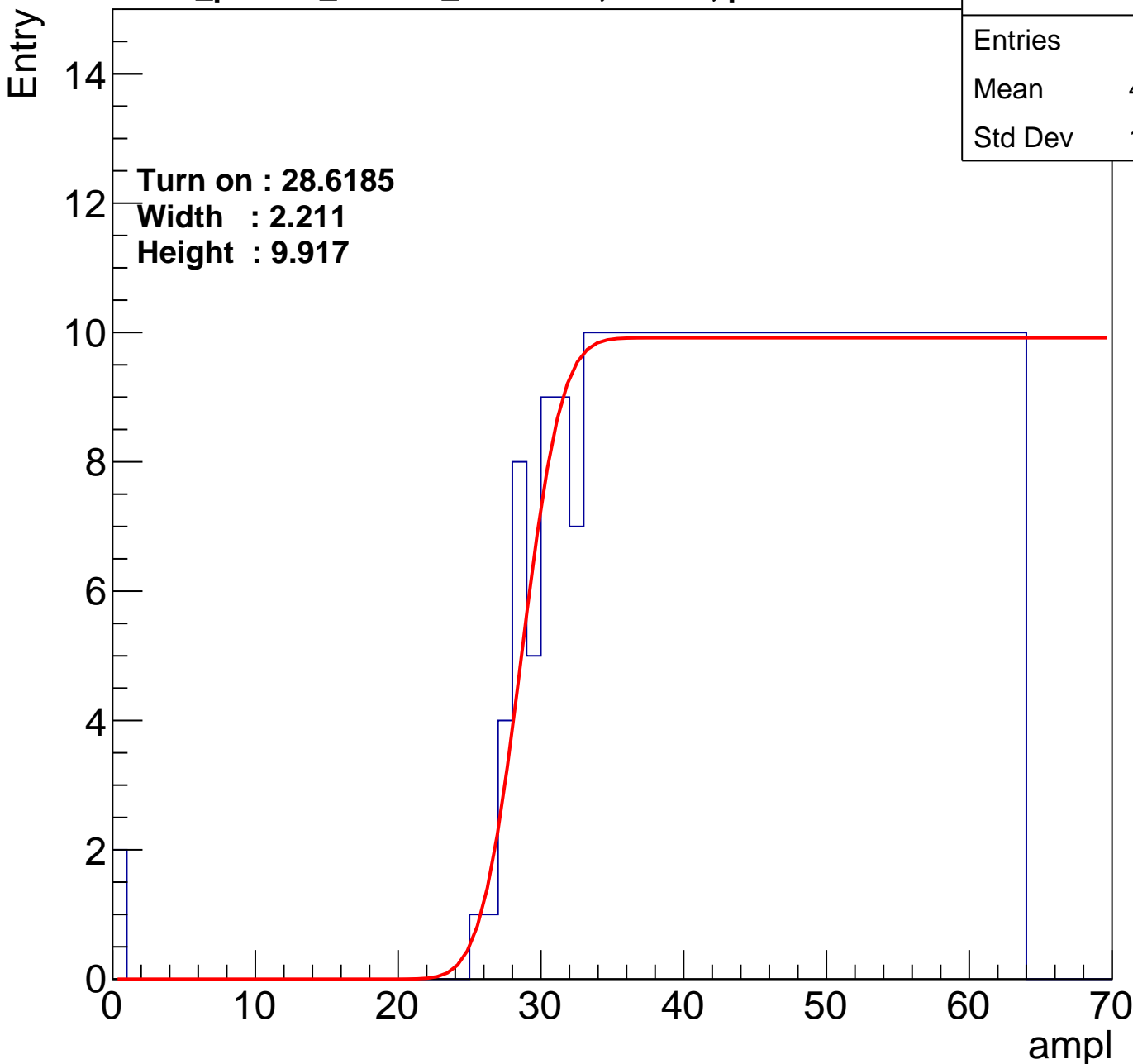
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 356   |
| Mean    | 45.45 |
| Std Dev | 10.89 |

**Turn on : 28.6185**

**Width : 2.211**

**Height : 9.917**



# B1L003S, U13-ch48

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 389   |
| Mean    | 43.83 |
| Std Dev | 11.73 |

Turn on : 25.6623

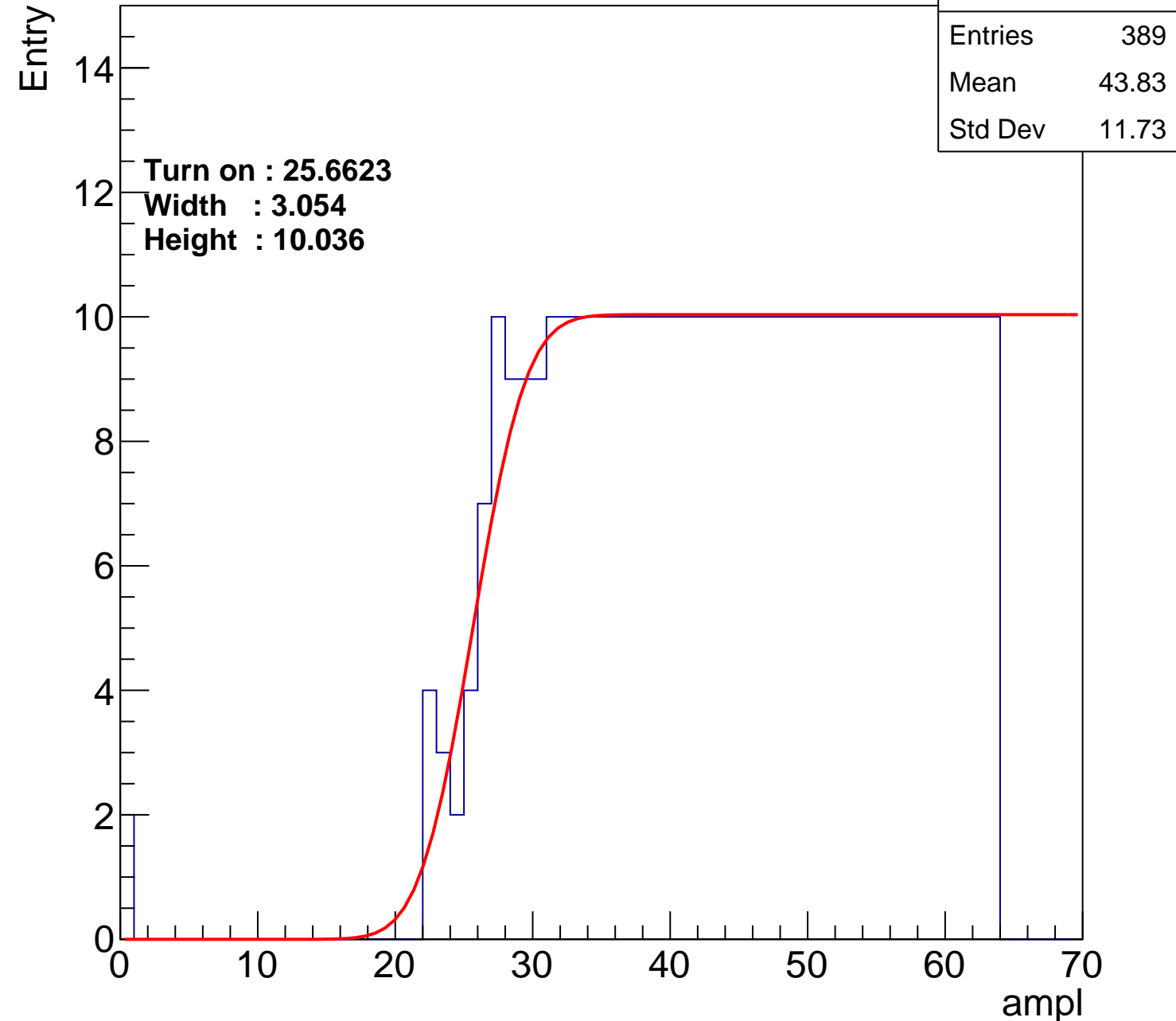
Width : 3.054

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch49

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.67 |
| Std Dev | 11.61 |

Turn on : 27.3766

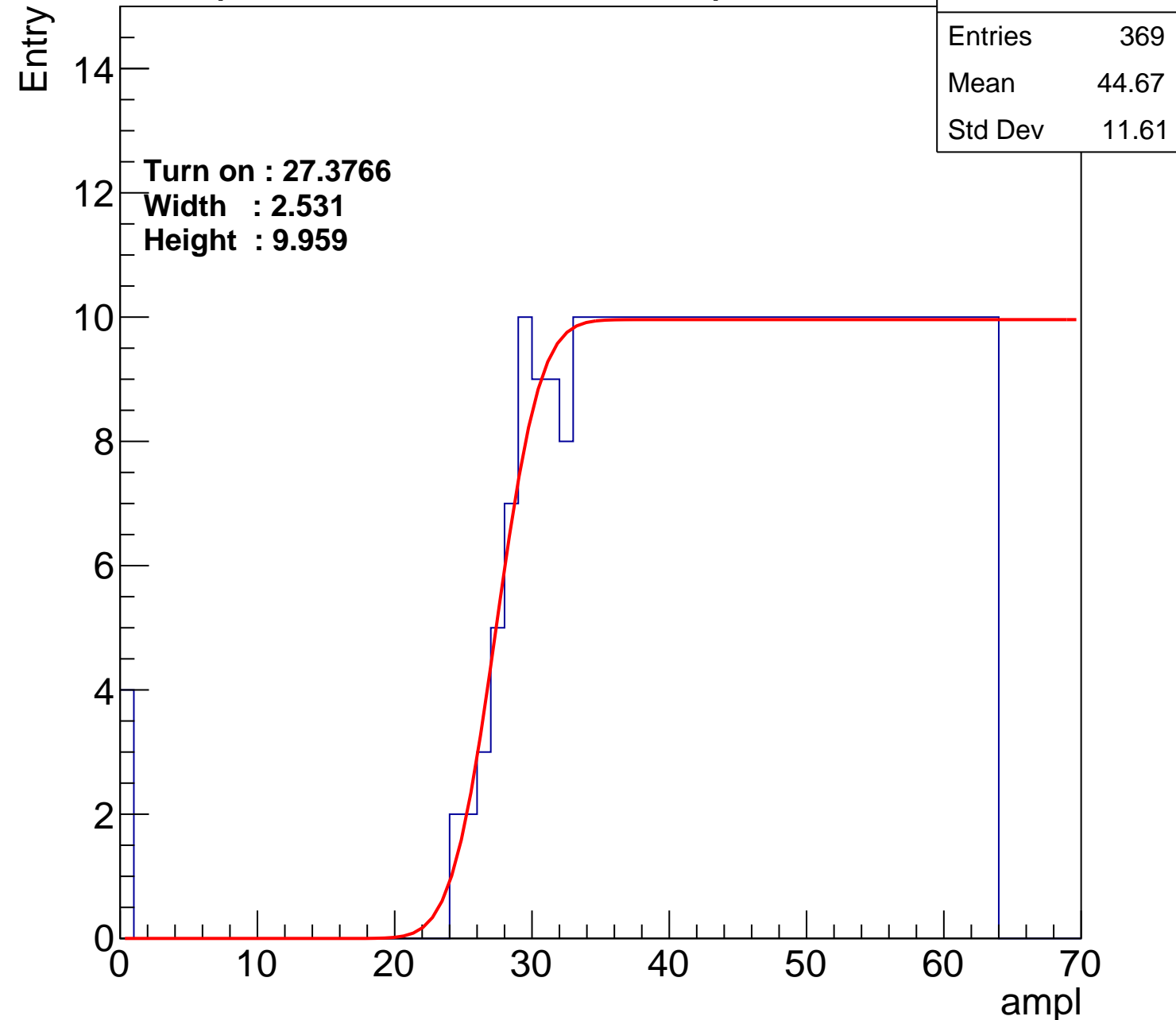
Width : 2.531

Height : 9.959

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch50

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |     |
|---------|-----|
| Entries | 373 |
|---------|-----|

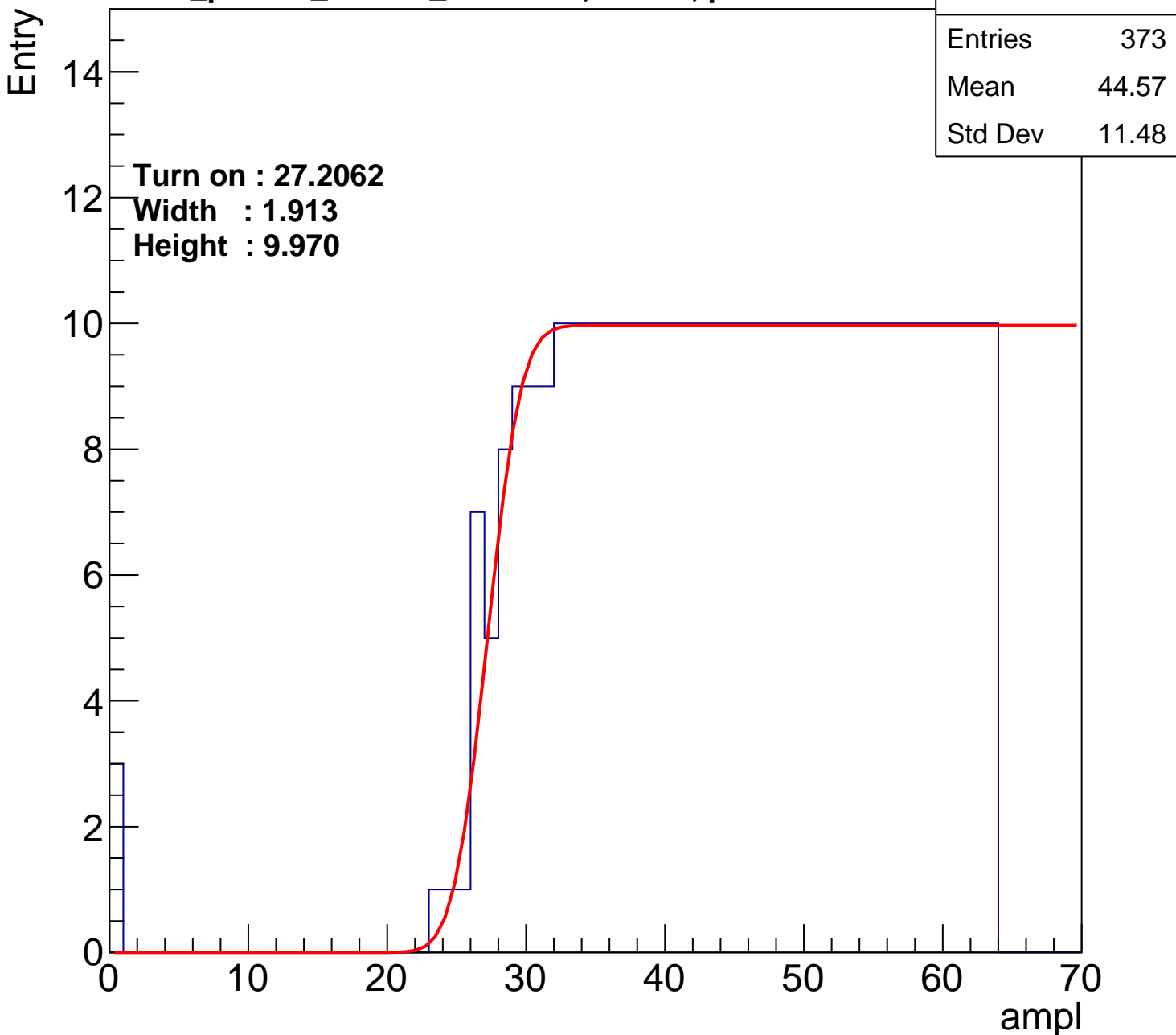
|      |       |
|------|-------|
| Mean | 44.57 |
|------|-------|

|         |       |
|---------|-------|
| Std Dev | 11.48 |
|---------|-------|

**Turn on : 27.2062**

**Width : 1.913**

**Height : 9.970**



# B1L003S, U13-ch51

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 335   |
| Mean    | 46.46 |
| Std Dev | 10.41 |

Turn on : 31.0398

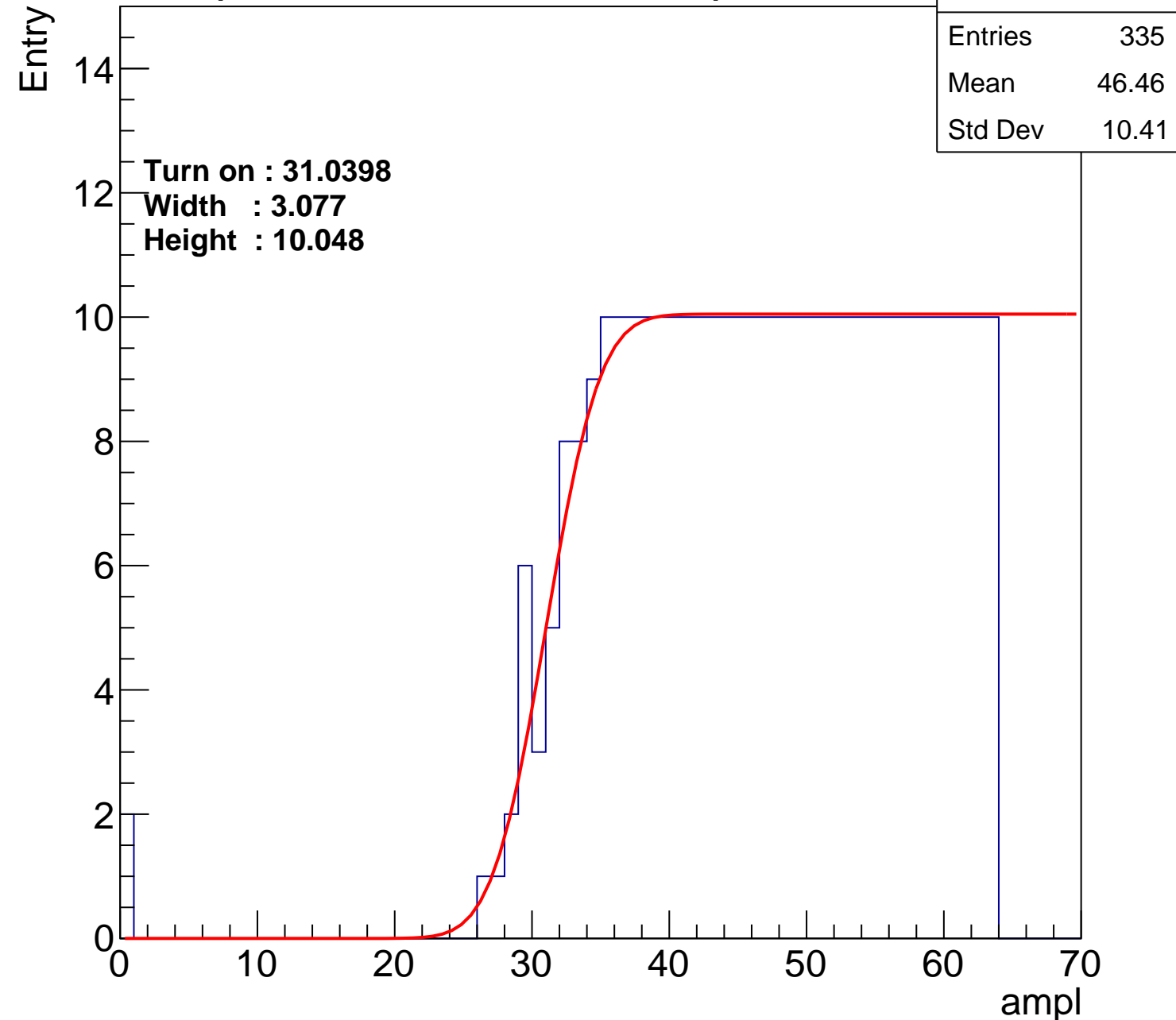
Width : 3.077

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch52

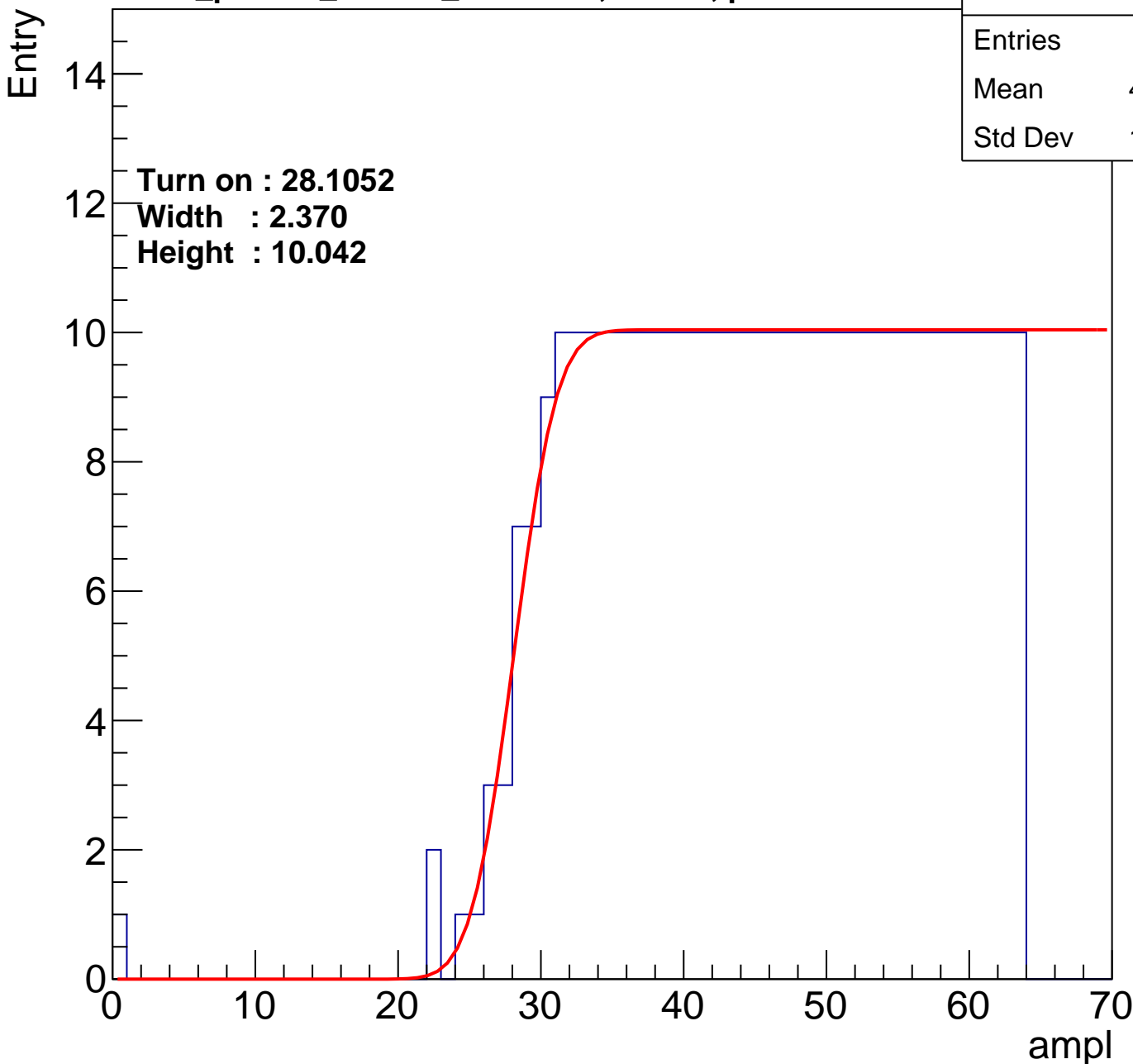
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 364   |
| Mean    | 45.14 |
| Std Dev | 10.88 |

**Turn on : 28.1052**

**Width : 2.370**

**Height : 10.042**



# B1L003S, U13-ch53

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.44 |
| Std Dev | 11.71 |

Turn on : 27.0906

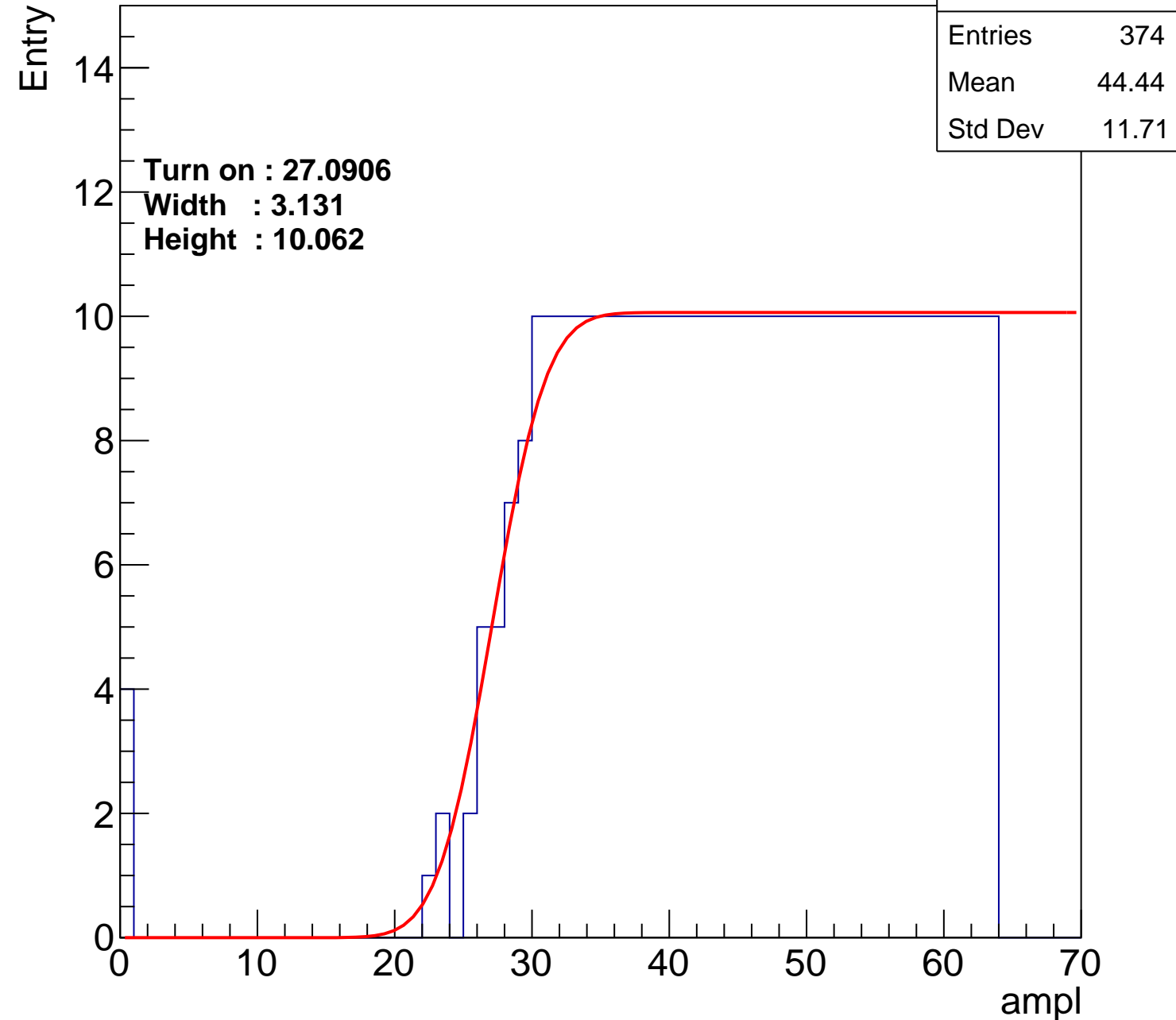
Width : 3.131

Height : 10.062

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch54

calib\_packv5\_042523\_0143.root, FC#13, port D2

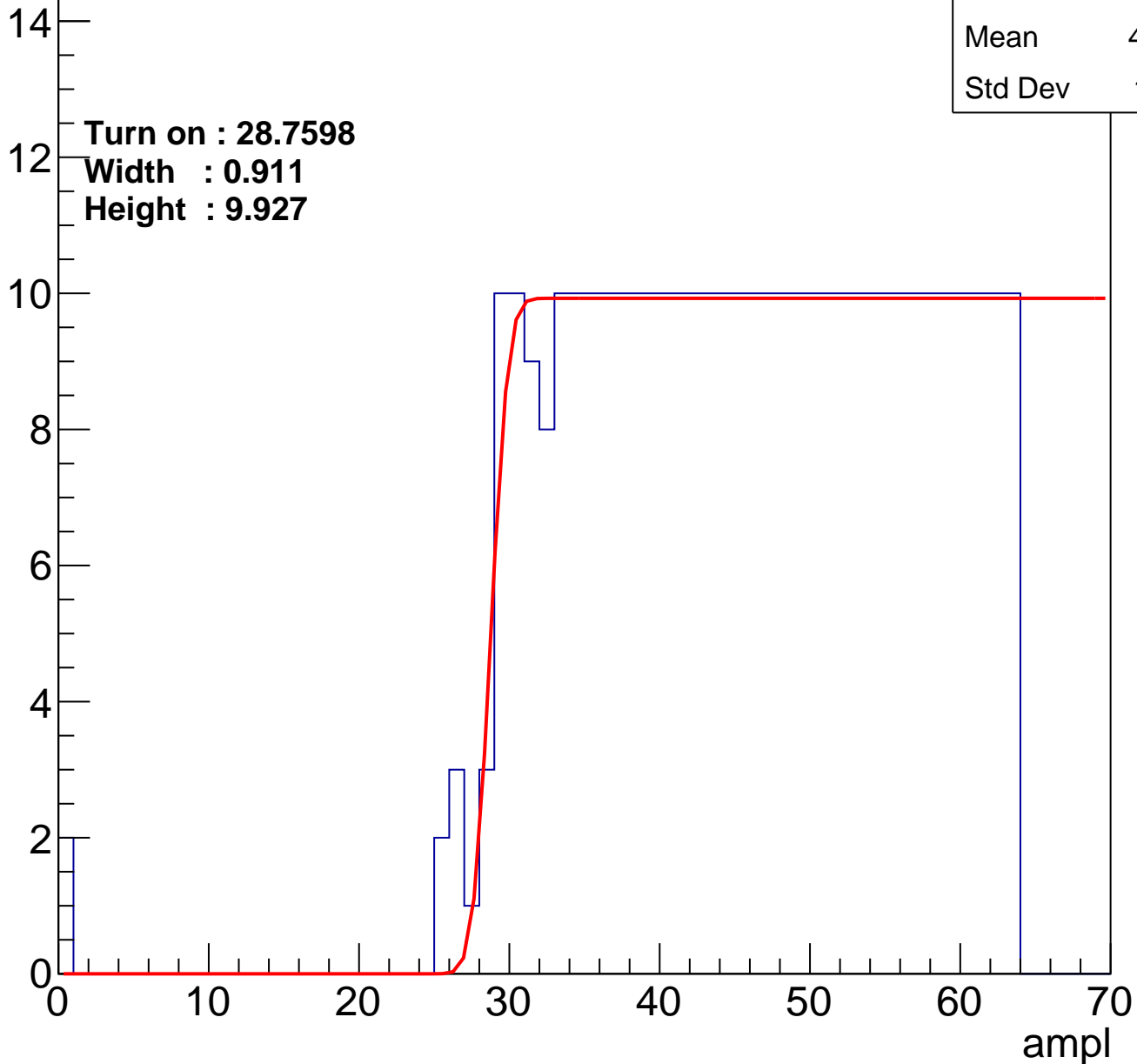
|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.37 |
| Std Dev | 10.91 |

Turn on : 28.7598

Width : 0.911

Height : 9.927

Entry





# B1L003S, U13-ch55

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.2  |
| Std Dev | 11.23 |

**Turn on : 28.8385**

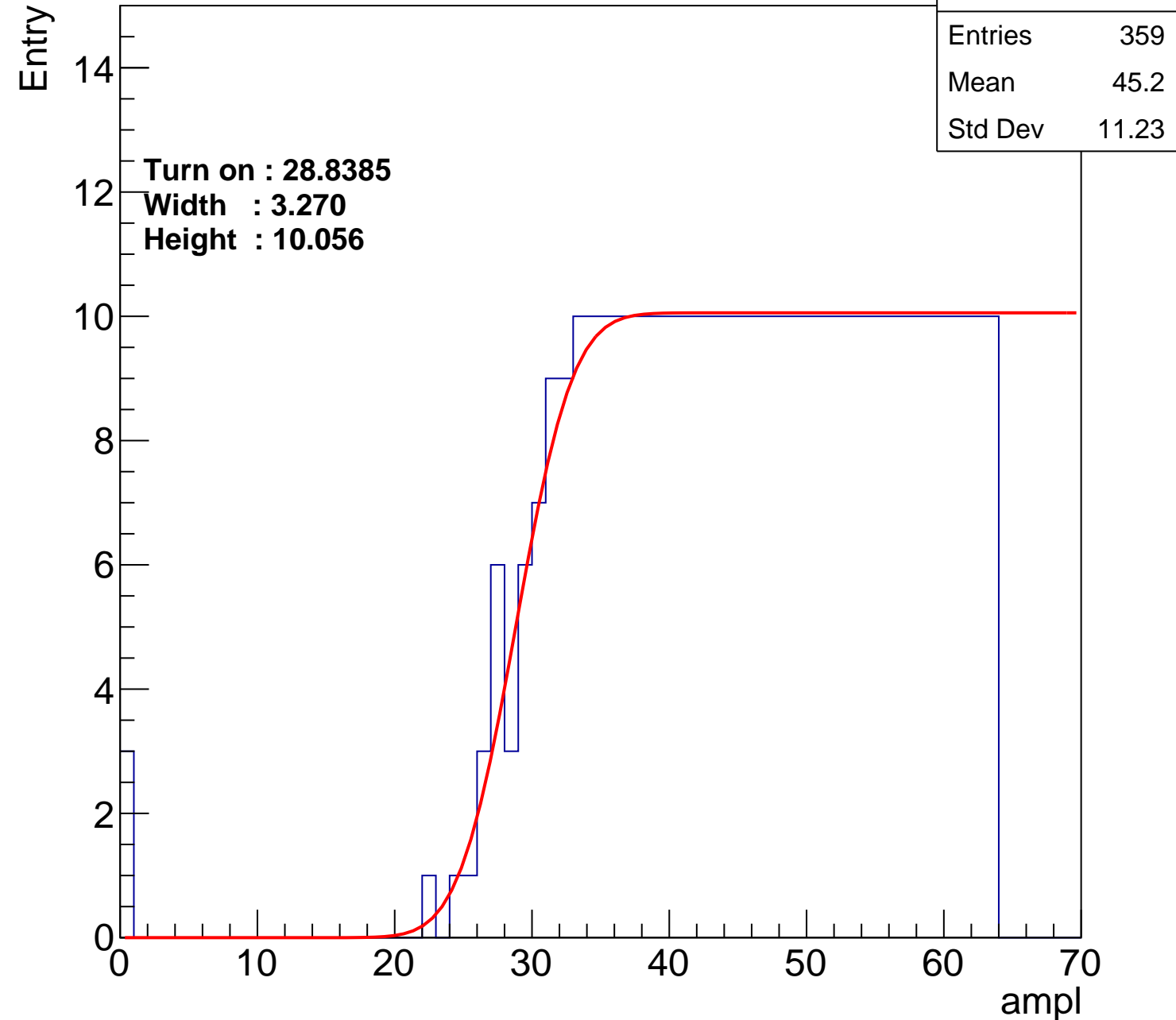
**Width : 3.270**

**Height : 10.056**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch56

calib\_packv5\_042523\_0143.root, FC#13, port D2

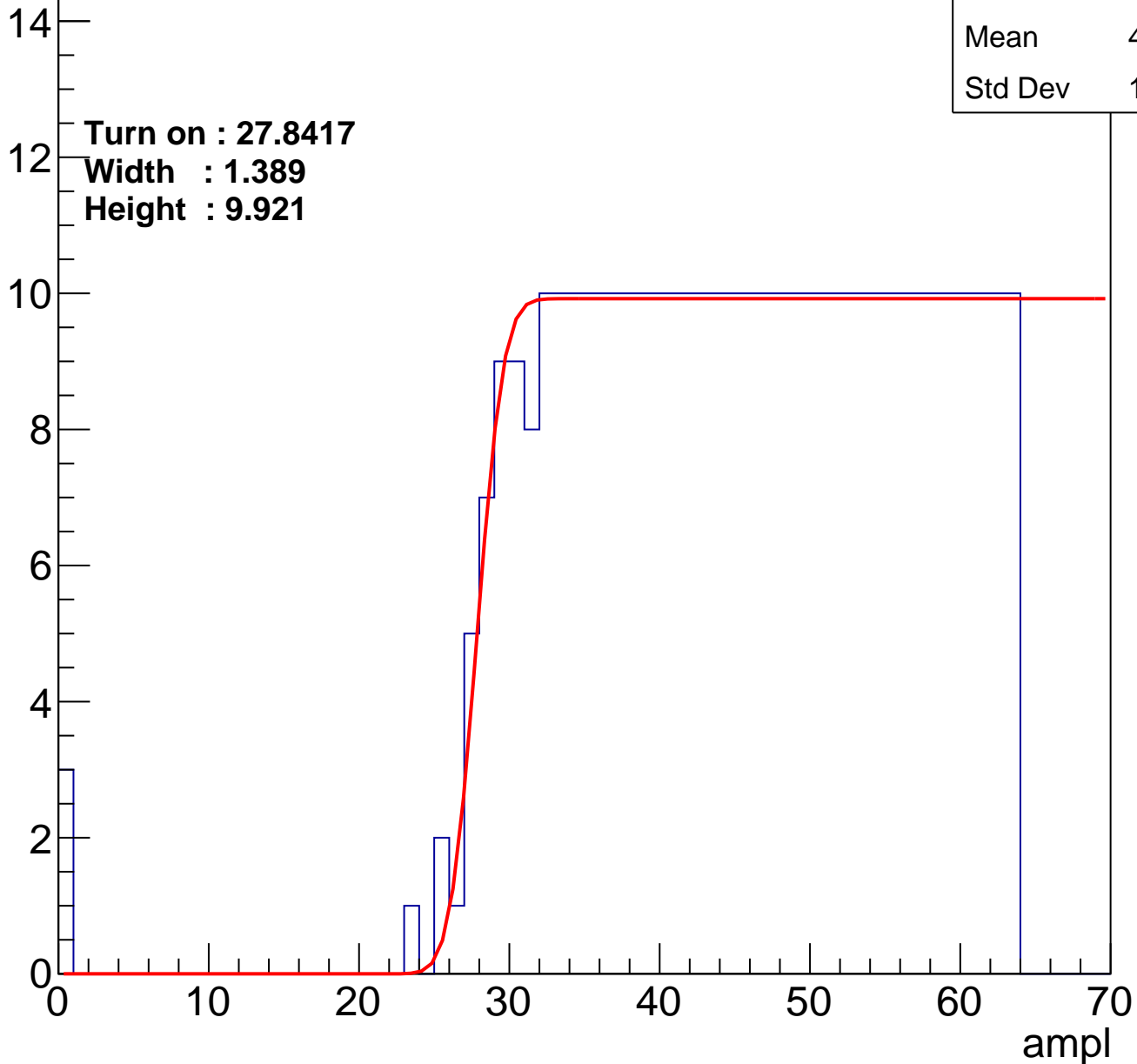
Entry

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 44.96 |
| Std Dev | 11.29 |

Turn on : 27.8417

Width : 1.389

Height : 9.921



# B1L003S, U13-ch57

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 347   |
| Mean    | 45.97 |
| Std Dev | 10.44 |

Turn on : 29.5494

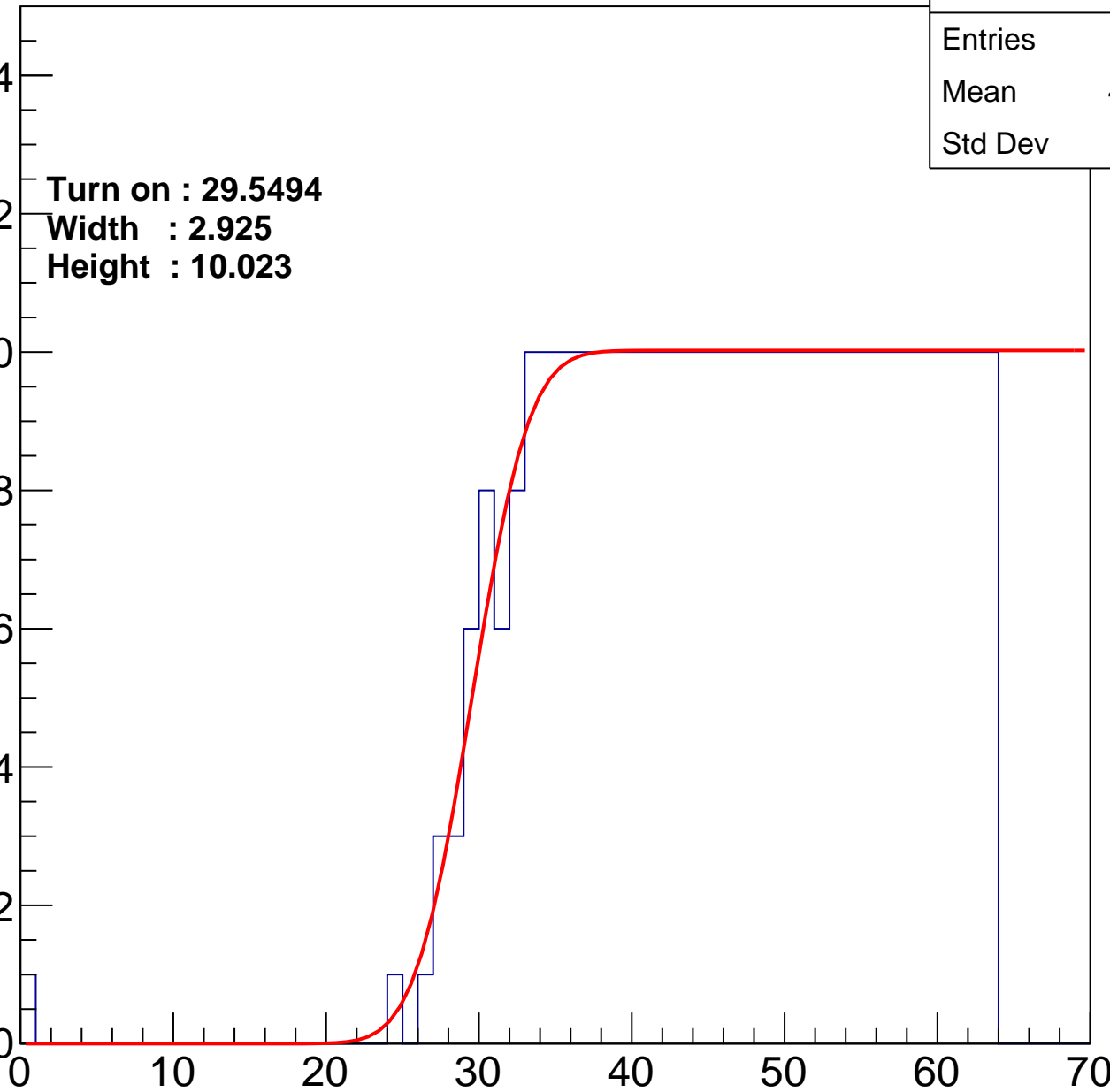
Width : 2.925

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch58

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.68 |
| Std Dev | 10.95 |

Turn on : 29.0019

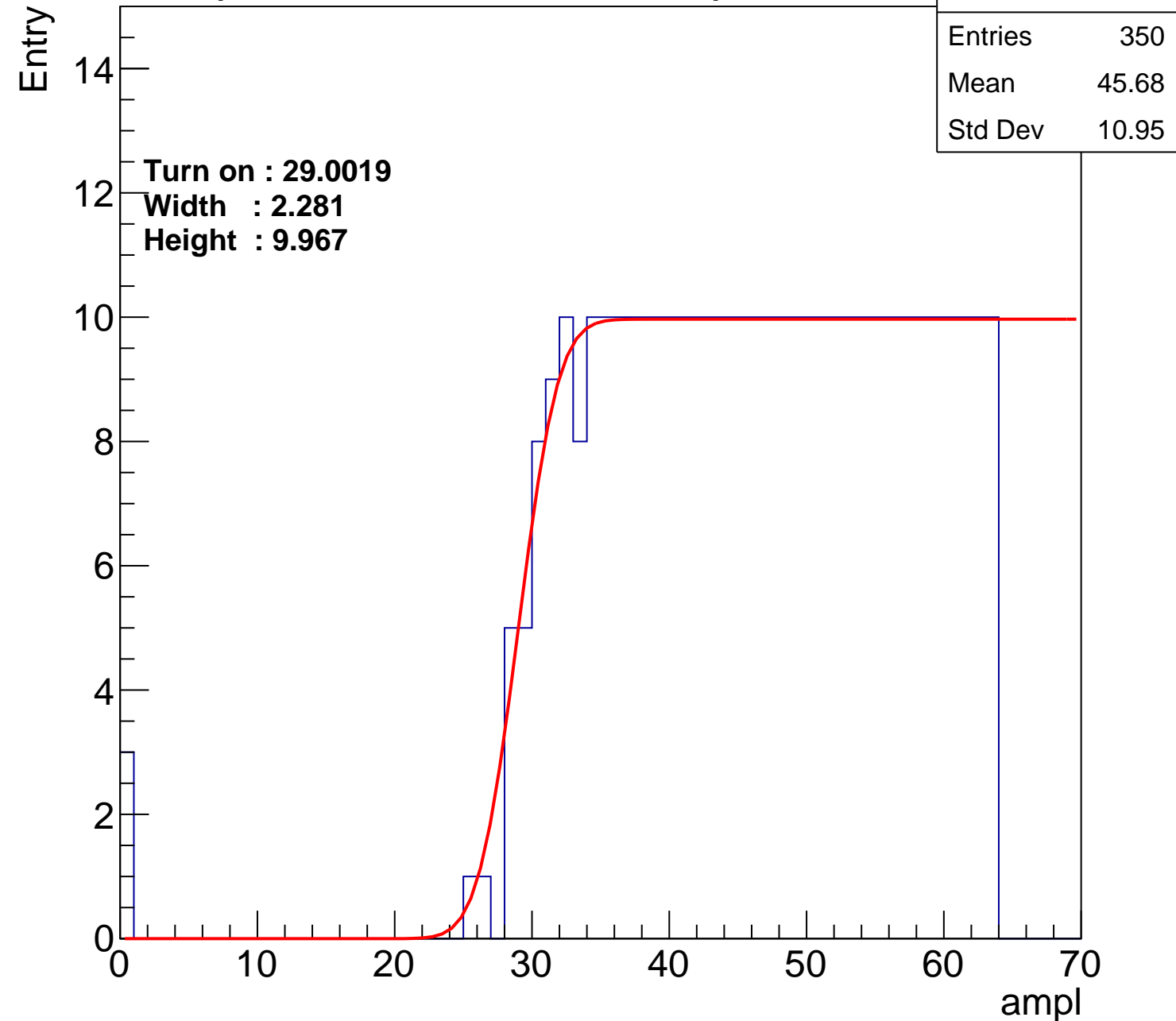
Width : 2.281

Height : 9.967

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch59

calib\_packv5\_042523\_0143.root, FC#13, port D2

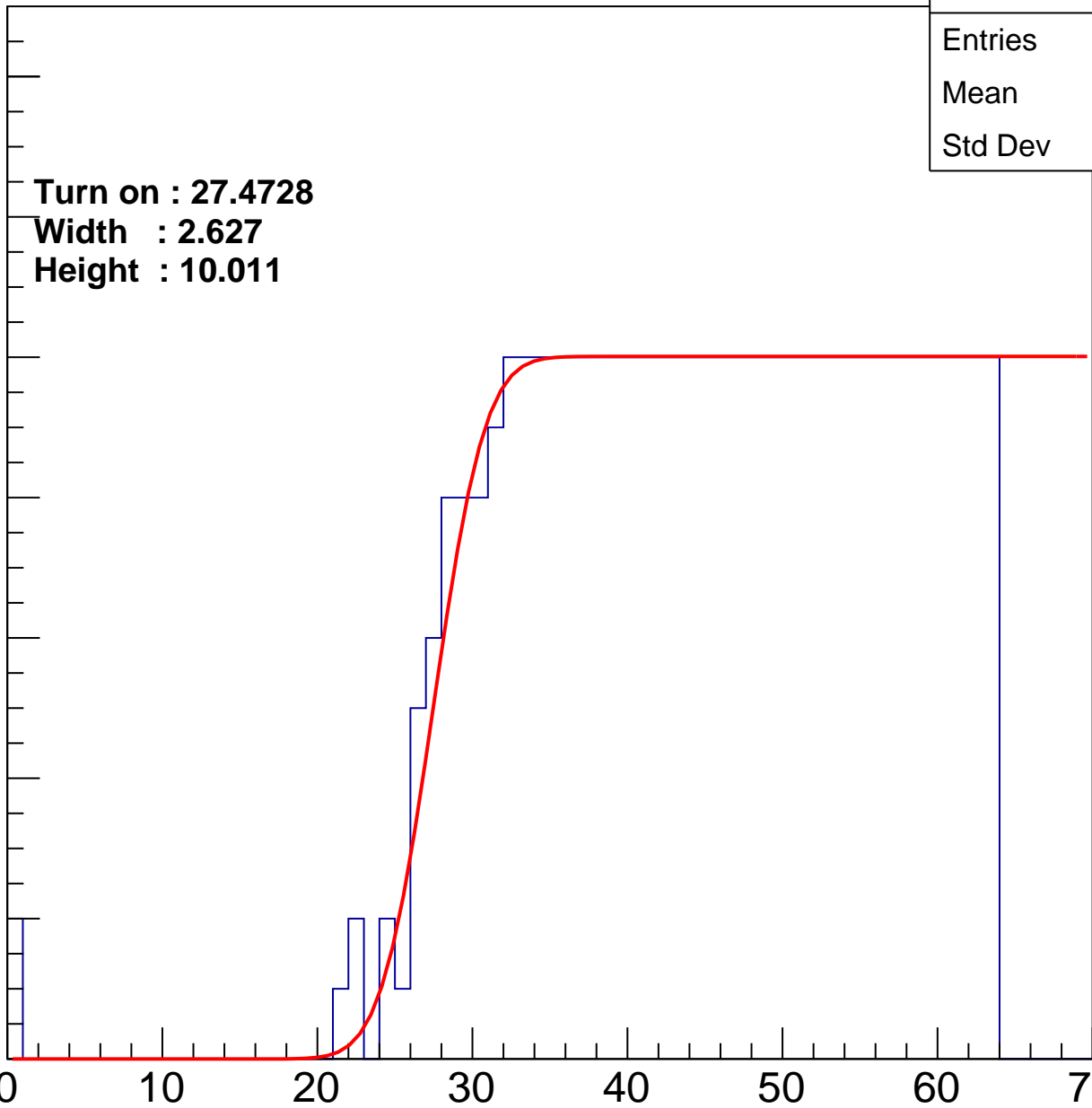
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 27.4728  
Width : 2.627  
Height : 10.011

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.64 |
| Std Dev | 11.34 |

ampl



# B1L003S, U13-ch60

calib\_packv5\_042523\_0143.root, FC#13, port D2

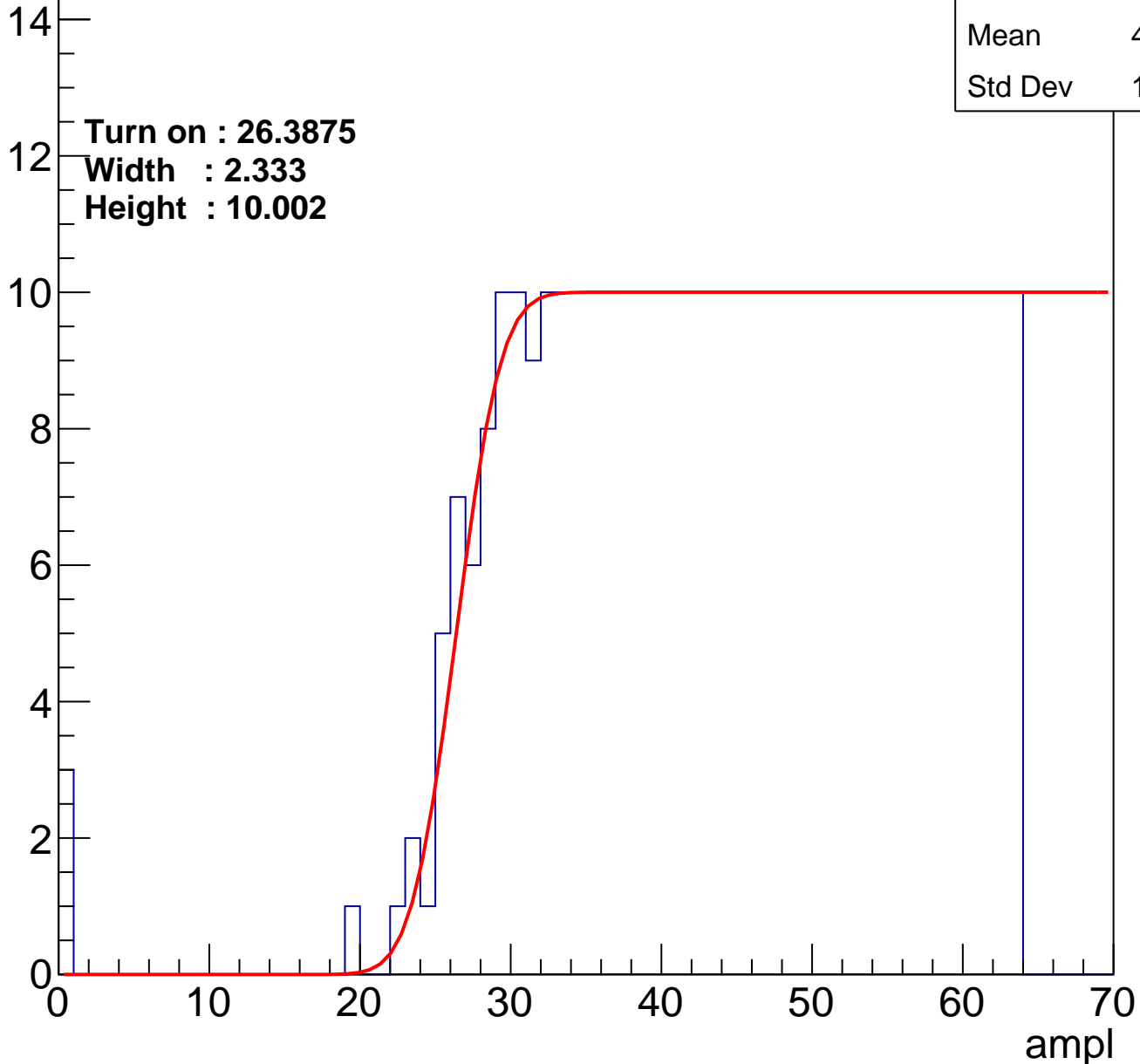
|         |       |
|---------|-------|
| Entries | 383   |
| Mean    | 44.05 |
| Std Dev | 11.76 |

Turn on : 26.3875

Width : 2.333

Height : 10.002

Entry



# B1L003S, U13-ch61

calib\_packv5\_042523\_0143.root, FC#13, port D2

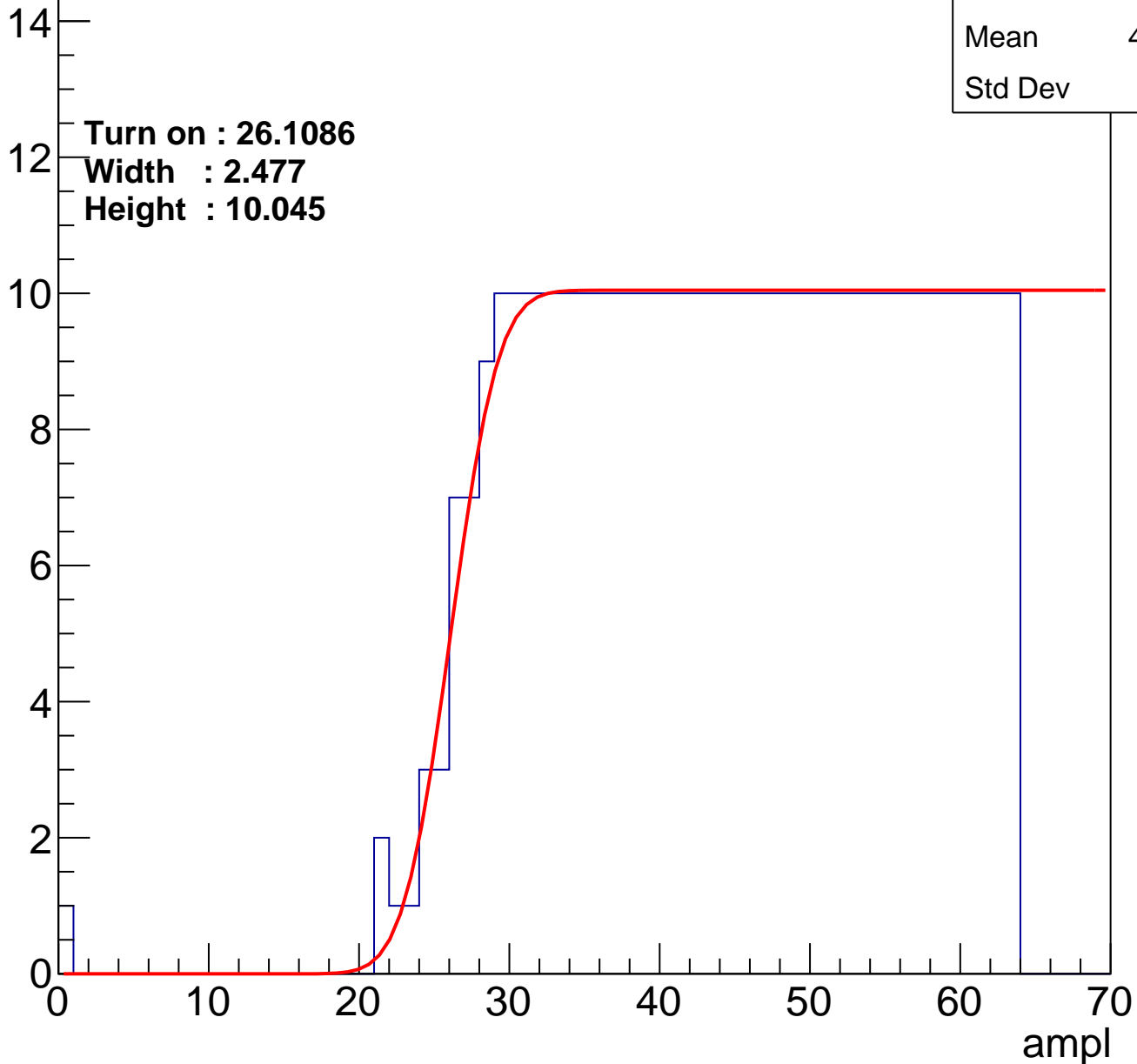
|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 44.16 |
| Std Dev | 11.4  |

Turn on : 26.1086

Width : 2.477

Height : 10.045

Entry



# B1L003S, U13-ch62

calib\_packv5\_042523\_0143.root, FC#13, port D2

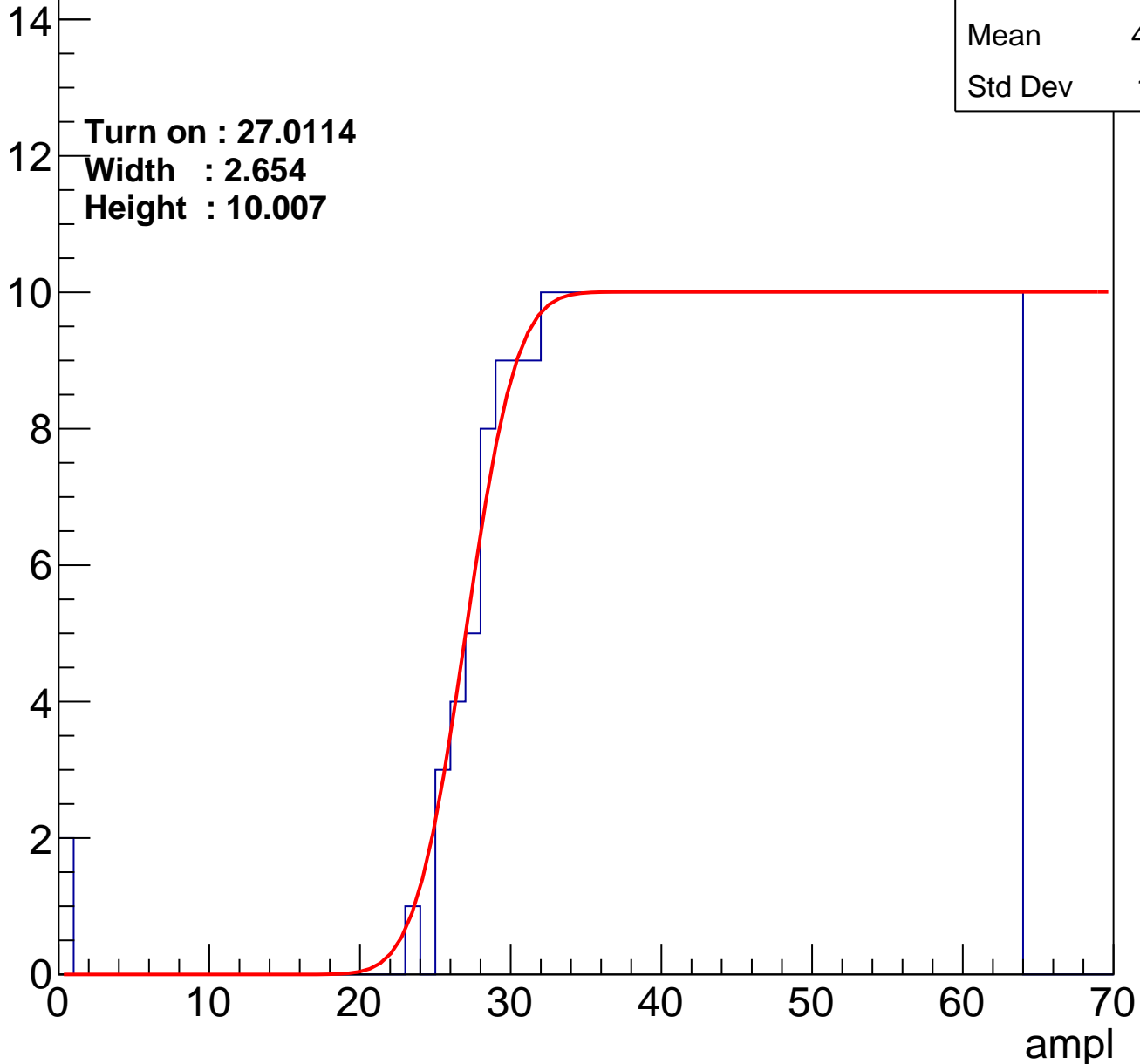
|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.79 |
| Std Dev | 11.21 |

Turn on : 27.0114

Width : 2.654

Height : 10.007

Entry





# B1L003S, U13-ch63

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.15 |
| Std Dev | 12.19 |

Turn on : 27.3570

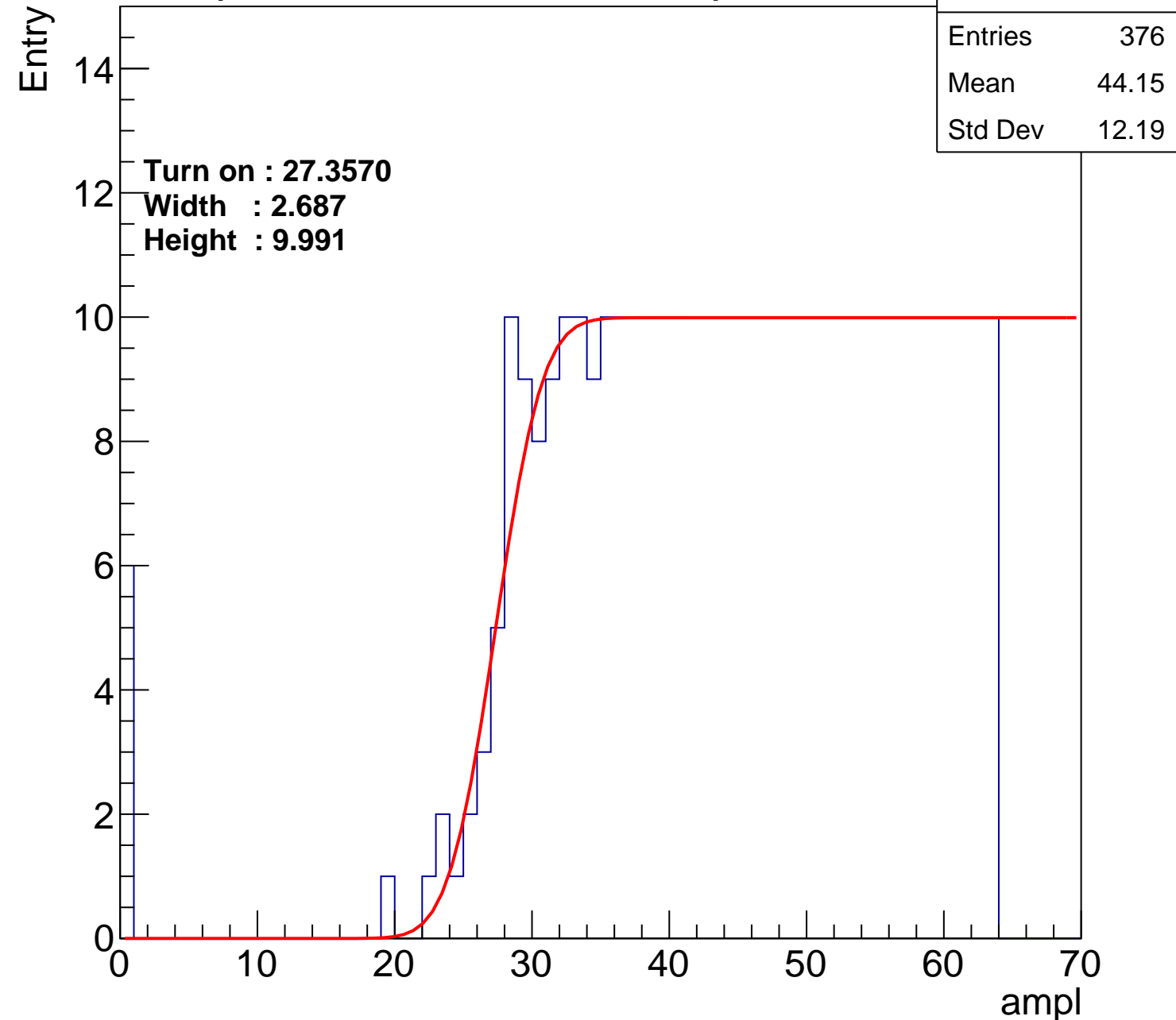
Width : 2.687

Height : 9.991

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch64

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.02 |
| Std Dev | 12.7  |

Turn on : 28.0322

Width : 3.721

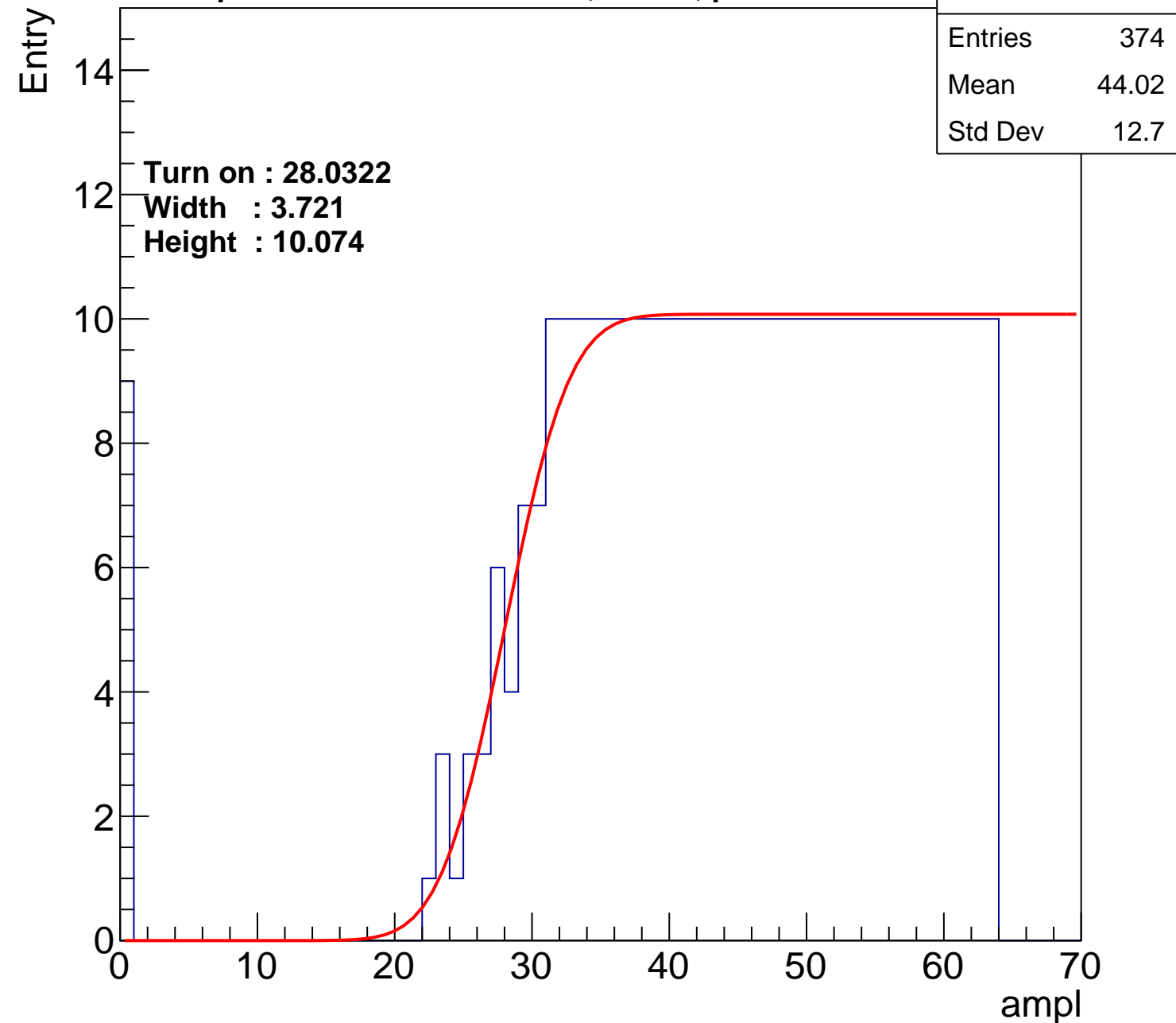
Height : 10.074

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L003S, U13-ch65

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.5  |
| Std Dev | 11.21 |

Turn on : 26.2349

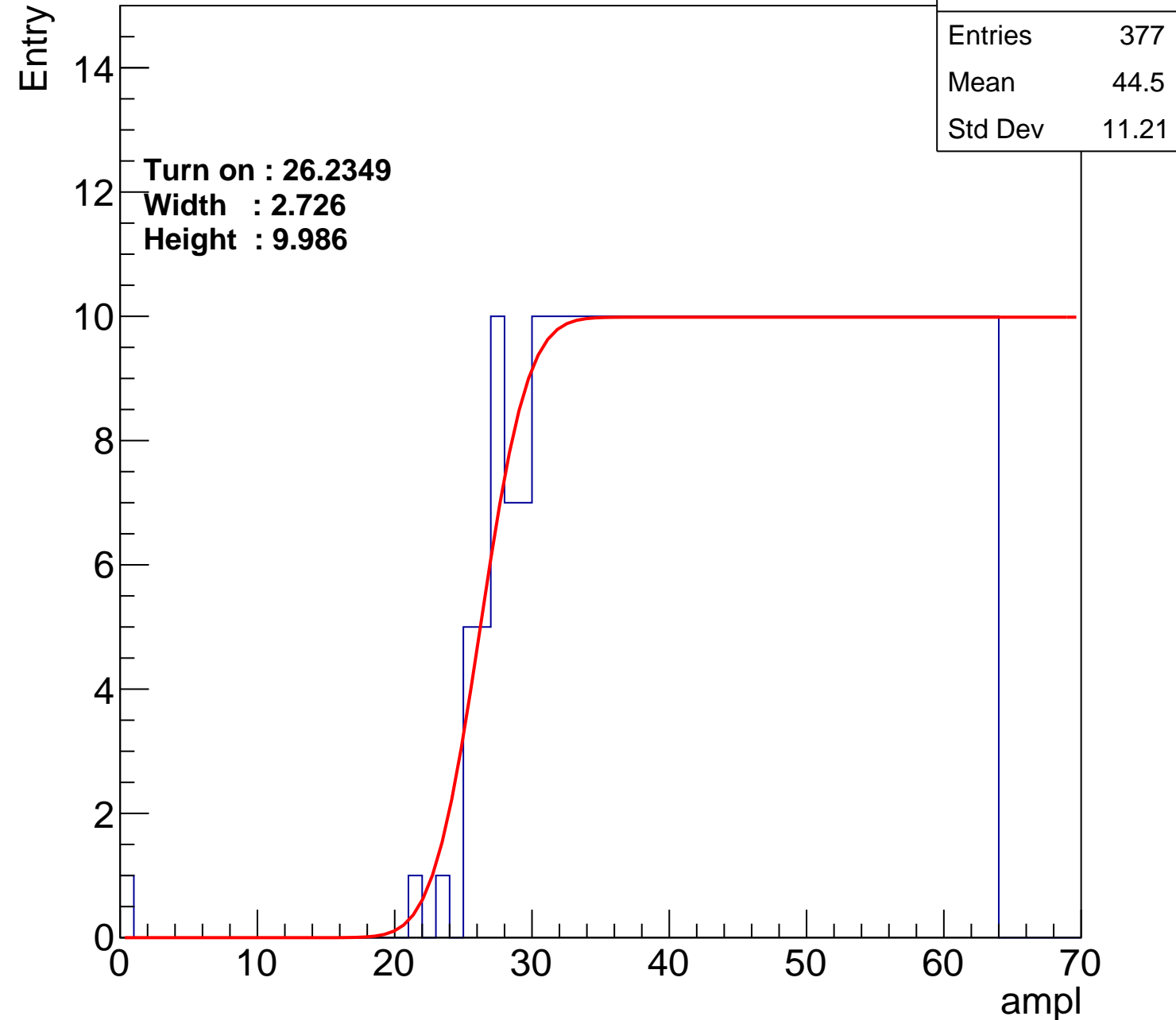
Width : 2.726

Height : 9.986

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch66

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.68 |
| Std Dev | 11.32 |

Turn on : 27.1327

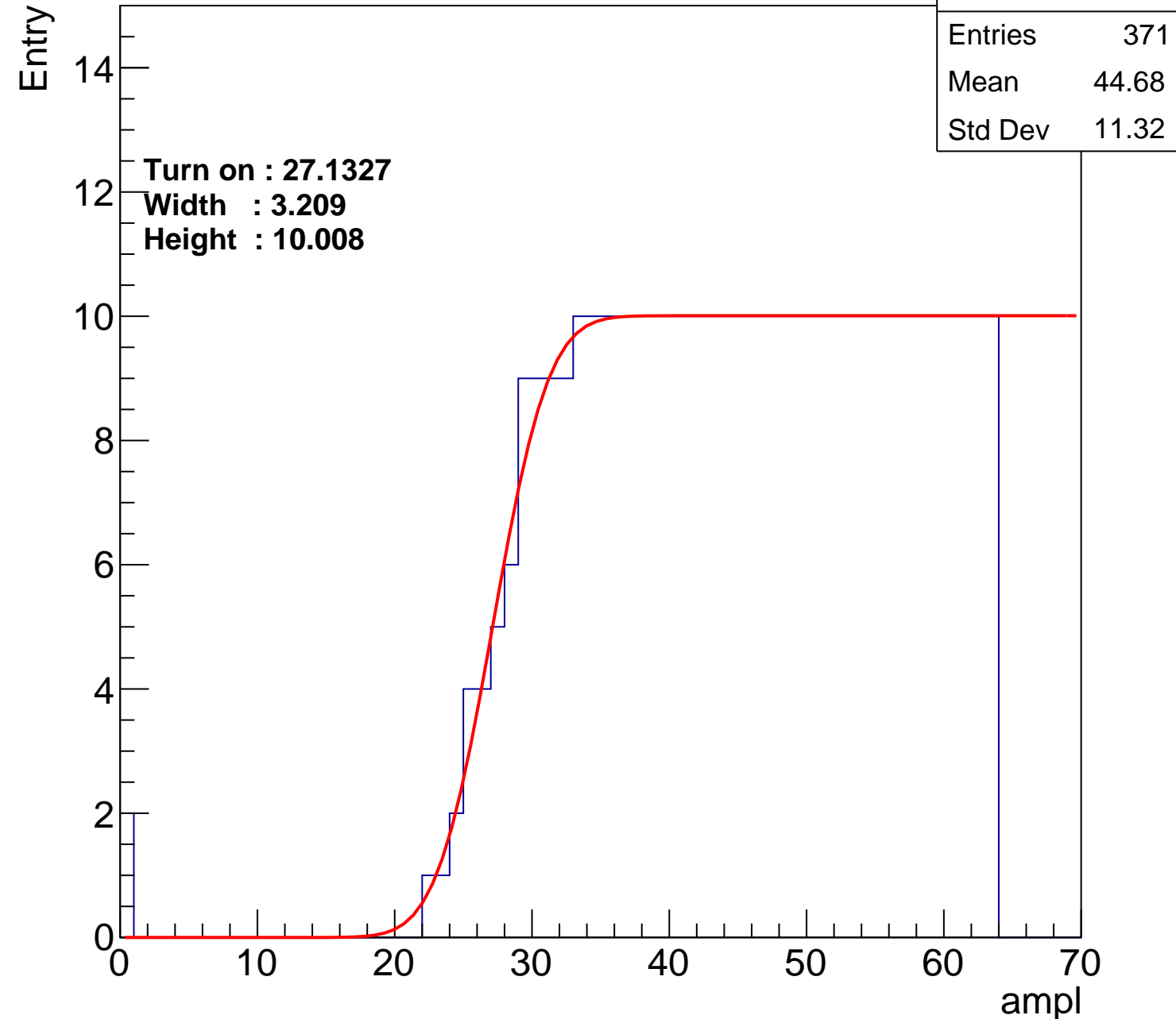
Width : 3.209

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch67

calib\_packv5\_042523\_0143.root, FC#13, port D2

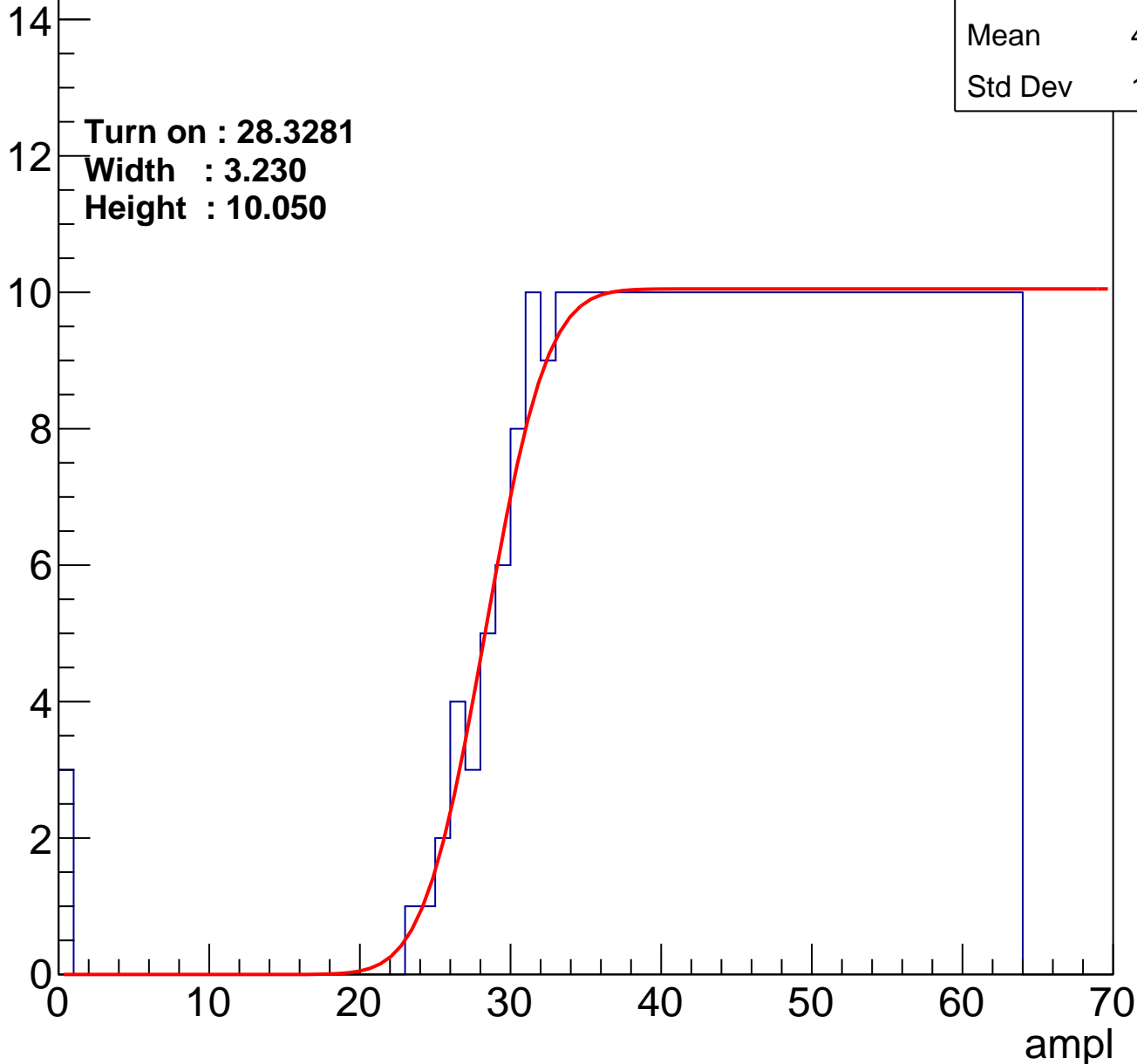
|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.07 |
| Std Dev | 11.28 |

Turn on : 28.3281

Width : 3.230

Height : 10.050

Entry



# B1L003S, U13-ch68

calib\_packv5\_042523\_0143.root, FC#13, port D2

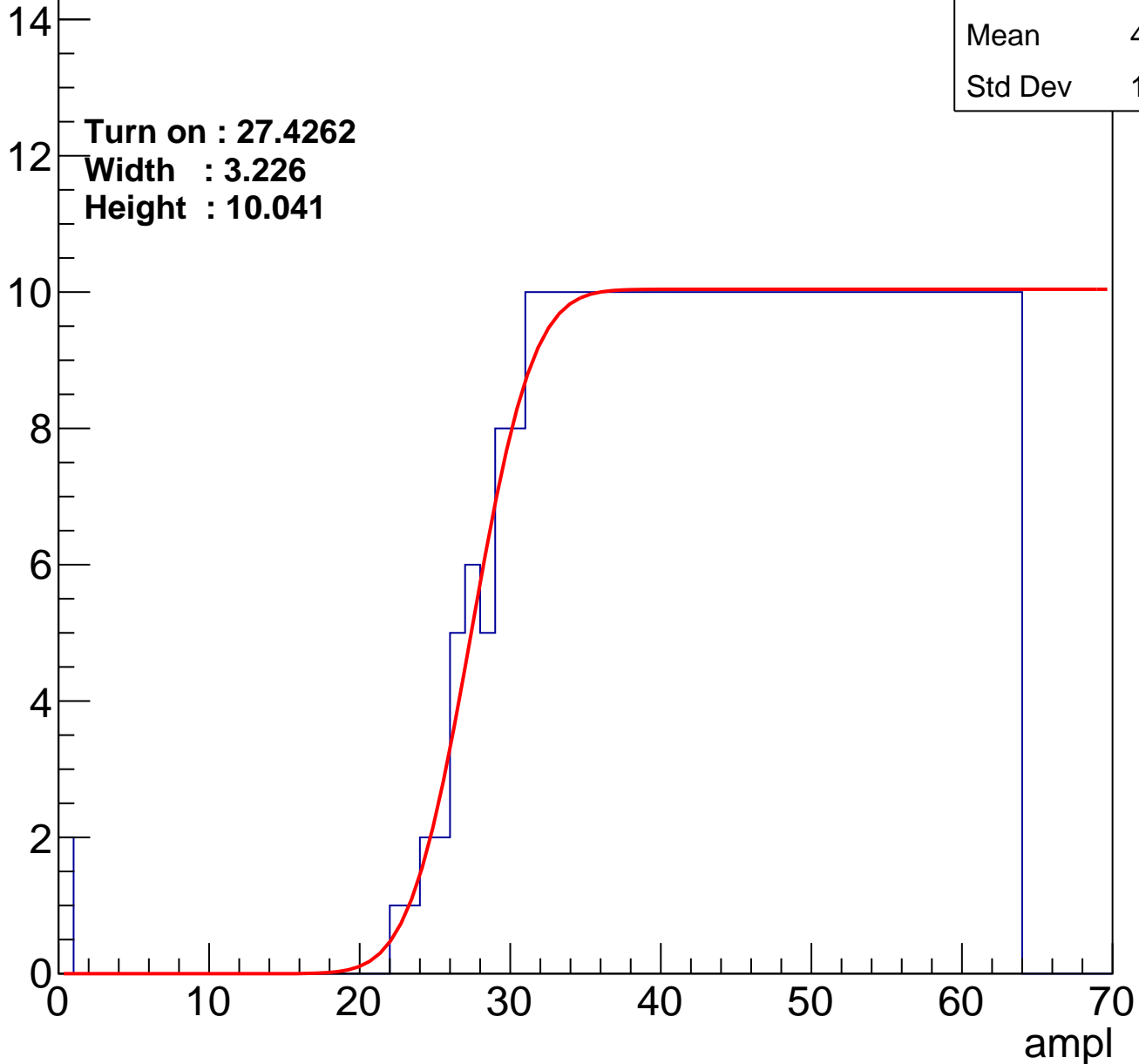
|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.75 |
| Std Dev | 11.27 |

Turn on : 27.4262

Width : 3.226

Height : 10.041

Entry



# B1L003S, U13-ch69

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 387   |
| Mean    | 43.81 |
| Std Dev | 11.94 |

**Turn on : 25.8405**

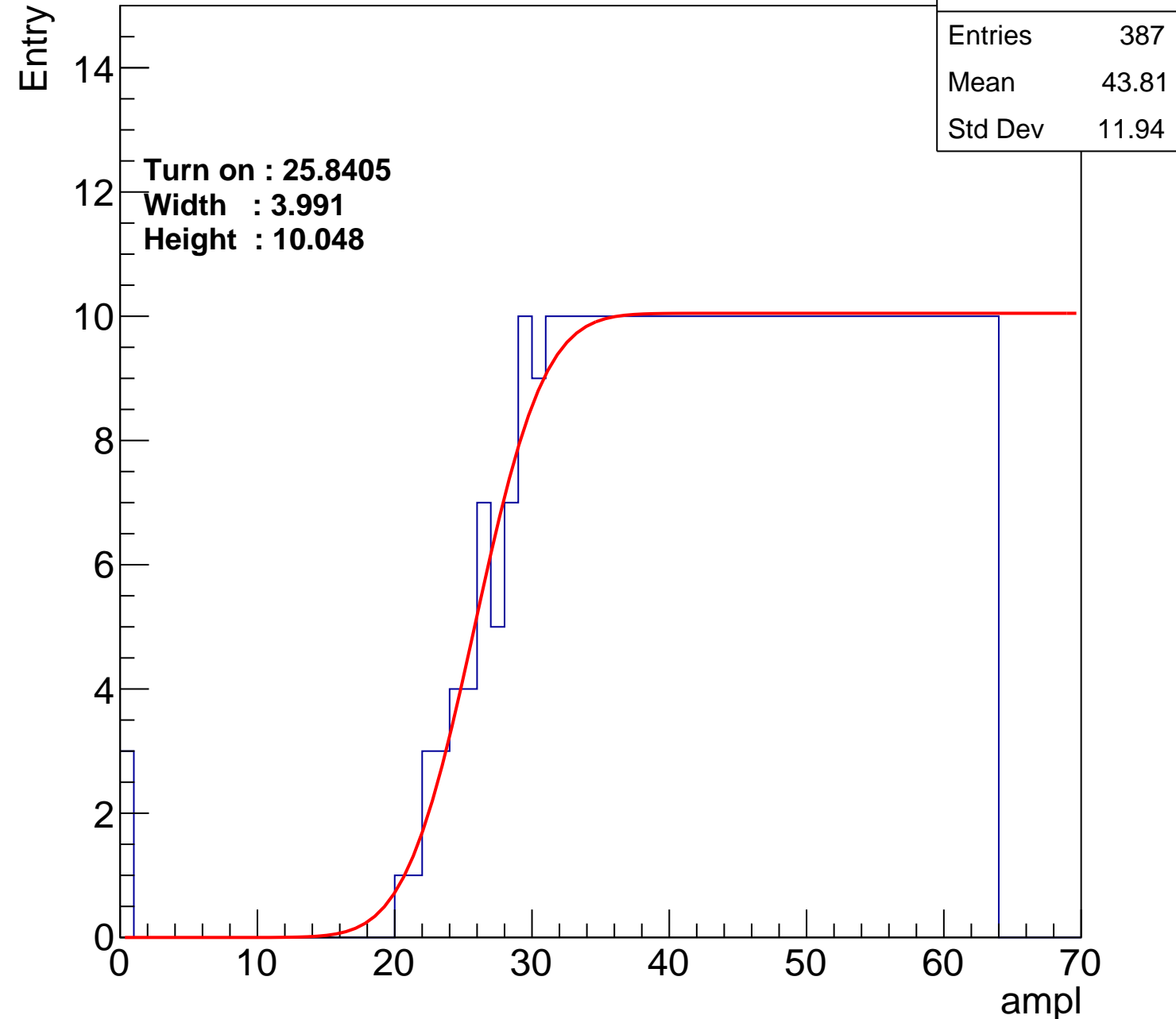
**Width : 3.991**

**Height : 10.048**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch70

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.46 |
| Std Dev | 12.03 |

Turn on : 27.7433

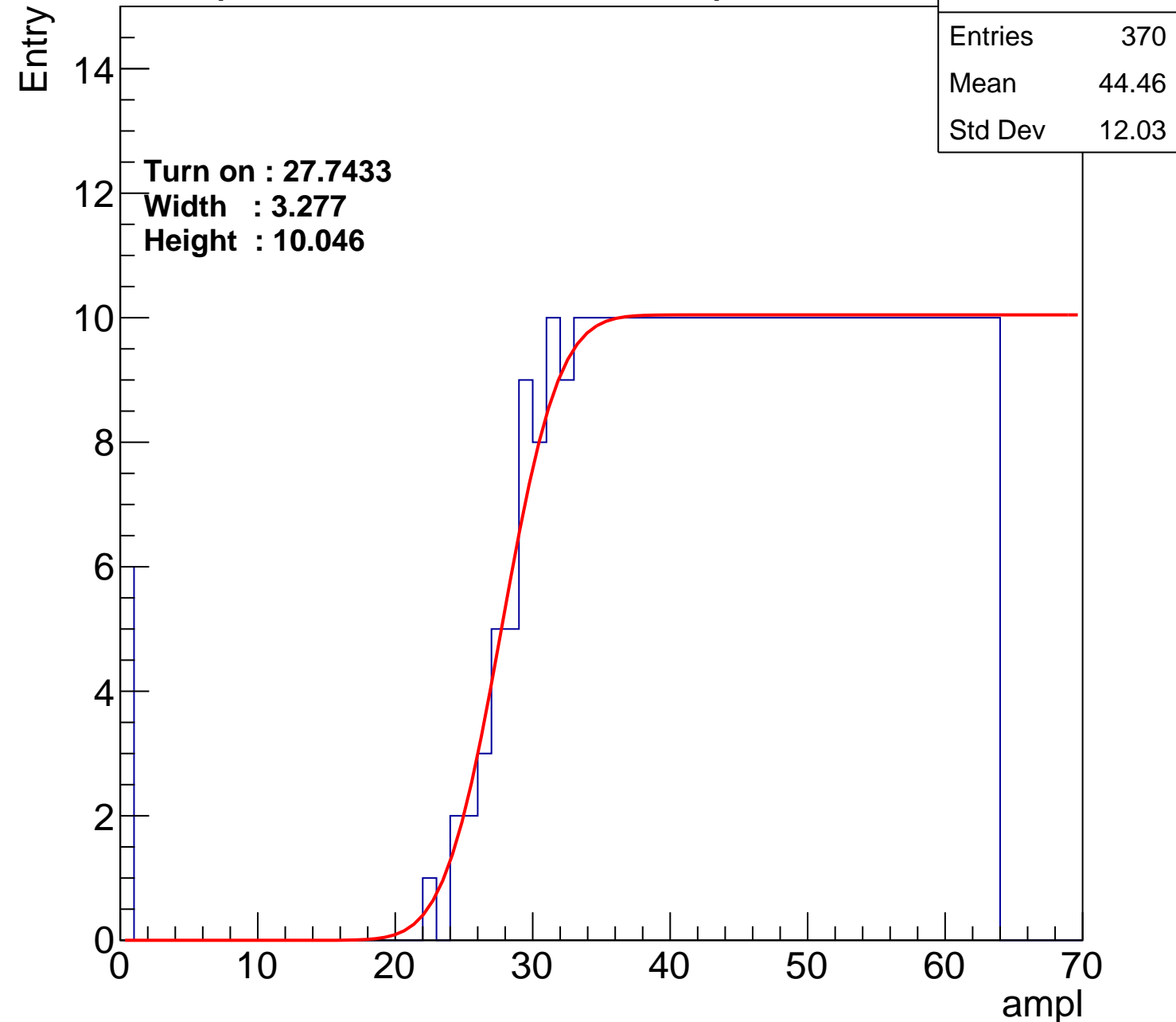
Width : 3.277

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch71

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.27 |
| Std Dev | 11.94 |

Turn on : 27.5046

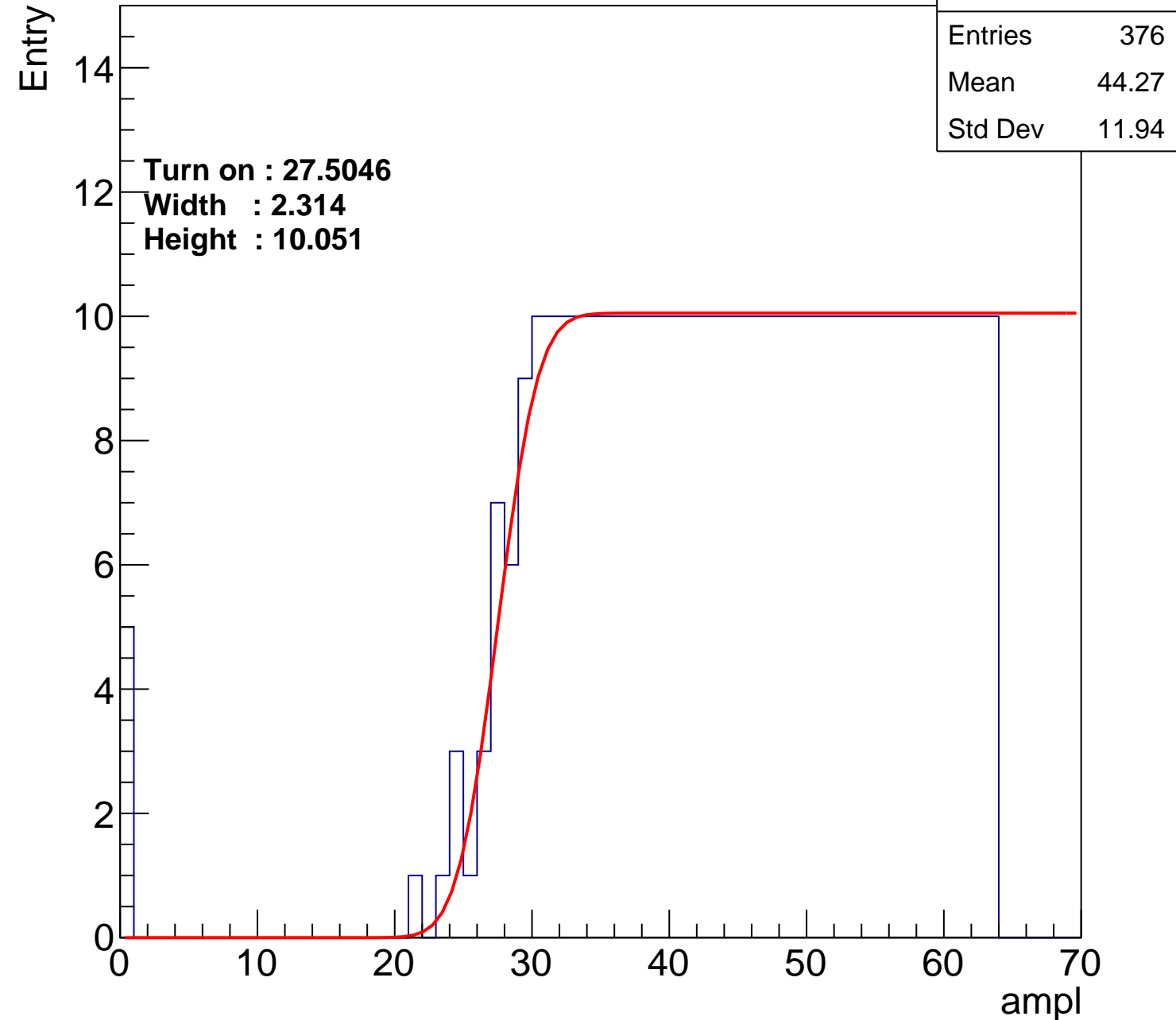
Width : 2.314

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch72

calib\_packv5\_042523\_0143.root, FC#13, port D2

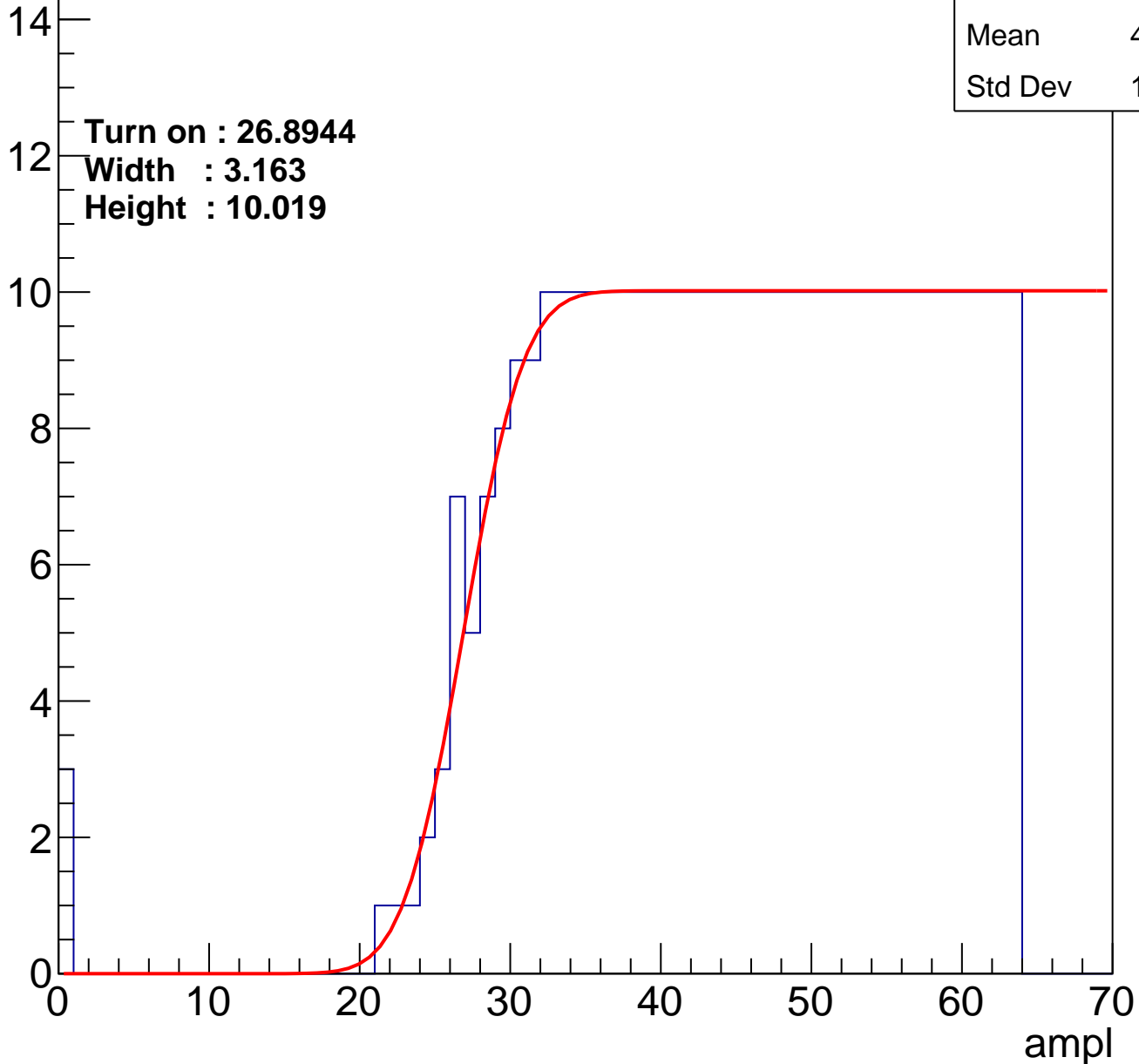
|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.37 |
| Std Dev | 11.63 |

Turn on : 26.8944

Width : 3.163

Height : 10.019

Entry



# B1L003S, U13-ch73

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 375   |
| Mean    | 44.21 |
| Std Dev | 12.15 |

Turn on : 27.6670

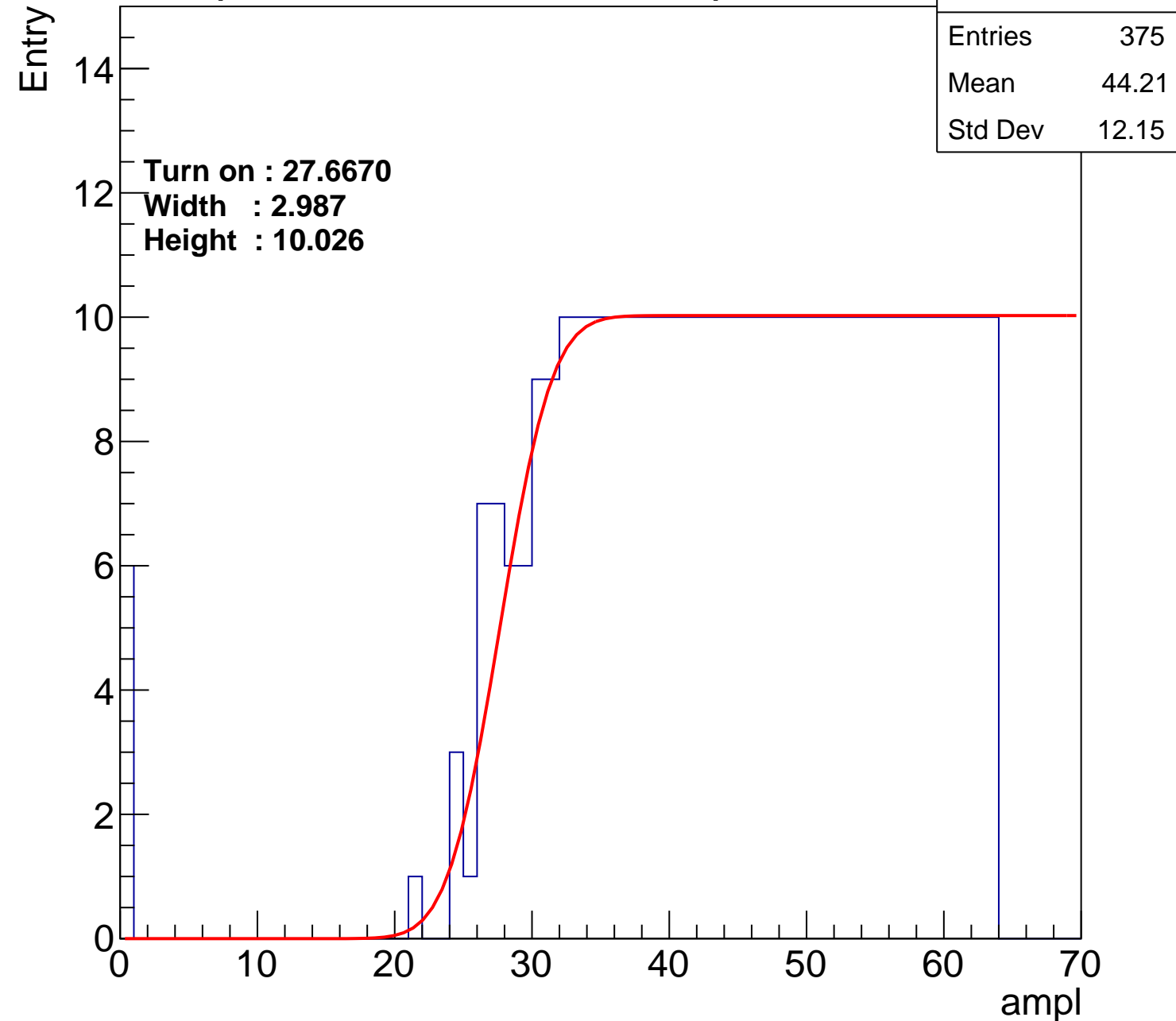
Width : 2.987

Height : 10.026

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch74

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.76 |
| Std Dev | 11.11 |

Turn on : 27.1113

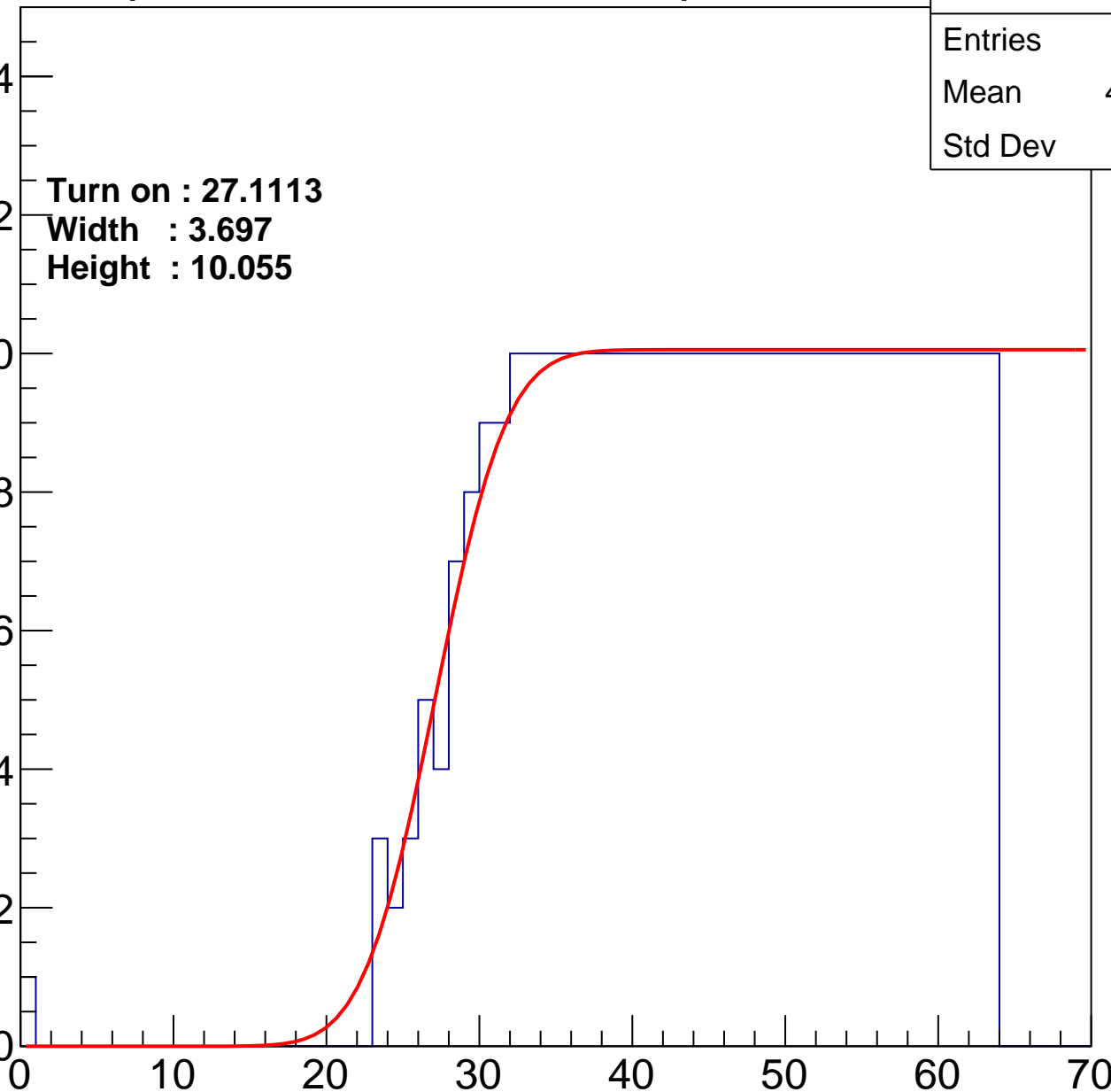
Width : 3.697

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch75

**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |     |
|---------|-----|
| Entries | 383 |
|---------|-----|

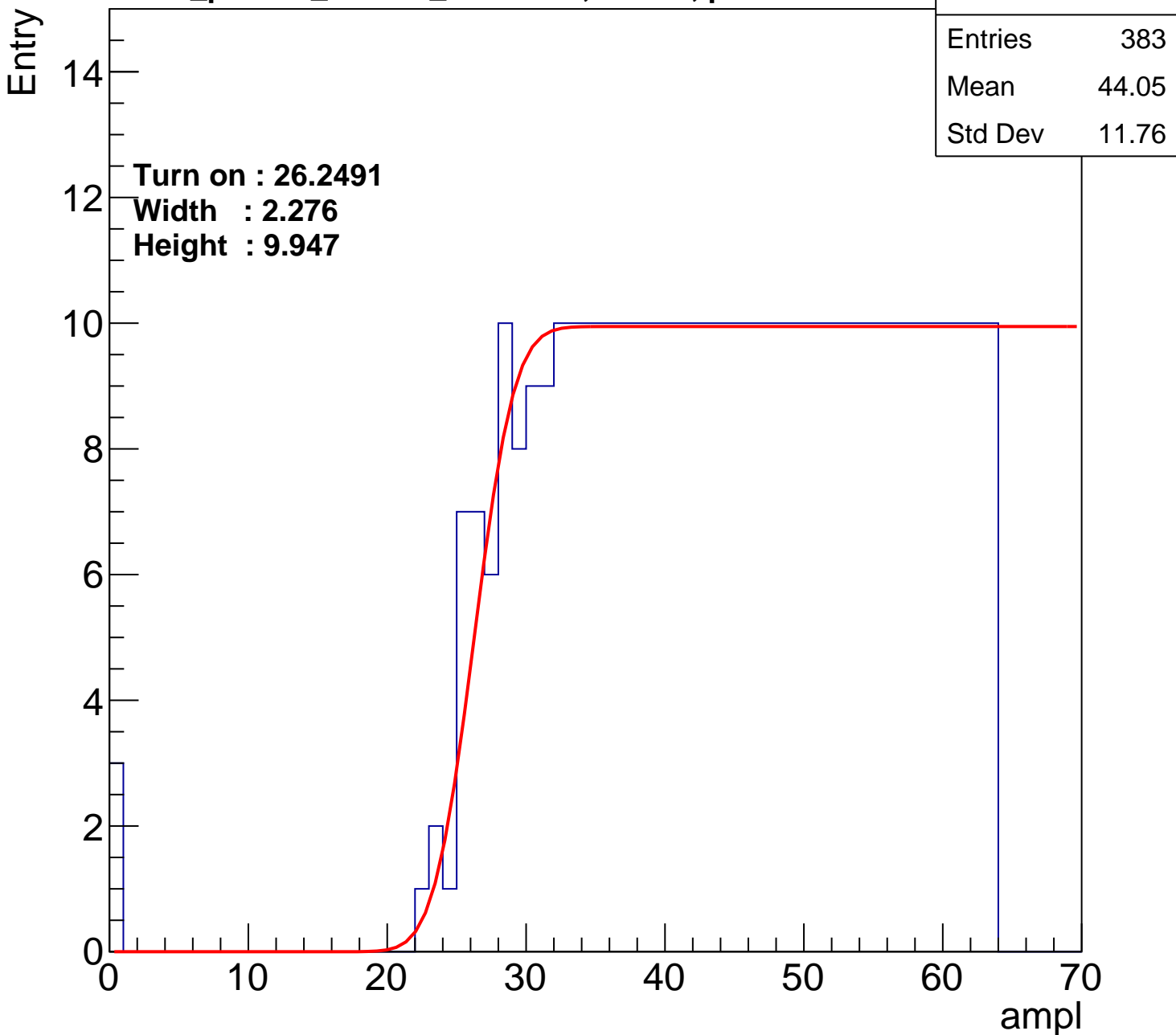
|      |       |
|------|-------|
| Mean | 44.05 |
|------|-------|

|         |       |
|---------|-------|
| Std Dev | 11.76 |
|---------|-------|

**Turn on : 26.2491**

**Width : 2.276**

**Height : 9.947**



# B1L003S, U13-ch76

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 45.13 |
| Std Dev | 11.09 |

Turn on : 28.9108

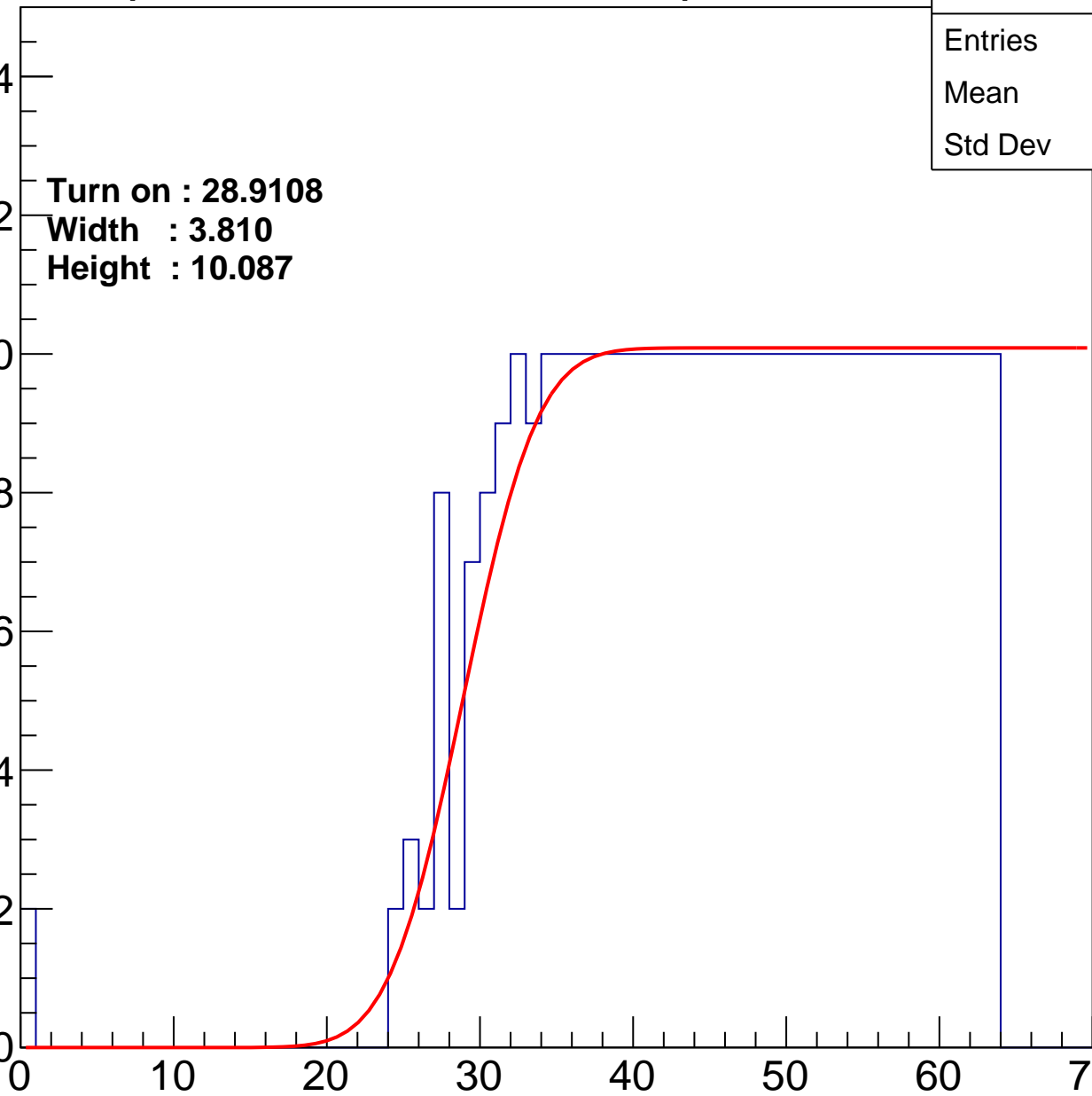
Width : 3.810

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



B1L003S, U13-ch77

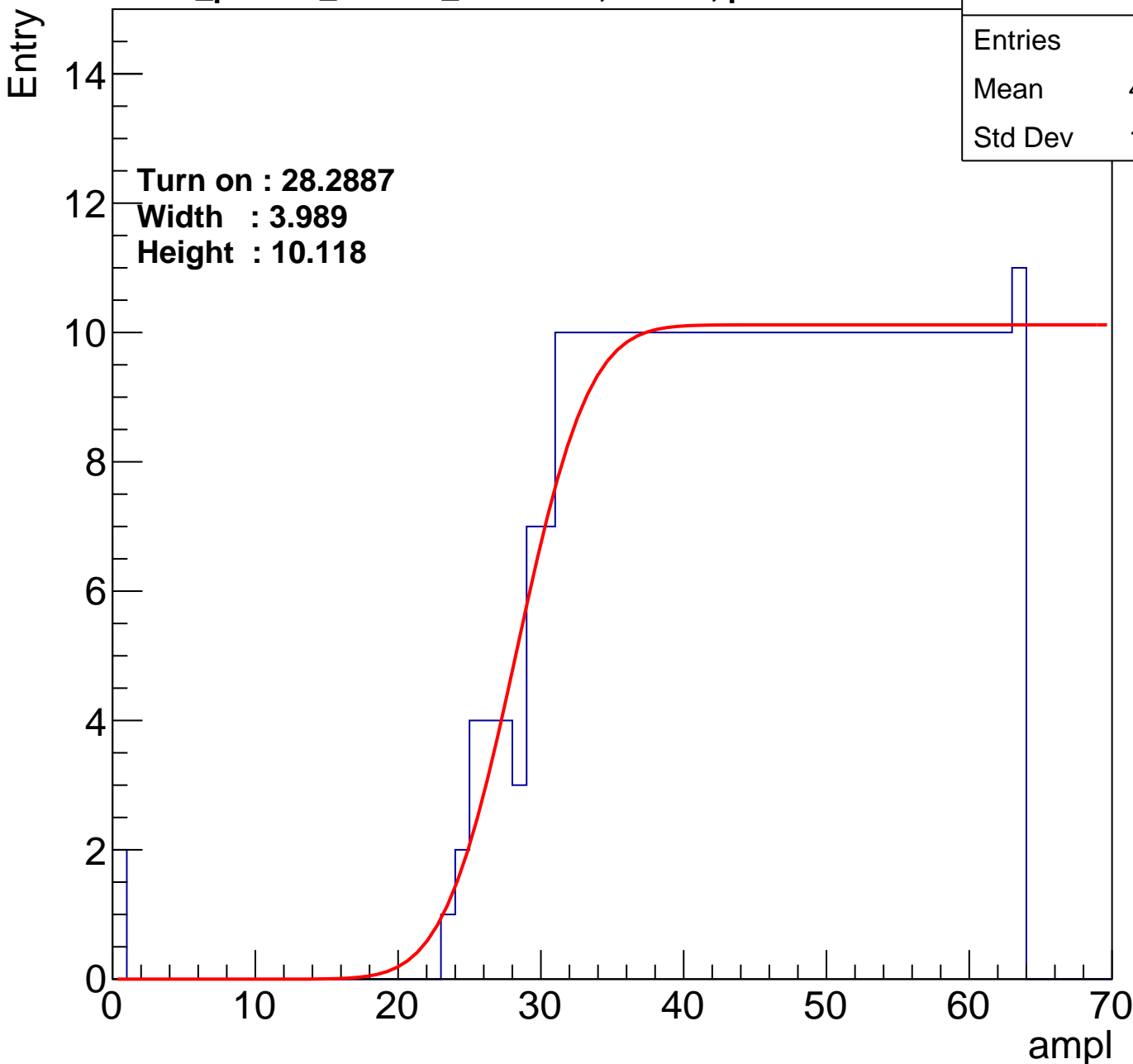
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 45.08 |
| Std Dev | 11.17 |

**Turn on : 28.2887**

**Width : 3.989**

**Height : 10.118**



# B1L003S, U13-ch78

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 353   |
| Mean    | 45.68 |
| Std Dev | 10.58 |

Turn on : 29.0486

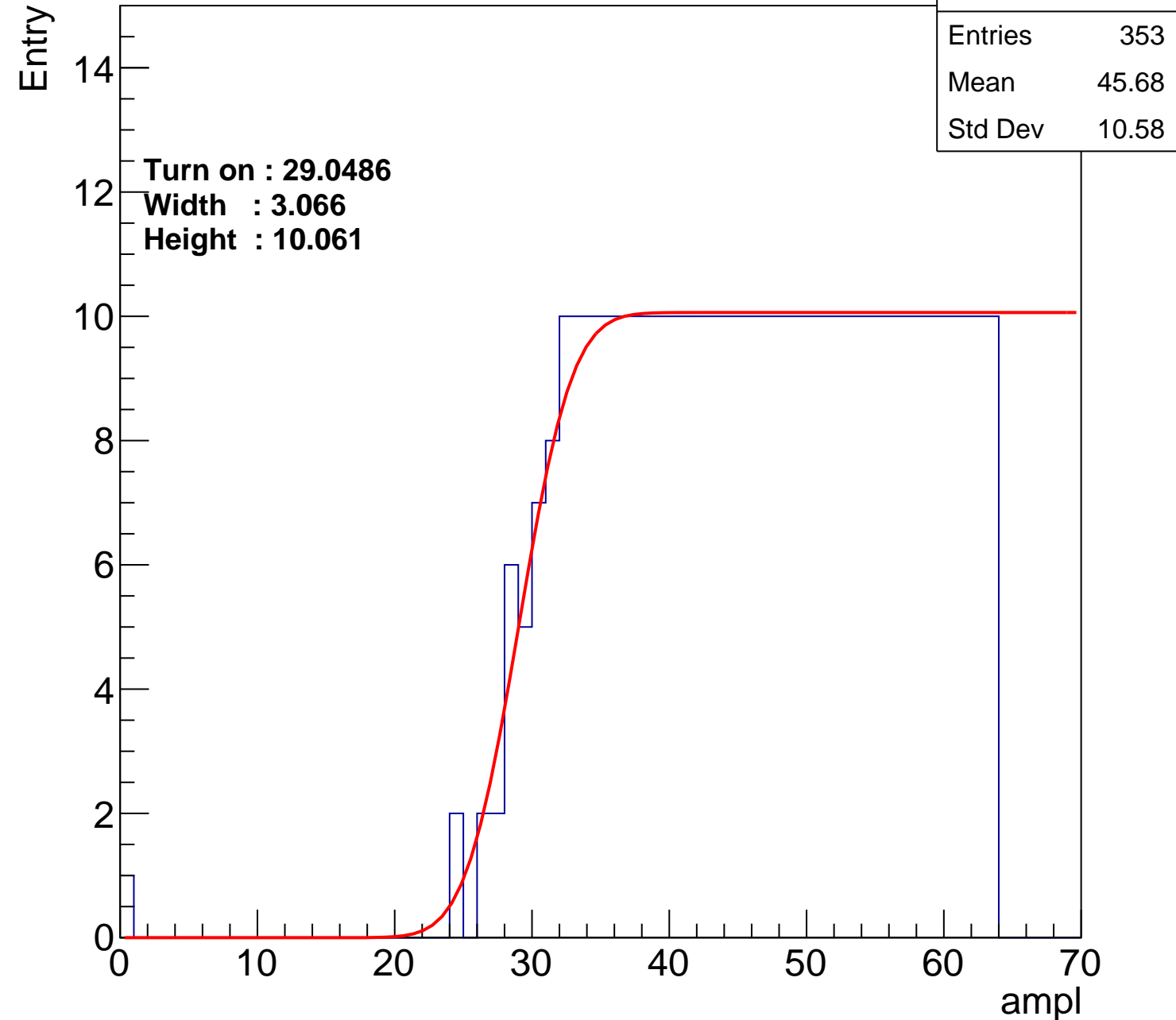
Width : 3.066

Height : 10.061

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch79

calib\_packv5\_042523\_0143.root, FC#13, port D2

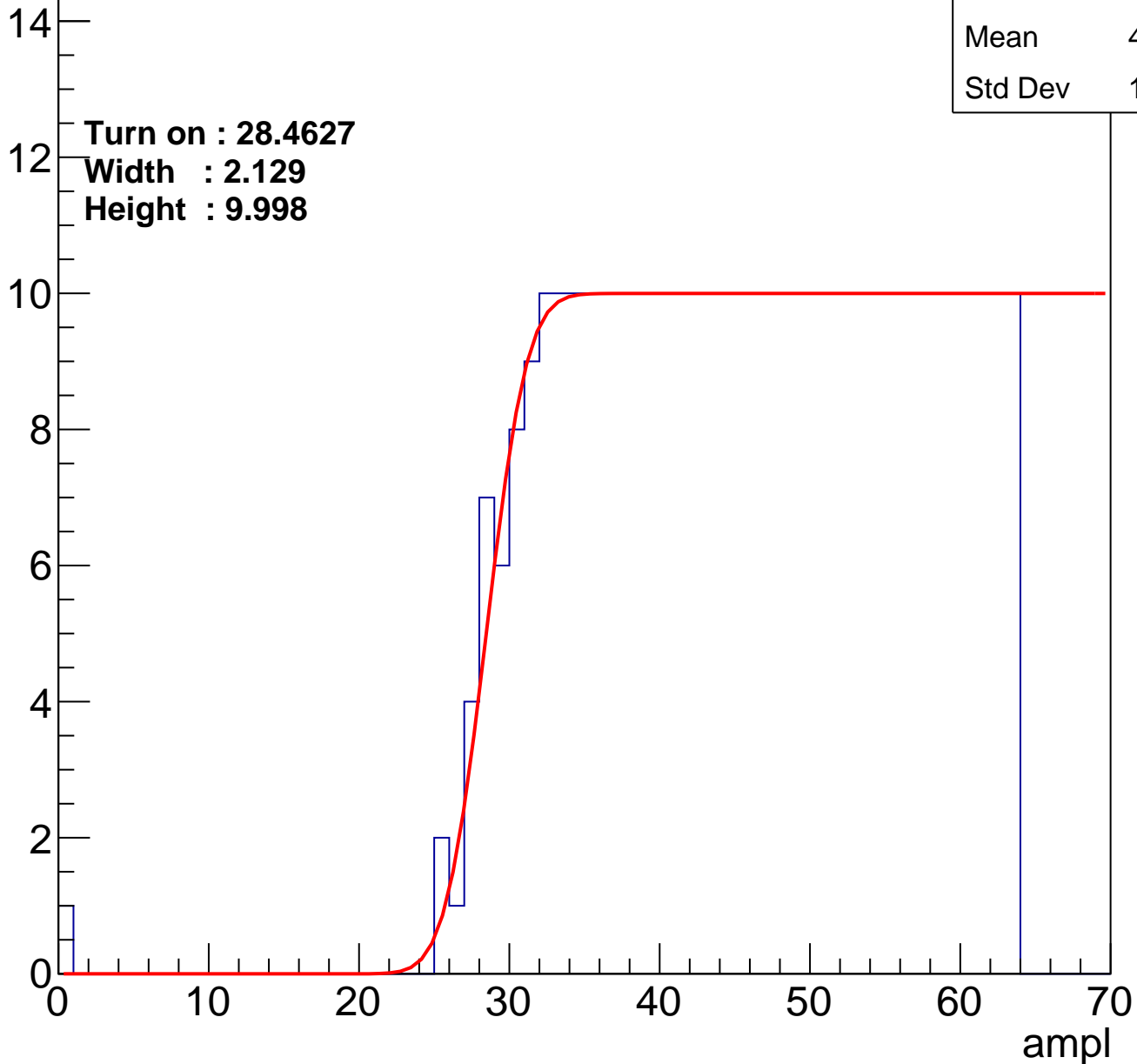
|         |       |
|---------|-------|
| Entries | 358   |
| Mean    | 45.46 |
| Std Dev | 10.68 |

Turn on : 28.4627

Width : 2.129

Height : 9.998

Entry



# B1L003S, U13-ch80

calib\_packv5\_042523\_0143.root, FC#13, port D2

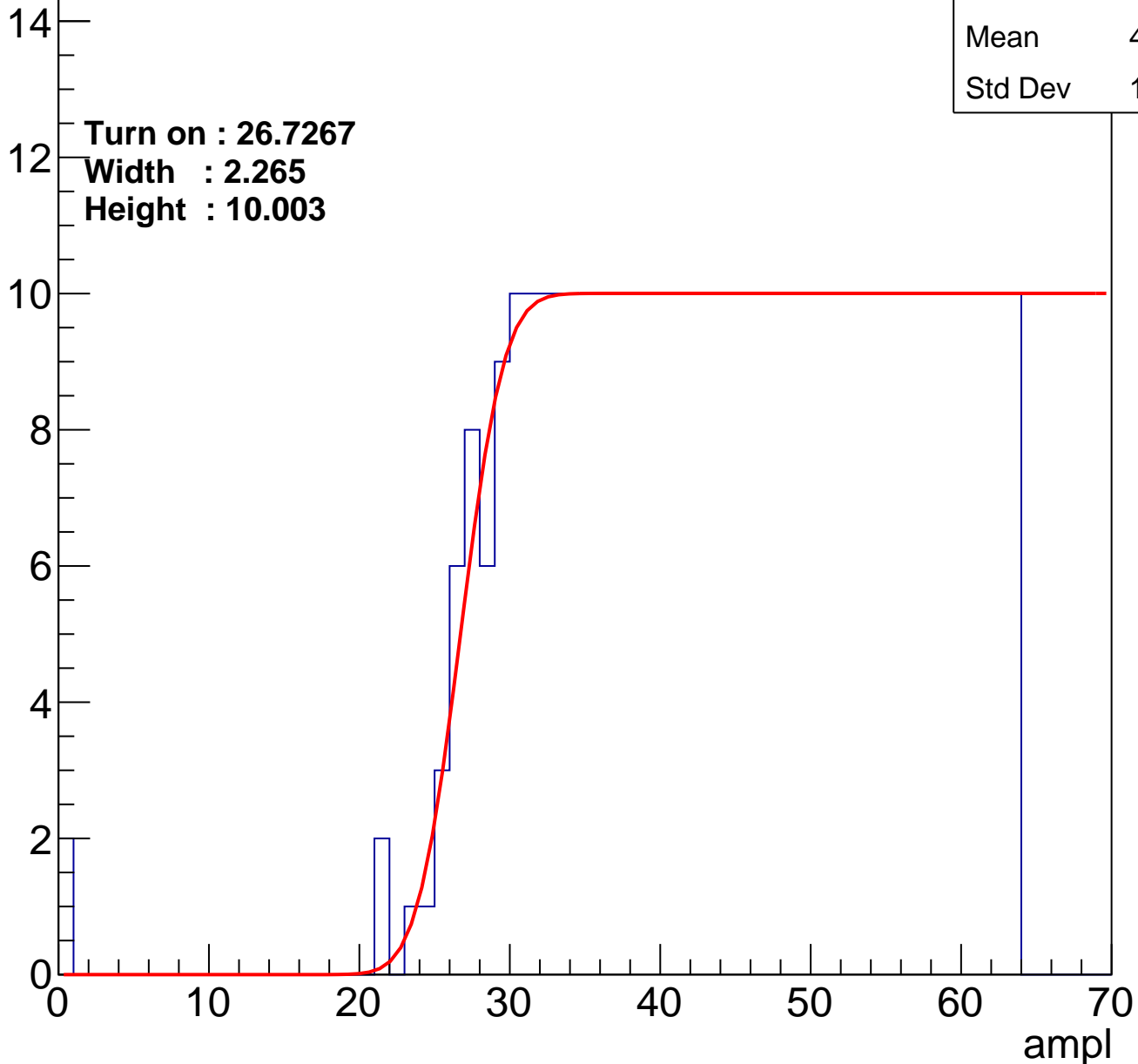
|         |       |
|---------|-------|
| Entries | 378   |
| Mean    | 44.38 |
| Std Dev | 11.44 |

Turn on : 26.7267

Width : 2.265

Height : 10.003

Entry



# B1L003S, U13-ch81

calib\_packv5\_042523\_0143.root, FC#13, port D2

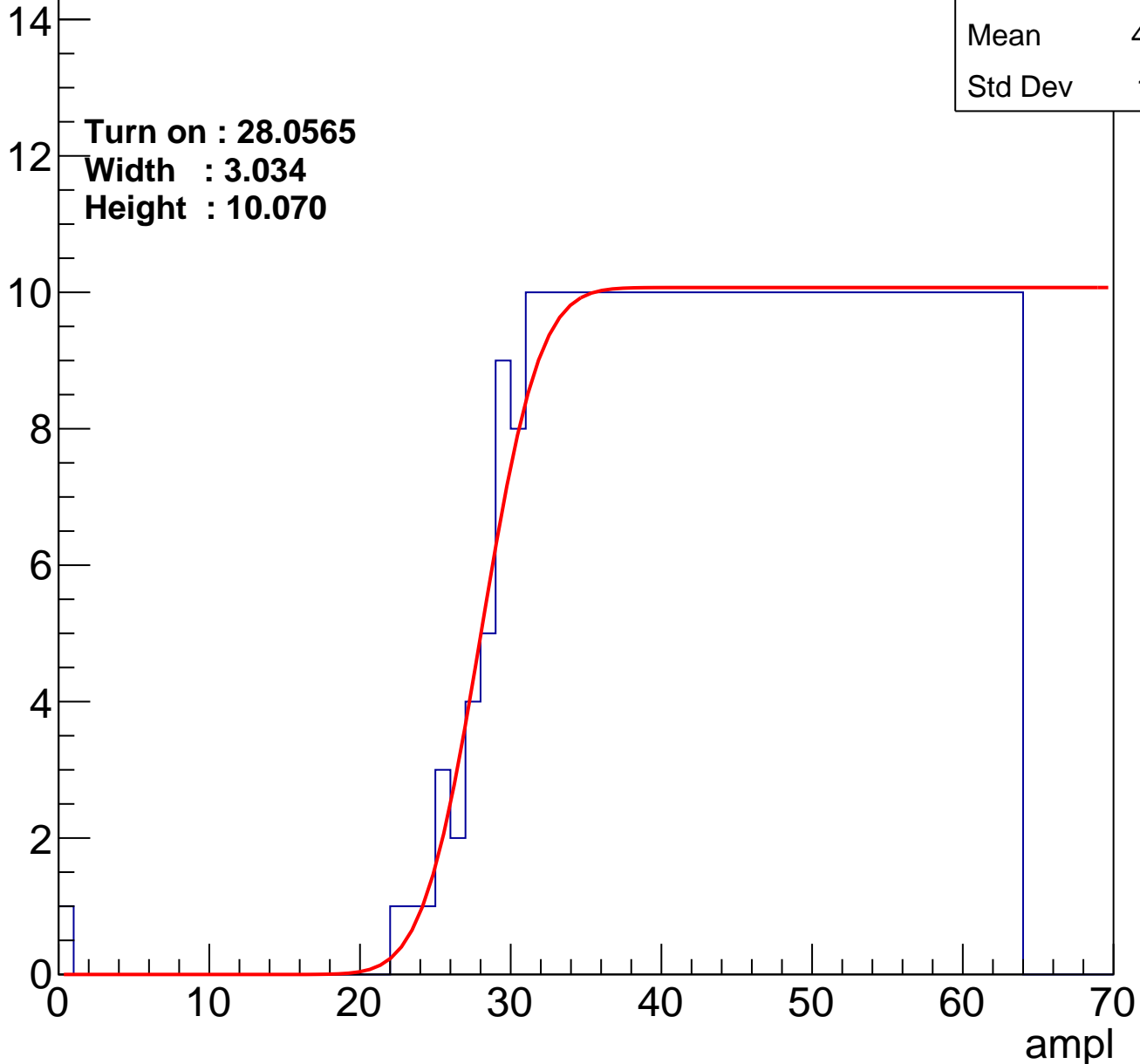
|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 45.08 |
| Std Dev | 10.91 |

Turn on : 28.0565

Width : 3.034

Height : 10.070

Entry



# B1L003S, U13-ch82

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.72 |
| Std Dev | 11.13 |

Turn on : 27.2952

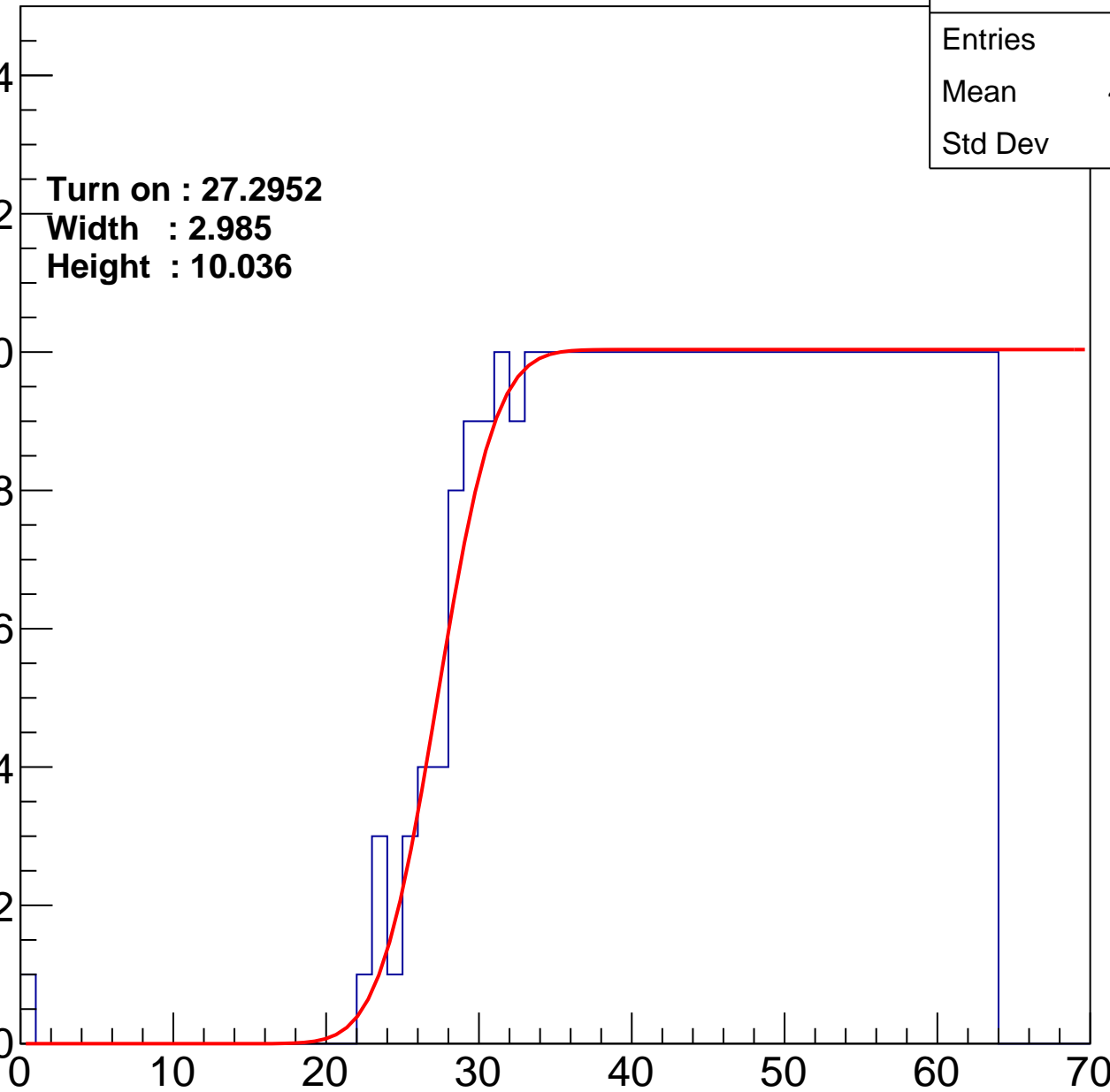
Width : 2.985

Height : 10.036

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch83

calib\_packv5\_042523\_0143.root, FC#13, port D2

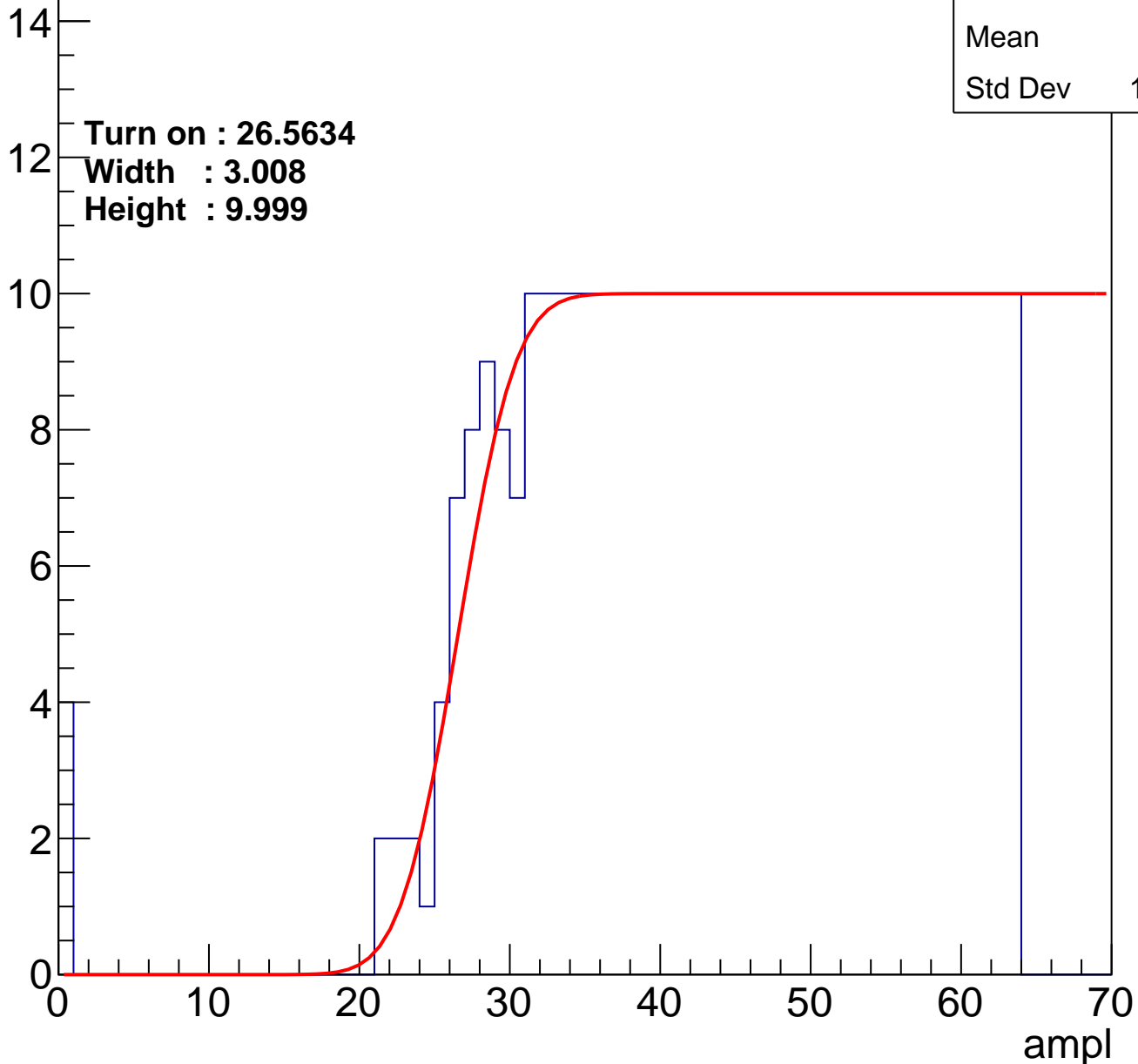
|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 43.9  |
| Std Dev | 12.02 |

Turn on : 26.5634

Width : 3.008

Height : 9.999

Entry



# B1L003S, U13-ch84

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 371   |
| Mean    | 44.73 |
| Std Dev | 11.25 |

**Turn on : 27.3843**

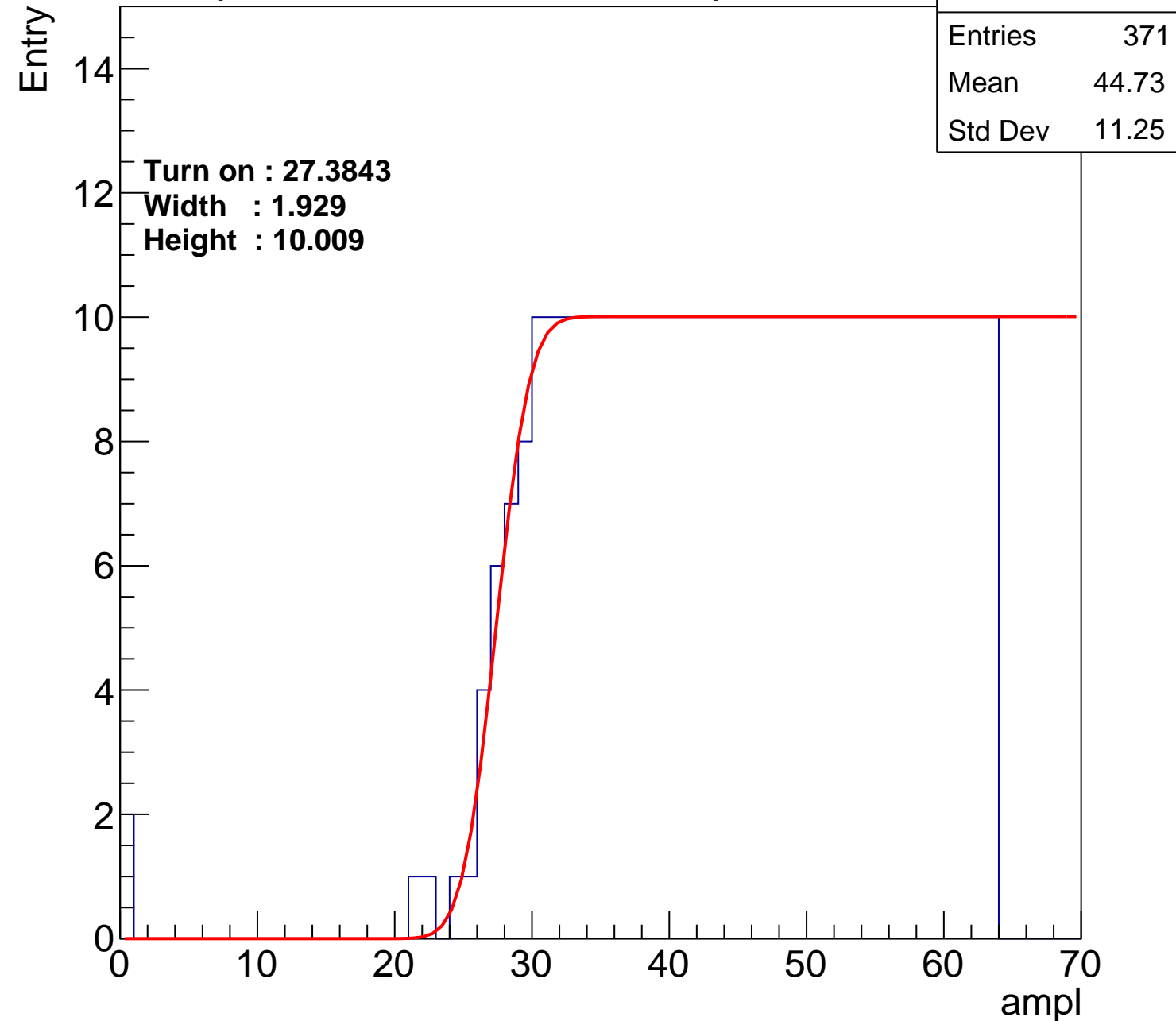
**Width : 1.929**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch85

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.83 |
| Std Dev | 11.46 |

Turn on : 28.4465

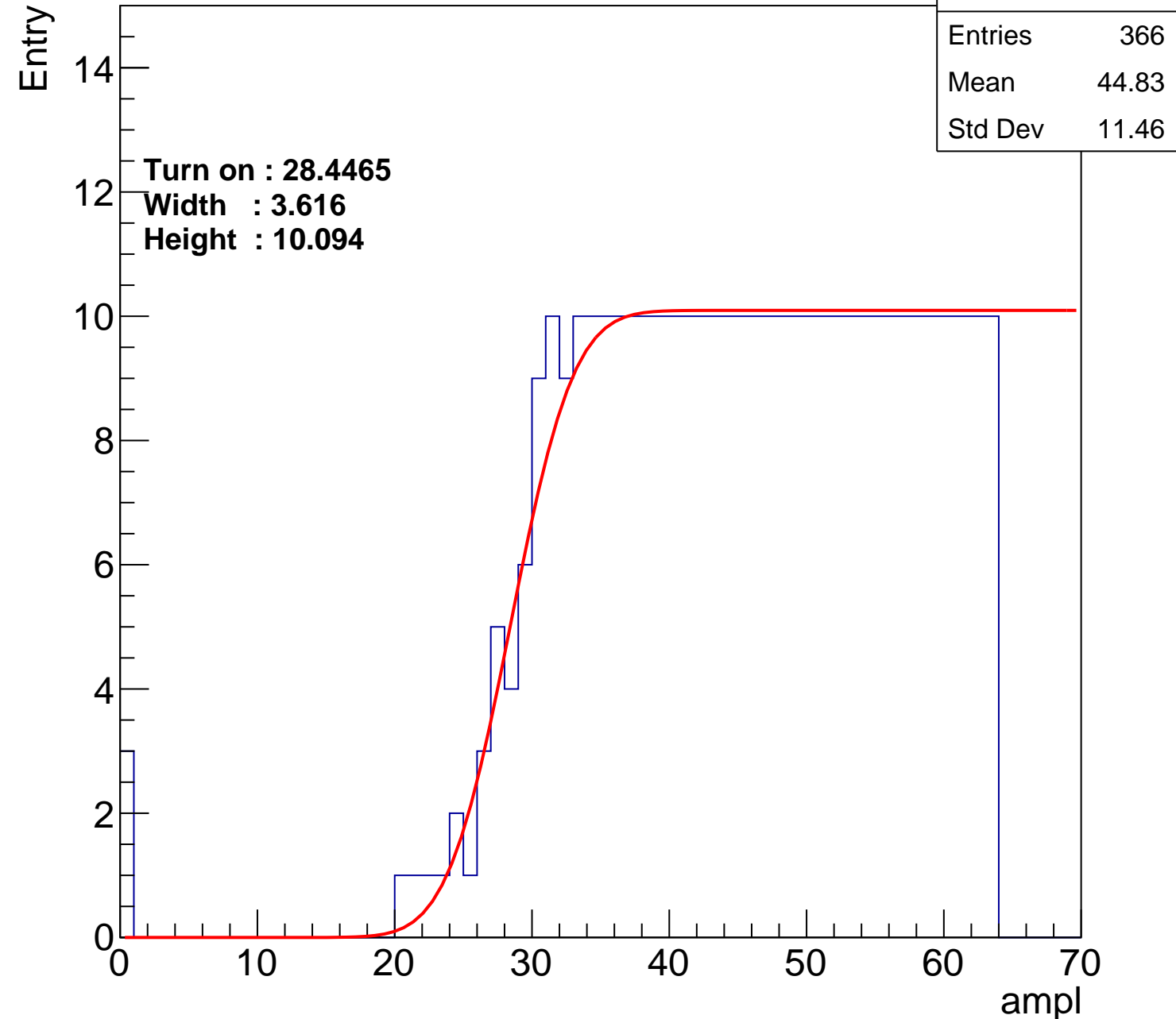
Width : 3.616

Height : 10.094

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch86

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 343   |
| Mean    | 45.94 |
| Std Dev | 11.02 |

Turn on : 29.9623

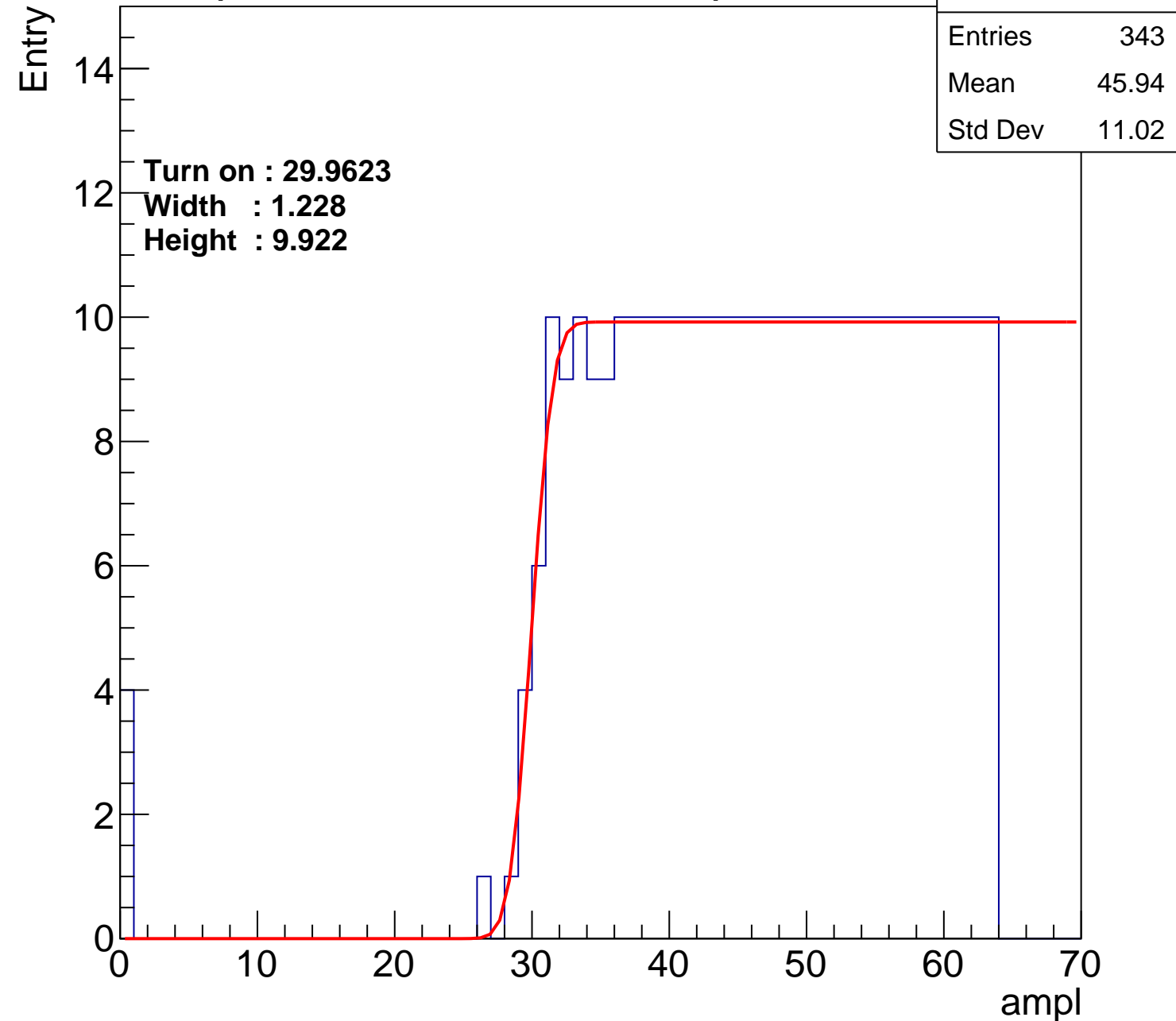
Width : 1.228

Height : 9.922

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch87

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 44.79 |
| Std Dev | 11.73 |

Turn on : 28.1859

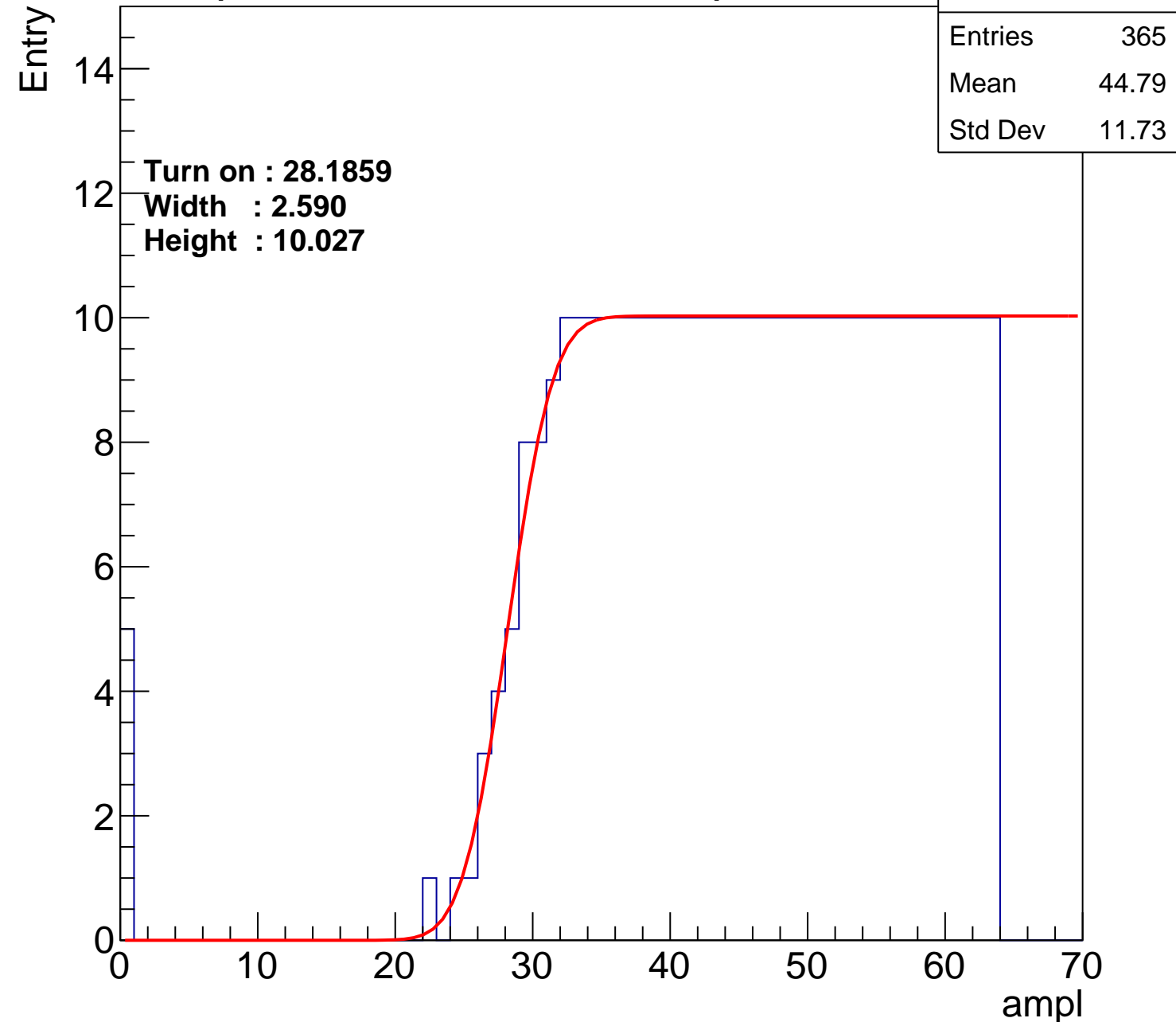
Width : 2.590

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch88

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 373   |
| Mean    | 44.45 |
| Std Dev | 11.76 |

Turn on : 28.5034

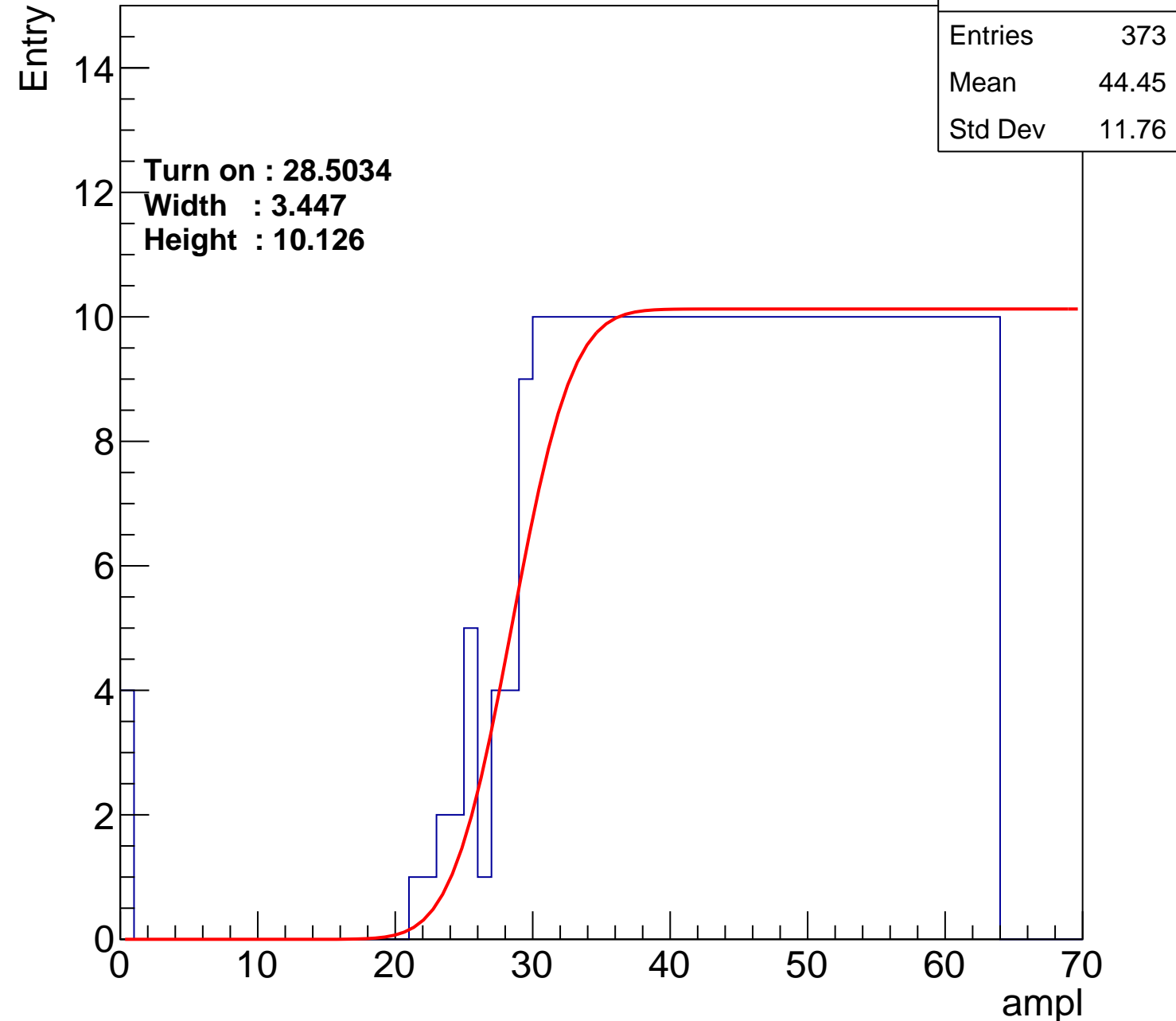
Width : 3.447

Height : 10.126

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch89

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 44.03 |
| Std Dev | 11.68 |

Turn on : 26.1931

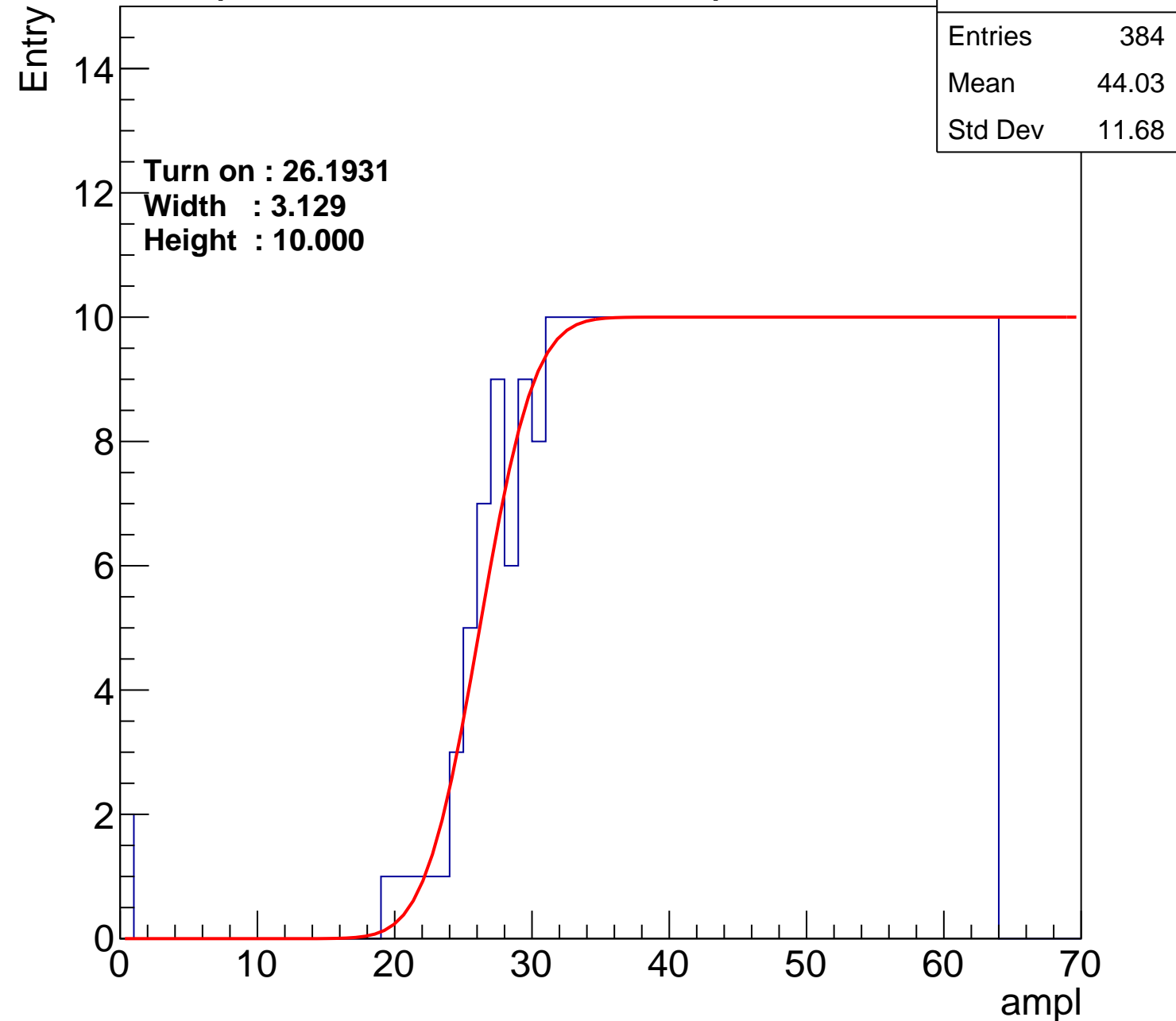
Width : 3.129

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch90

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.58 |
| Std Dev | 11.97 |

Turn on : 27.9014

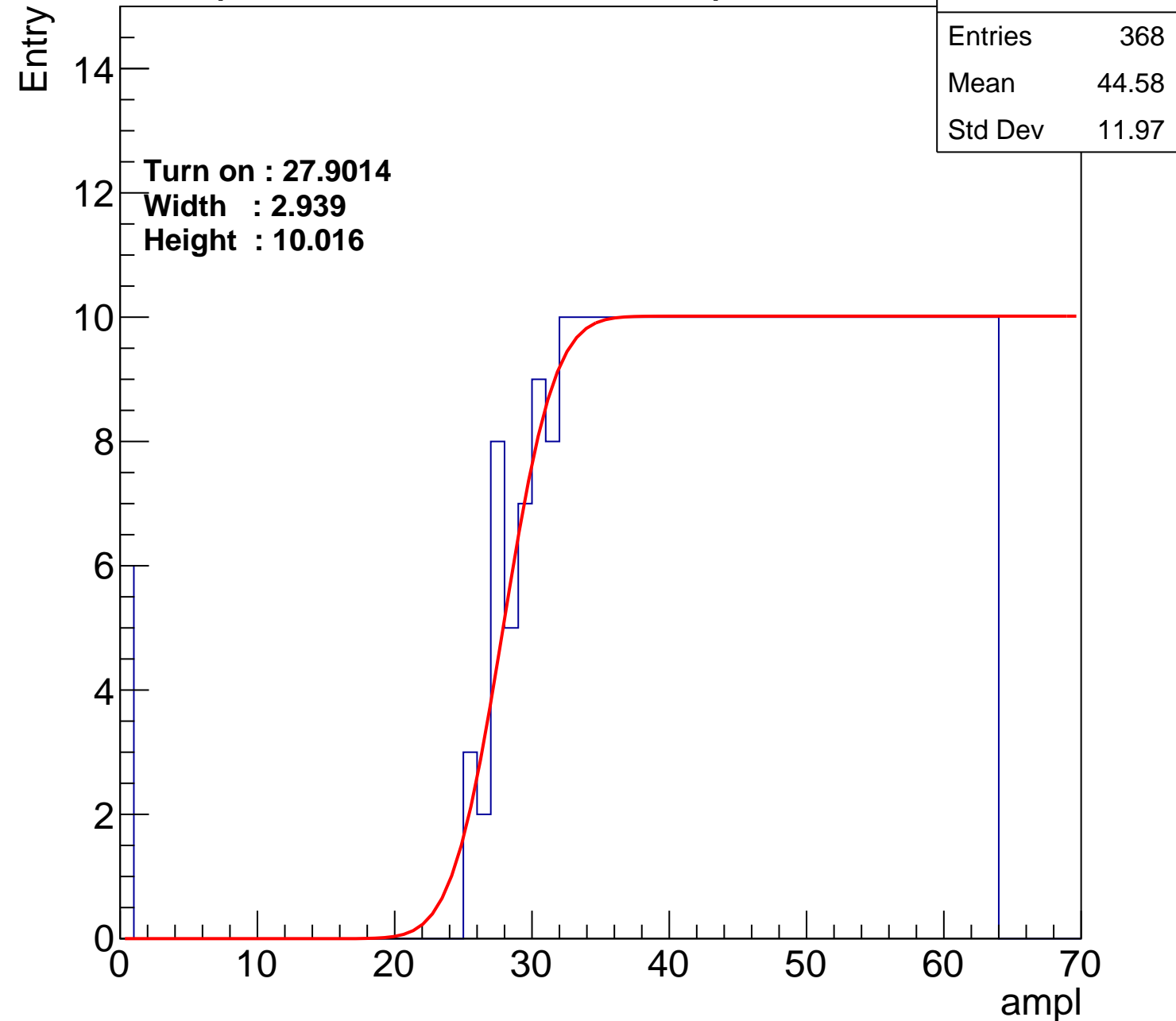
Width : 2.939

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch91

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 368   |
| Mean    | 44.77 |
| Std Dev | 11.43 |

Turn on : 27.9511

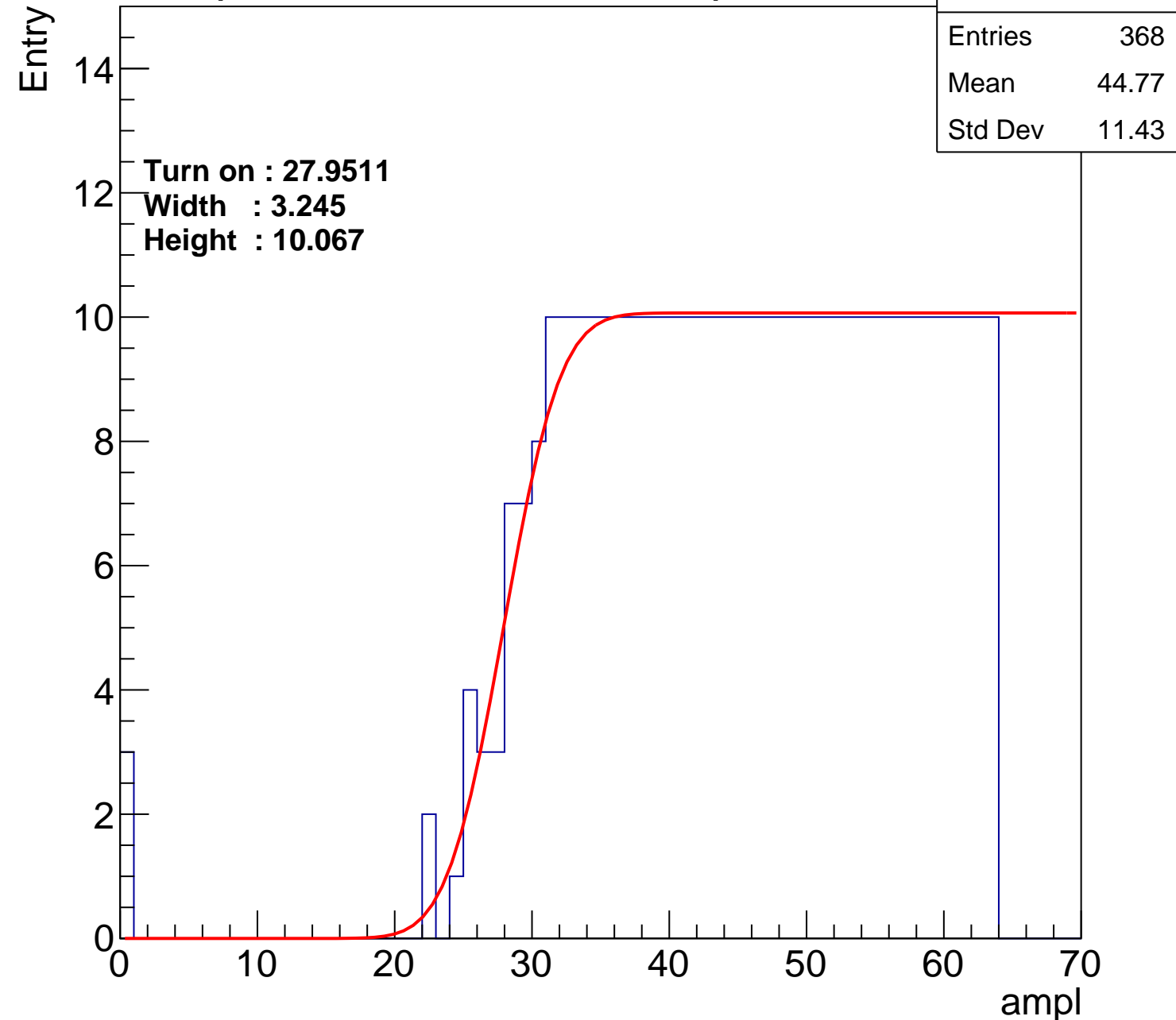
Width : 3.245

Height : 10.067

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch92

calib\_packv5\_042523\_0143.root, FC#13, port D2

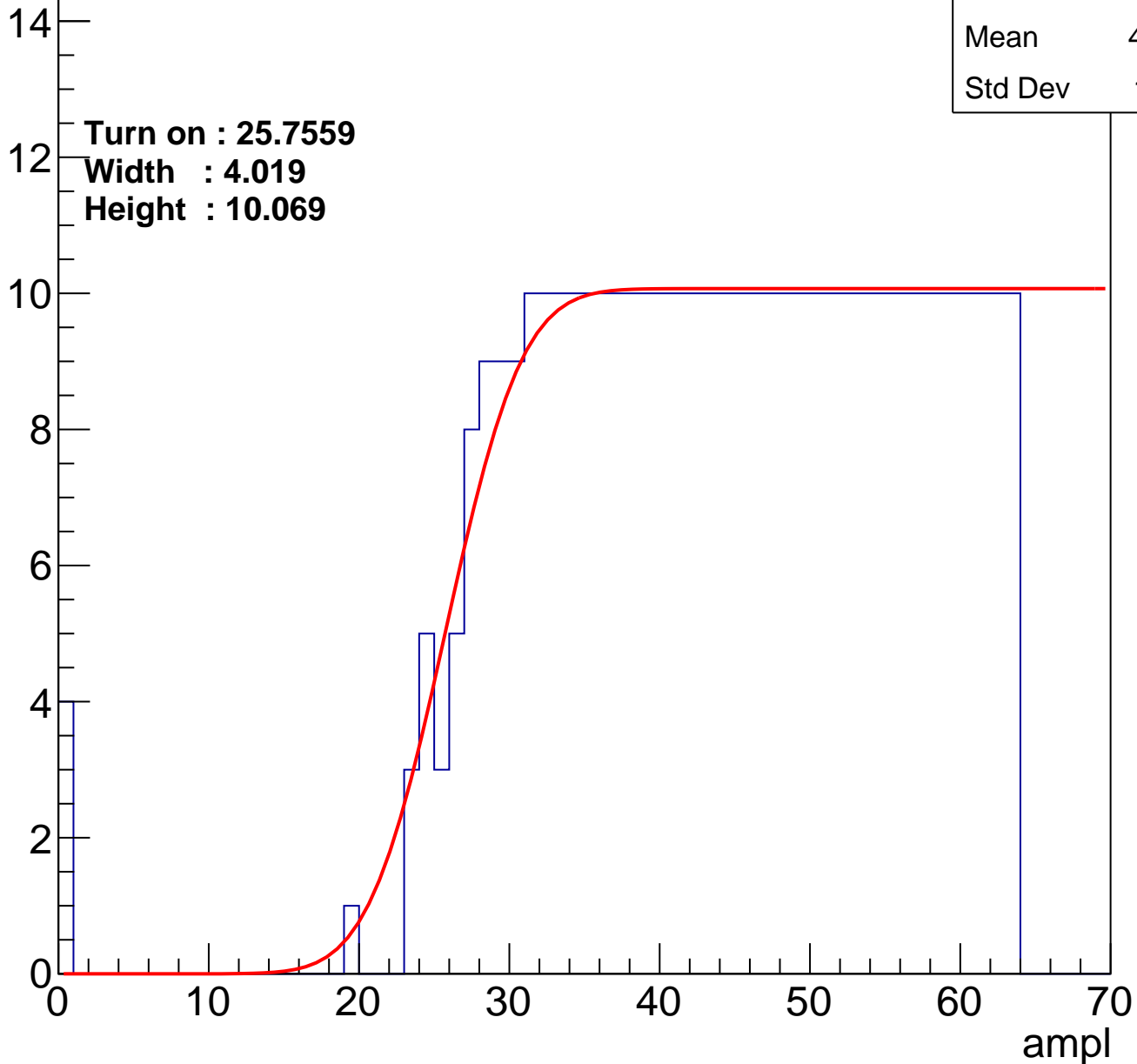
|         |       |
|---------|-------|
| Entries | 386   |
| Mean    | 43.84 |
| Std Dev | 12.01 |

**Turn on : 25.7559**

**Width : 4.019**

**Height : 10.069**

Entry



# B1L003S, U13-ch93

calib\_packv5\_042523\_0143.root, FC#13, port D2

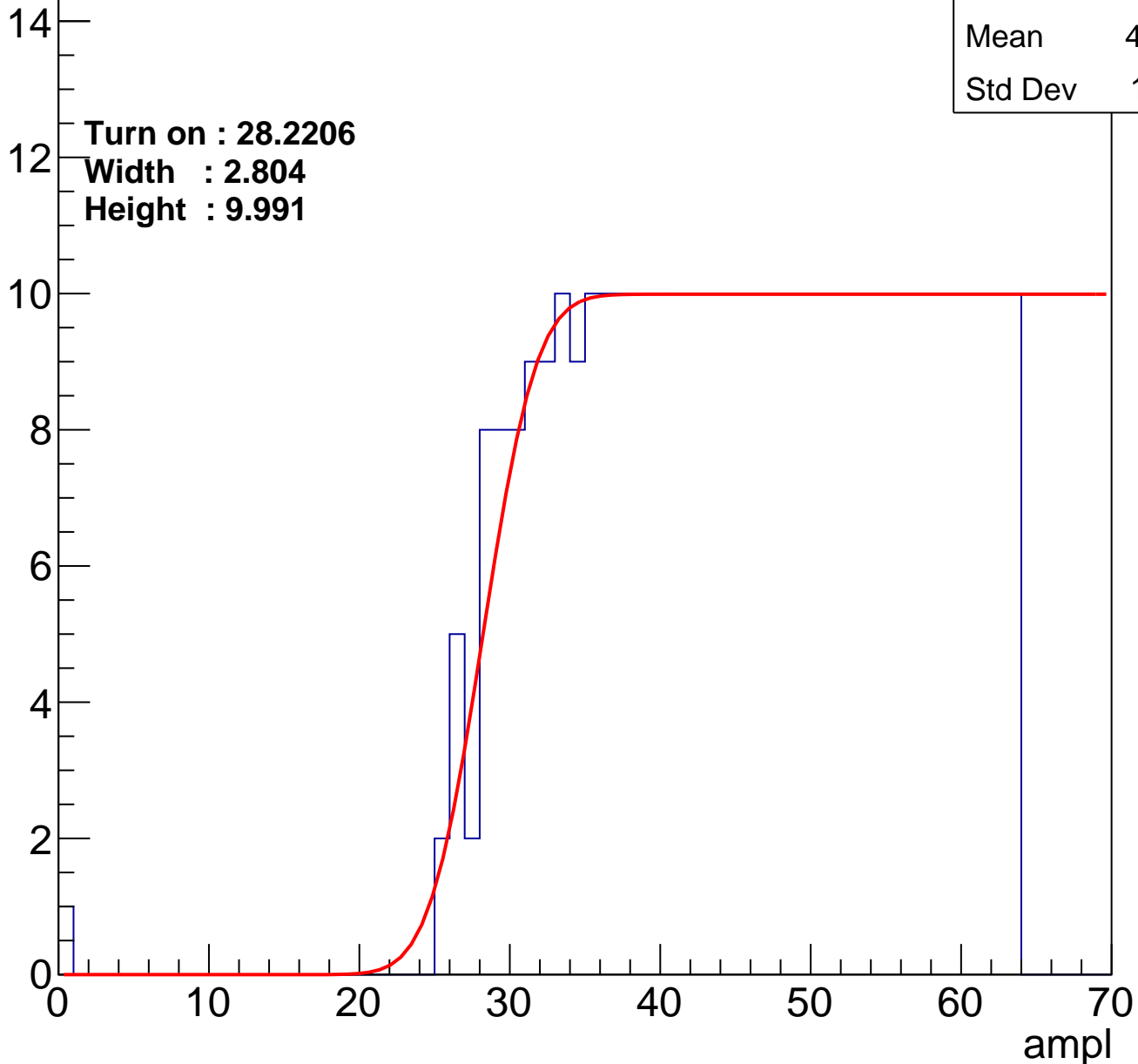
|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.27 |
| Std Dev | 10.81 |

Turn on : 28.2206

Width : 2.804

Height : 9.991

Entry



# B1L003S, U13-ch94

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 379   |
| Mean    | 44.12 |
| Std Dev | 11.94 |

Turn on : 26.9303

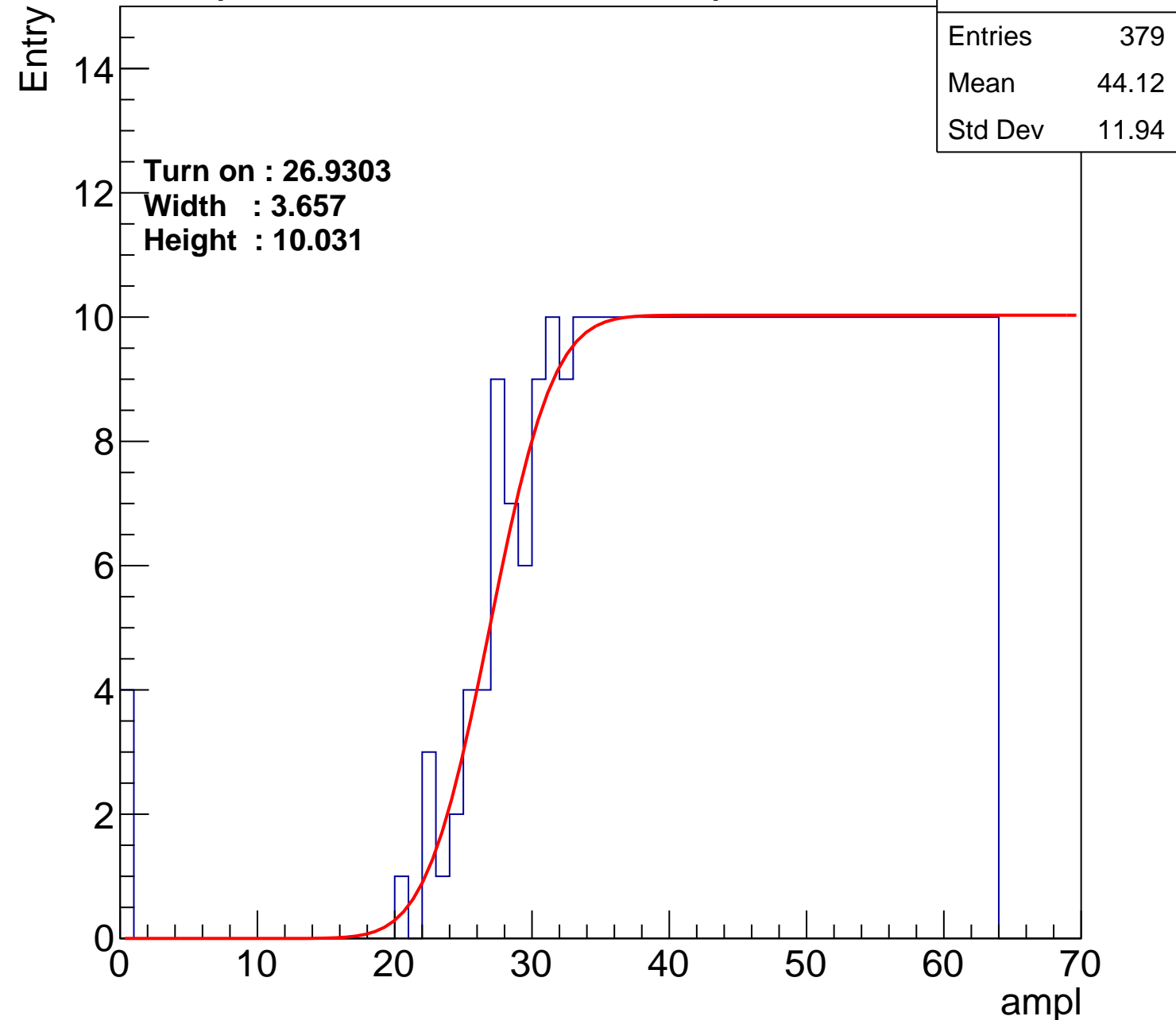
Width : 3.657

Height : 10.031

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch95

calib\_packv5\_042523\_0143.root, FC#13, port D2

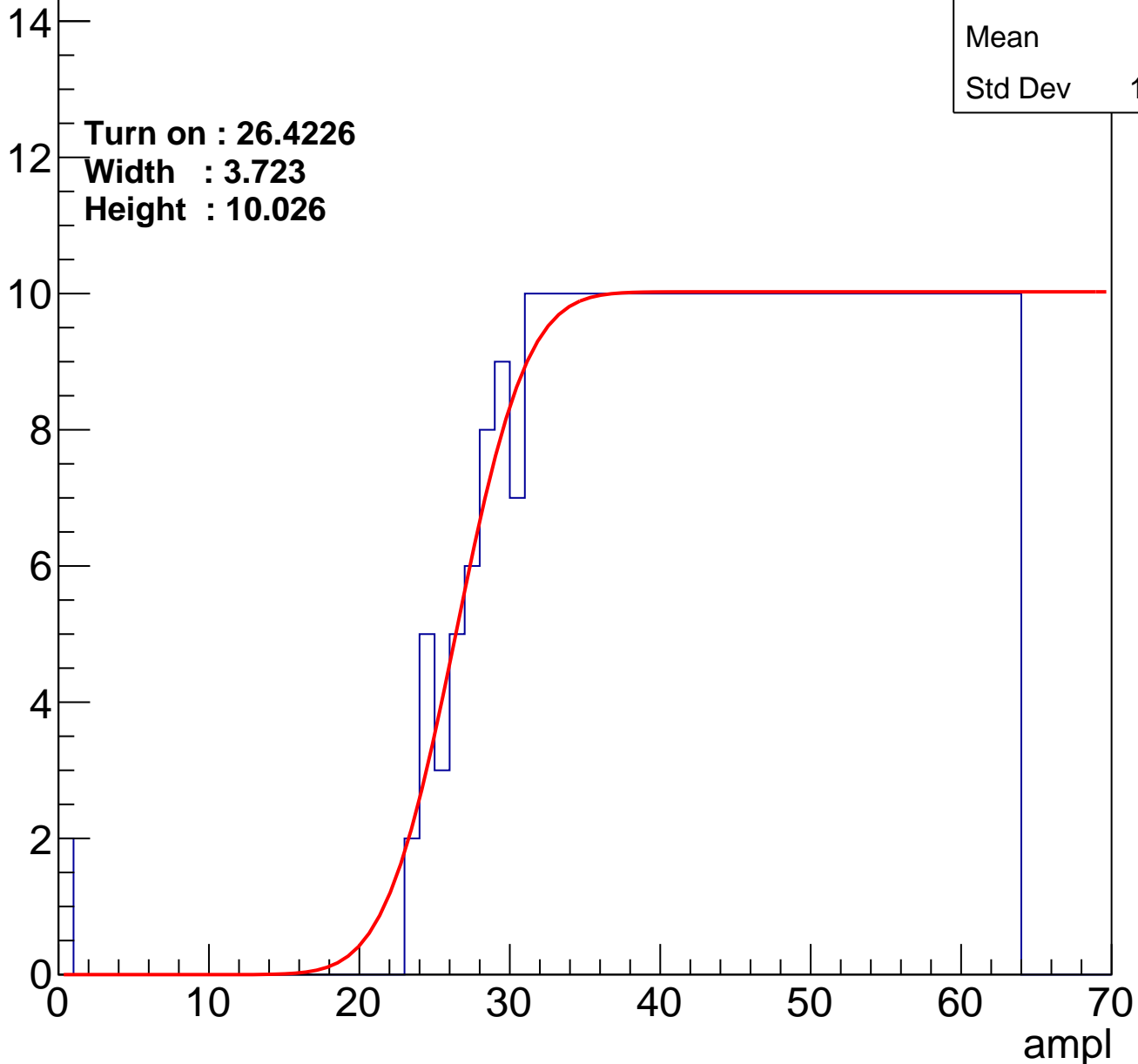
|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.4  |
| Std Dev | 11.45 |

Turn on : 26.4226

Width : 3.723

Height : 10.026

Entry



# B1L003S, U13-ch96

calib\_packv5\_042523\_0143.root, FC#13, port D2

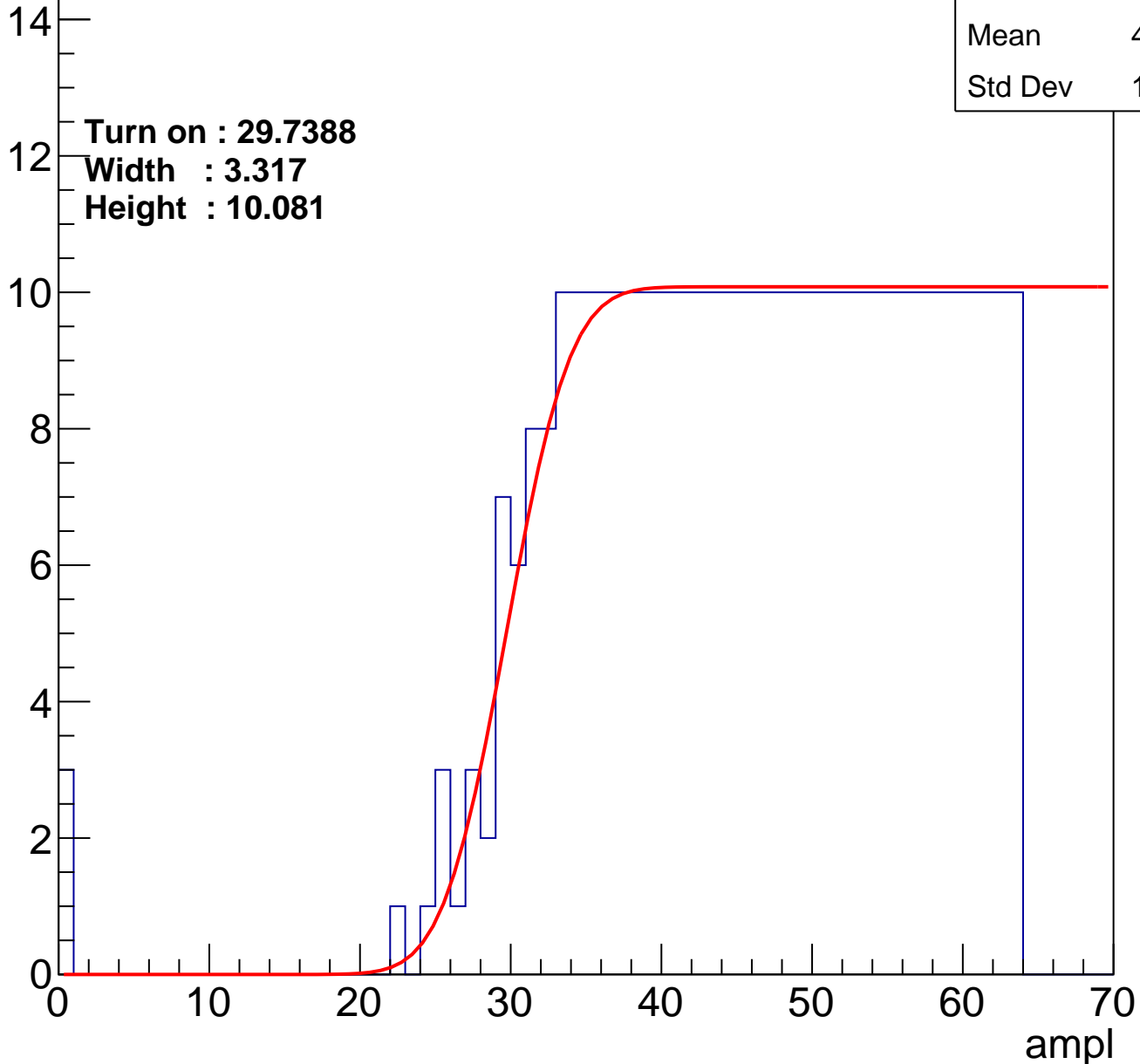
|         |       |
|---------|-------|
| Entries | 353   |
| Mean    | 45.47 |
| Std Dev | 11.13 |

Turn on : 29.7388

Width : 3.317

Height : 10.081

Entry



# B1L003S, U13-ch97

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 349   |
| Mean    | 45.65 |
| Std Dev | 11.06 |

Turn on : 30.3596

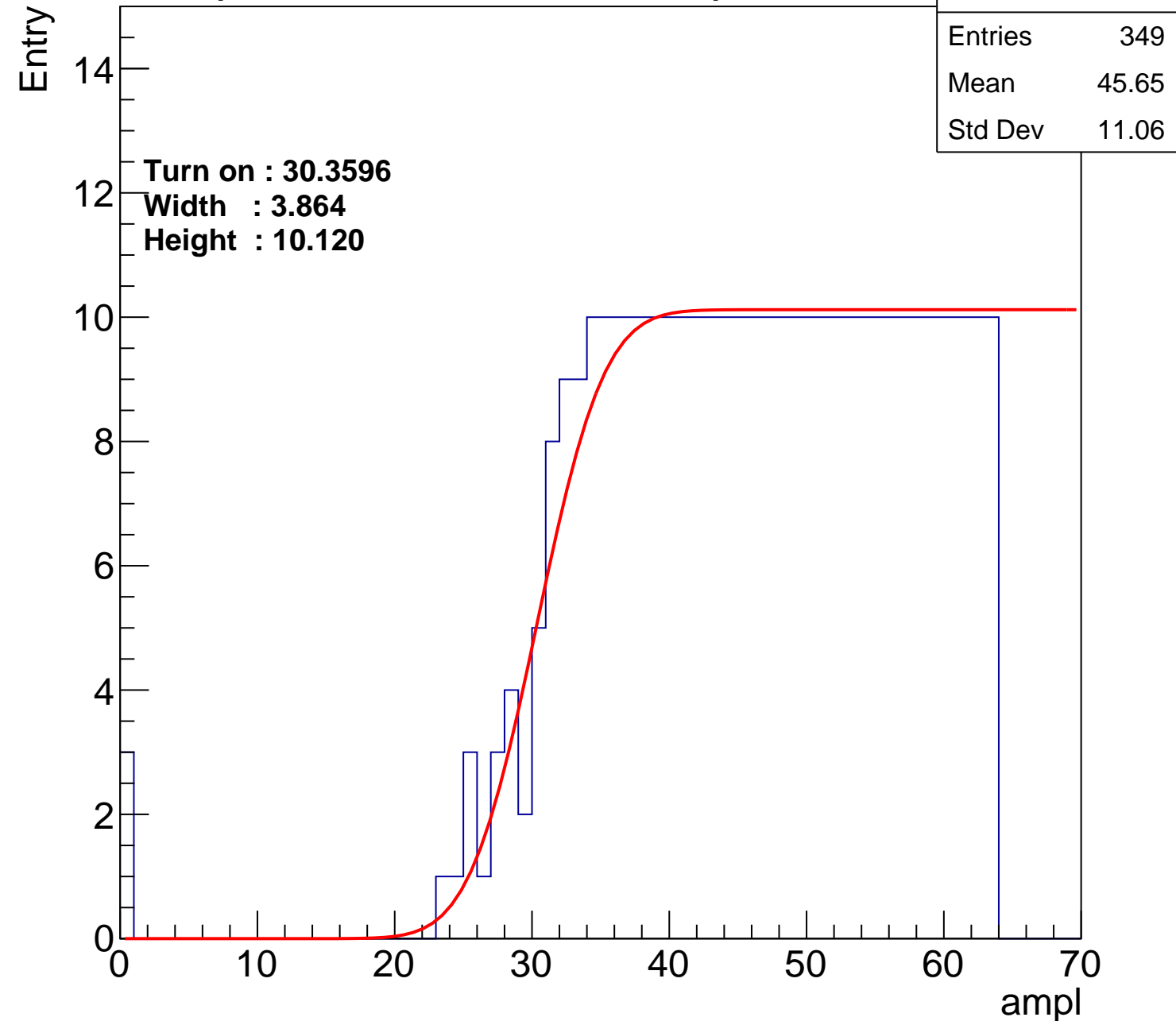
Width : 3.864

Height : 10.120

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch98

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 376   |
| Mean    | 44.41 |
| Std Dev | 11.57 |

Turn on : 26.9505

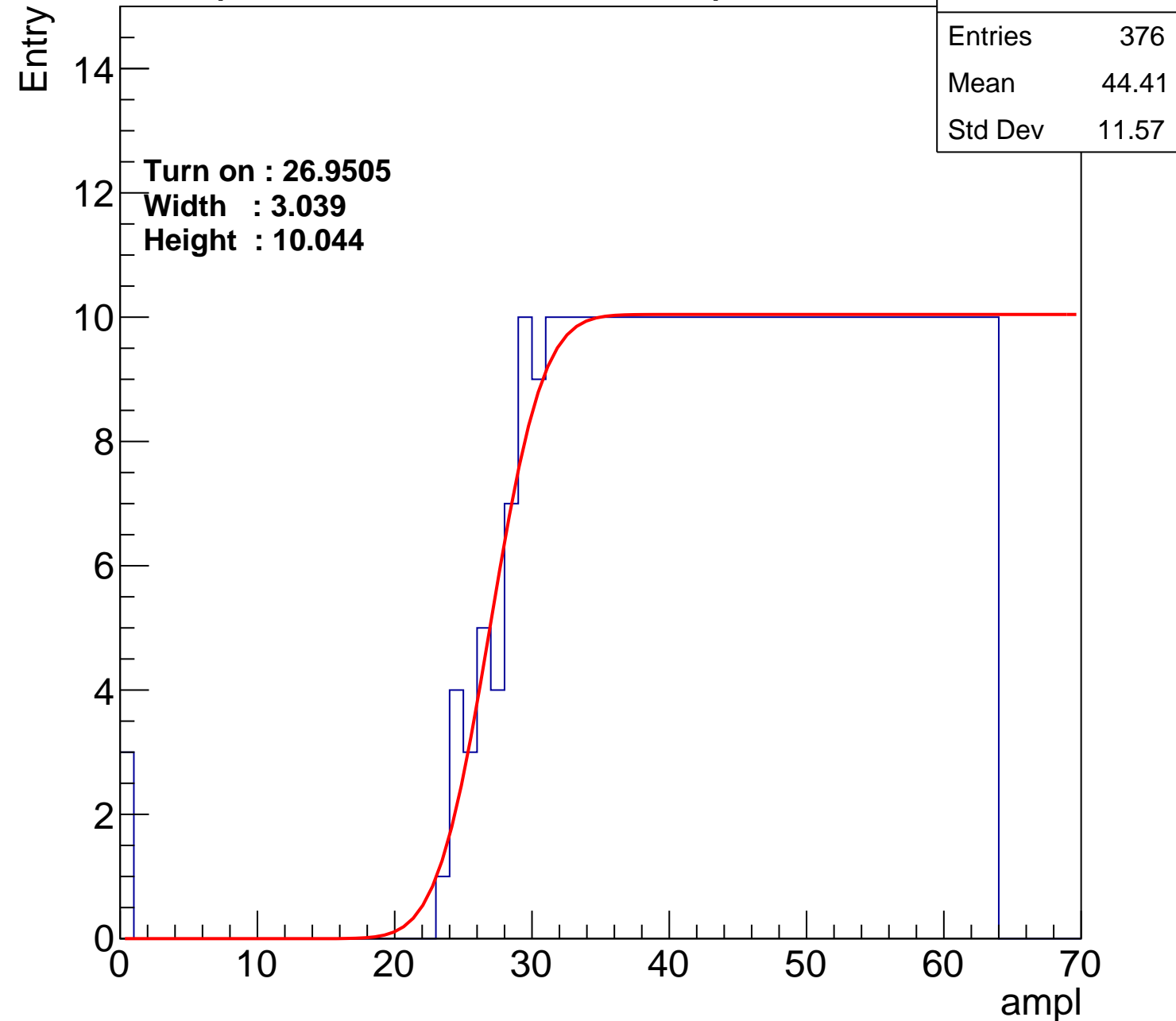
Width : 3.039

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch99

calib\_packv5\_042523\_0143.root, FC#13, port D2

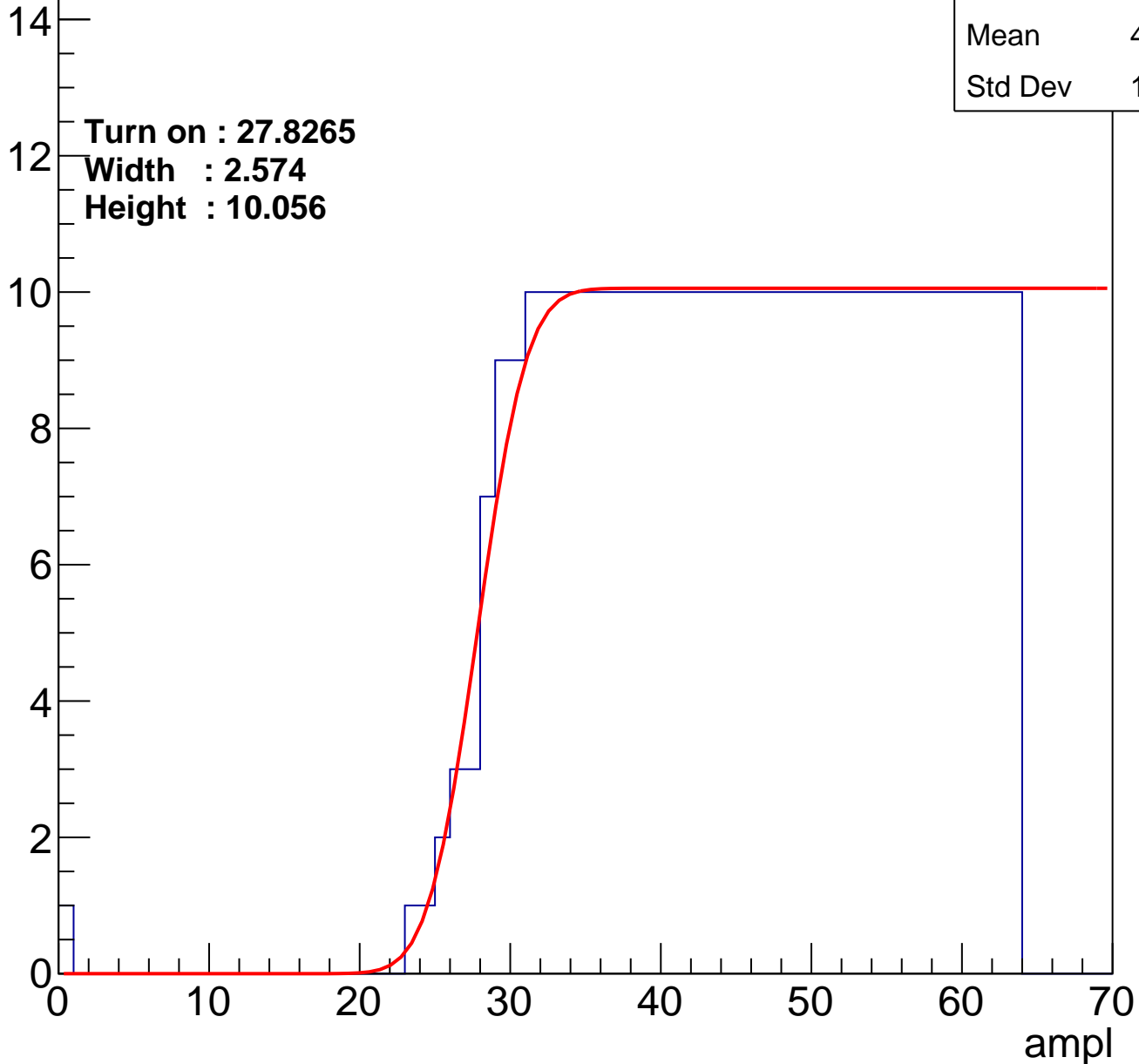
|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 45.06 |
| Std Dev | 10.89 |

Turn on : 27.8265

Width : 2.574

Height : 10.056

Entry



# B1L003S, U13-ch100

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.72 |
| Std Dev | 11.79 |

Turn on : 28.2510

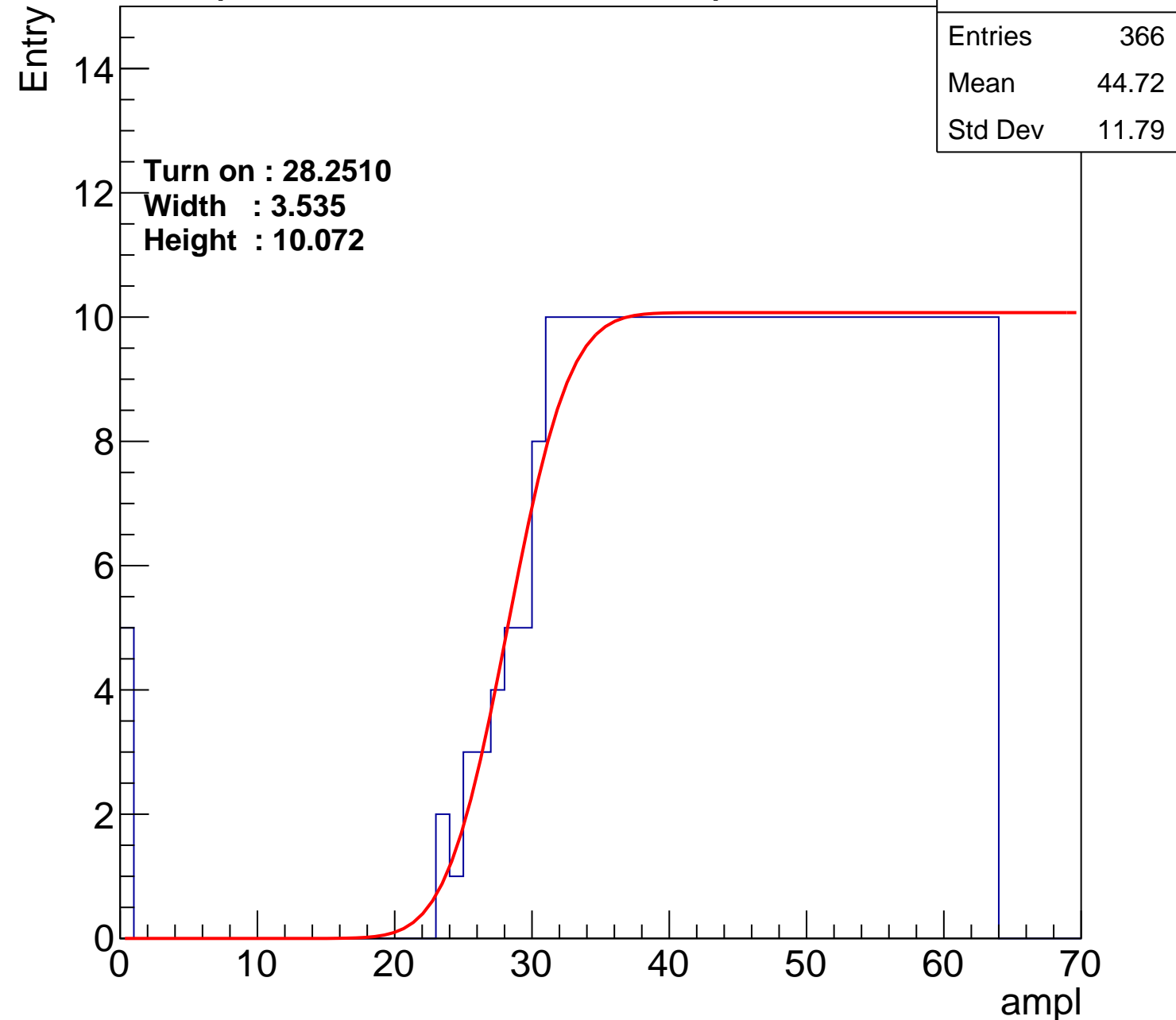
Width : 3.535

Height : 10.072

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch101

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.27 |
| Std Dev | 11.82 |

Turn on : 26.3253

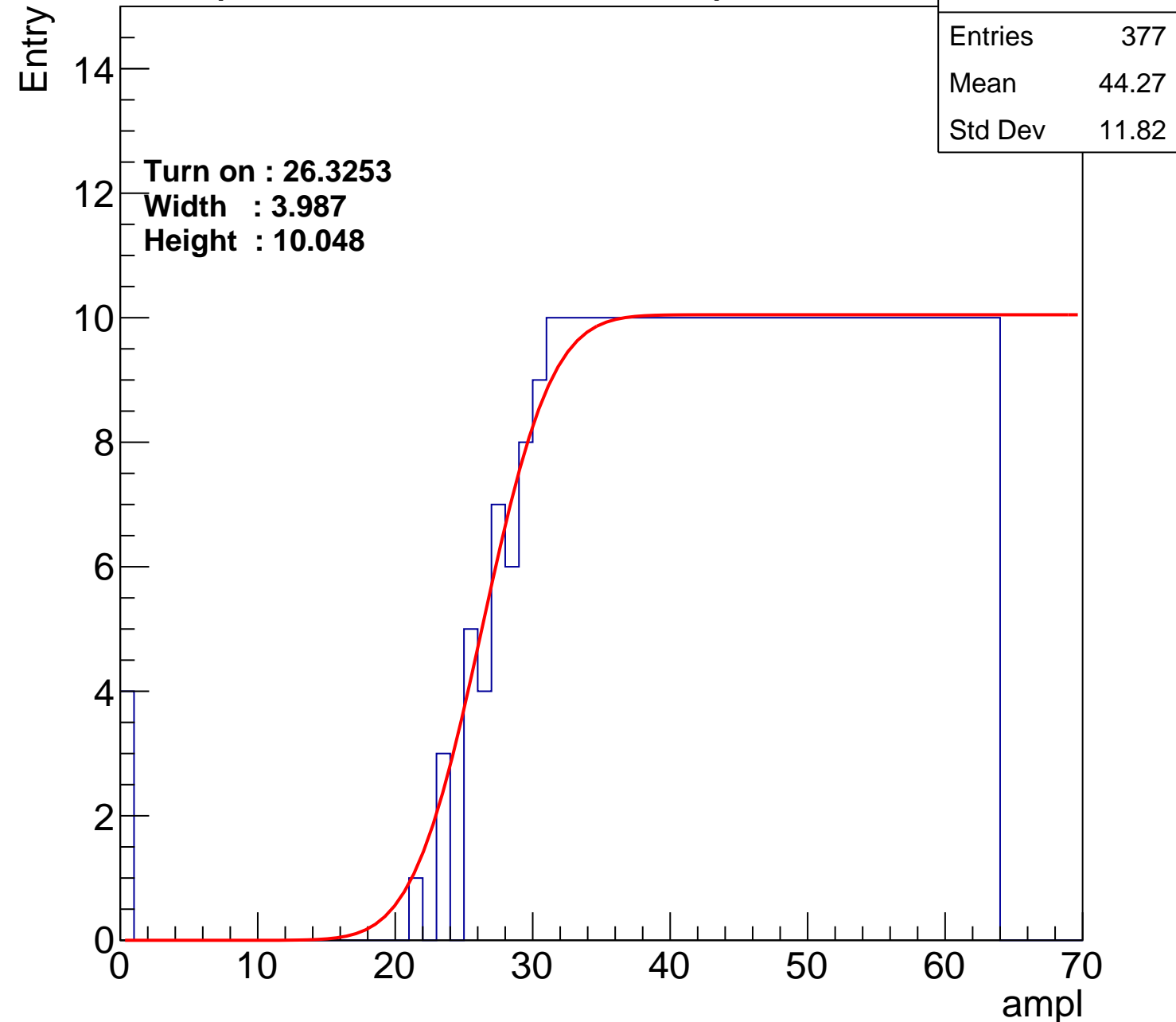
Width : 3.987

Height : 10.048

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch102

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 362   |
| Mean    | 44.97 |
| Std Dev | 11.51 |

Turn on : 28.3154

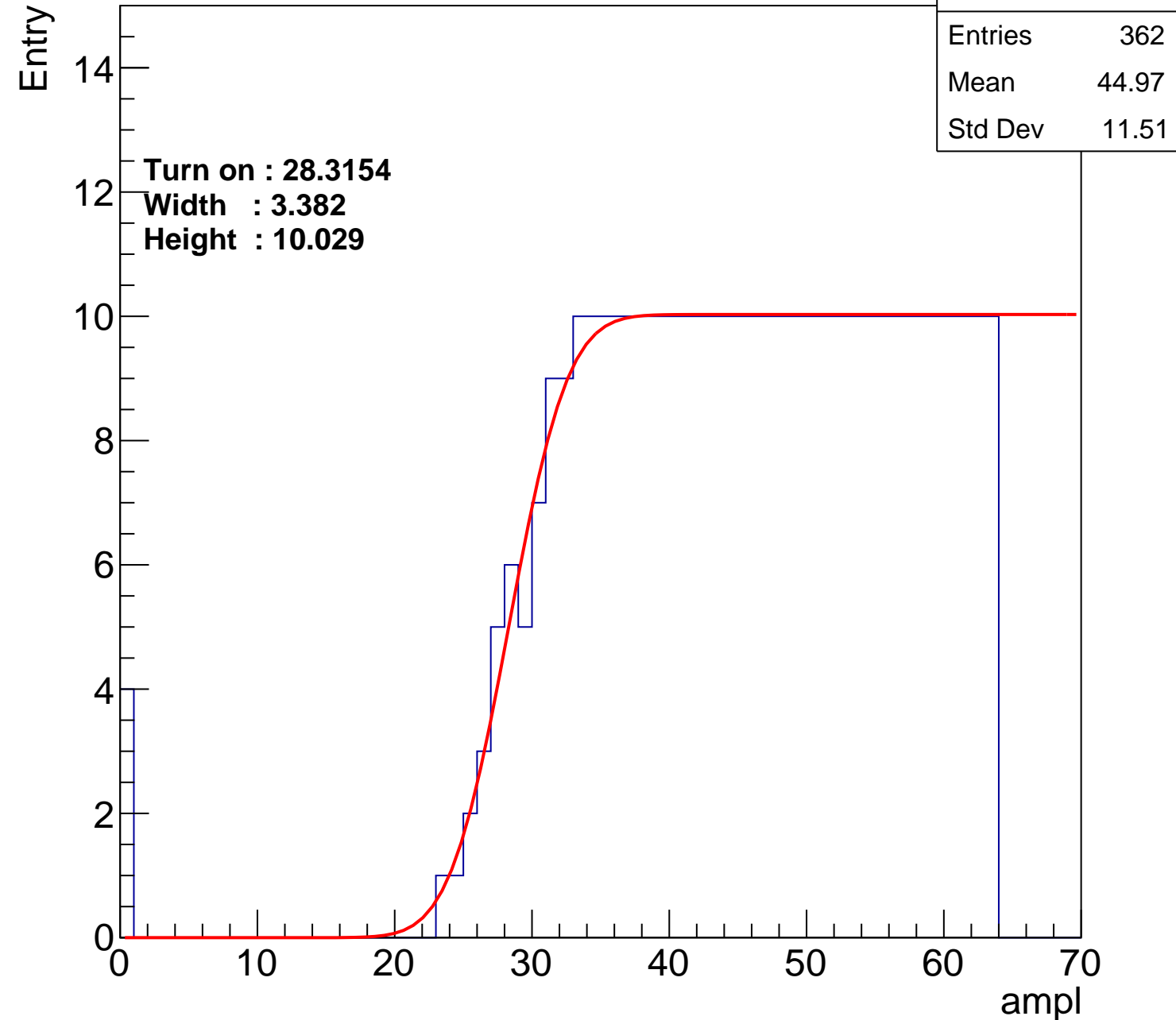
Width : 3.382

Height : 10.029

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch103

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 357   |
| Mean    | 45.31 |
| Std Dev | 11.15 |

**Turn on : 28.8727**

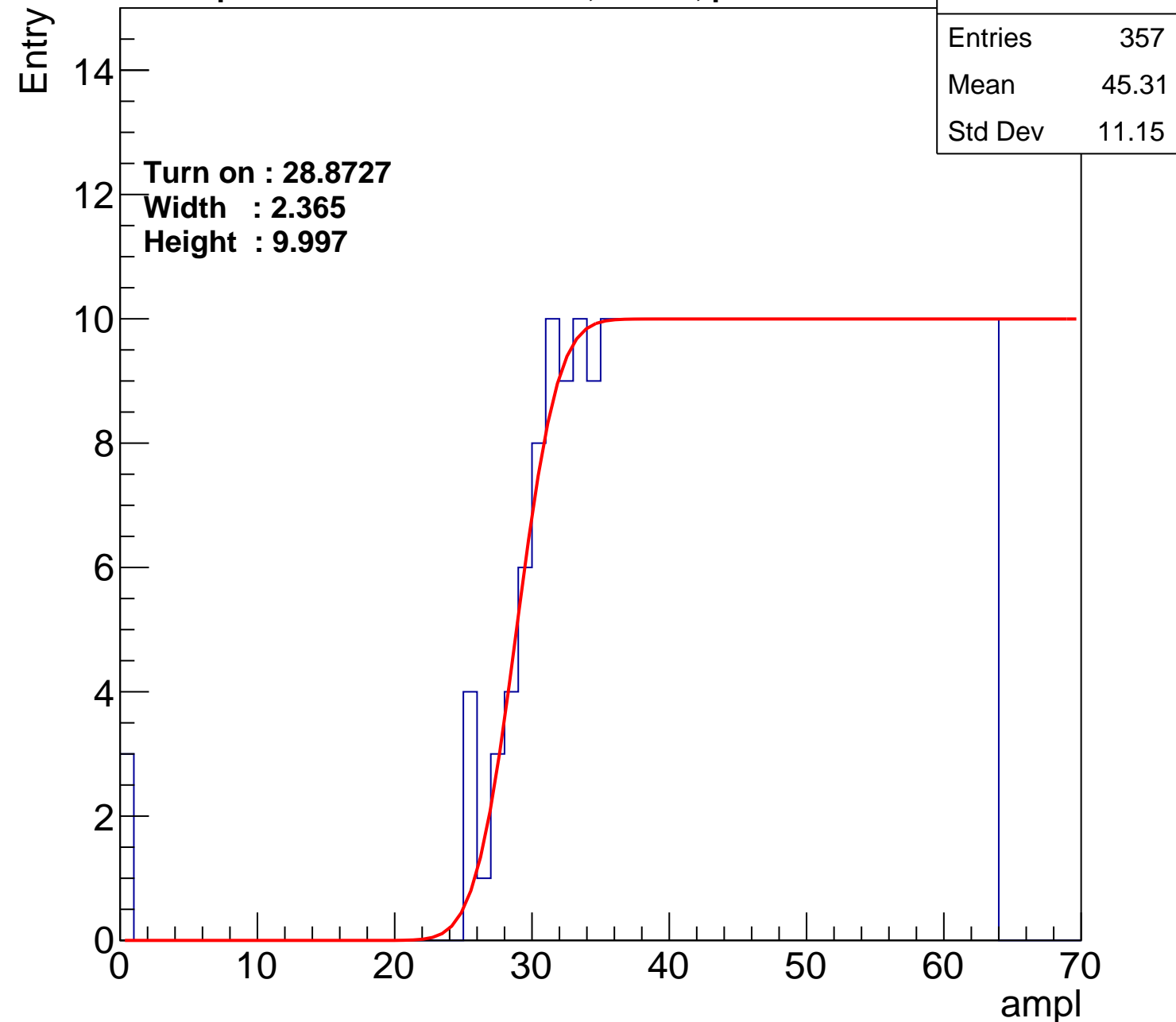
**Width : 2.365**

**Height : 9.997**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch104

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 369   |
| Mean    | 44.72 |
| Std Dev | 11.46 |

Turn on : 27.4555

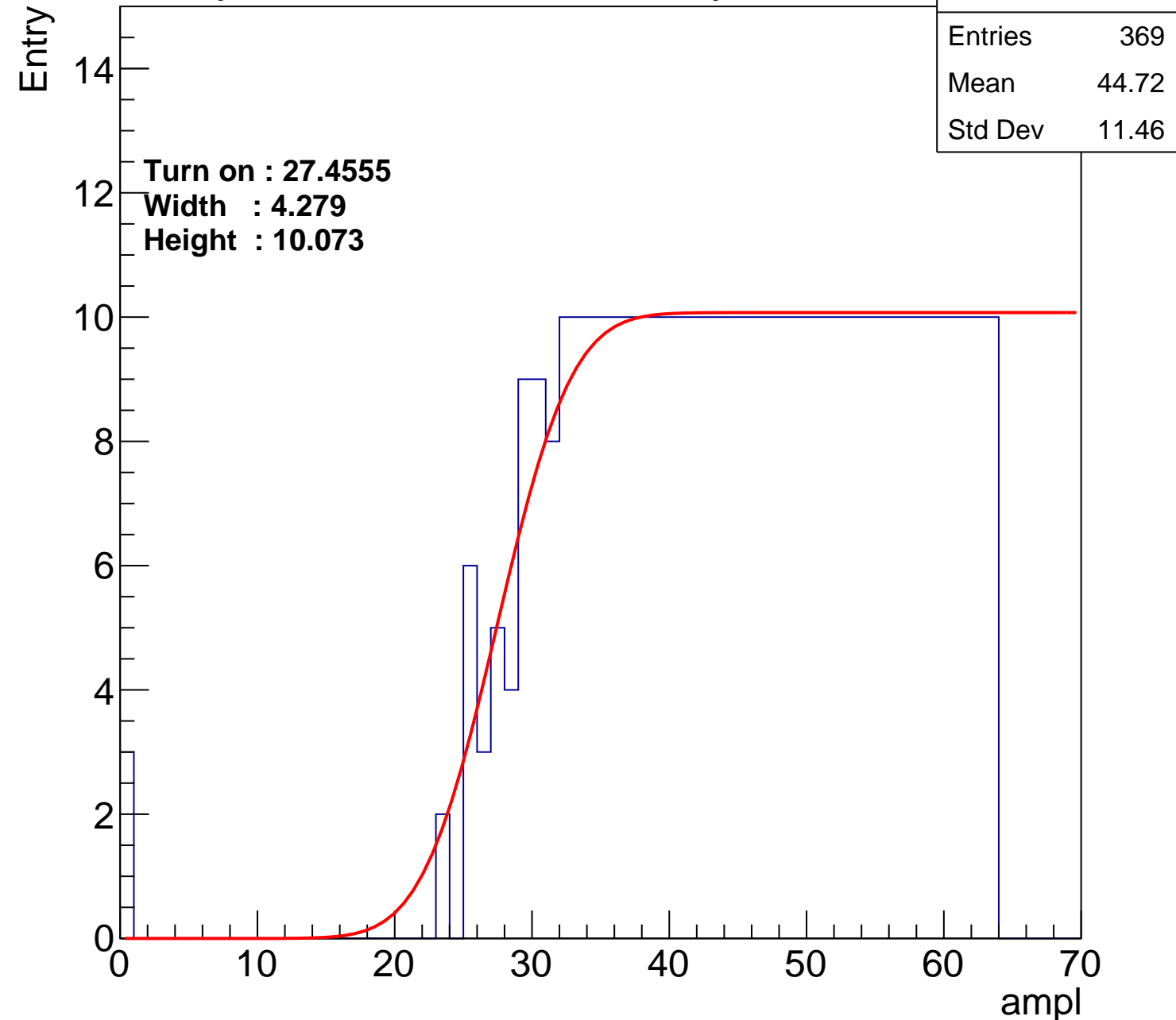
Width : 4.279

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch105

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 377   |
| Mean    | 44.38 |
| Std Dev | 11.48 |

Turn on : 28.0052

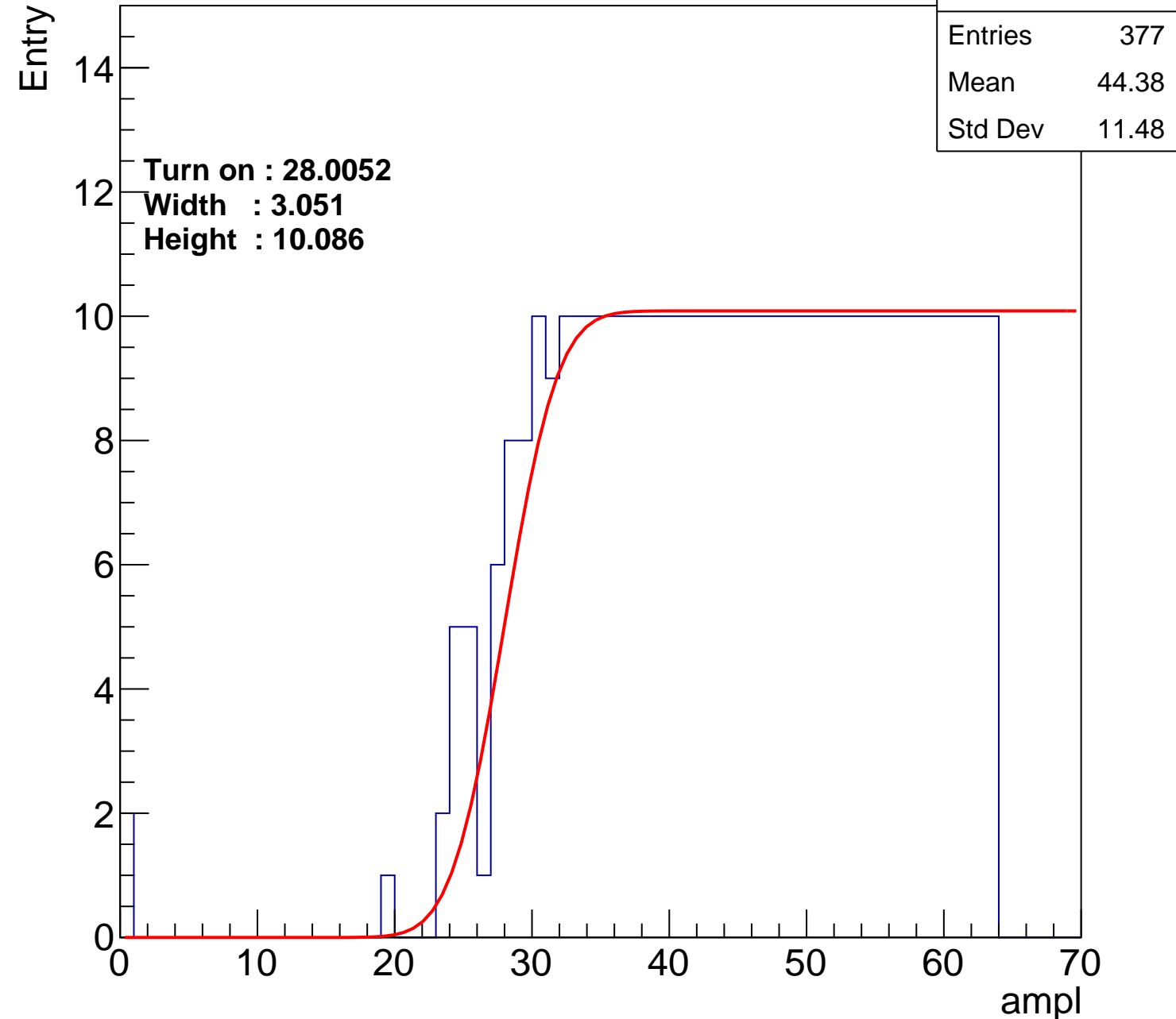
Width : 3.051

Height : 10.086

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch106

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 381   |
| Mean    | 43.93 |
| Std Dev | 12.27 |

Turn on : 27.2445

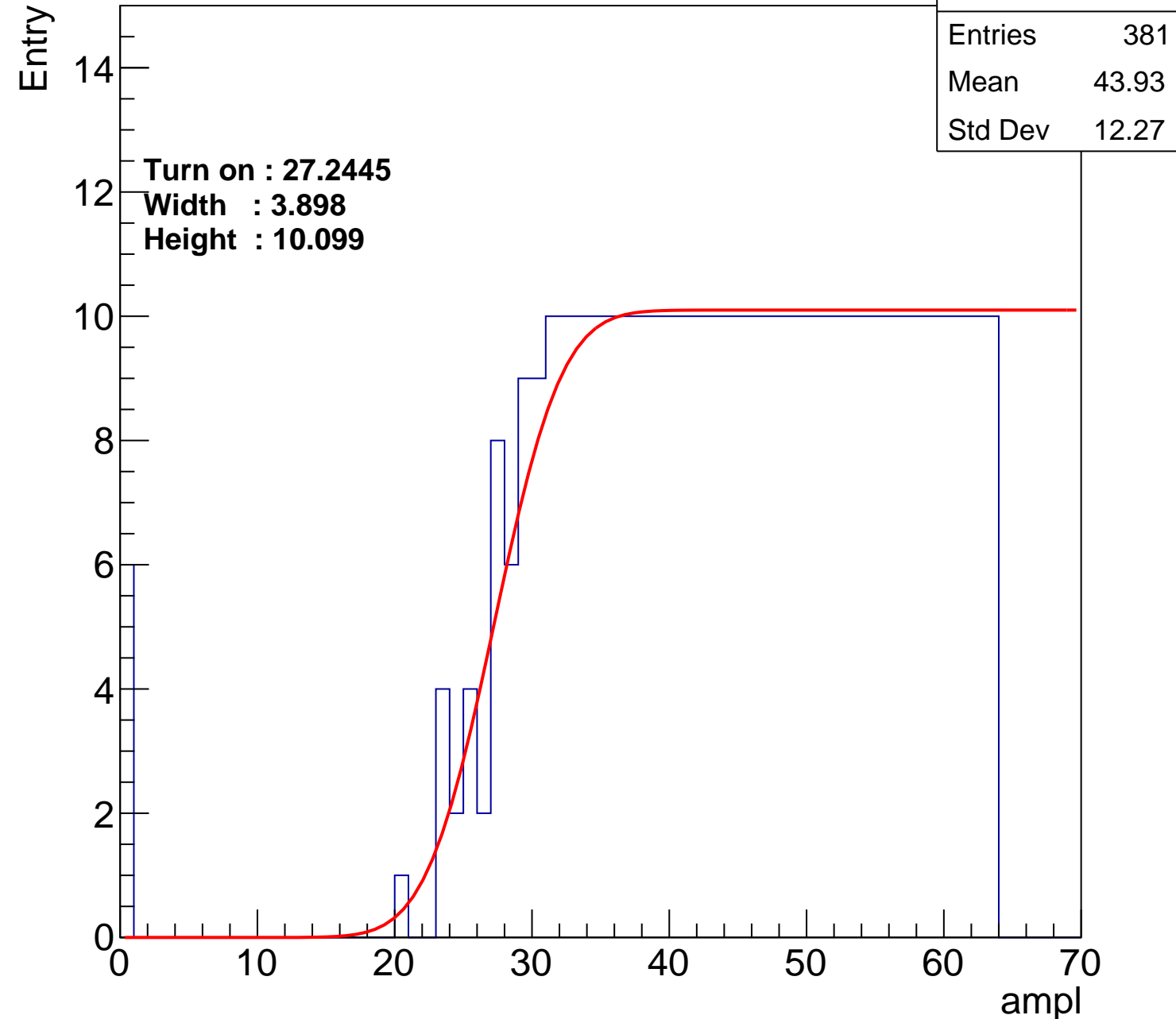
Width : 3.898

Height : 10.099

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch107

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.79 |
| Std Dev | 11.11 |

Turn on : 27.0603

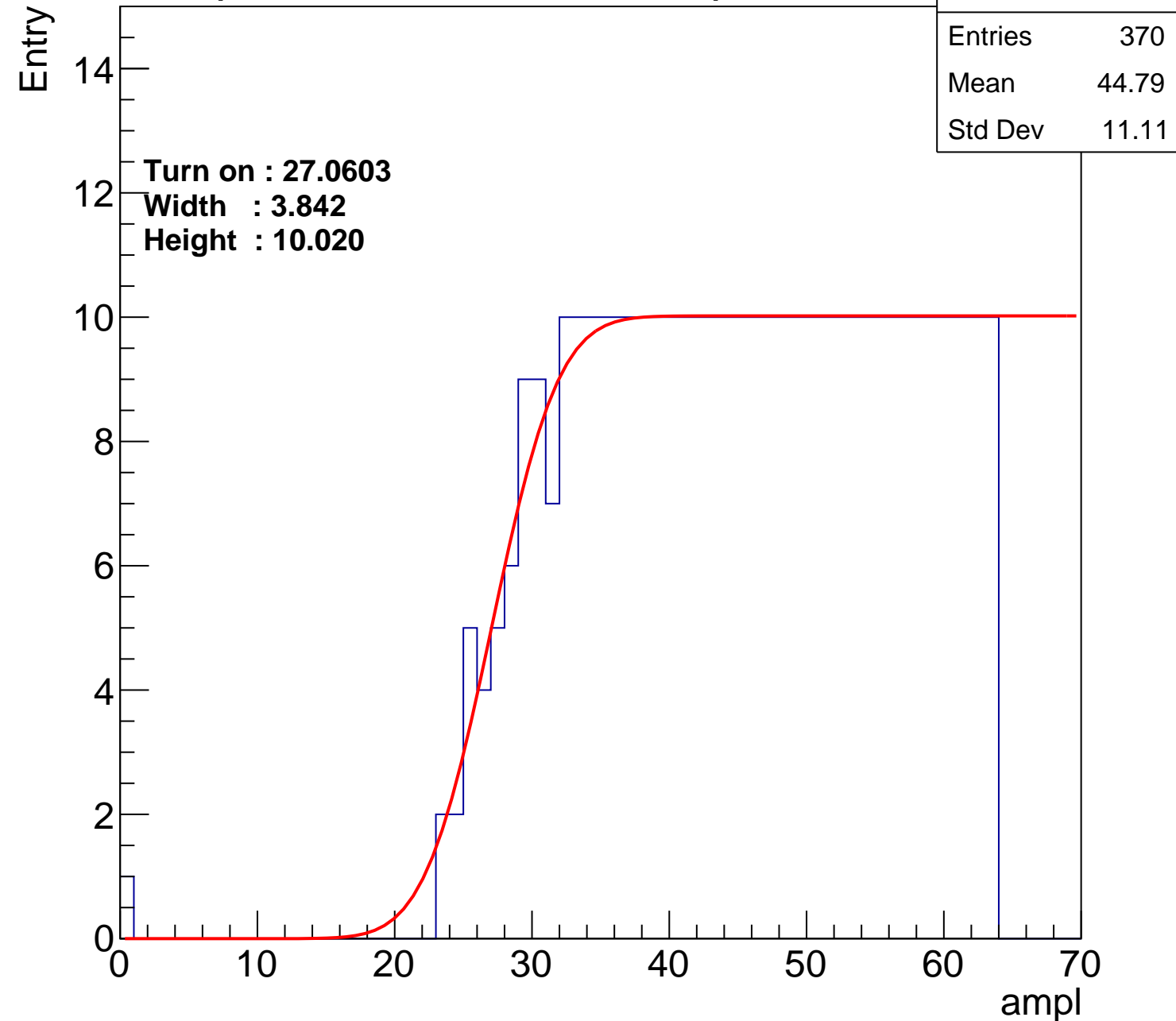
Width : 3.842

Height : 10.020

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch108

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 384   |
| Mean    | 43.81 |
| Std Dev | 12.3  |

Turn on : 27.8284

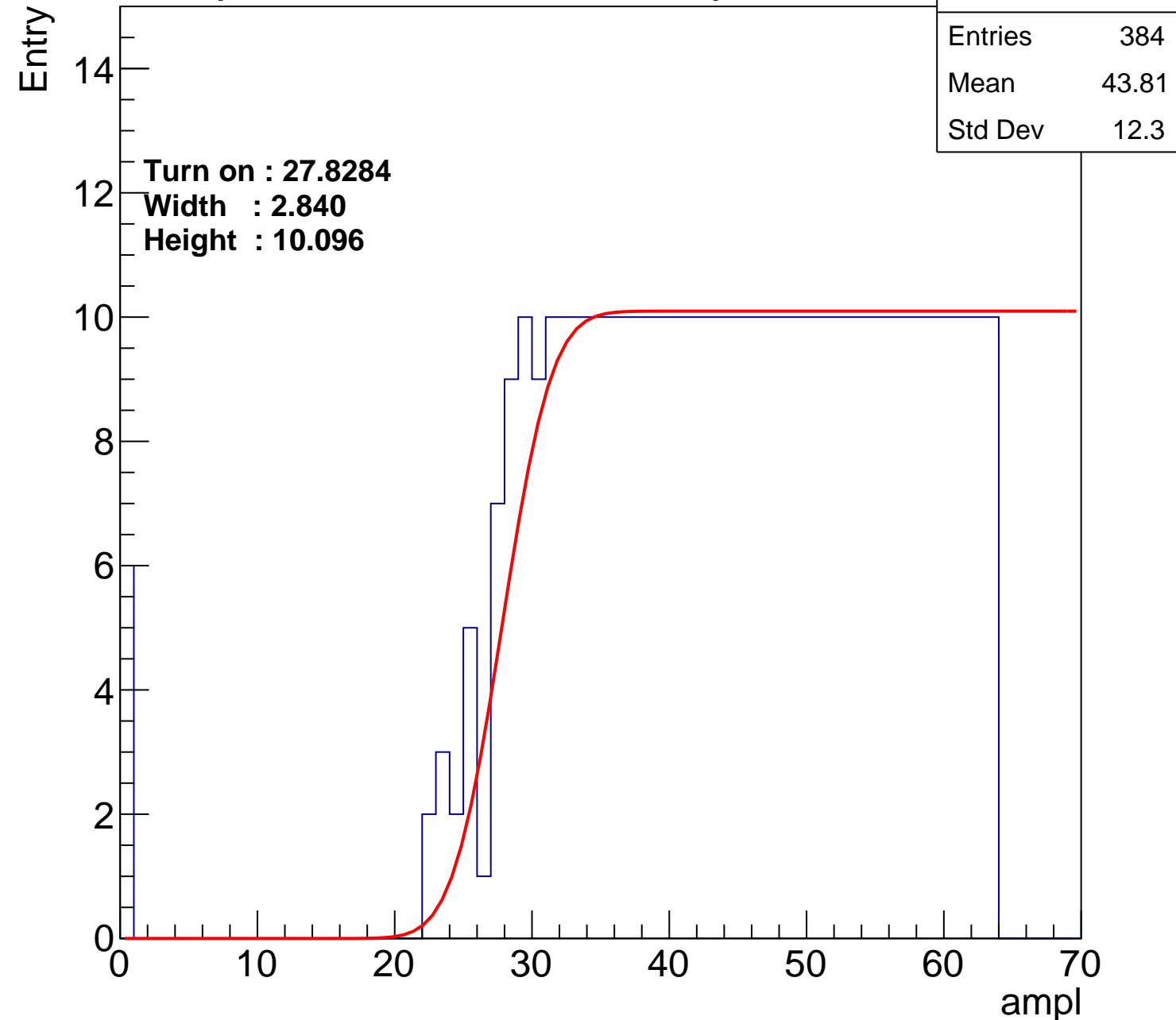
Width : 2.840

Height : 10.096

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch109

calib\_packv5\_042523\_0143.root, FC#13, port D2

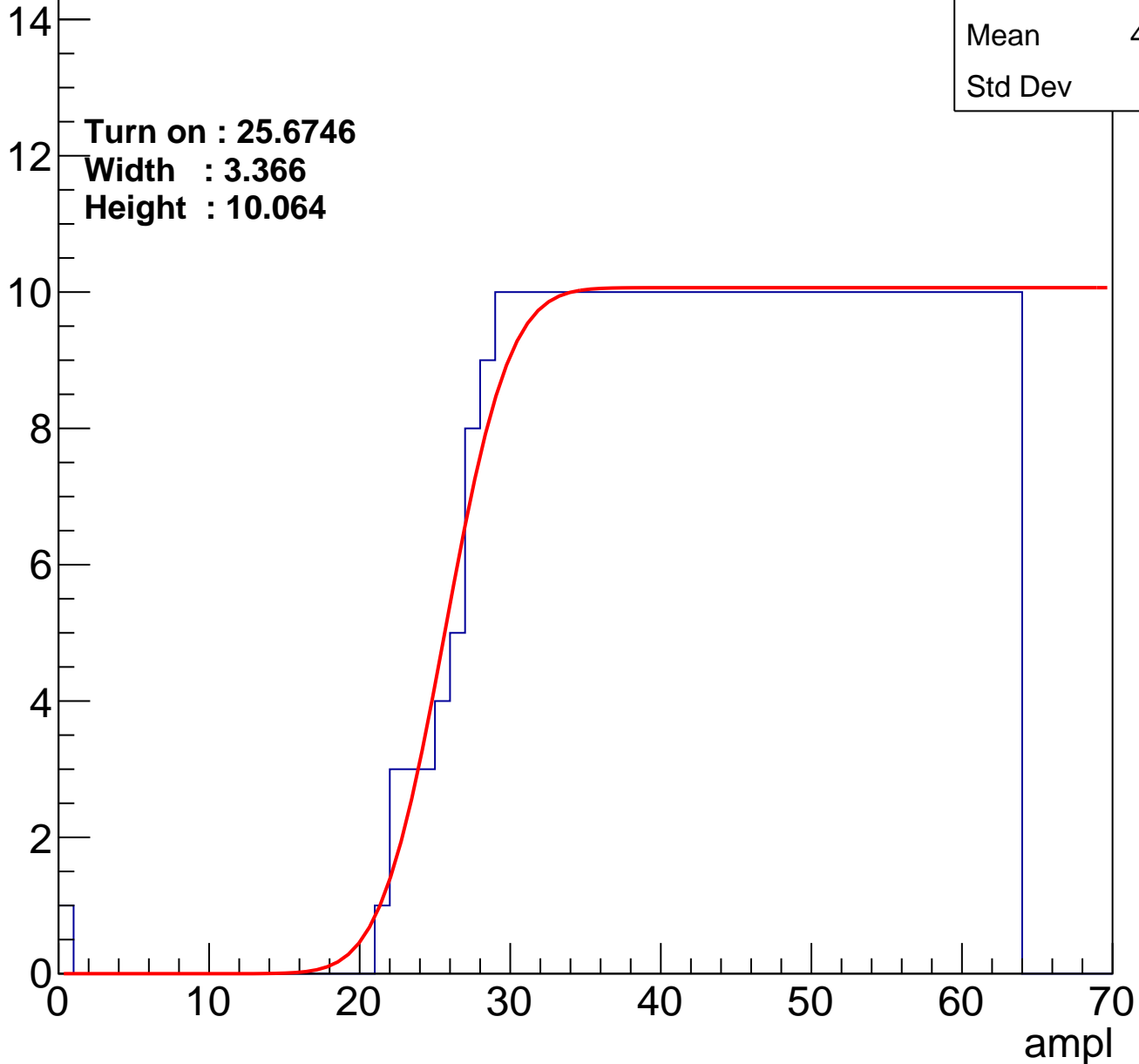
|         |       |
|---------|-------|
| Entries | 387   |
| Mean    | 43.99 |
| Std Dev | 11.5  |

Turn on : 25.6746

Width : 3.366

Height : 10.064

Entry



# B1L003S, U13-ch110

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 363   |
| Mean    | 44.77 |
| Std Dev | 11.95 |

Turn on : 28.7471

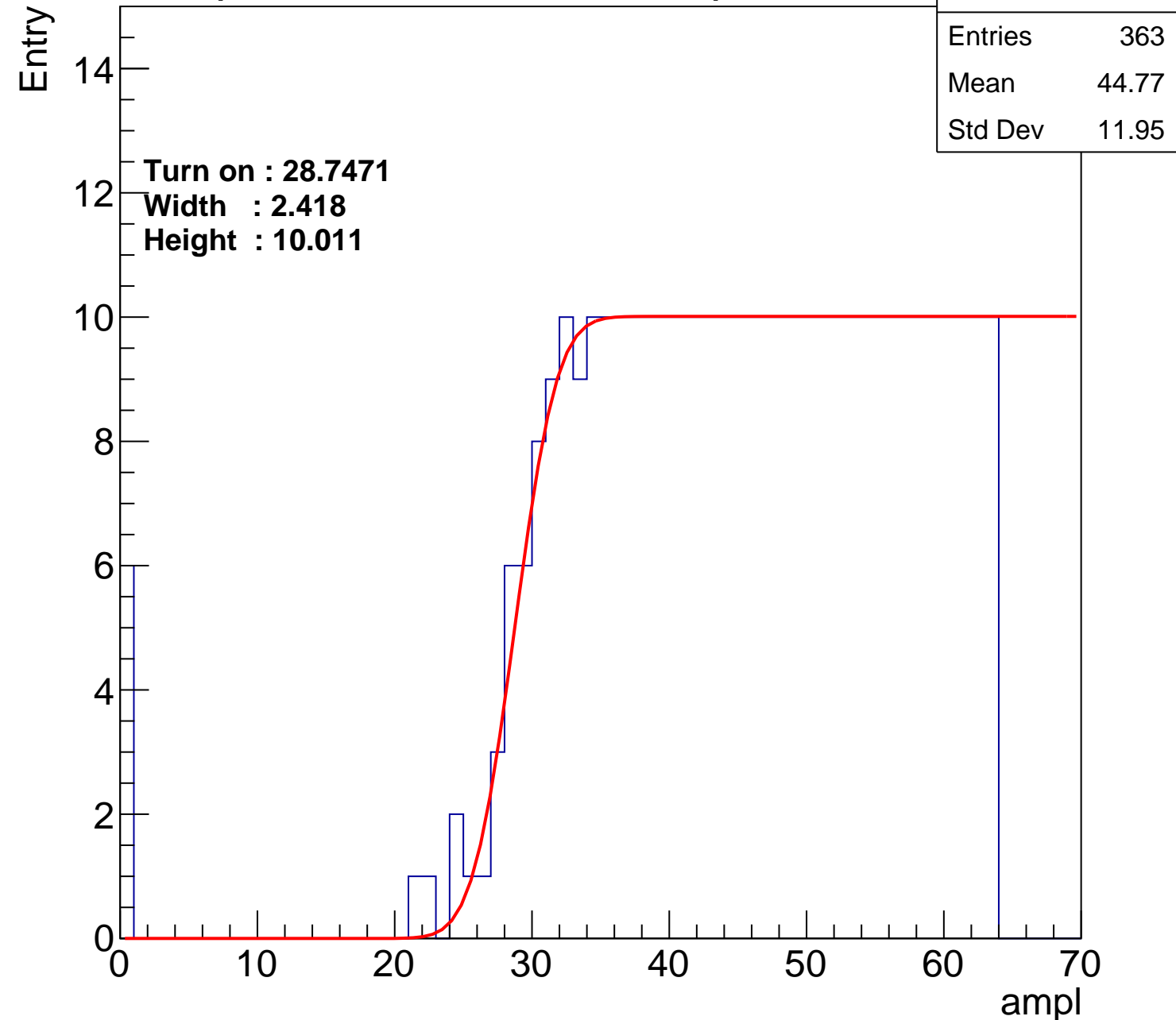
Width : 2.418

Height : 10.011

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B1L003S, U13-ch111

calib\_packv5\_042523\_0143.root, FC#13, port D2

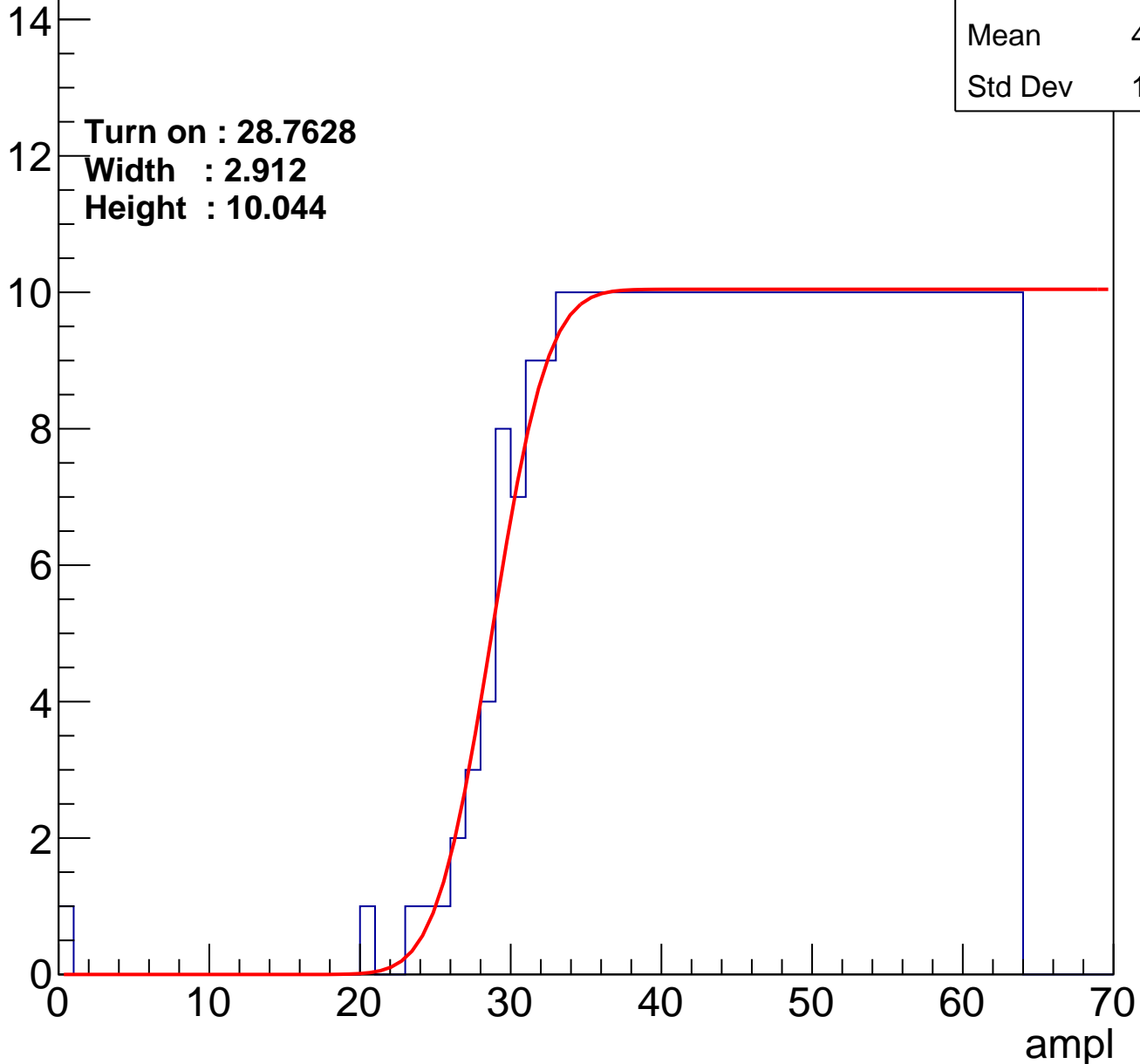
|         |       |
|---------|-------|
| Entries | 357   |
| Mean    | 45.45 |
| Std Dev | 10.75 |

Turn on : 28.7628

Width : 2.912

Height : 10.044

Entry



# B1L003S, U13-ch112

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 370   |
| Mean    | 44.69 |
| Std Dev | 11.35 |

Turn on : 27.5974

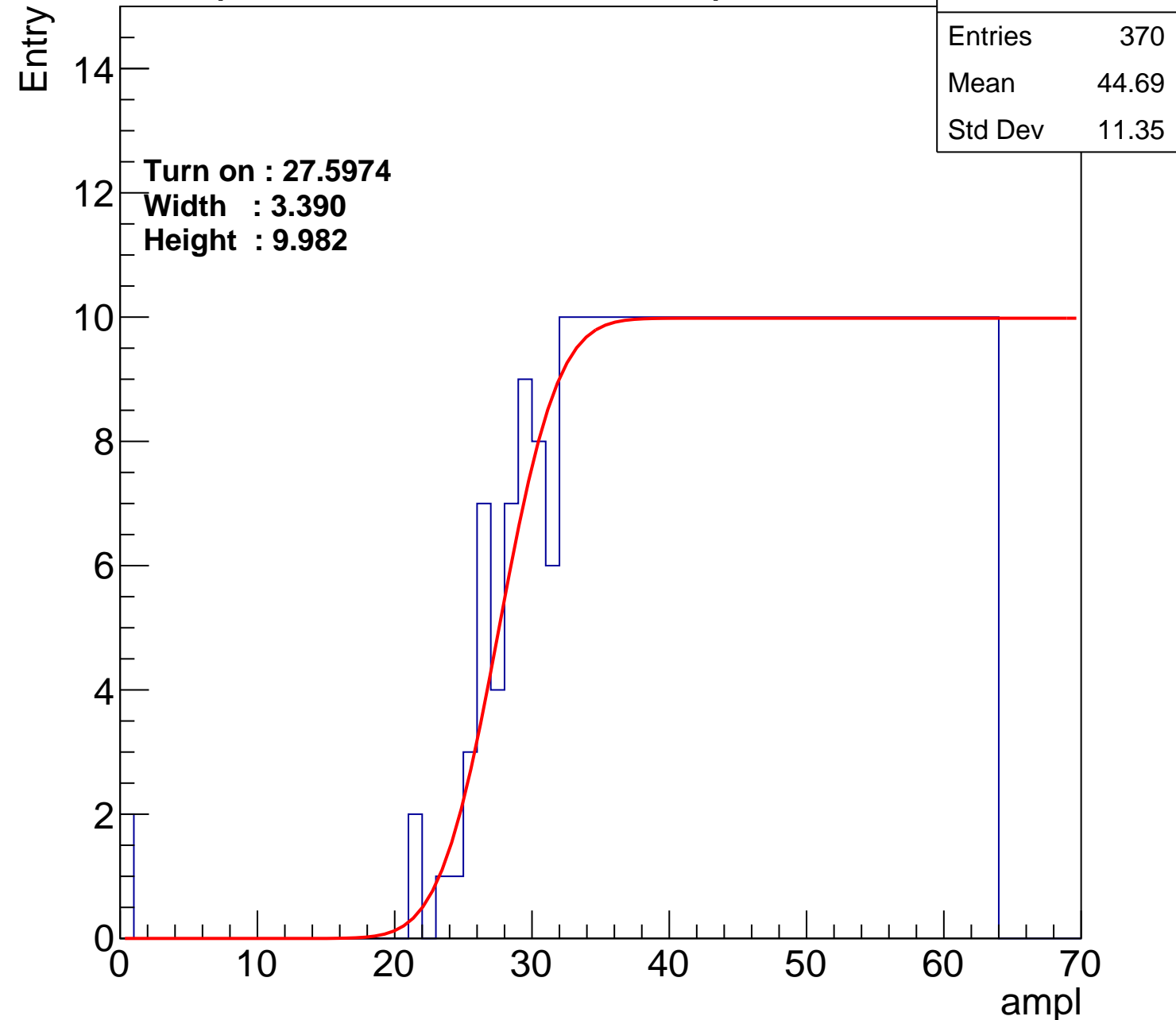
Width : 3.390

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch113

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.8  |
| Std Dev | 10.55 |

Turn on : 29.5980

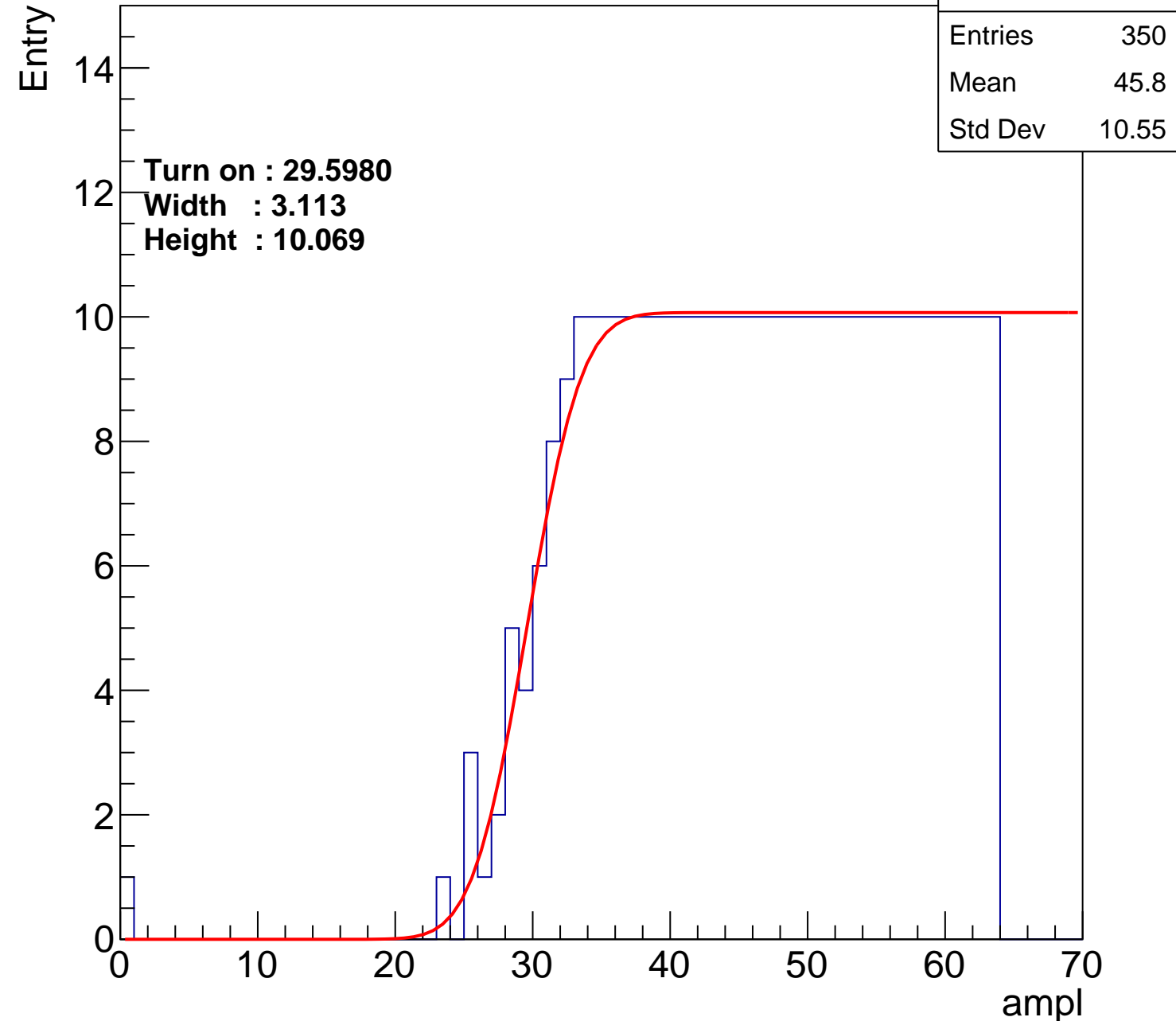
Width : 3.113

Height : 10.069

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch114

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 366   |
| Mean    | 44.85 |
| Std Dev | 11.41 |

Turn on : 28.5586

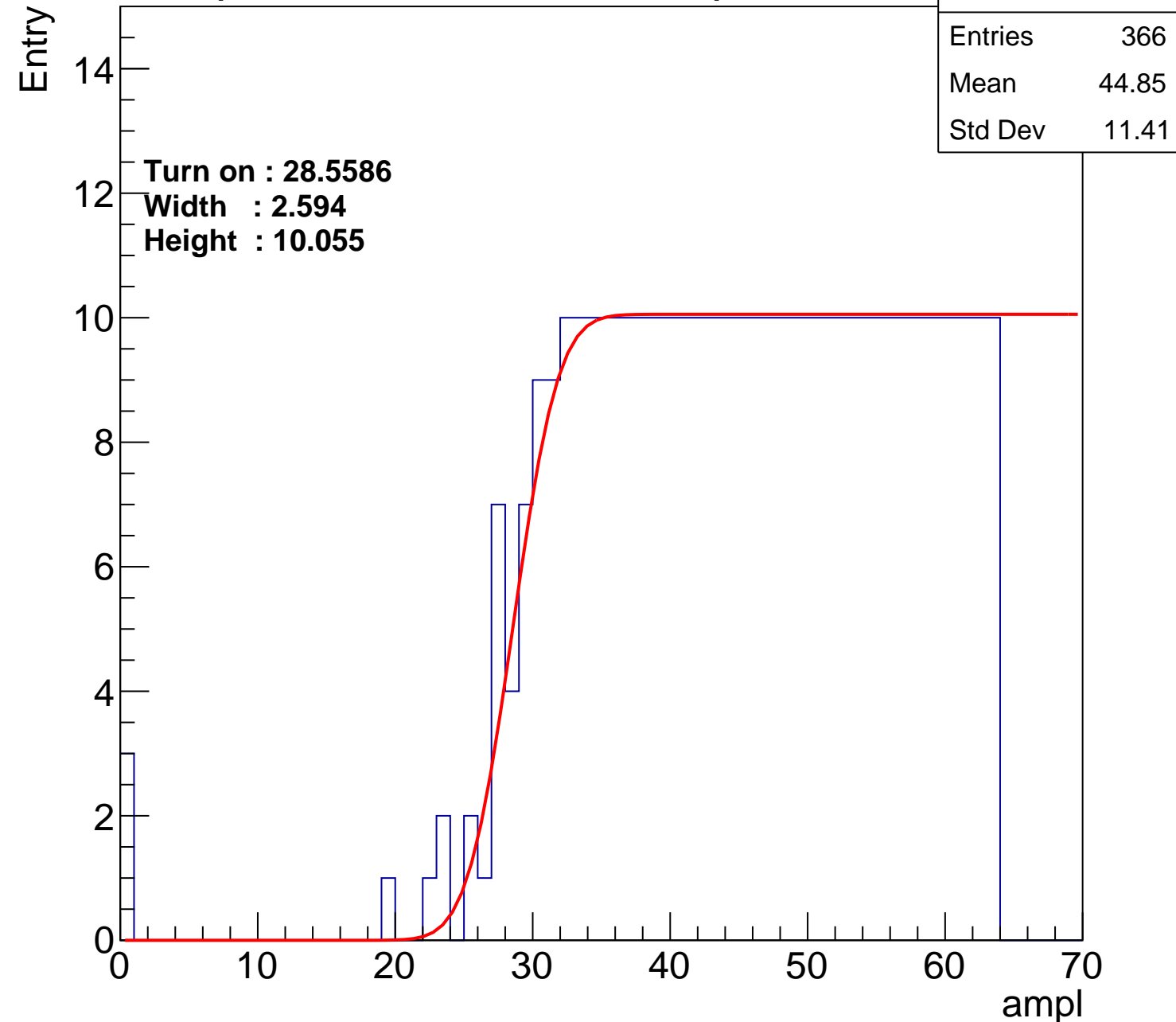
Width : 2.594

Height : 10.055

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch115

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 45.07 |
| Std Dev | 11.41 |

Turn on : 28.5353

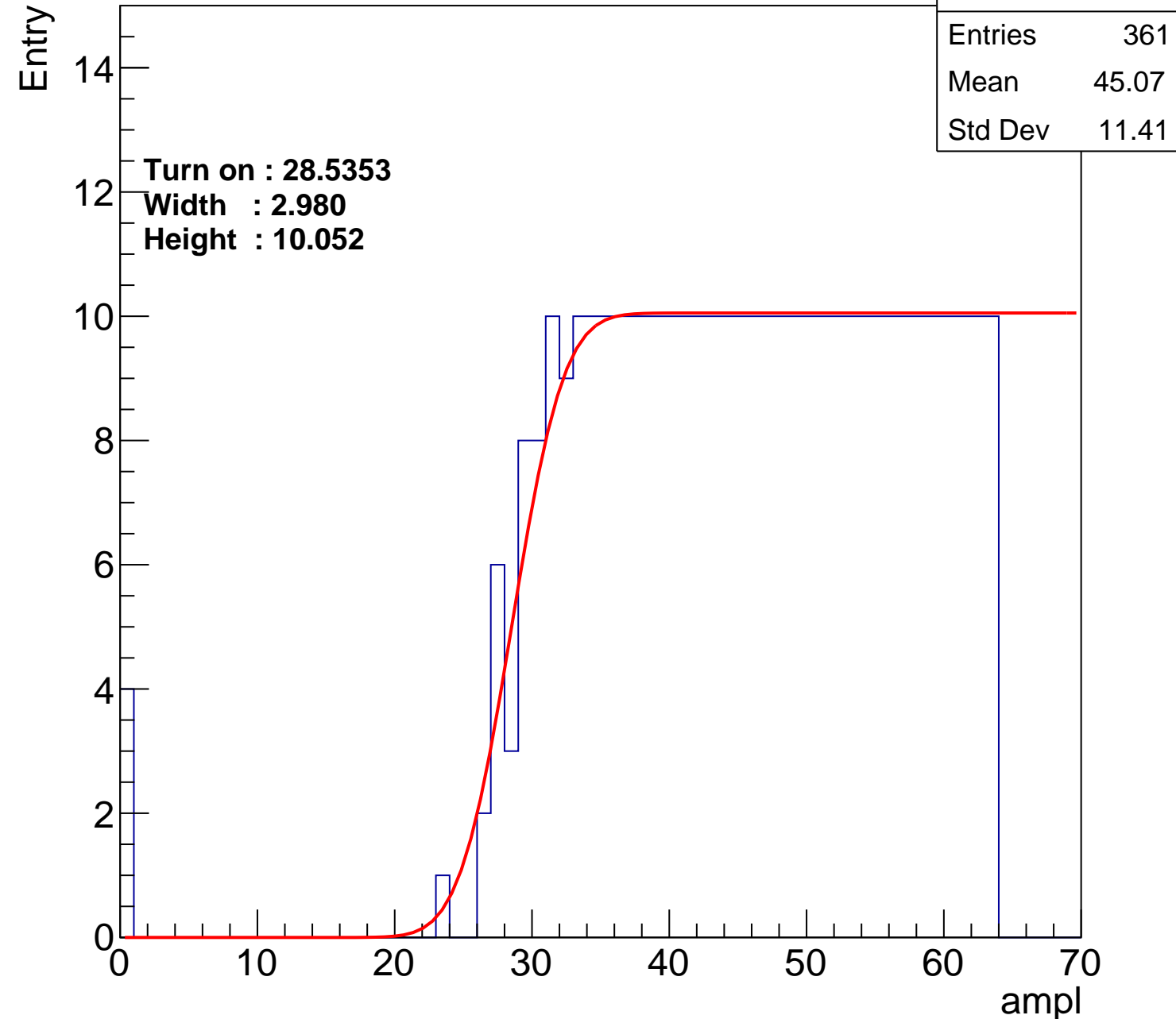
Width : 2.980

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch116

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 365   |
| Mean    | 45.11 |
| Std Dev | 10.86 |

**Turn on : 27.7585**

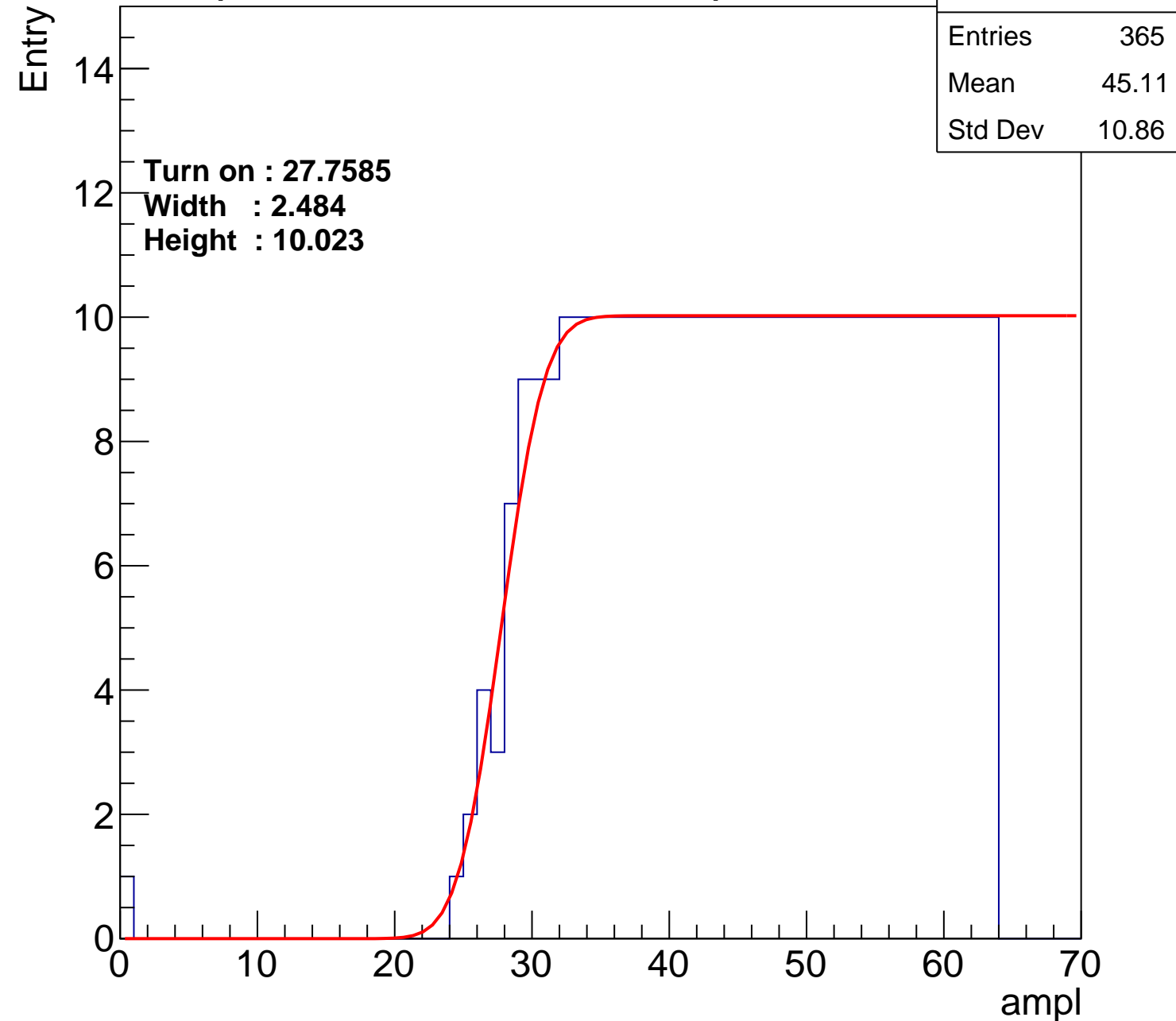
**Width : 2.484**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch117

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 361   |
| Mean    | 44.98 |
| Std Dev | 11.55 |

Turn on : 28.8374

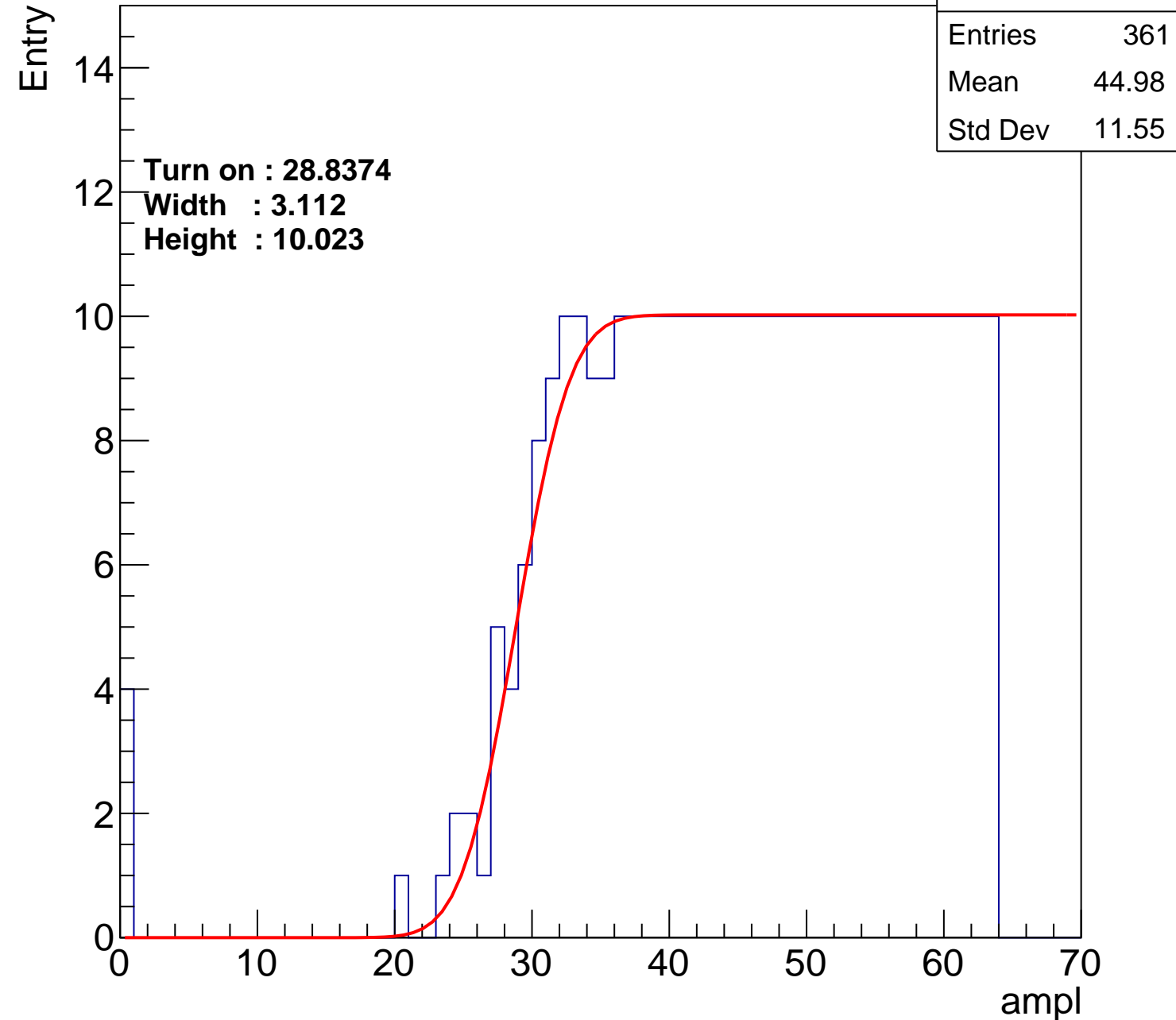
Width : 3.112

Height : 10.023

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



B1L003S, U13-ch118

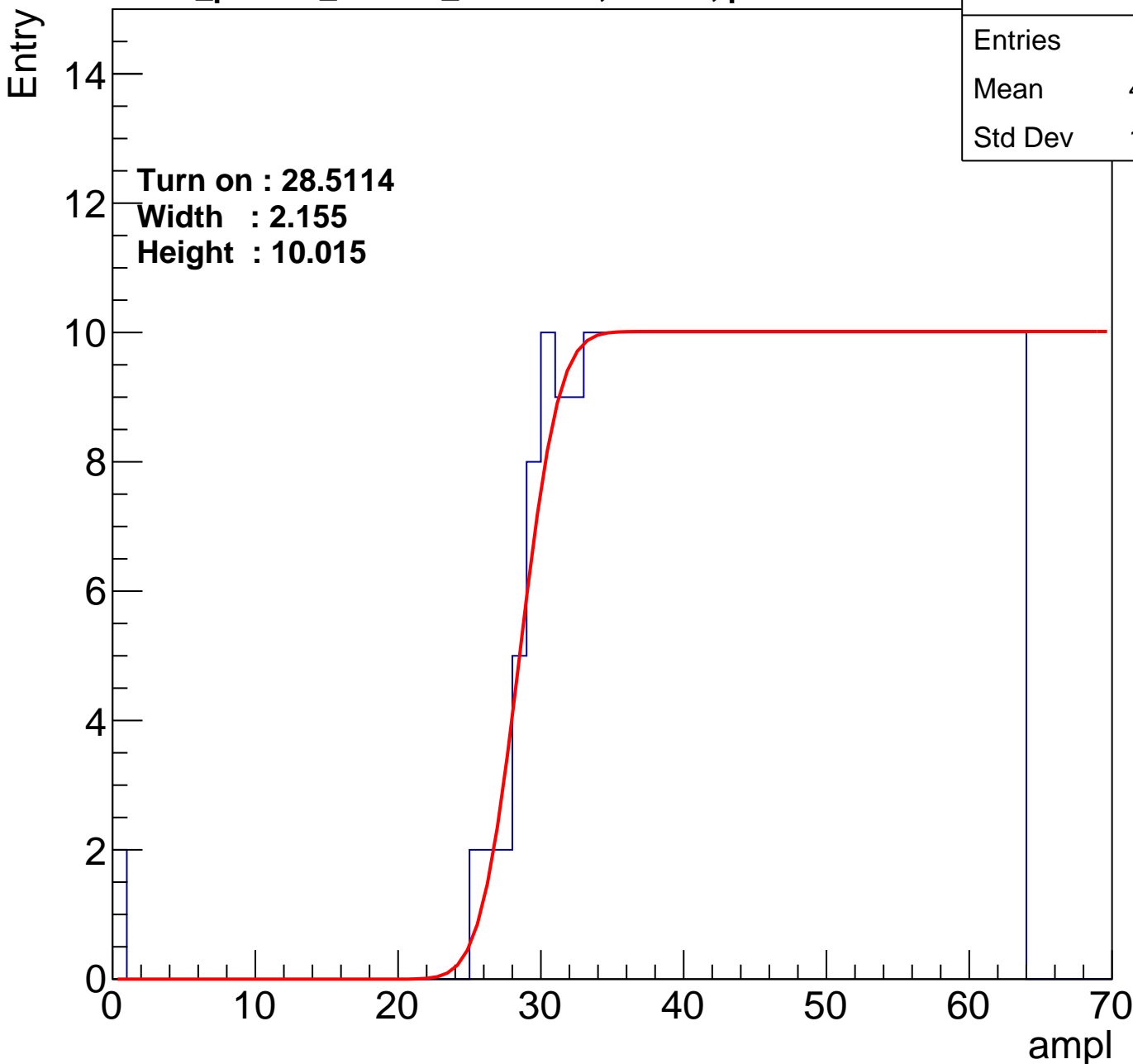
**calib\_packv5\_042523\_0143.root, FC#13, port D2**

|         |       |
|---------|-------|
| Entries | 359   |
| Mean    | 45.33 |
| Std Dev | 10.92 |

**Turn on : 28.5114**

**Width : 2.155**

**Height : 10.015**





# B1L003S, U13-ch119

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 350   |
| Mean    | 45.79 |
| Std Dev | 10.55 |

Turn on : 28.9376

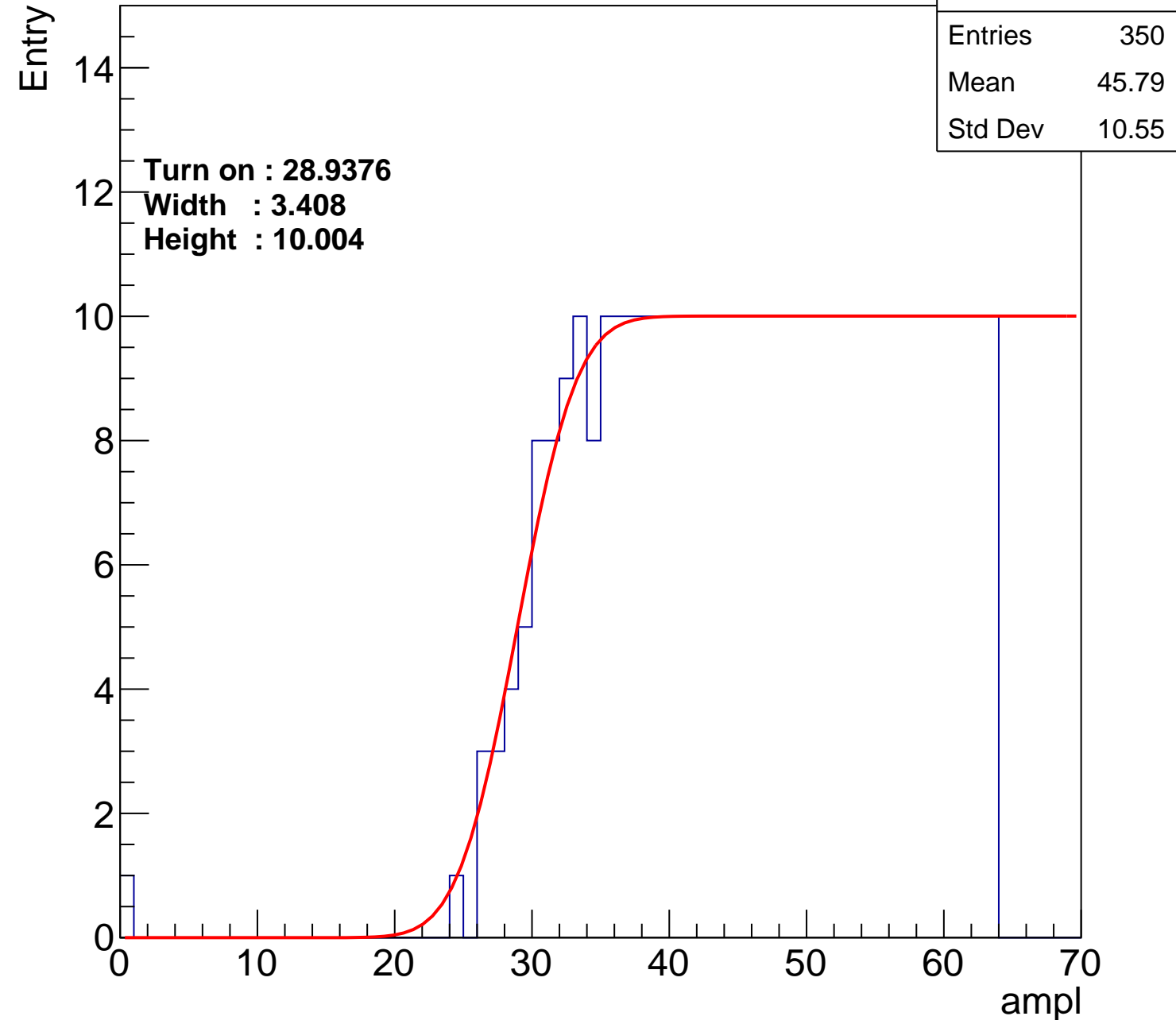
Width : 3.408

Height : 10.004

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch120

calib\_packv5\_042523\_0143.root, FC#13, port D2

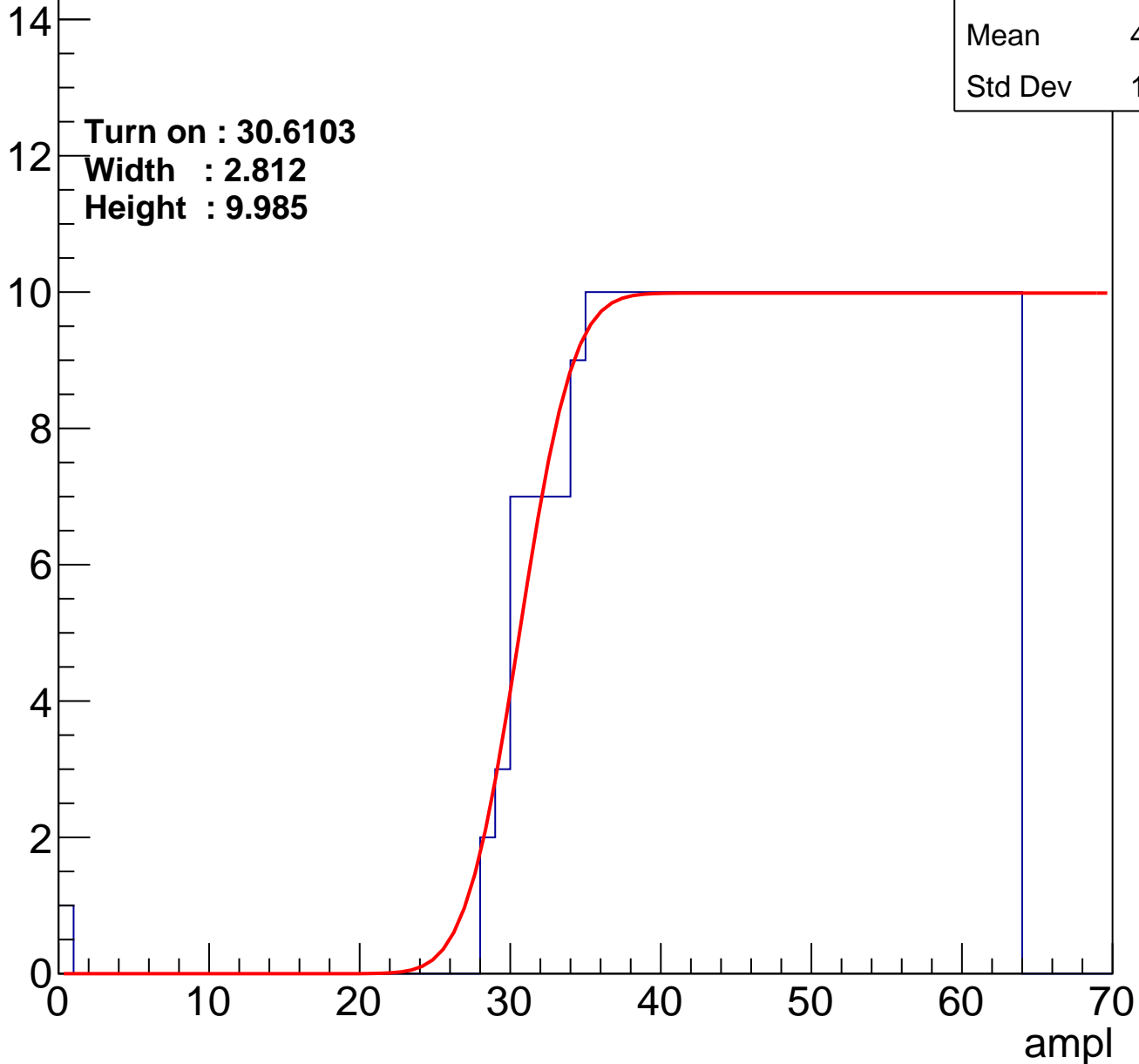
|         |       |
|---------|-------|
| Entries | 333   |
| Mean    | 46.67 |
| Std Dev | 10.05 |

Turn on : 30.6103

Width : 2.812

Height : 9.985

Entry



# B1L003S, U13-ch121

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 353   |
| Mean    | 45.56 |
| Std Dev | 10.89 |

Turn on : 29.7663

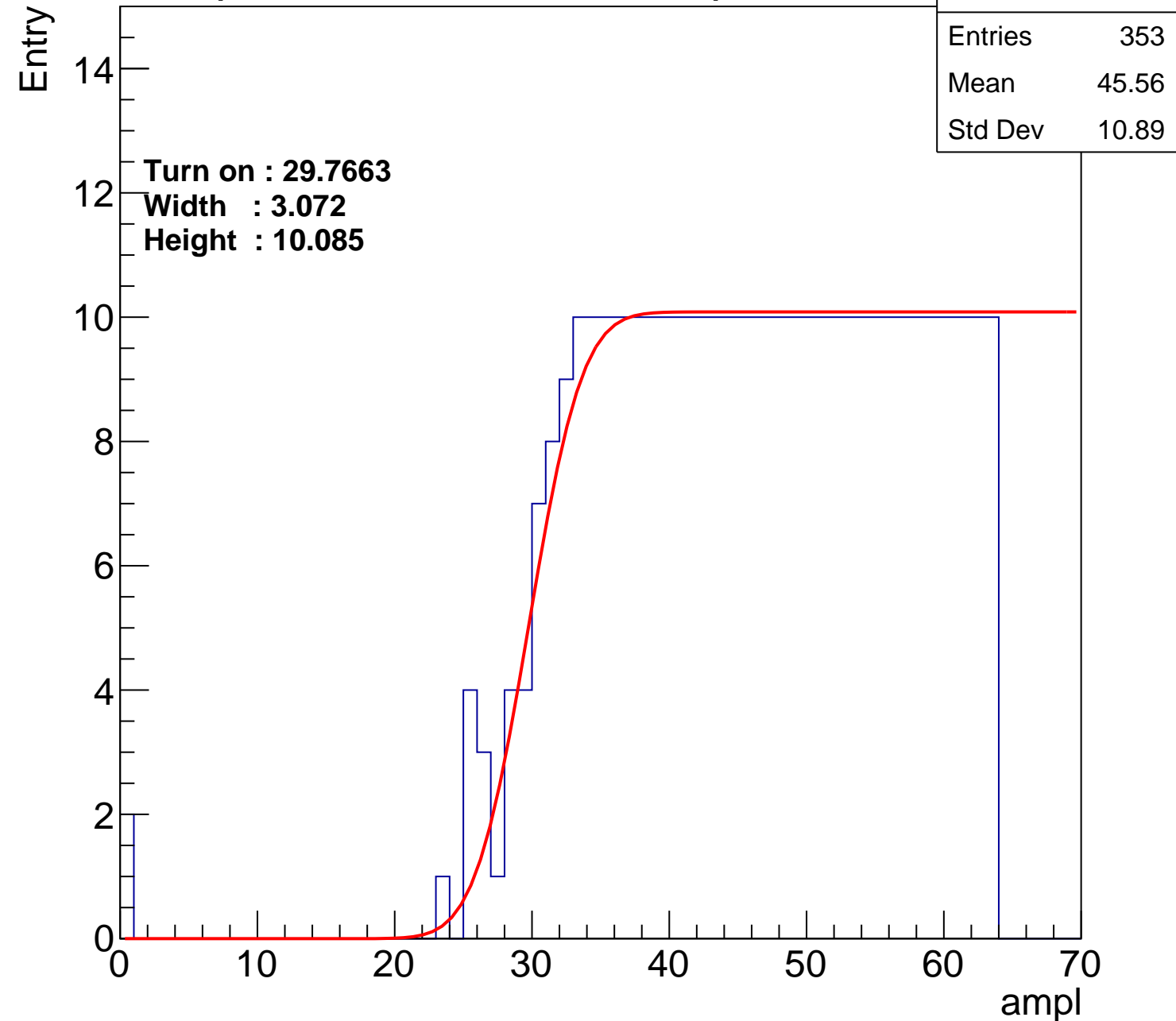
Width : 3.072

Height : 10.085

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch122

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.54 |
| Std Dev | 11.66 |

Turn on : 27.5781

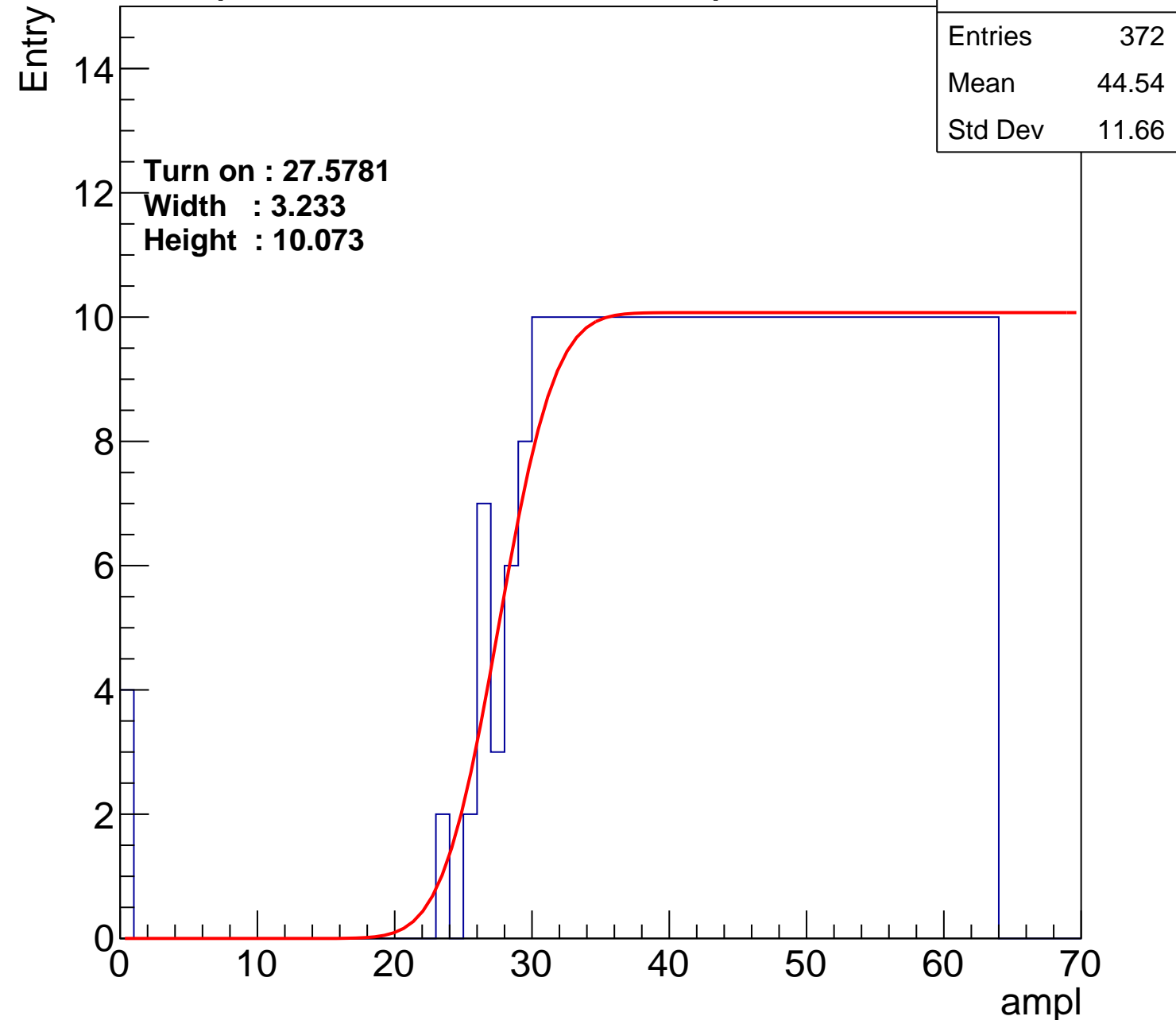
Width : 3.233

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch123

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 354   |
| Mean    | 45.44 |
| Std Dev | 11.12 |

**Turn on : 29.1865**

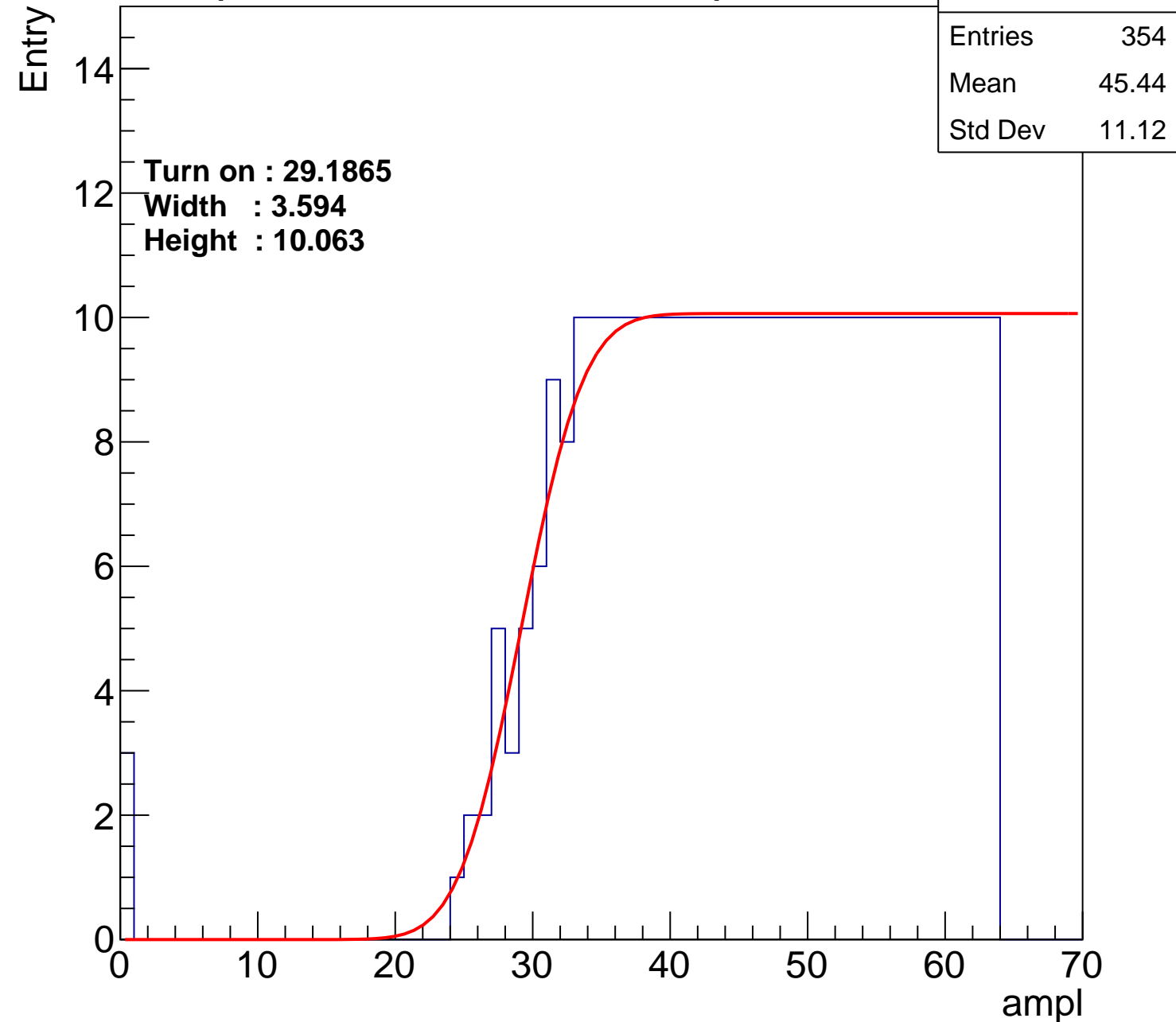
**Width : 3.594**

**Height : 10.063**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch124

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 374   |
| Mean    | 44.45 |
| Std Dev | 11.61 |

Turn on : 27.8679

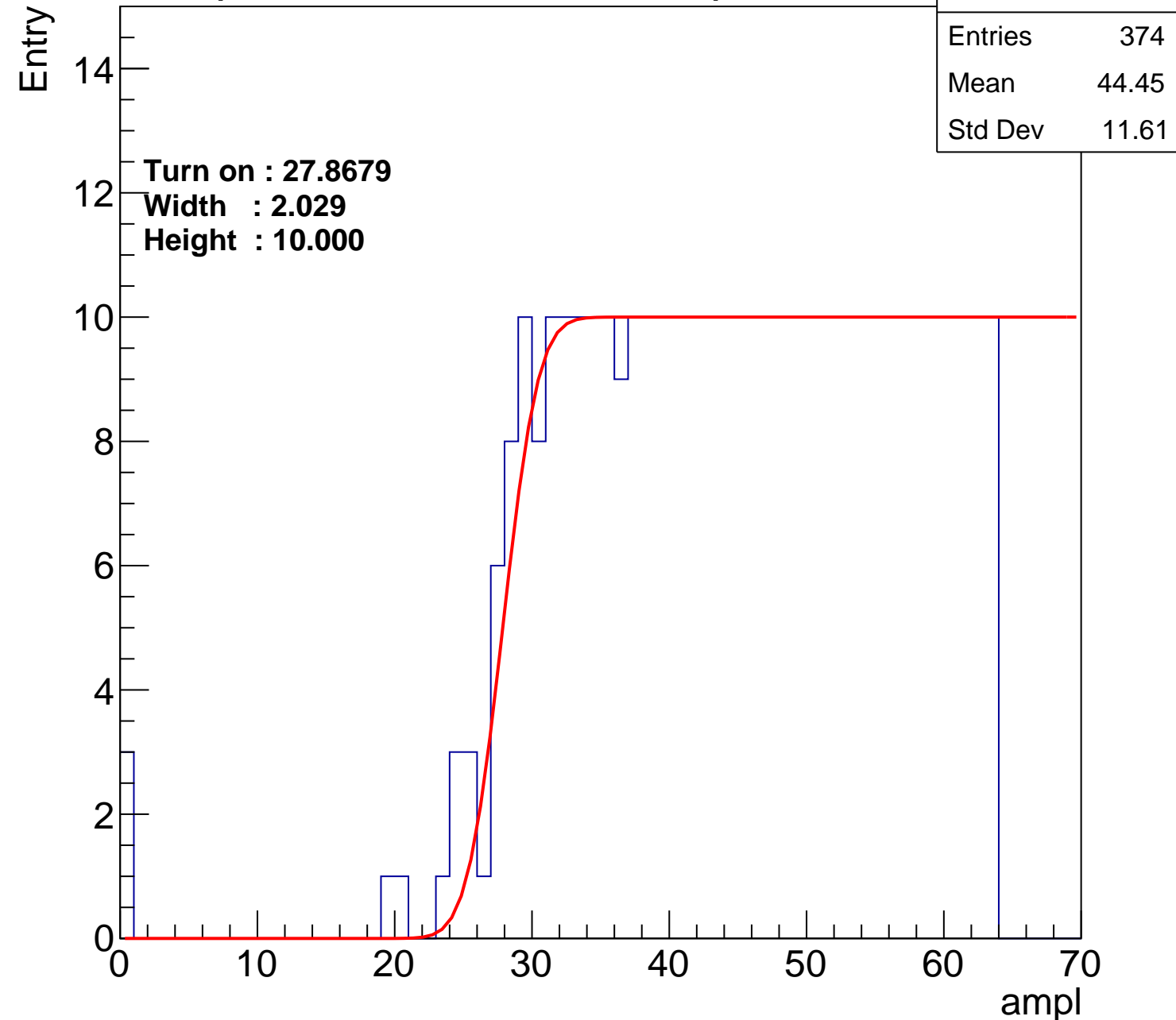
Width : 2.029

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch125

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 348   |
| Mean    | 45.73 |
| Std Dev | 10.98 |

Turn on : 29.7107

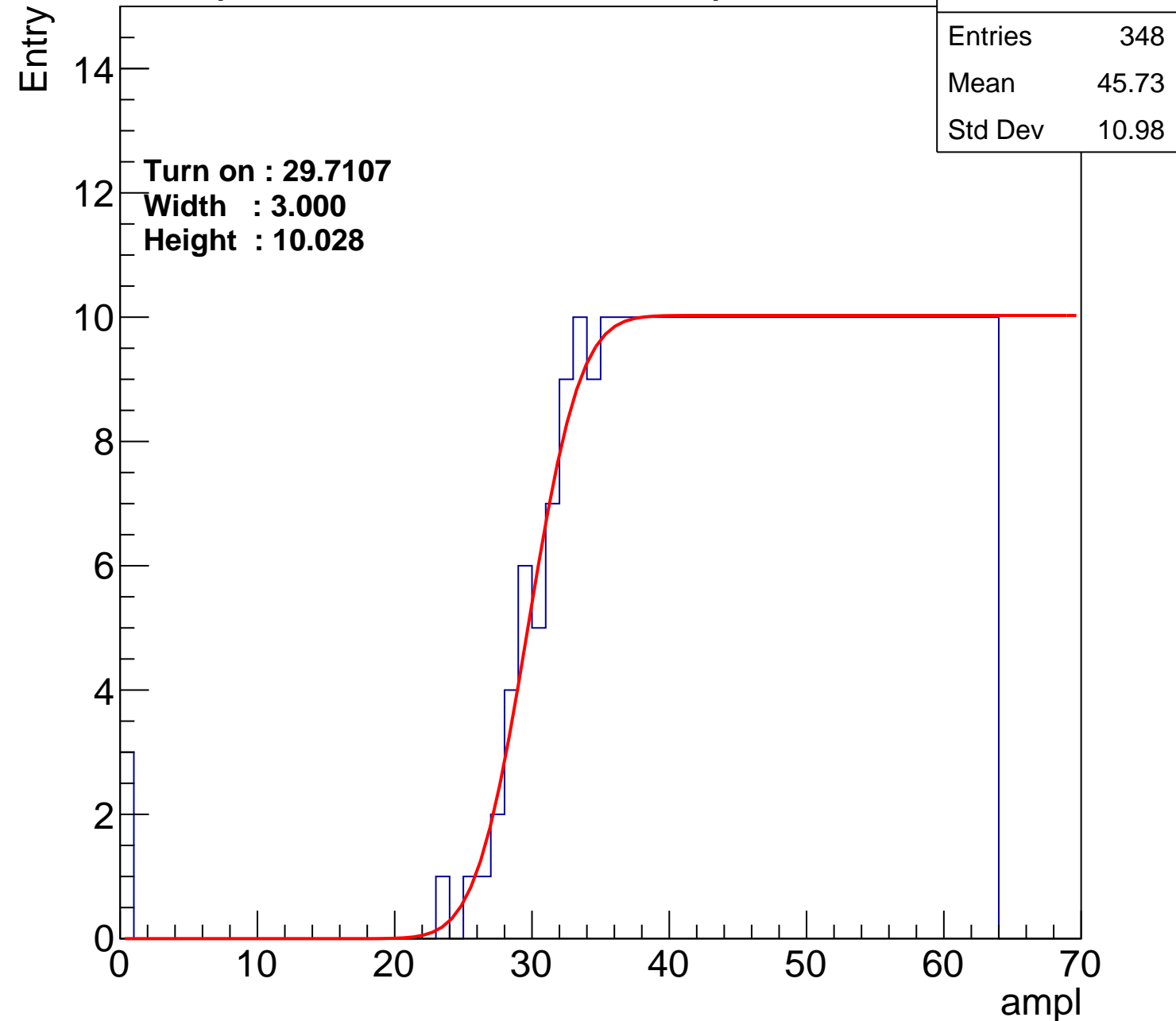
Width : 3.000

Height : 10.028

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch126

calib\_packv5\_042523\_0143.root, FC#13, port D2

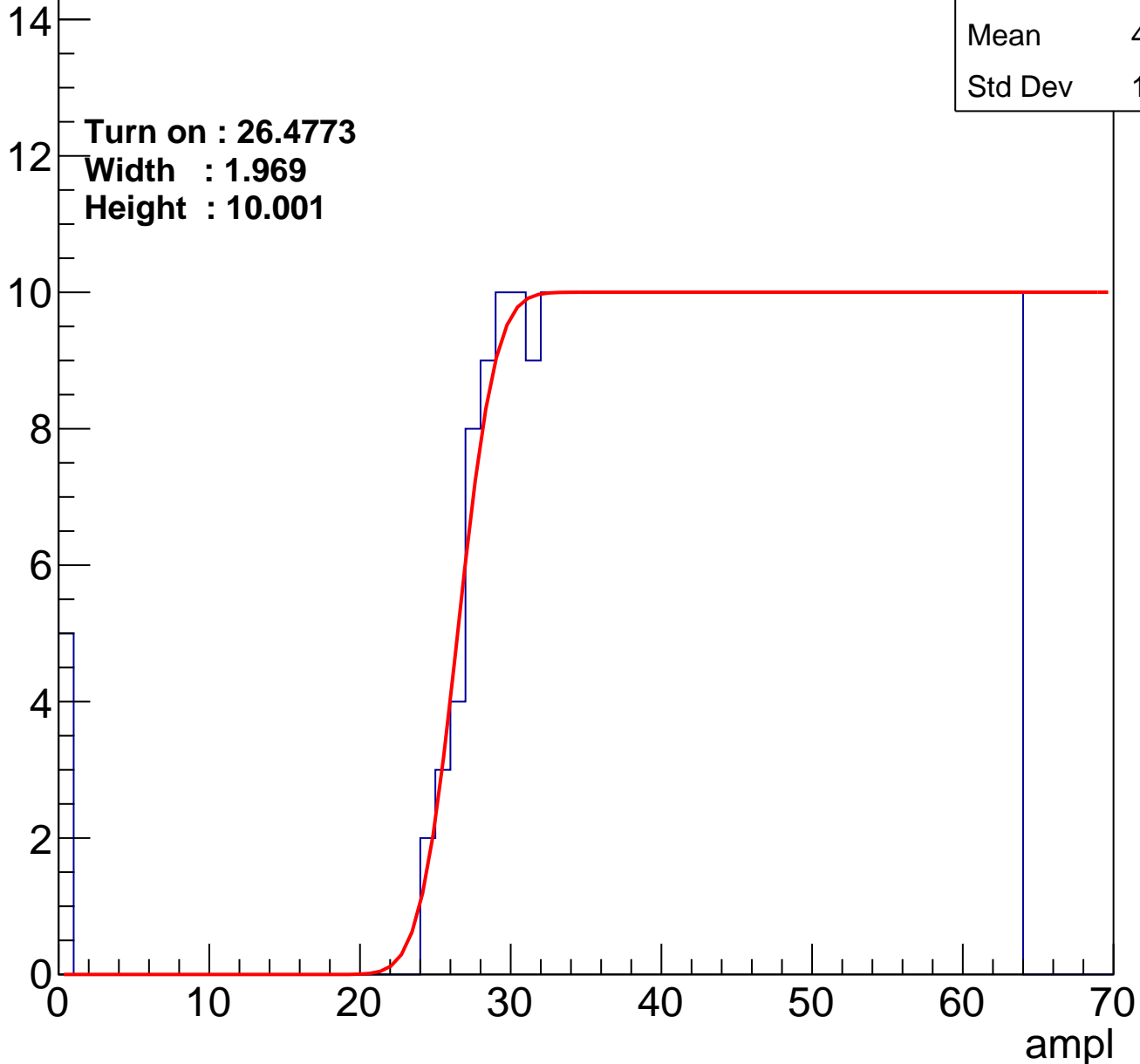
|         |       |
|---------|-------|
| Entries | 380   |
| Mean    | 44.12 |
| Std Dev | 11.96 |

Turn on : 26.4773

Width : 1.969

Height : 10.001

Entry





# B1L003S, U13-ch127

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.54 |
| Std Dev | 11.65 |

Turn on : 27.3064

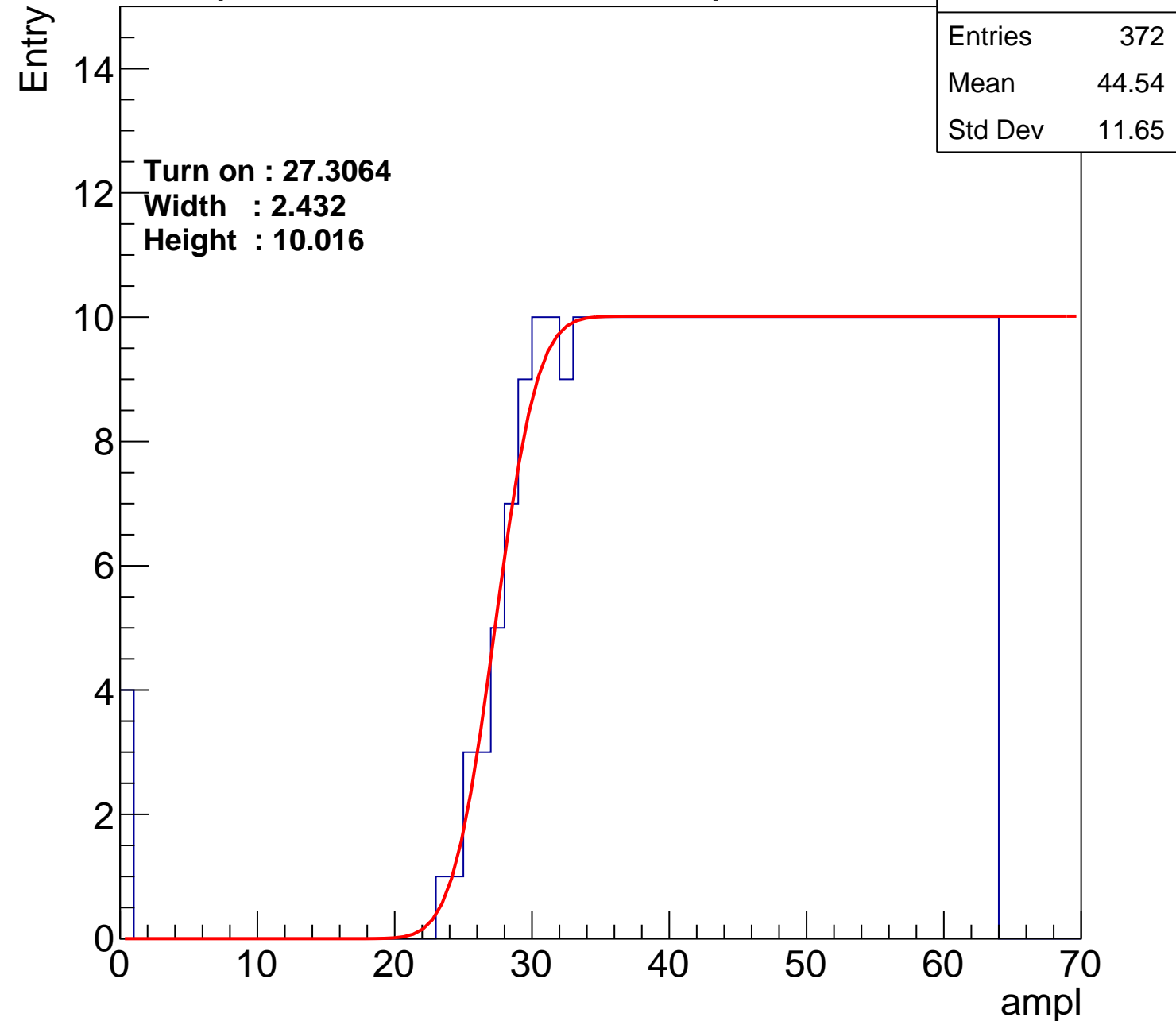
Width : 2.432

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B1L003S, U13-ch127

calib\_packv5\_042523\_0143.root, FC#13, port D2

|         |       |
|---------|-------|
| Entries | 372   |
| Mean    | 44.54 |
| Std Dev | 11.65 |

Turn on : 27.3064

Width : 2.432

Height : 10.016

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

