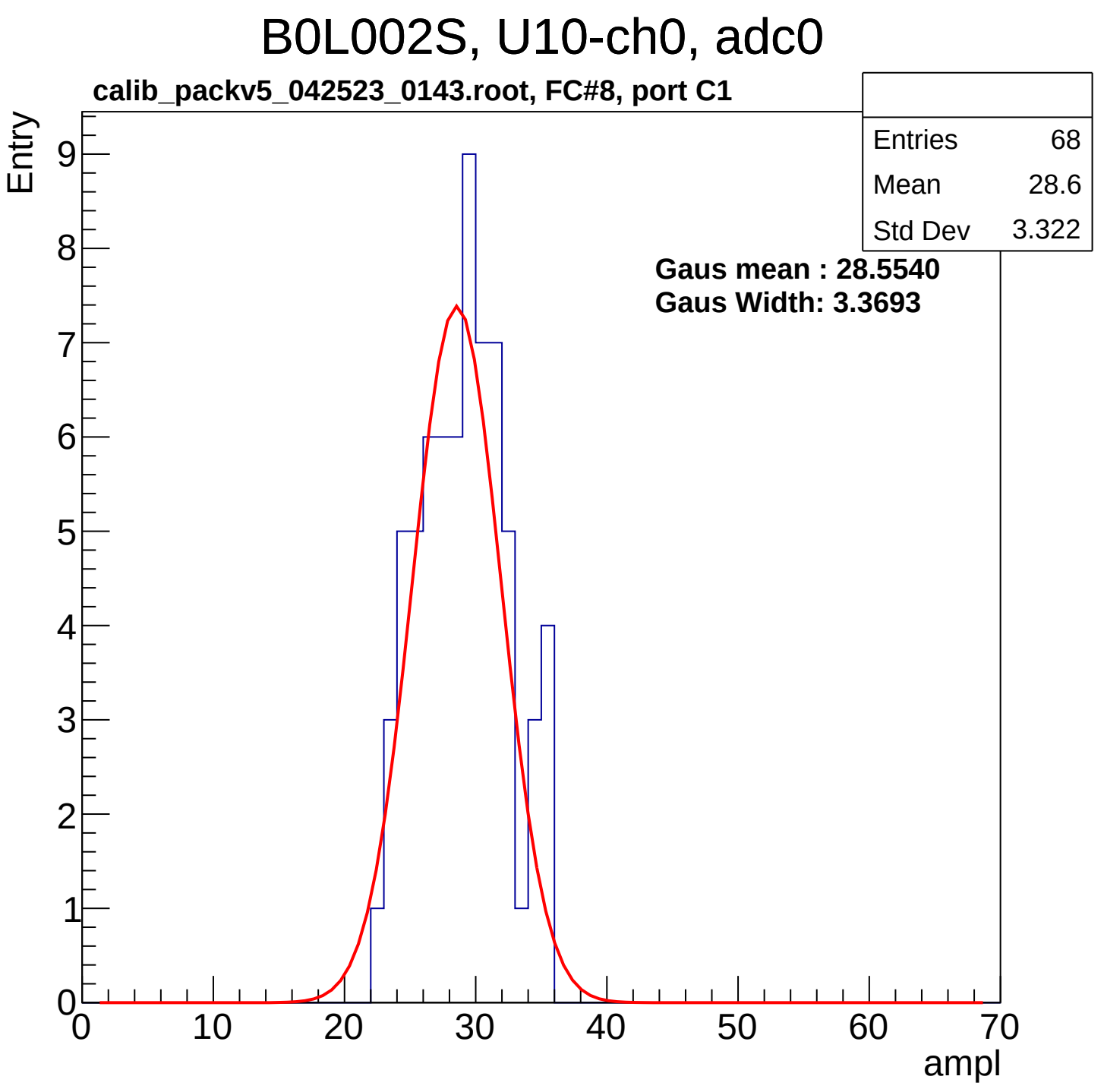
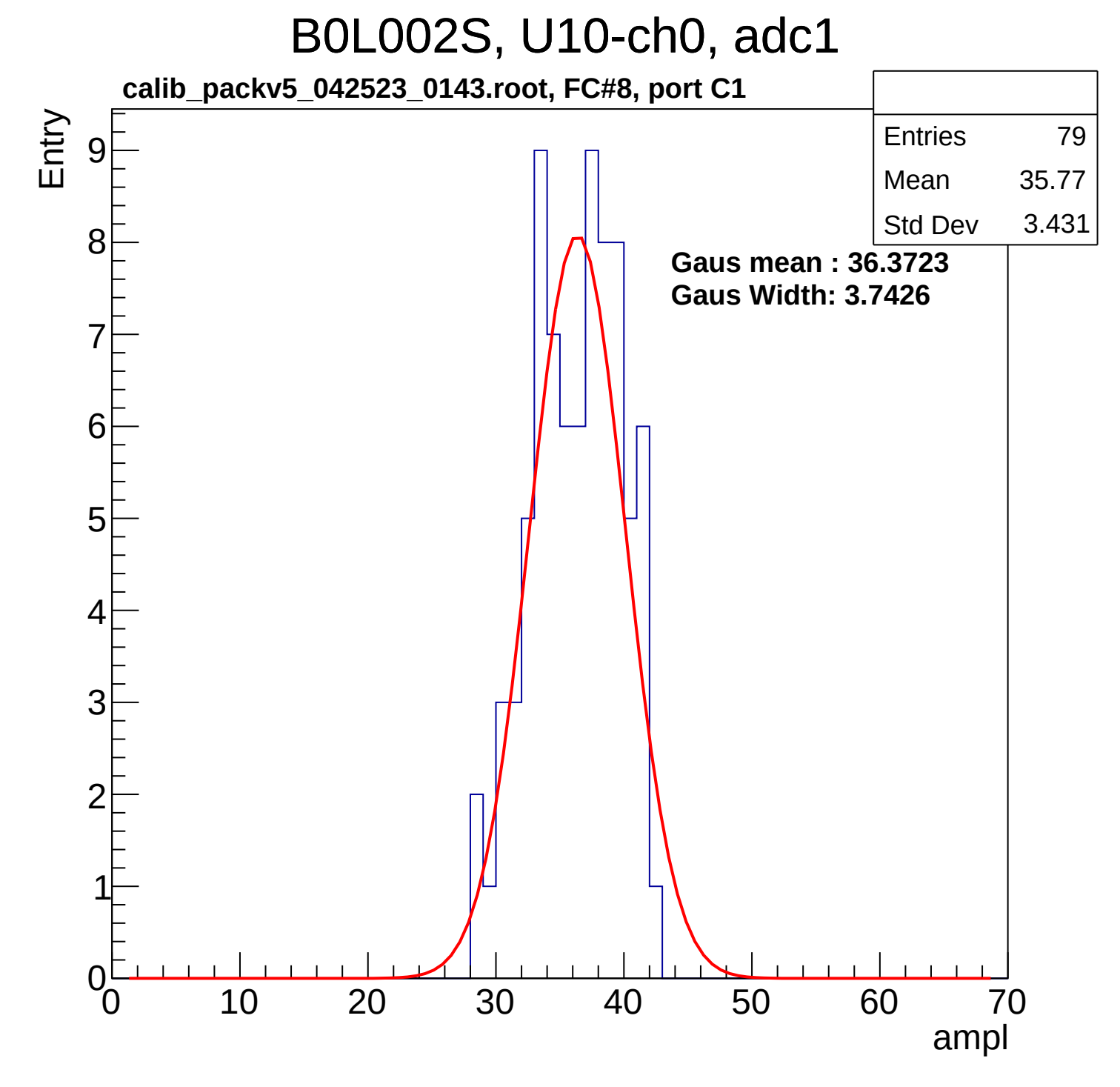
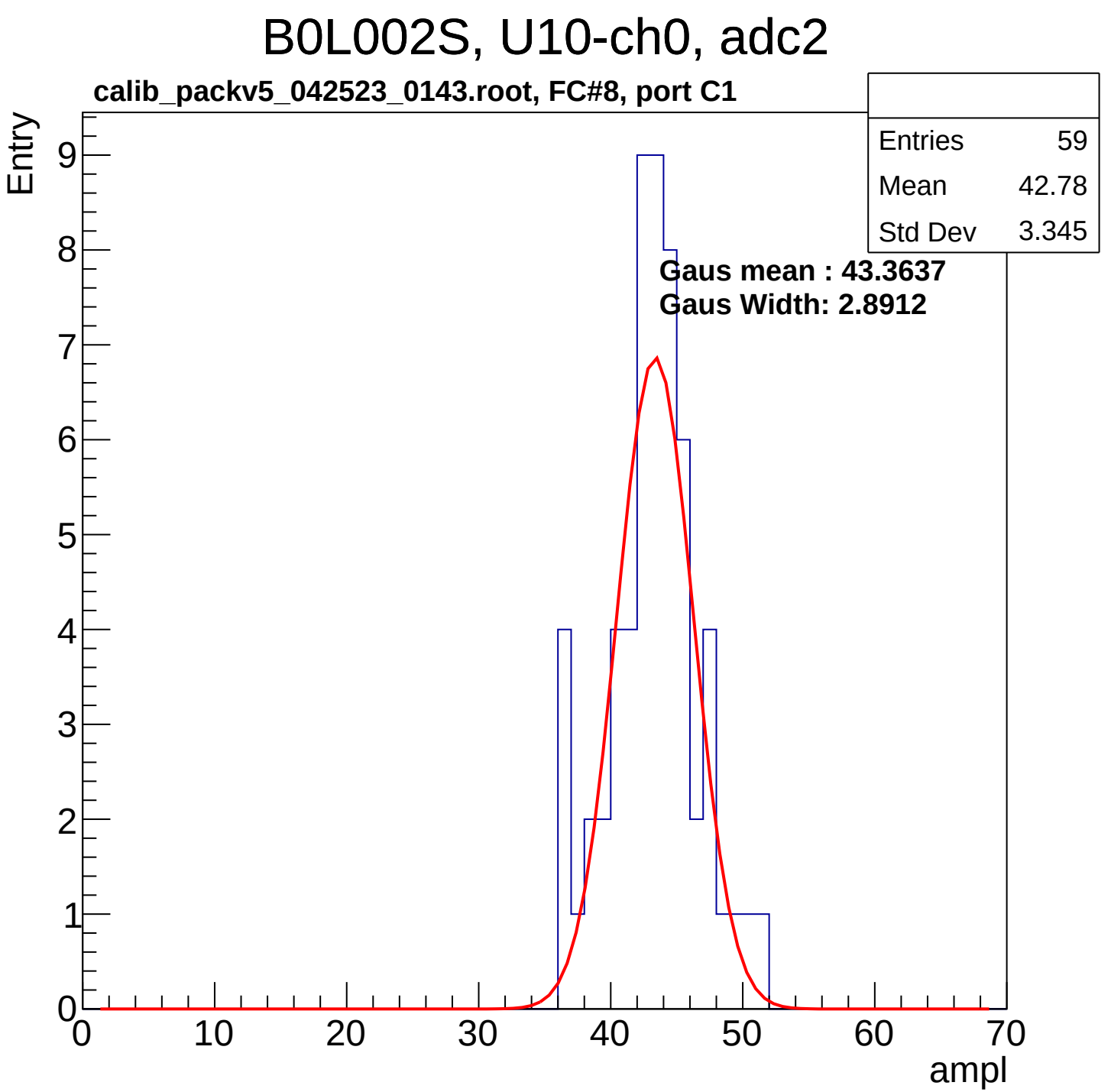
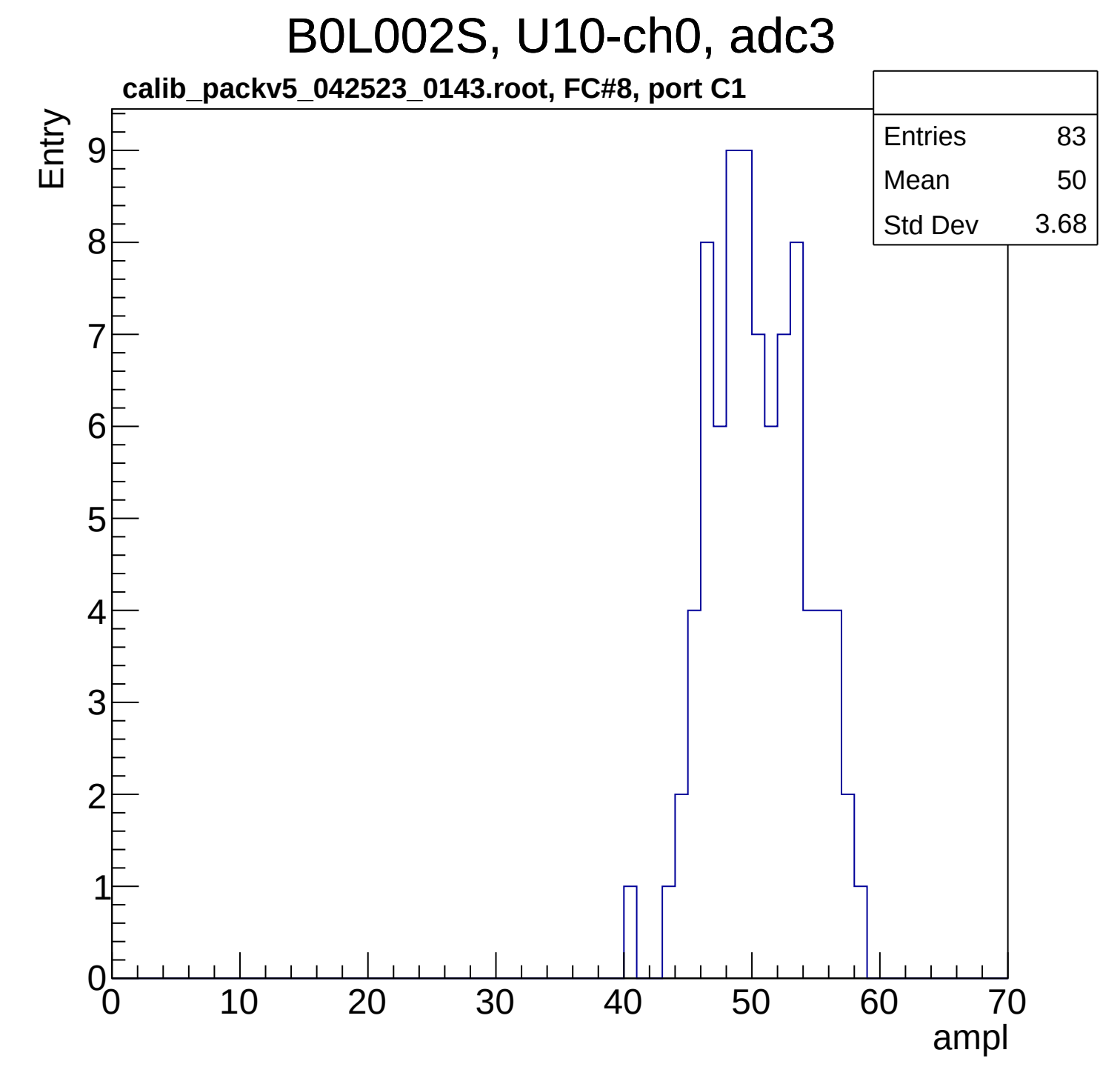
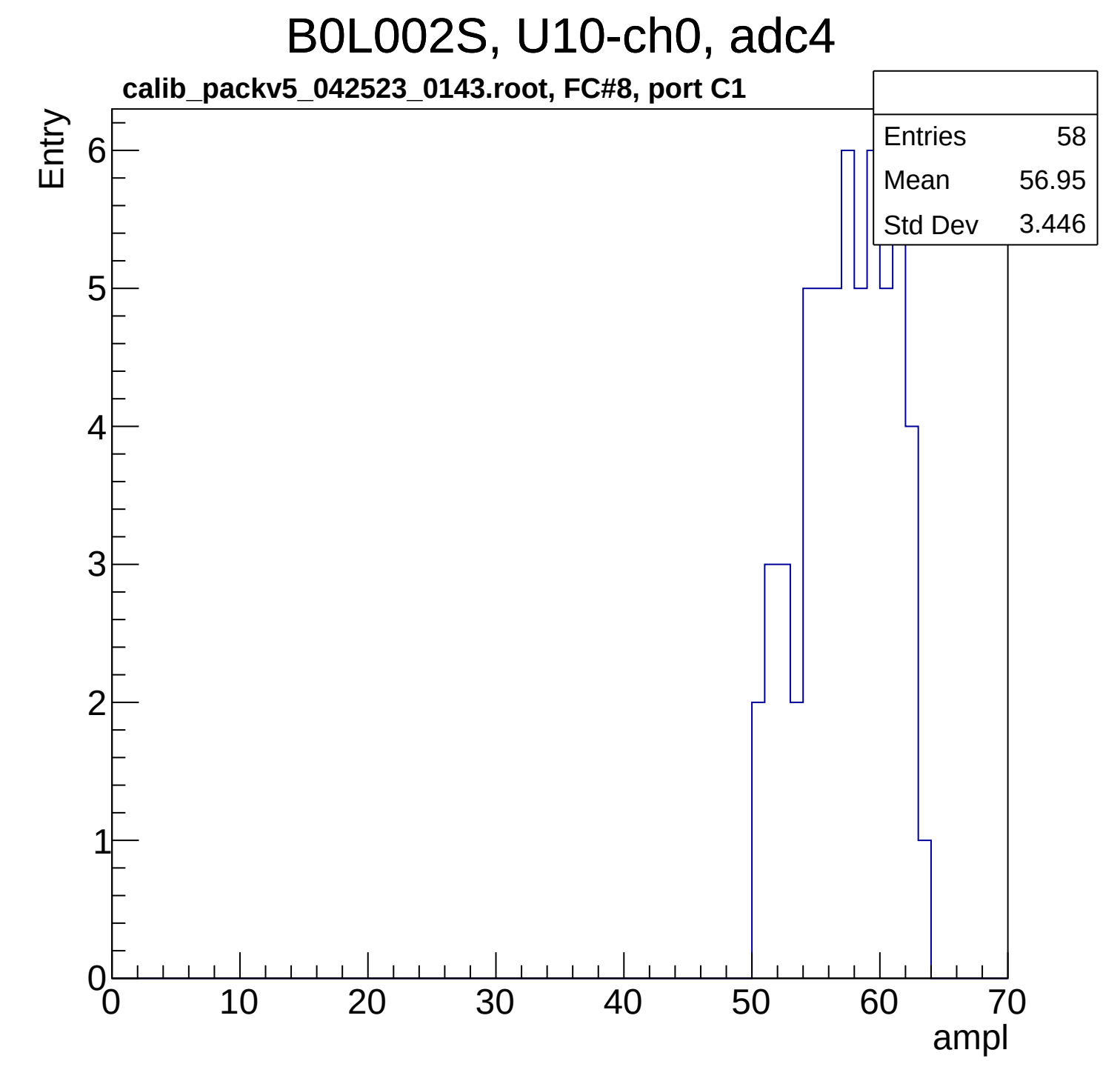
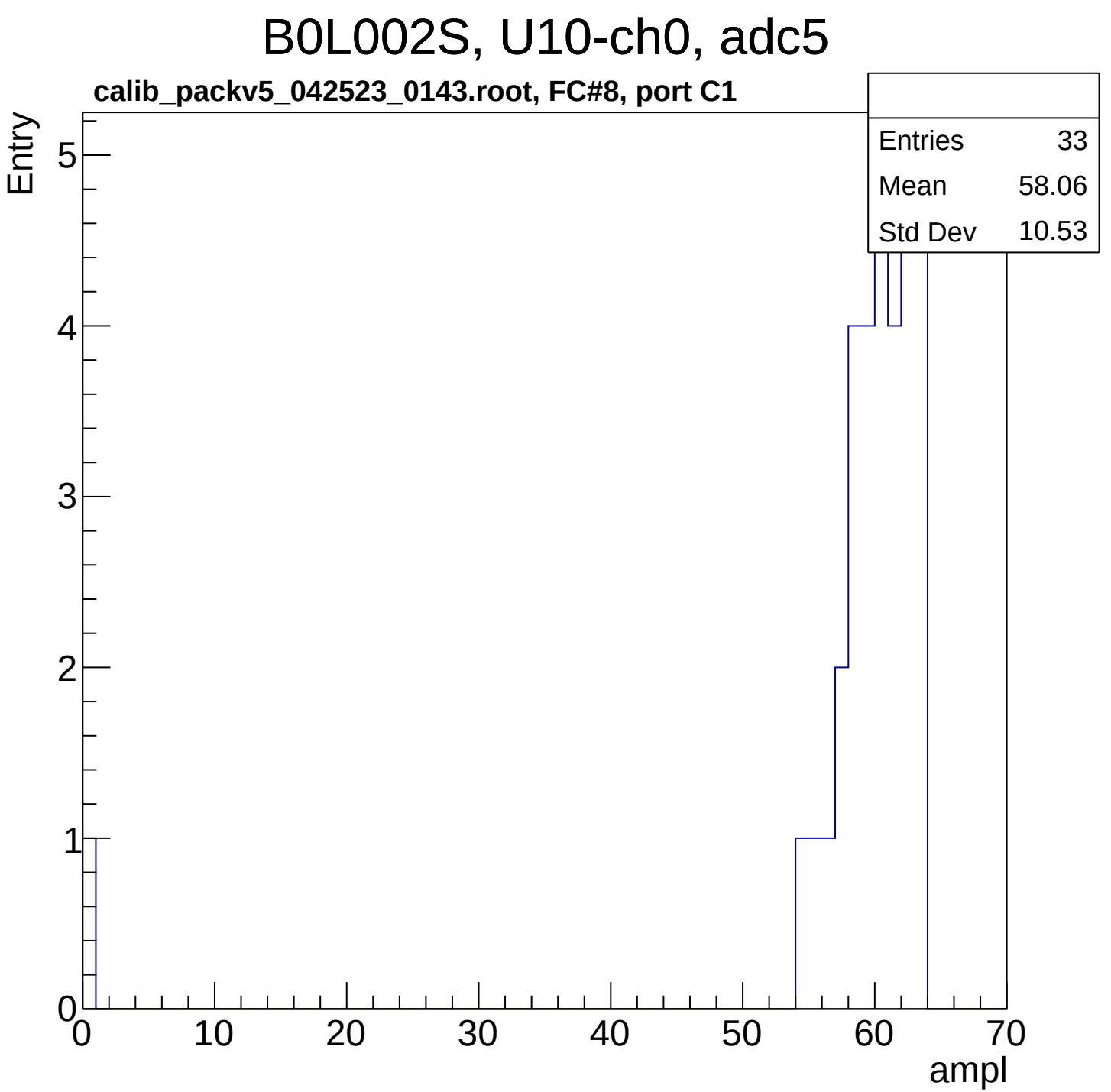

B0L002S, U10-ch0, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 28.6
Std Dev 3.322
Gaus mean : 28.5540
Gaus Width: 3.3693
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

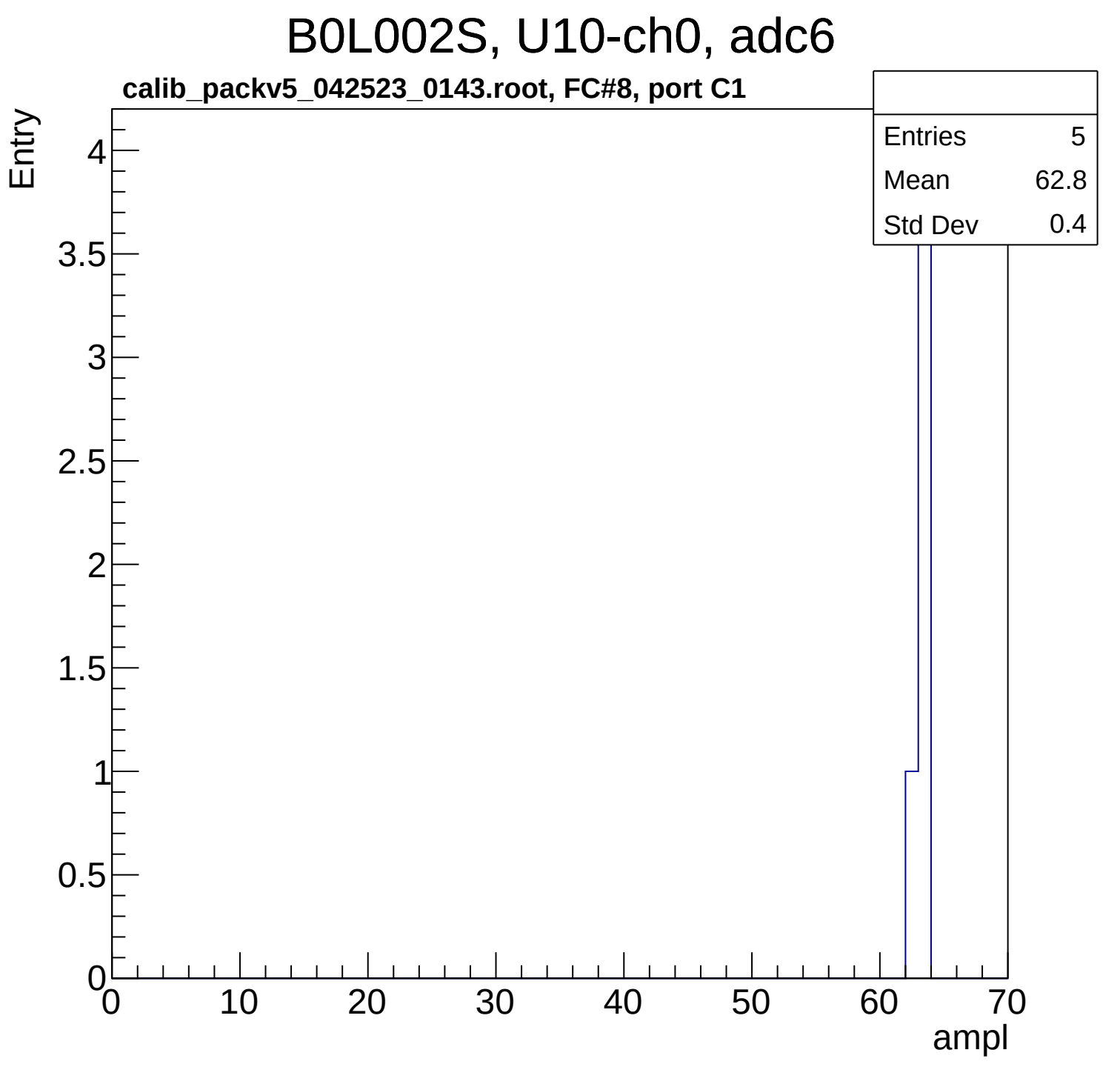

B0L002S, U10-ch0, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 35.77
Std Dev 3.431
Gaus mean : 36.3723
Gaus Width: 3.7426
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

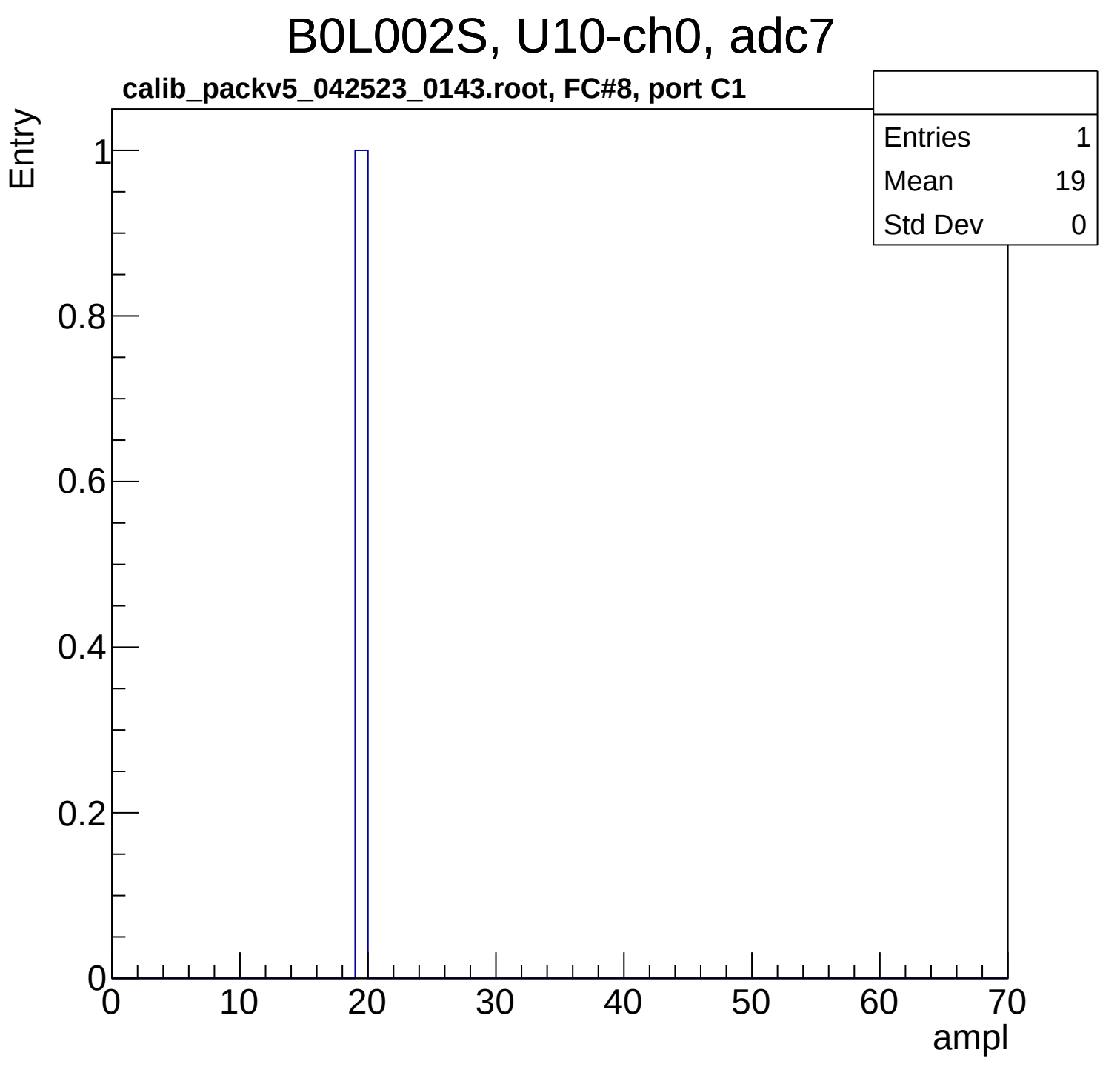

B0L002S, U10-ch0, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 42.78
Std Dev 3.345
Gaus mean : 43.3637
Gaus Width: 2.8912
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

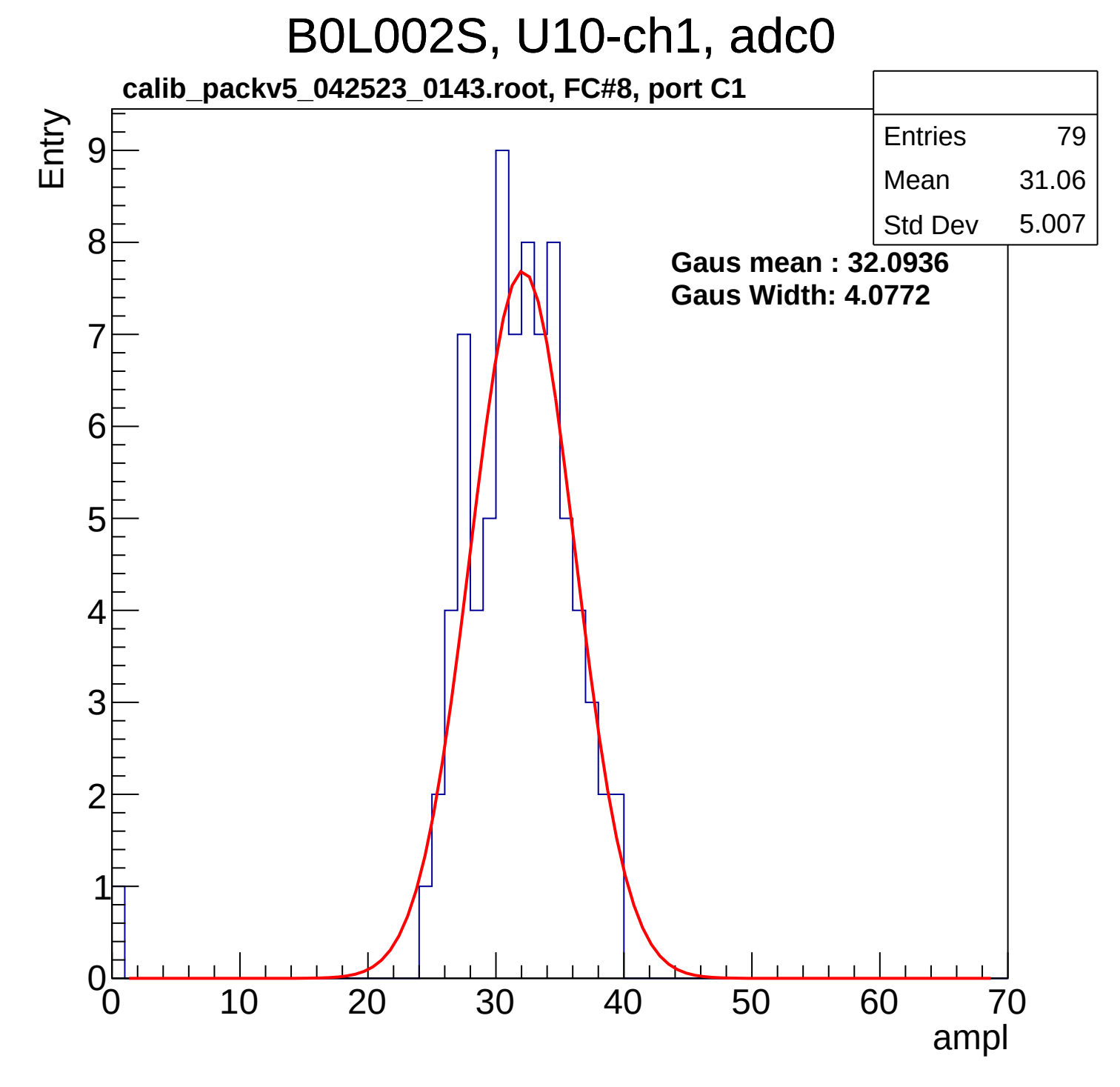

B0L002S, U10-ch0, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 50
Std Dev 3.68
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

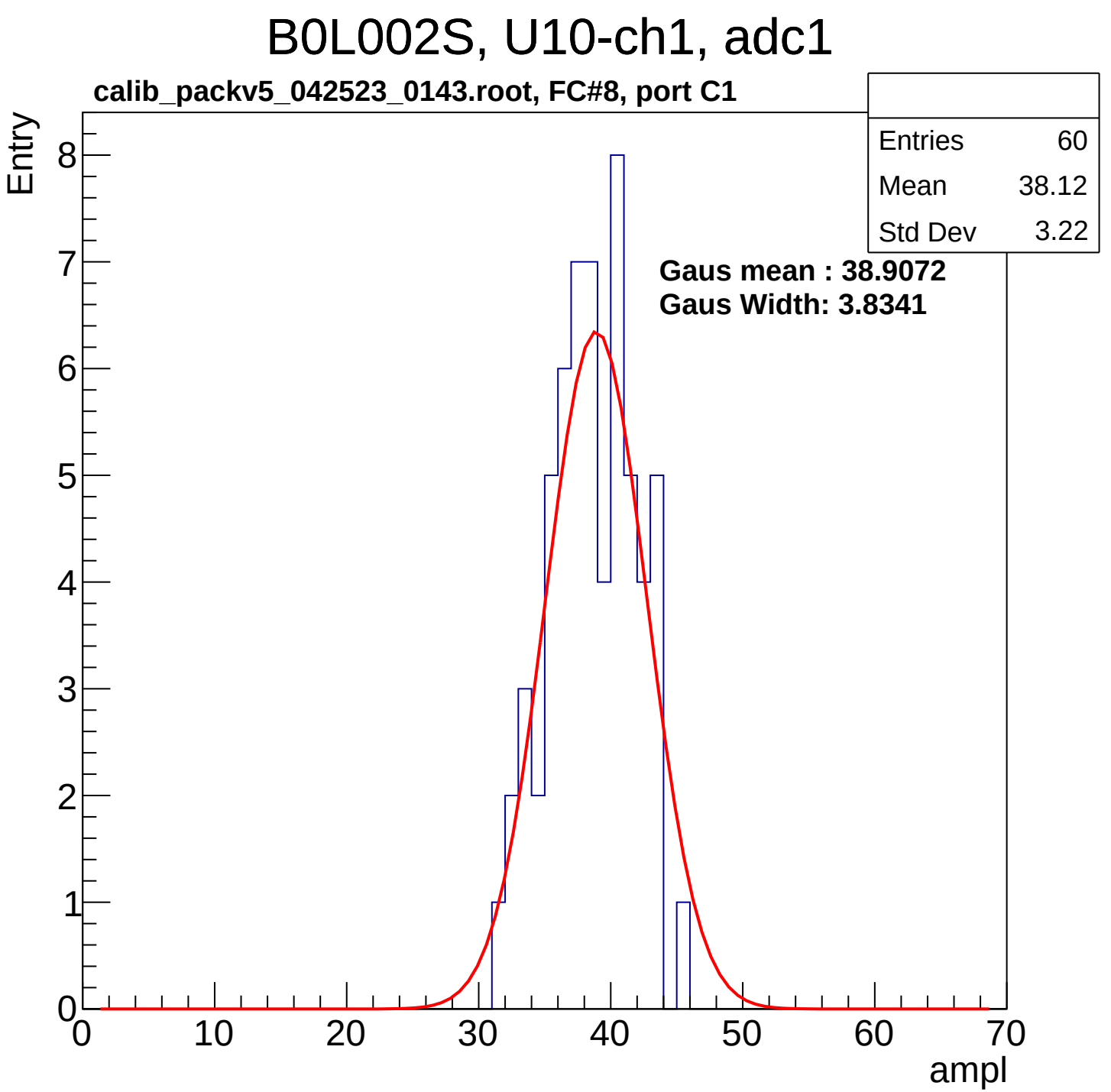

B0L002S, U10-ch0, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.95
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

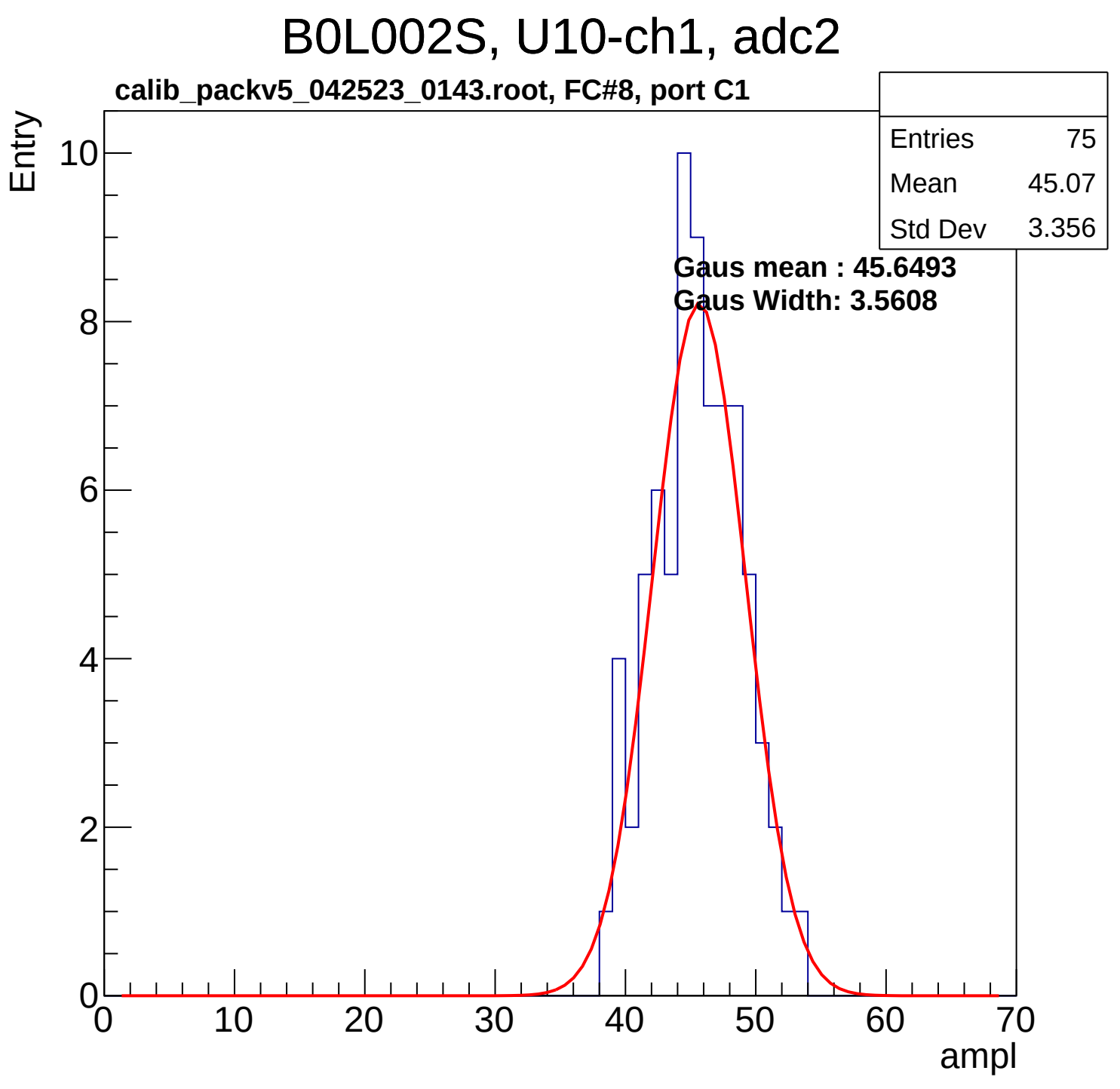

B0L002S, U10-ch0, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.06
Std Dev 10.53
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

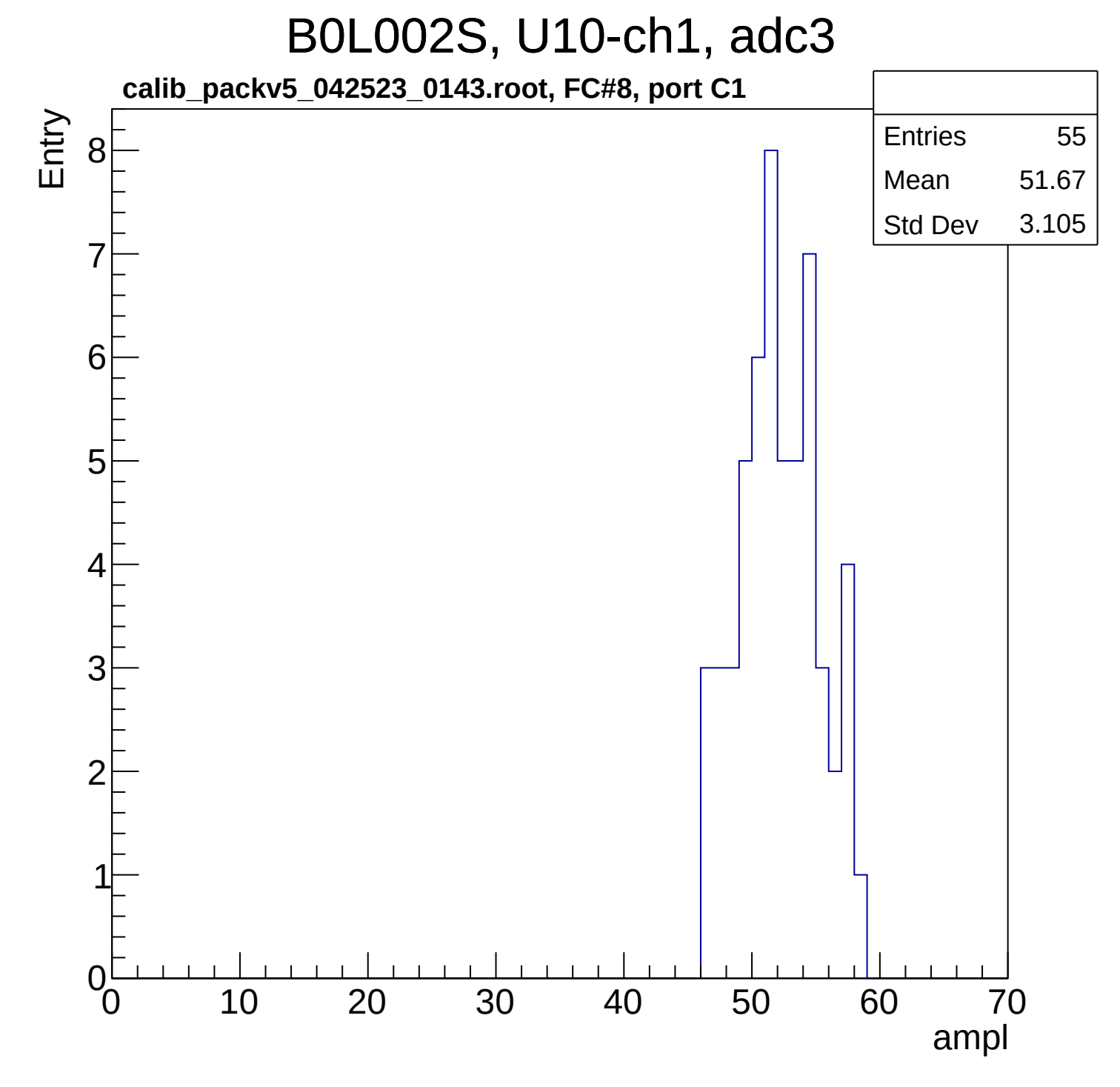

B0L002S, U10-ch0, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.8
Std Dev 0.4
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

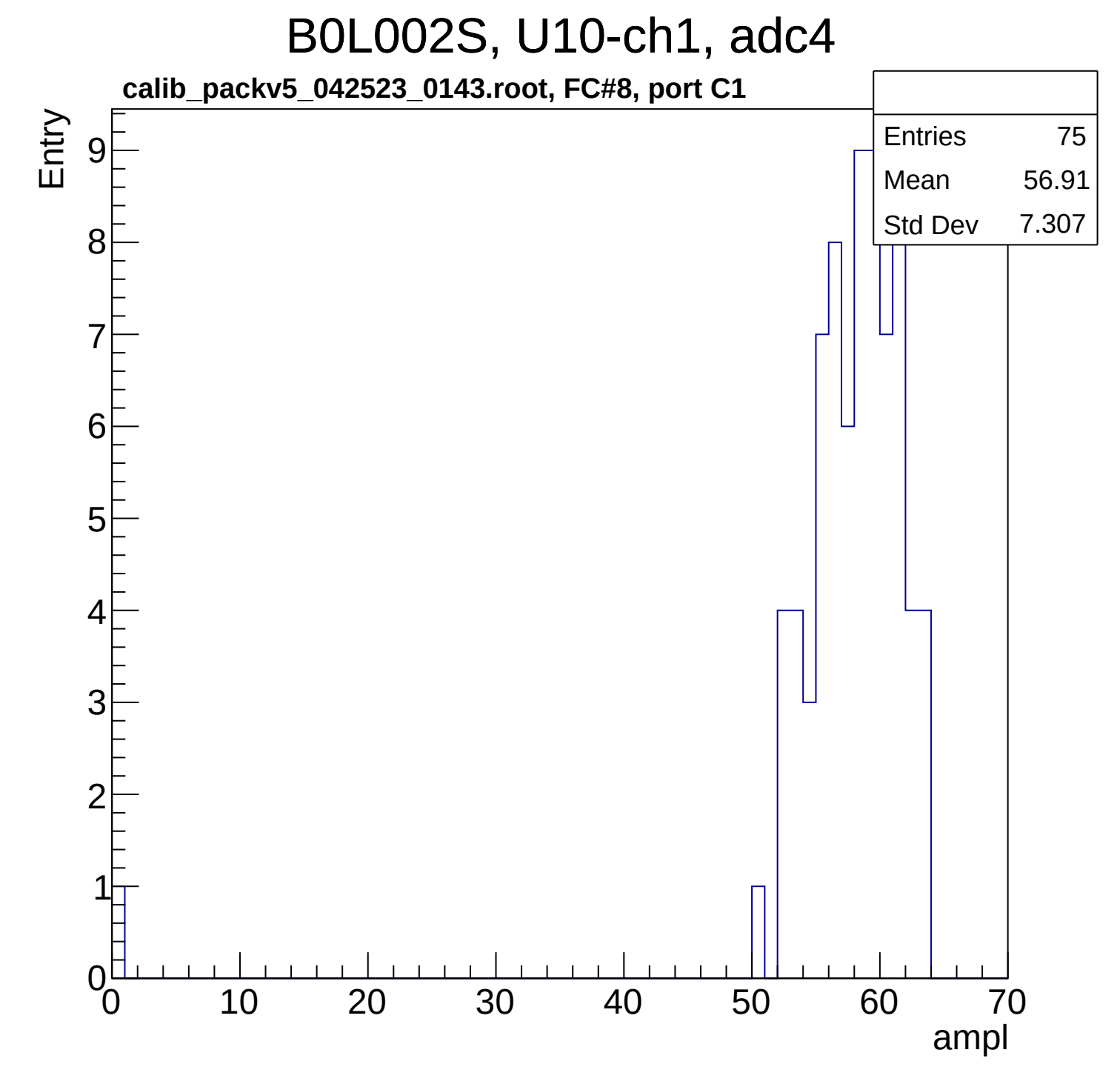

B0L002S, U10-ch0, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

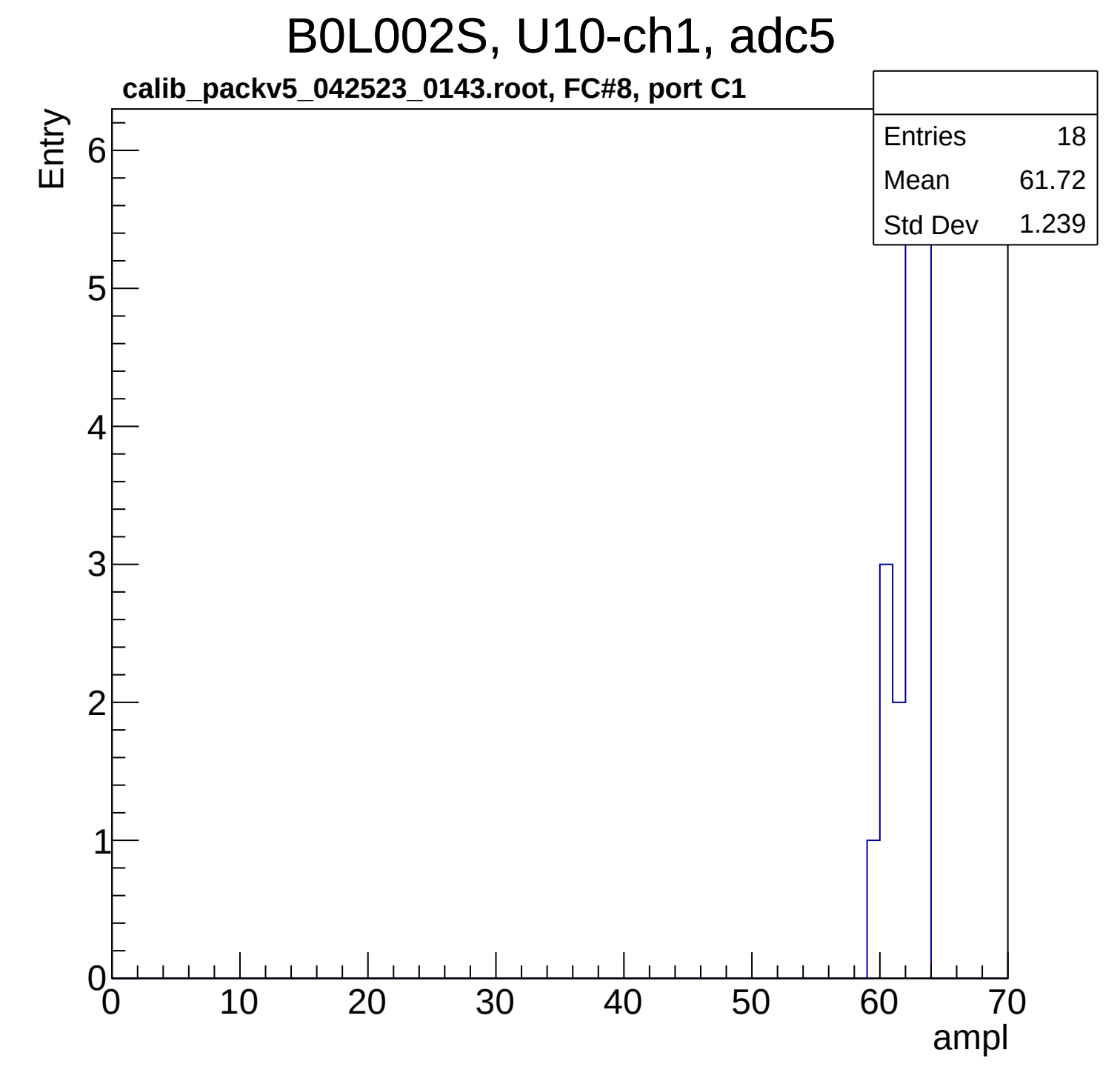

B0L002S, U10-ch1, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 31.06
Std Dev 5.007
Gaus mean : 32.0936
Gaus Width: 4.0772
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

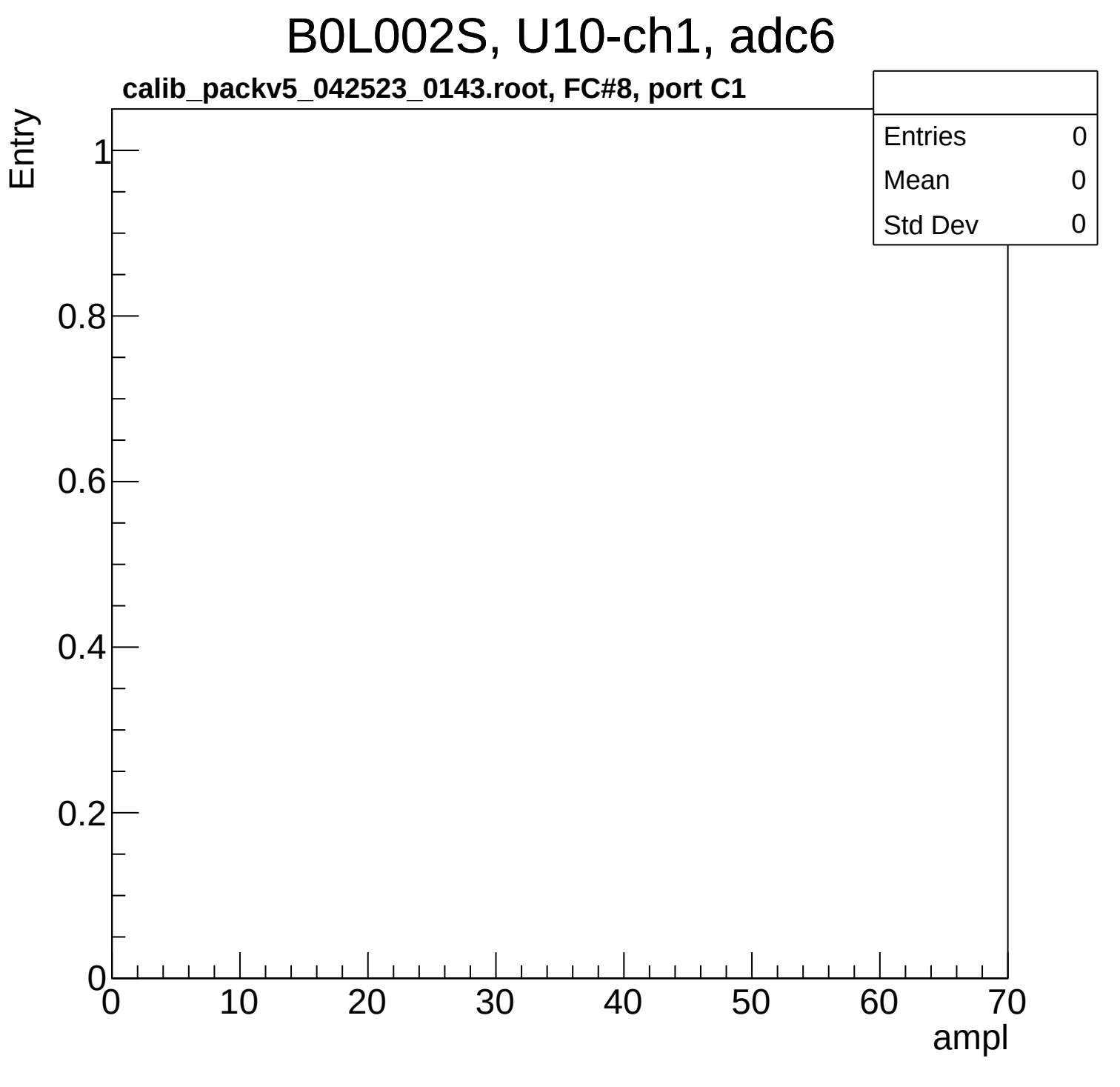

B0L002S, U10-ch1, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.12
Std Dev 3.22
Gaus mean : 38.9072
Gaus Width: 3.8341
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

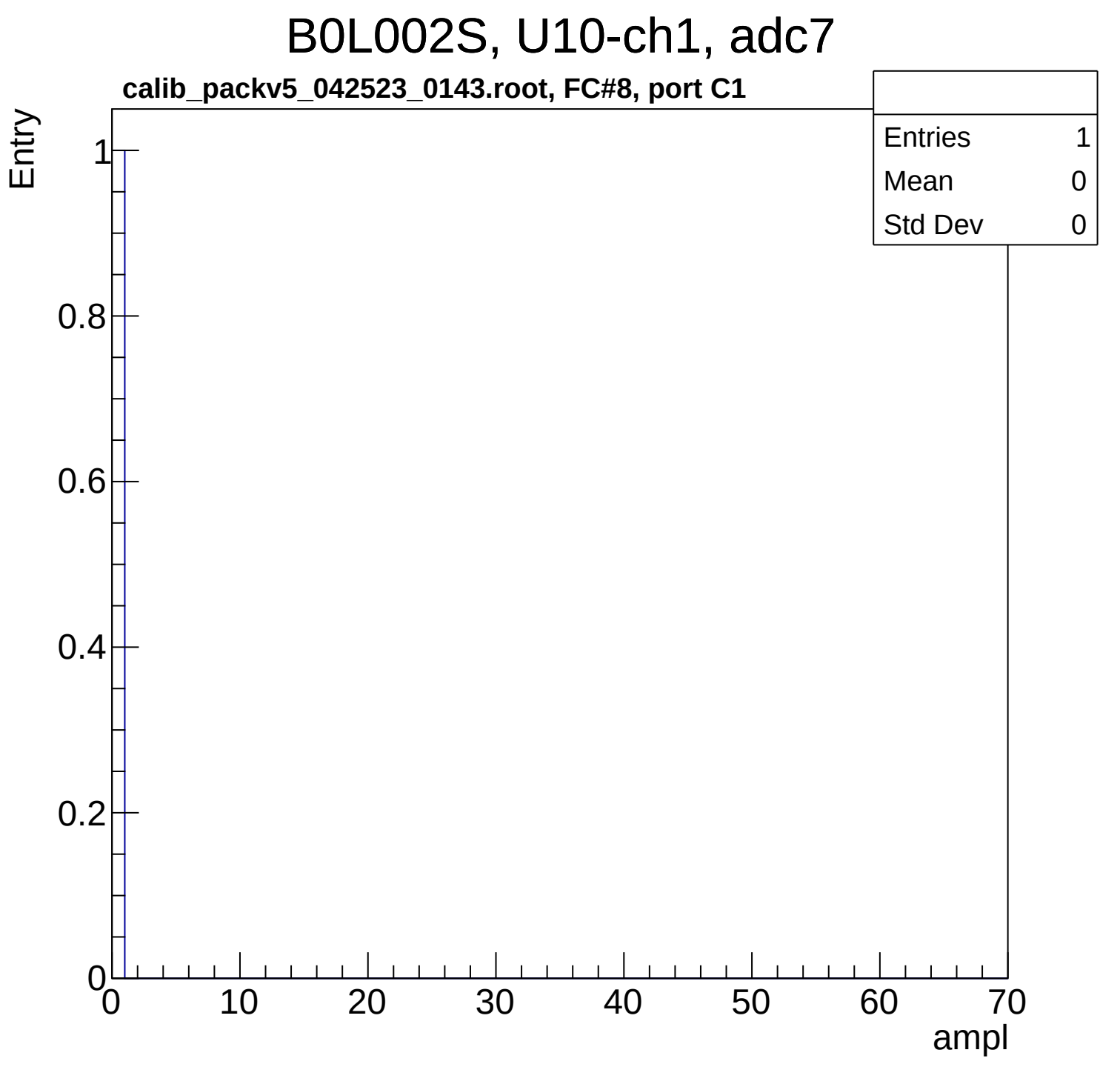

B0L002S, U10-ch1, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.07
Std Dev 3.356
Gaus mean : 45.6493
Gaus Width: 3.5608
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

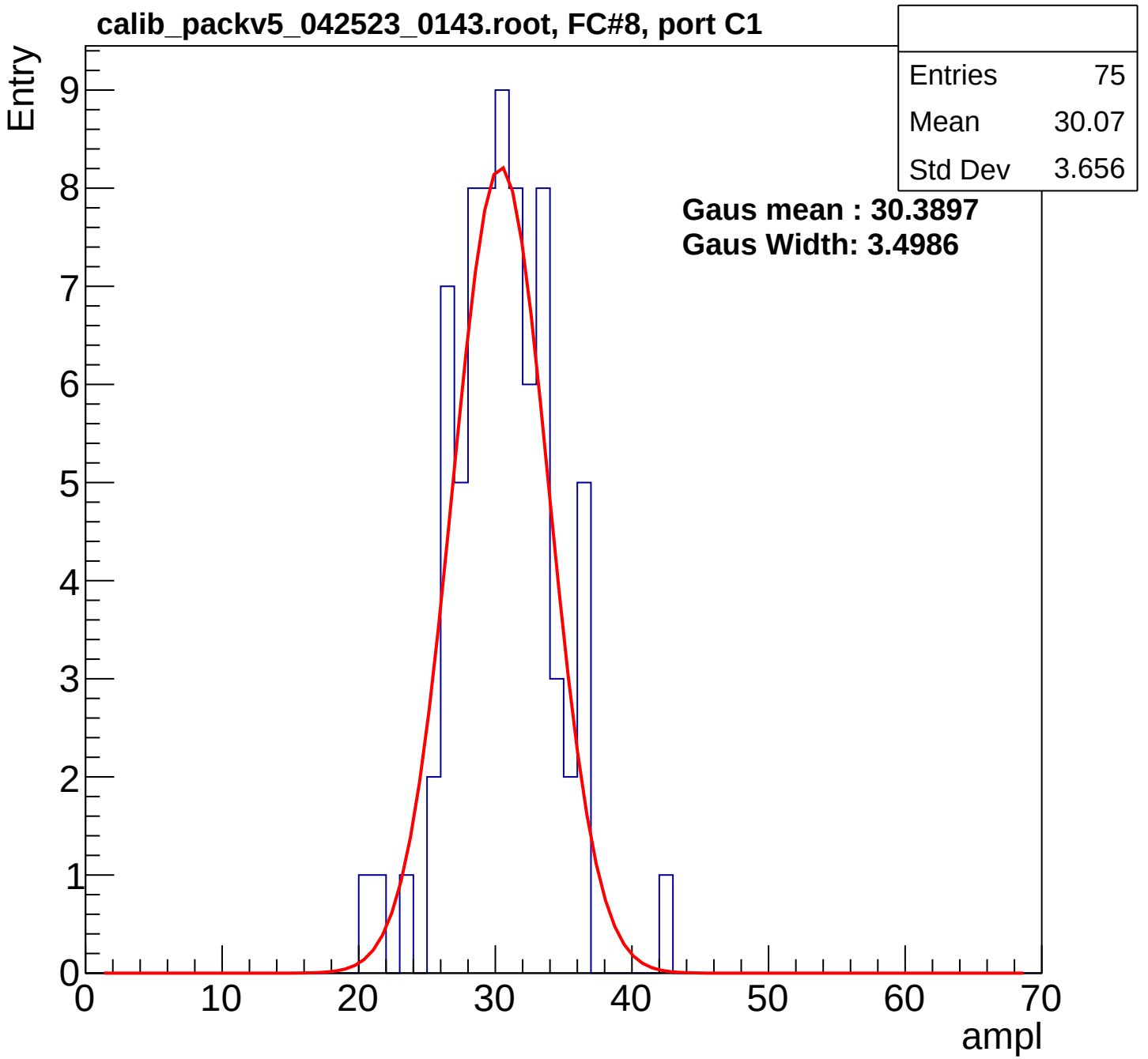

B0L002S, U10-ch1, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 51.67
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

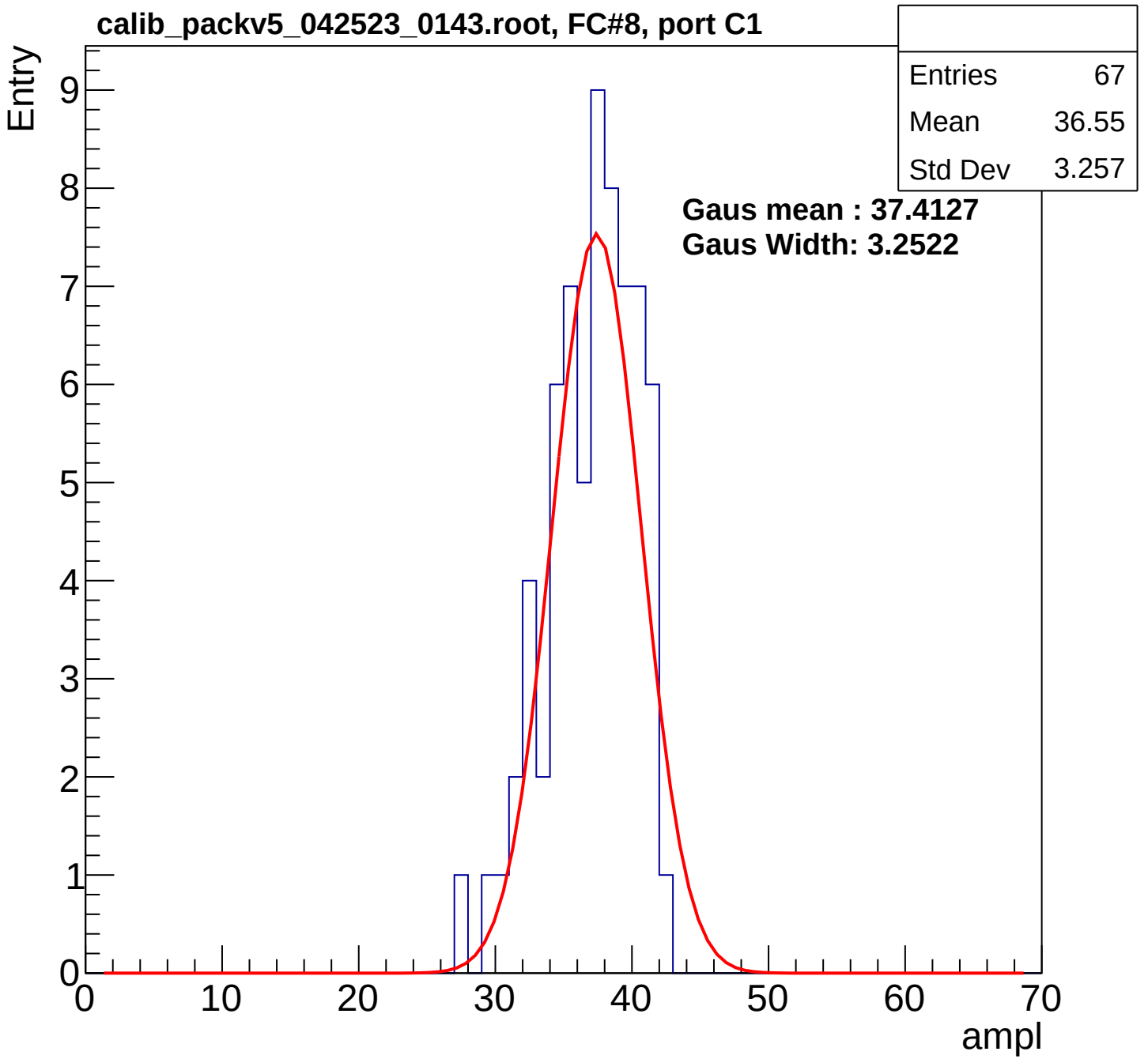

B0L002S, U10-ch1, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 56.91
Std Dev 7.307
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

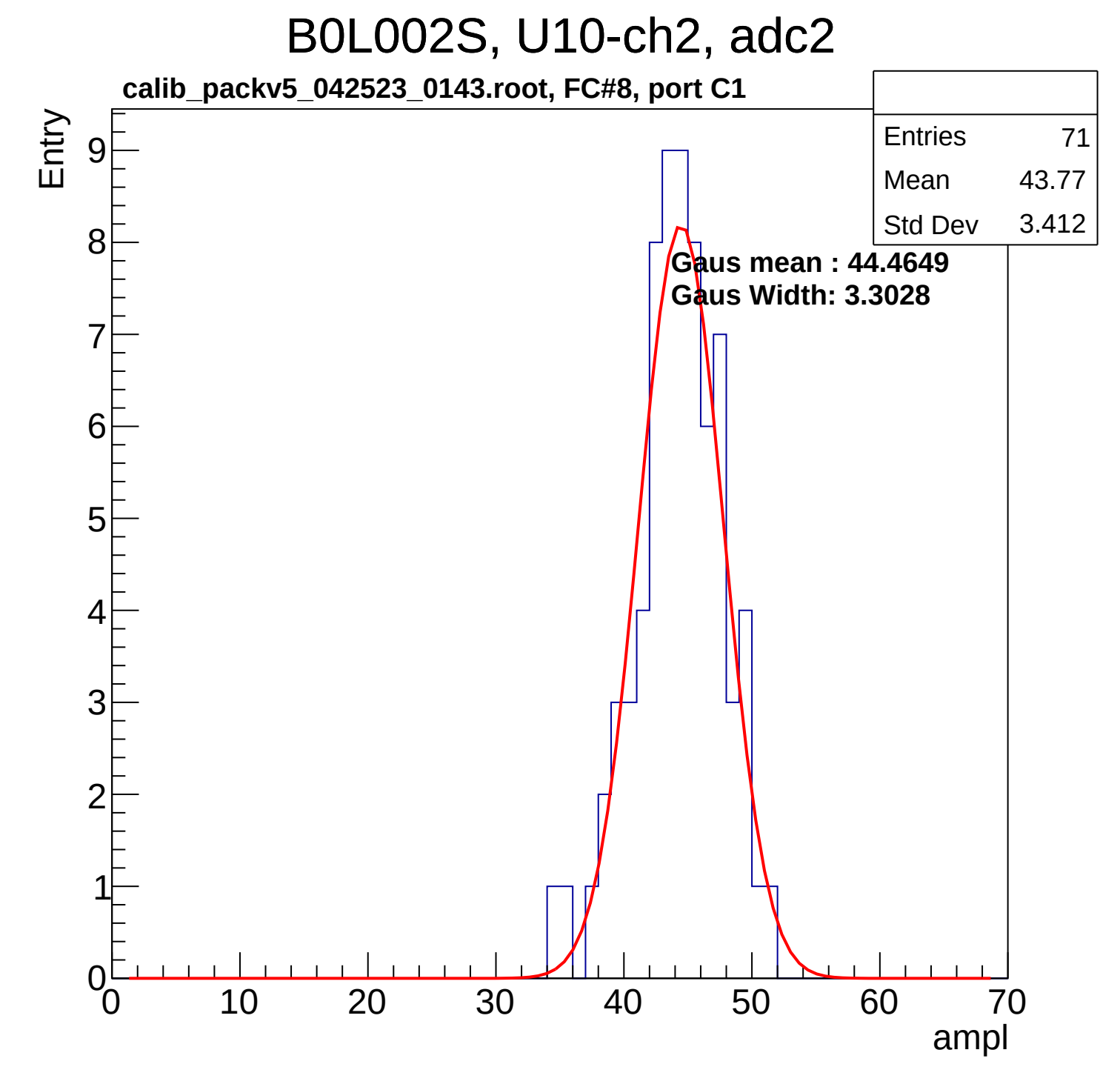

B0L002S, U10-ch1, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 61.72
Std Dev 1.239
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

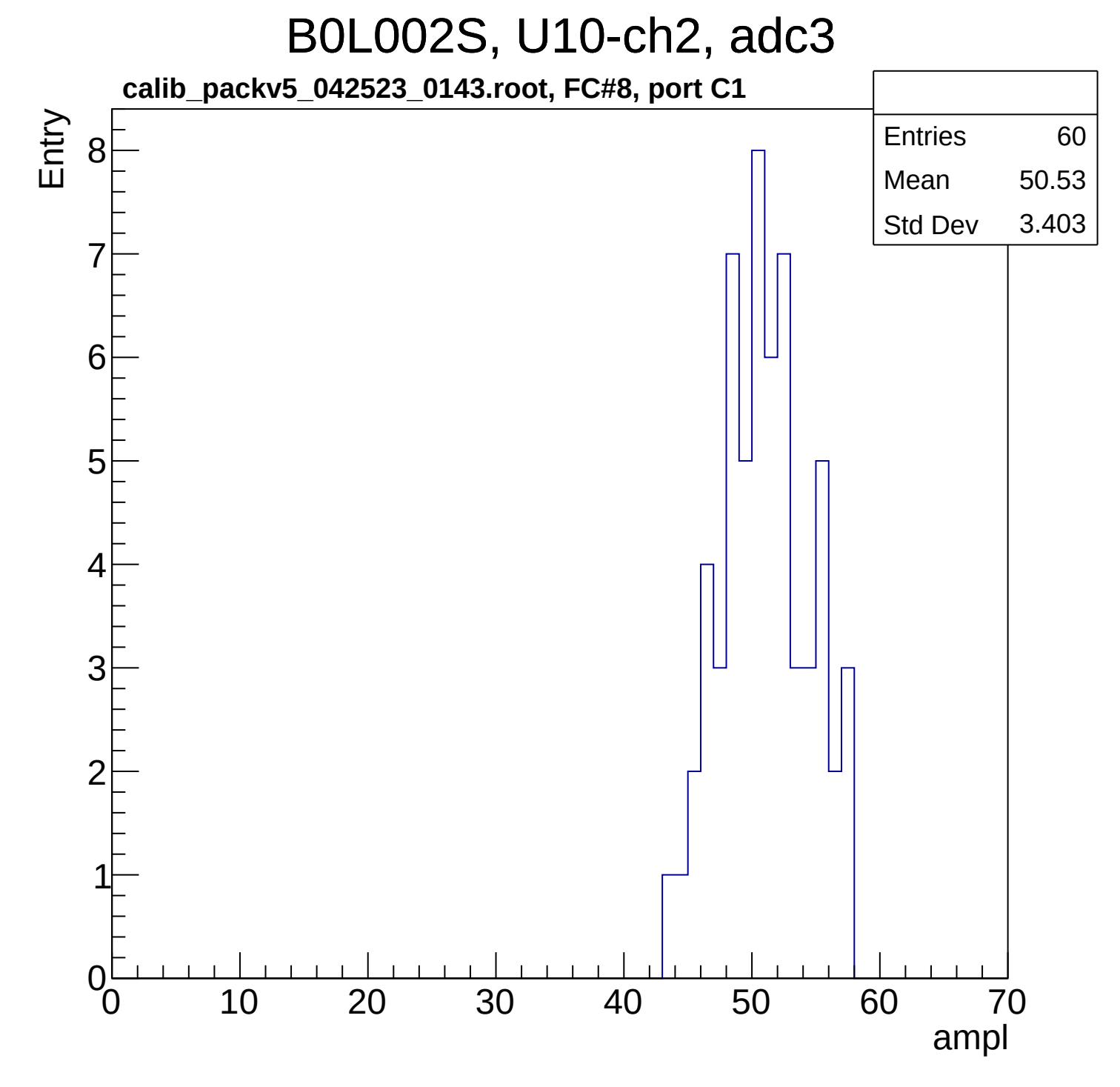

B0L002S, U10-ch1, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

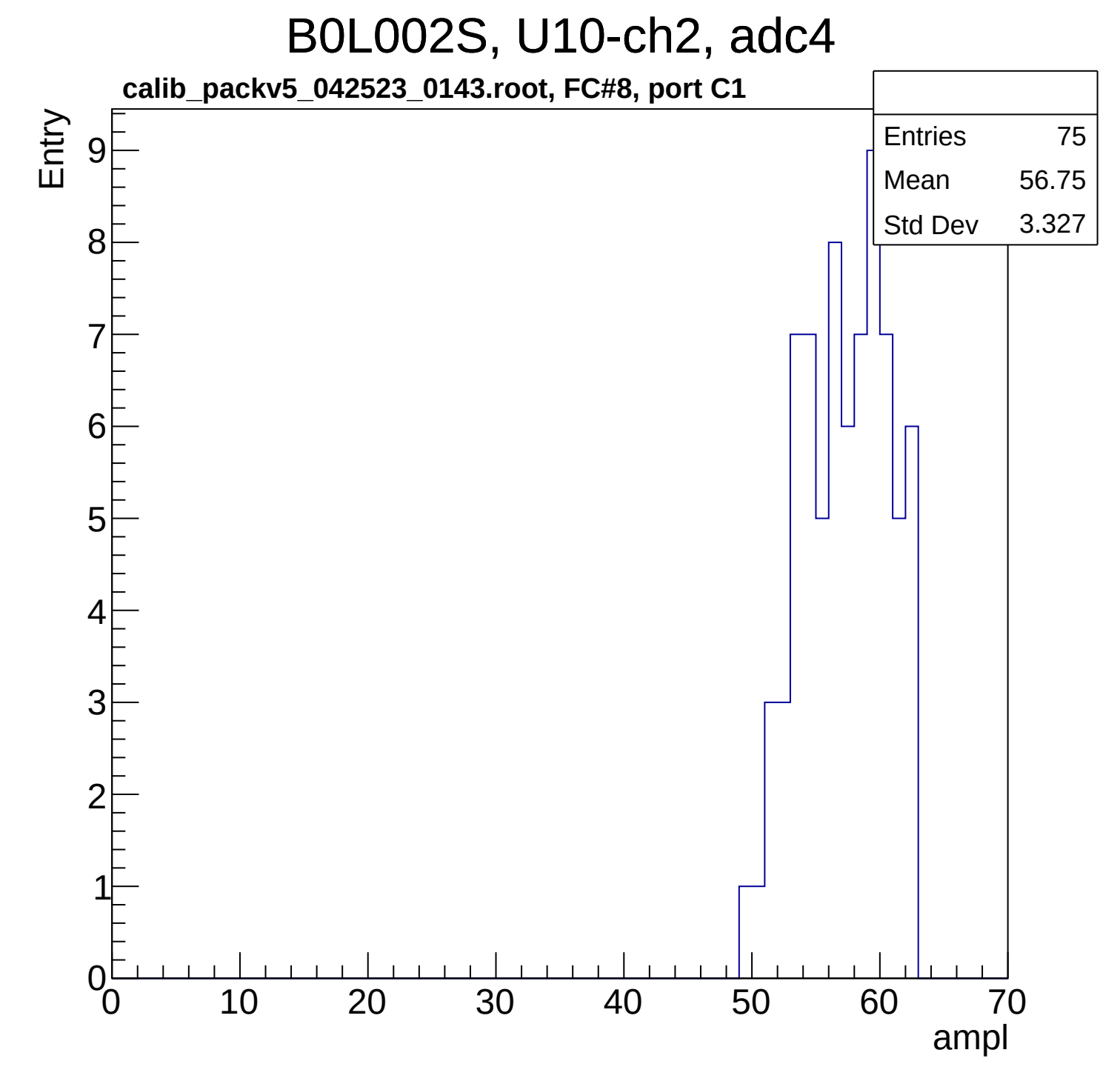

B0L002S, U10-ch1, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

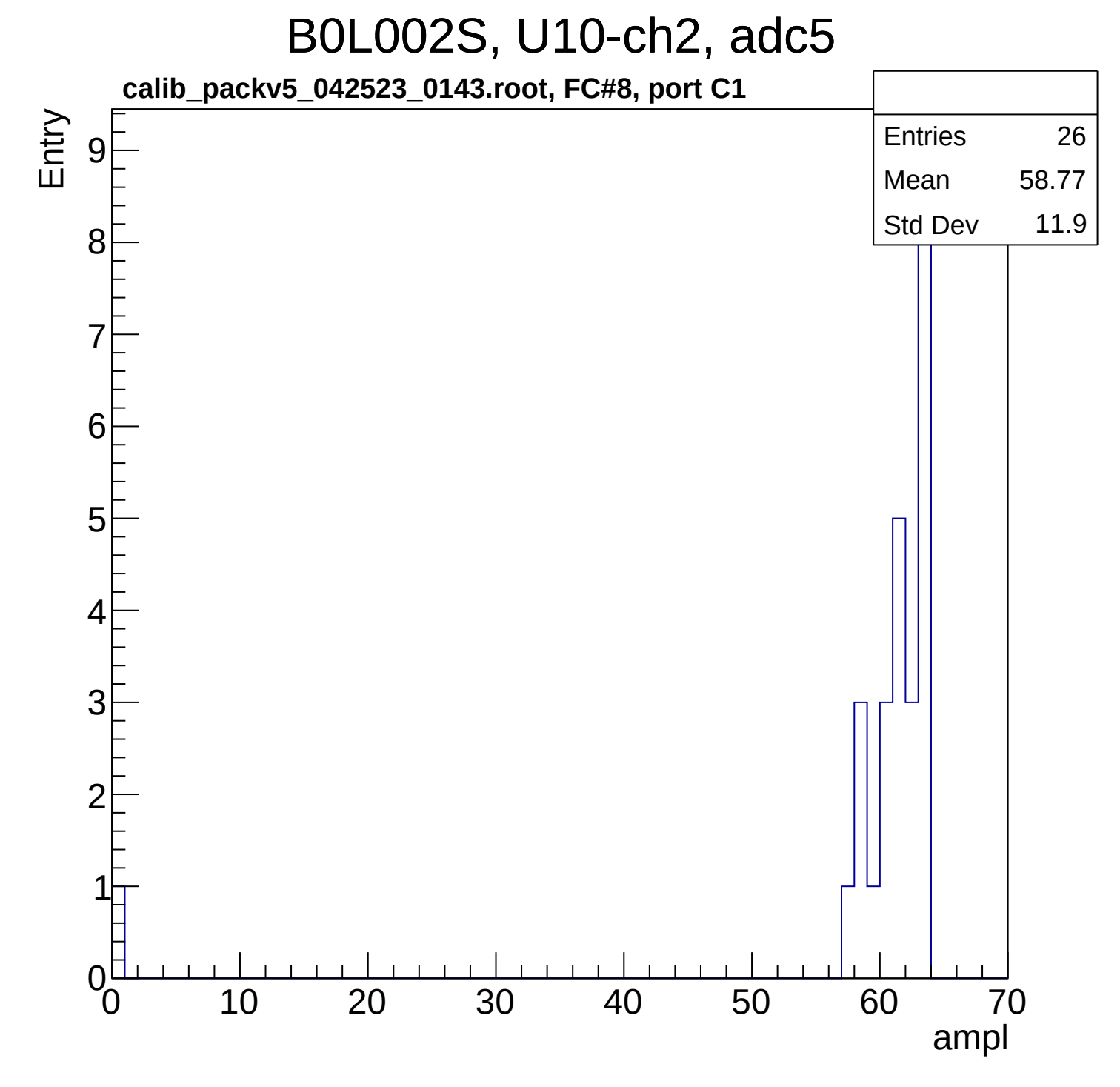

B0L002S, U10-ch2, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 30.07
Std Dev 3.656
Gaus mean : 30.3897
Gaus Width: 3.4986
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

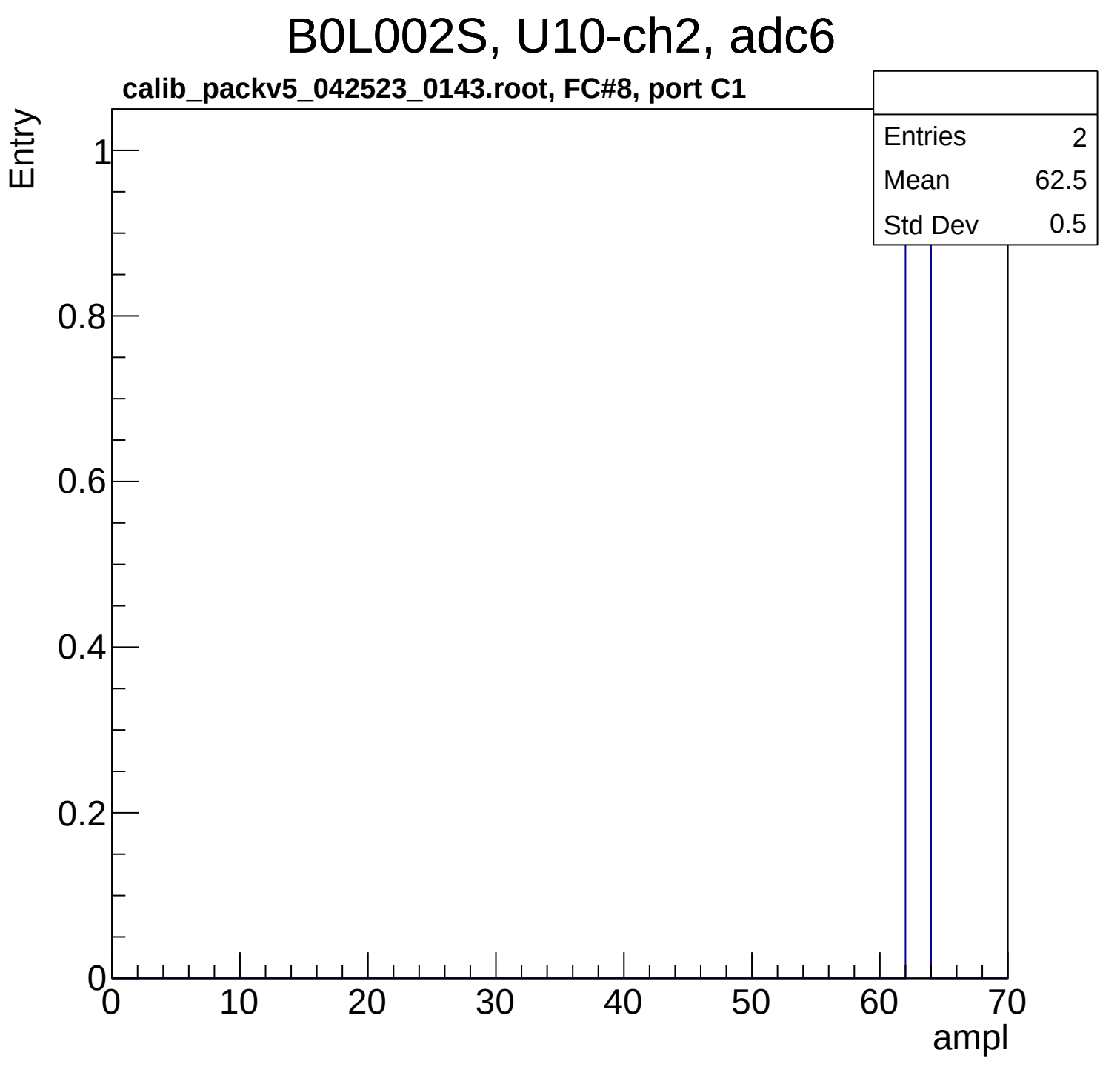

B0L002S, U10-ch2, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 36.55
Std Dev 3.257
Gaus mean : 37.4127
Gaus Width: 3.2522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

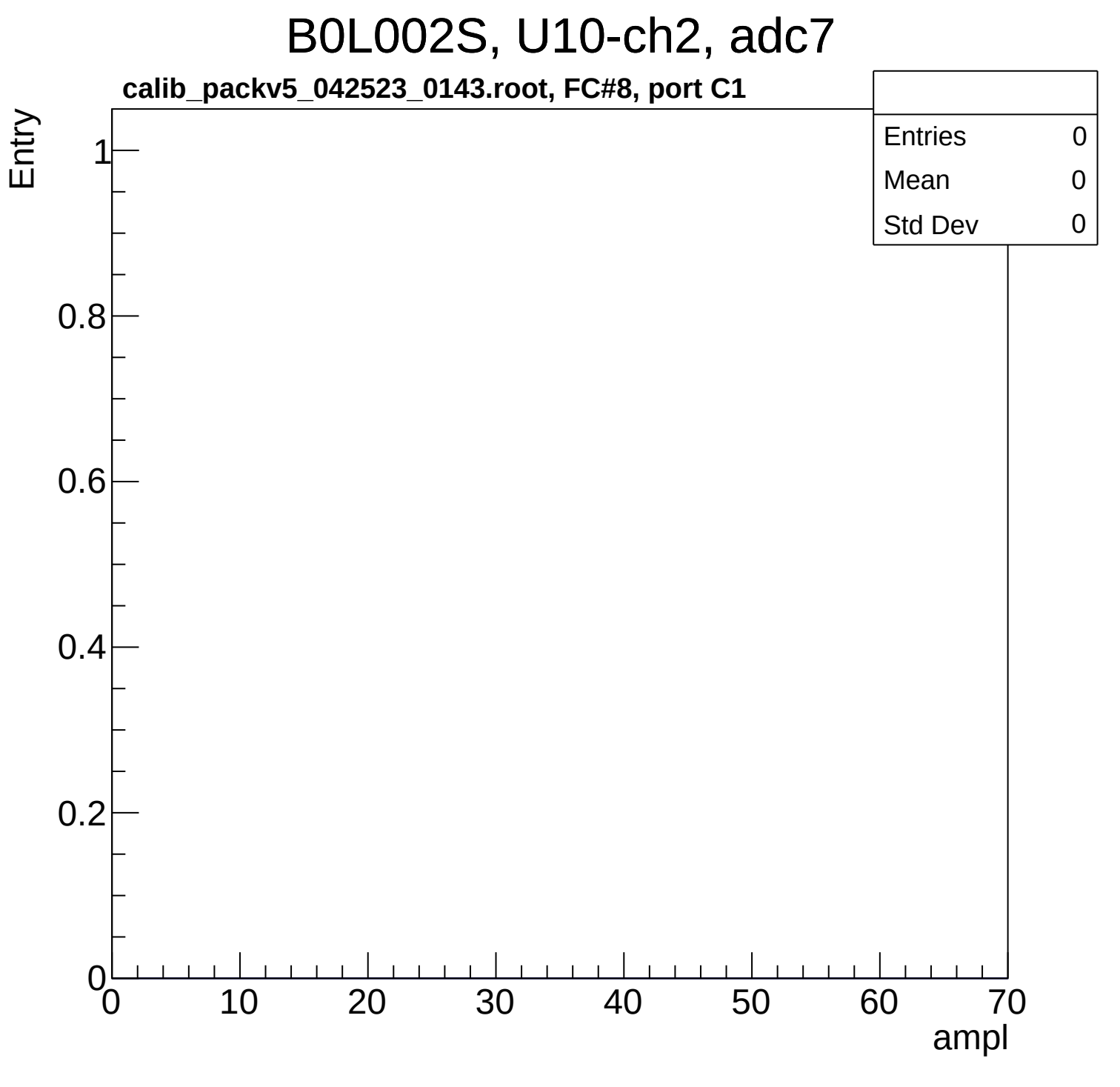

B0L002S, U10-ch2, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 43.77
Std Dev 3.412
Gaus mean : 44.4649
Gaus Width: 3.3028
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

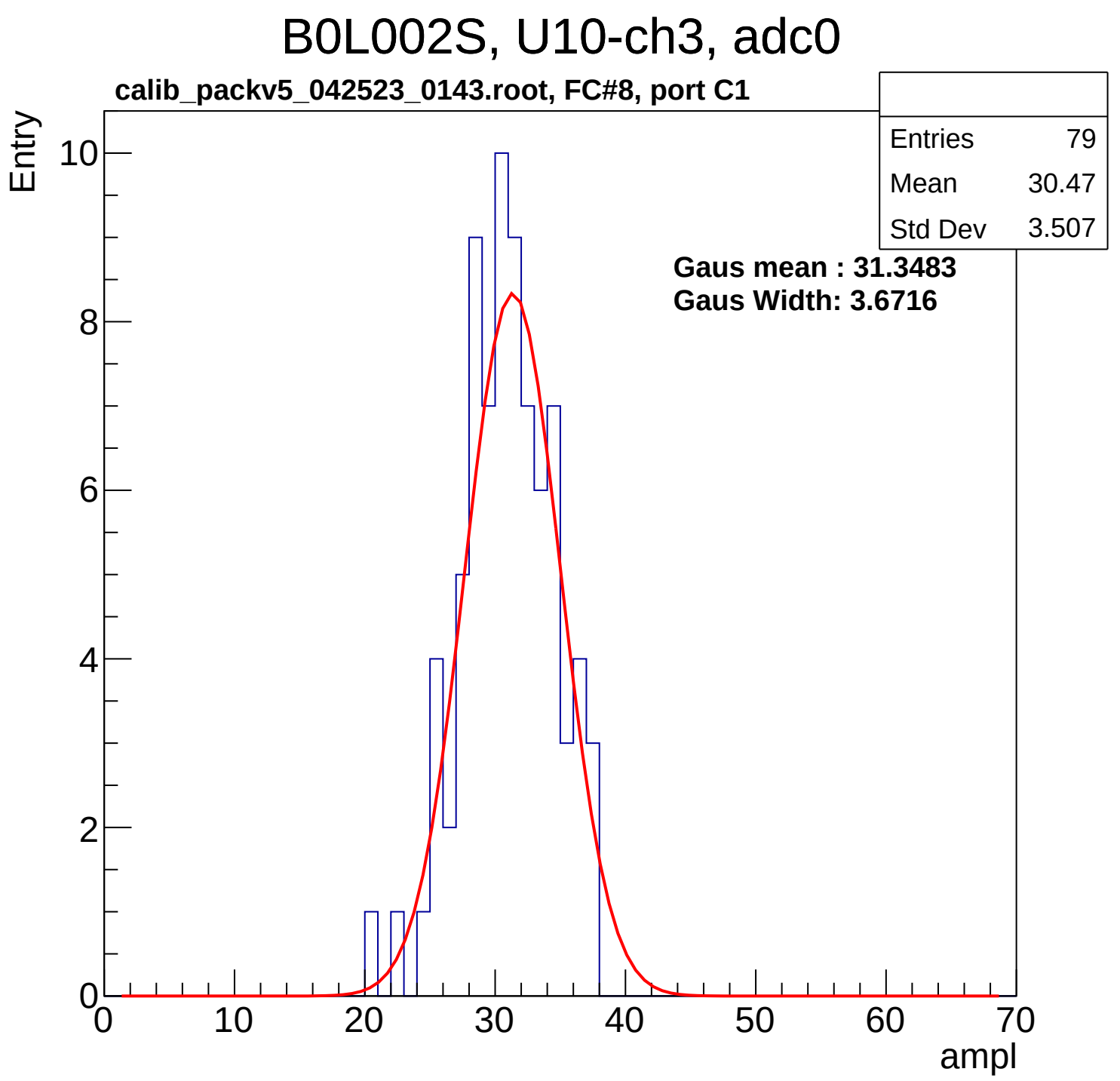

B0L002S, U10-ch2, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 50.53
Std Dev 3.403
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

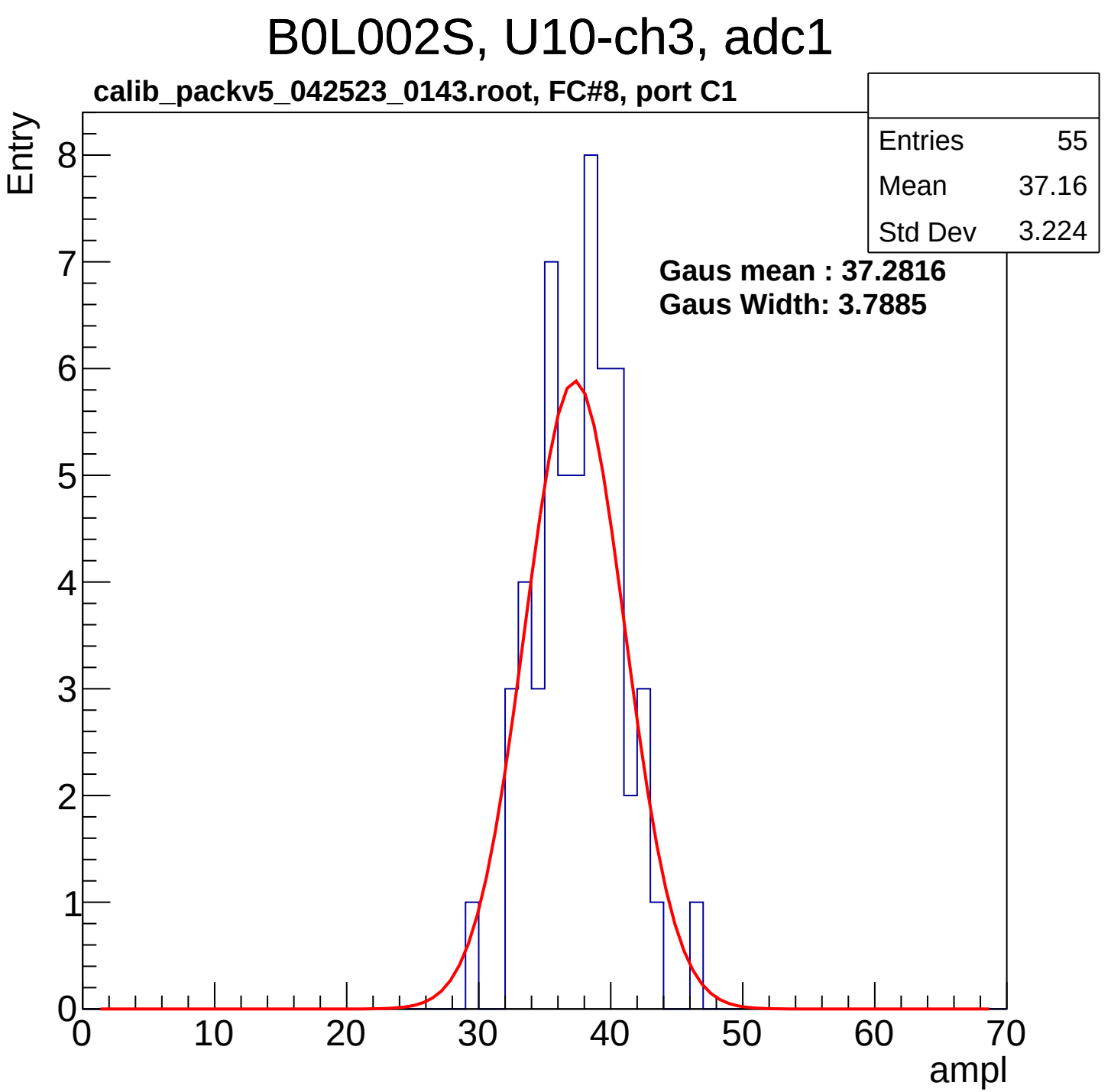

B0L002S, U10-ch2, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 56.75
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

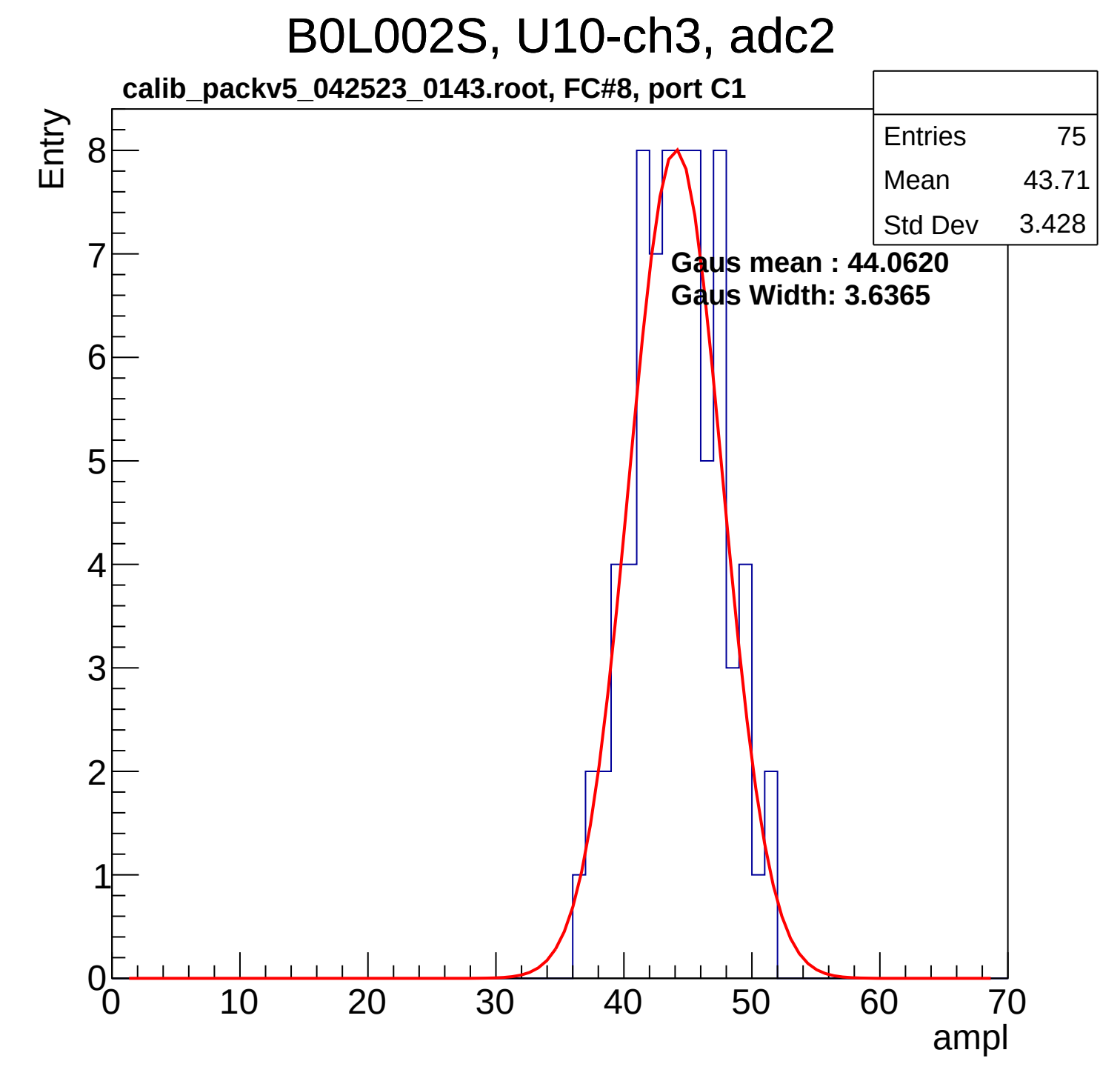

B0L002S, U10-ch2, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.77
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

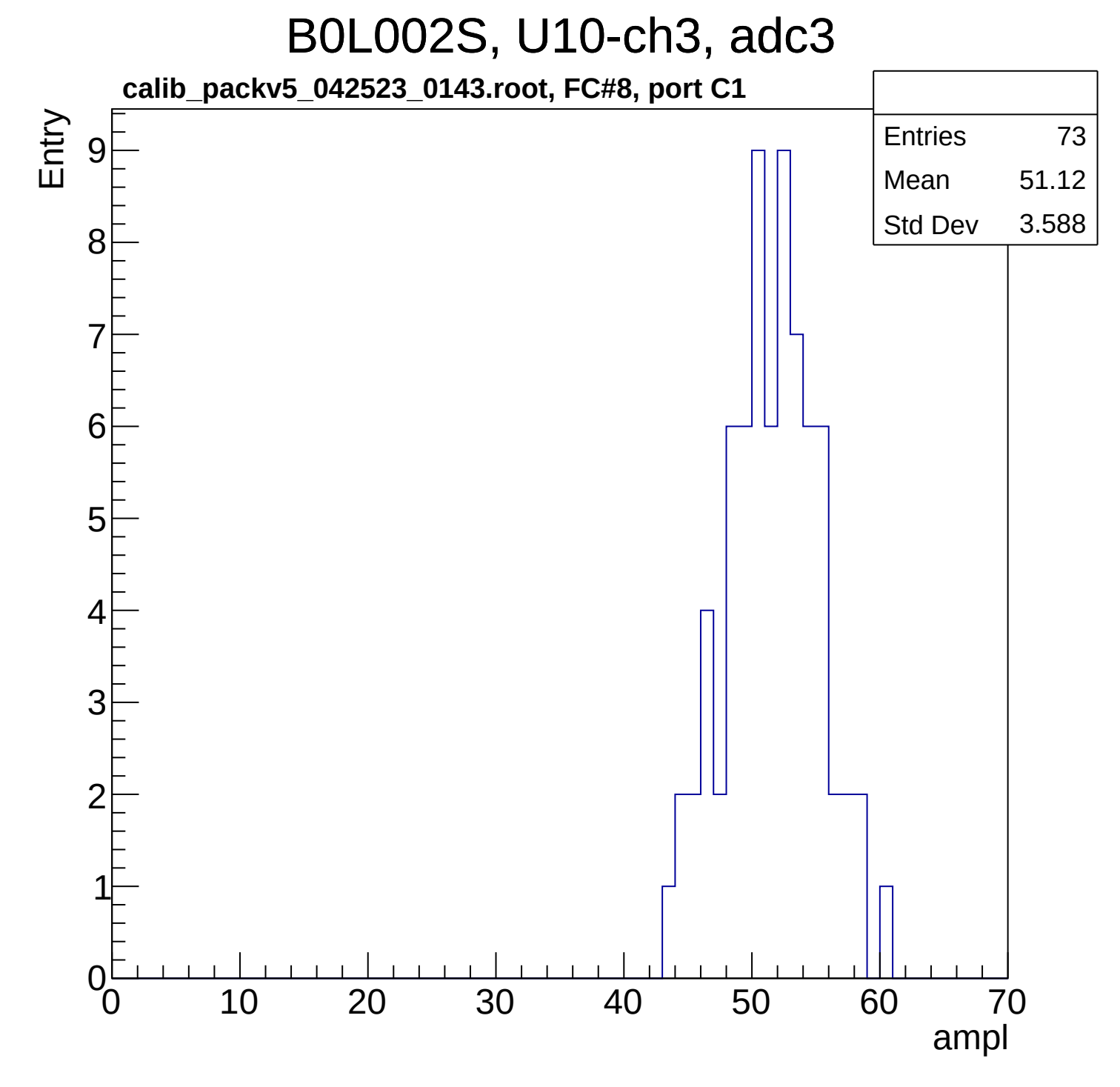

B0L002S, U10-ch2, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

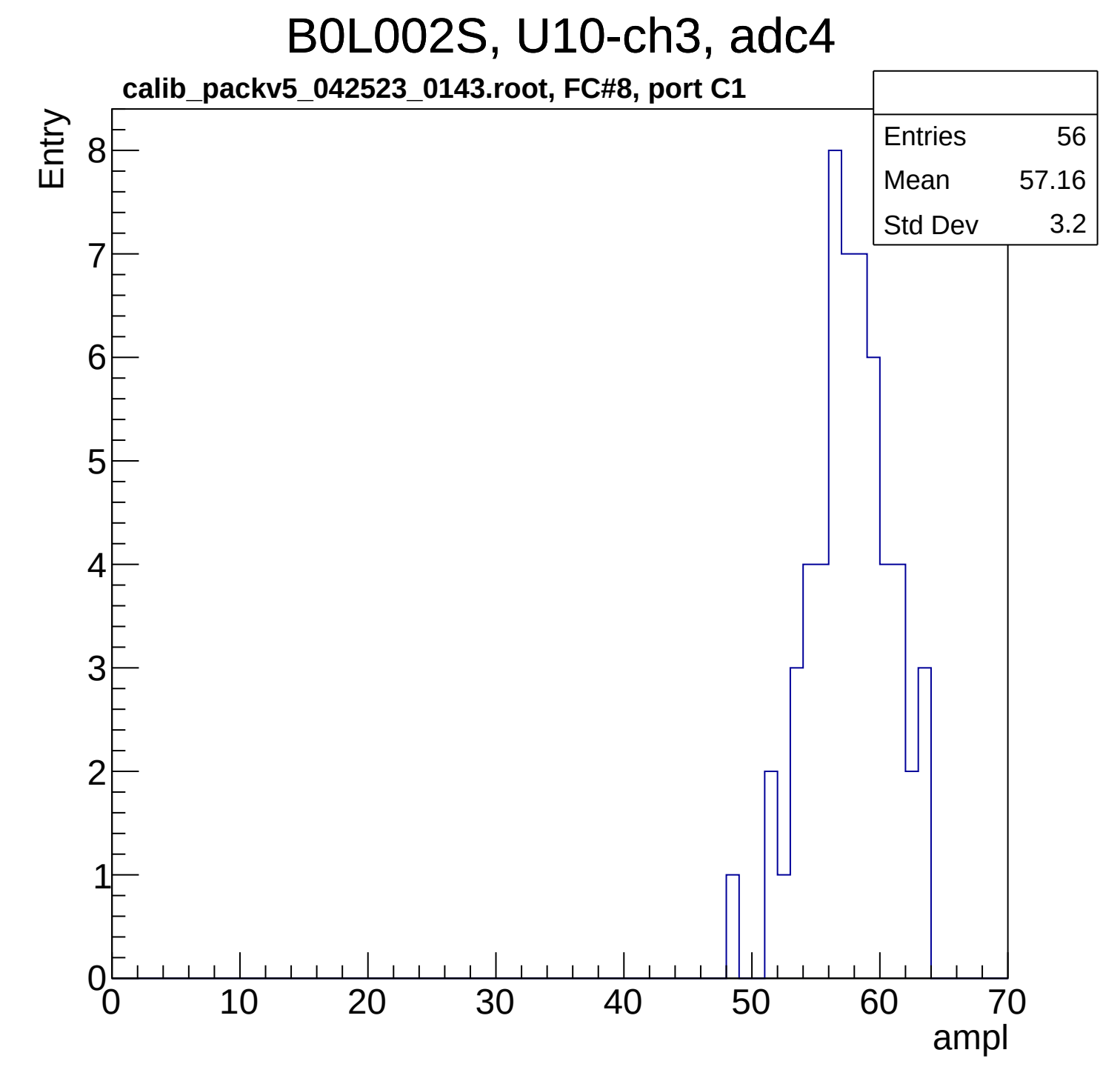

B0L002S, U10-ch2, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

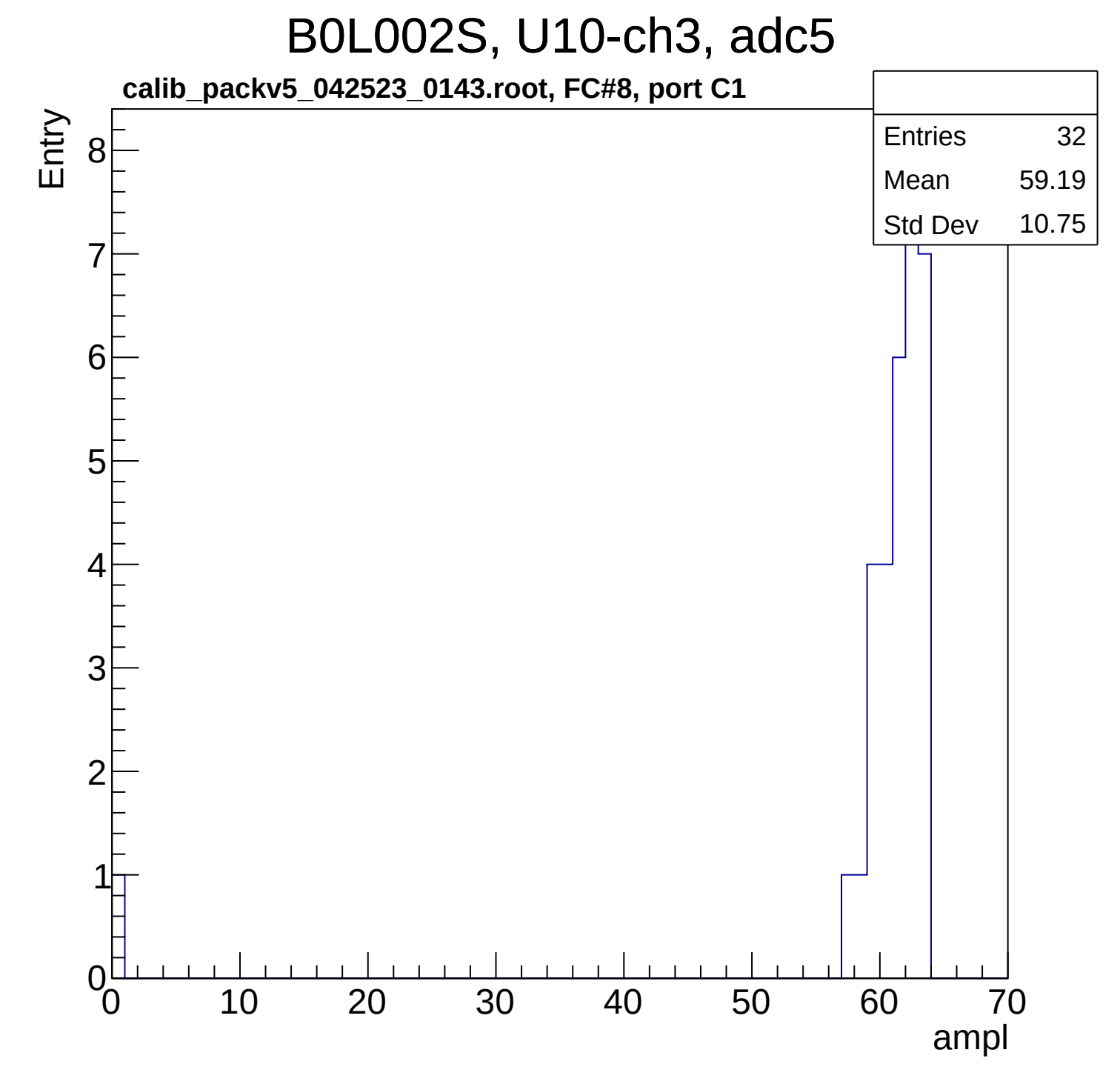

B0L002S, U10-ch3, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.47
Std Dev 3.507
Gaus mean : 31.3483
Gaus Width: 3.6716
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

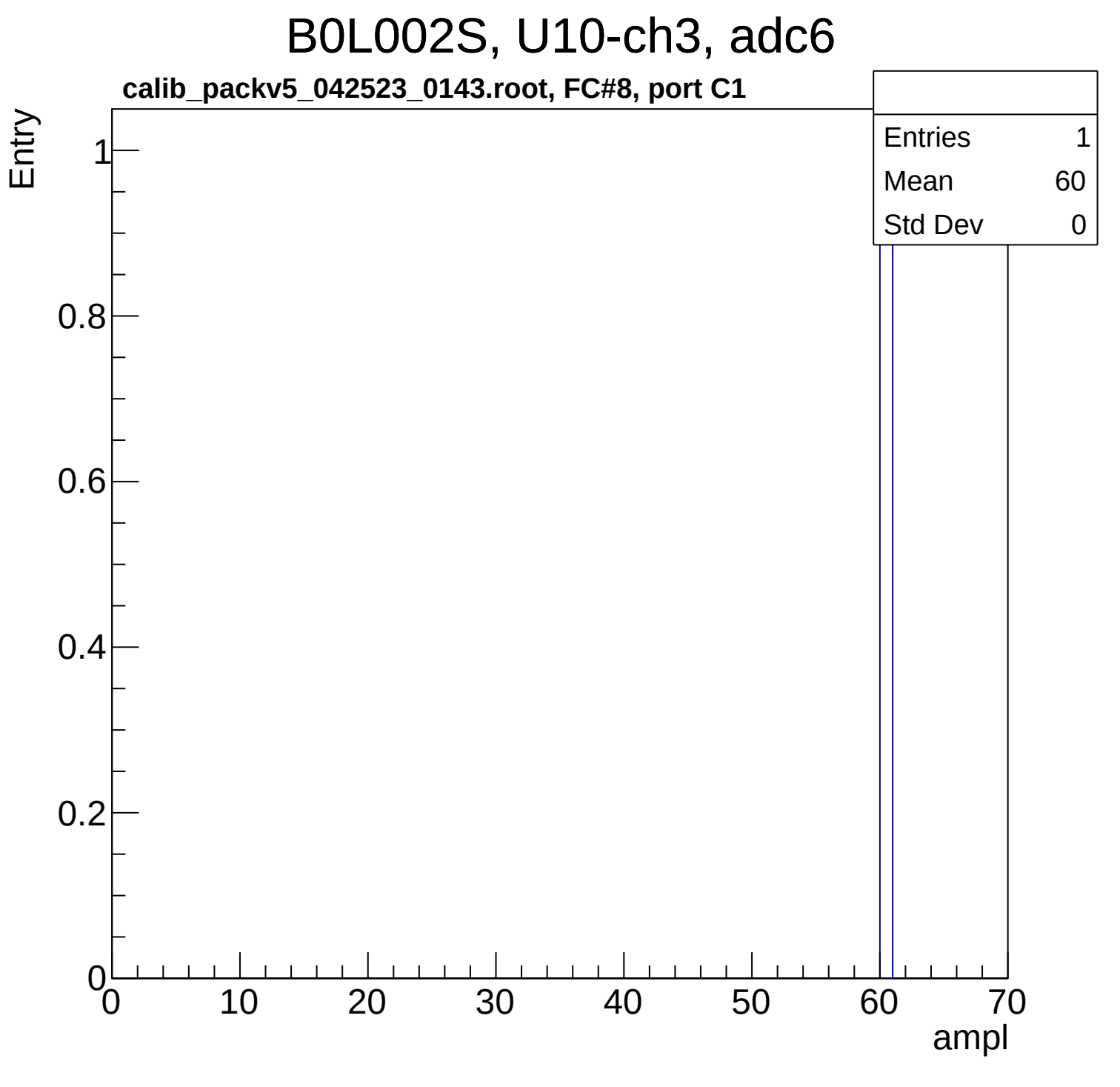

B0L002S, U10-ch3, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 37.16
Std Dev 3.224
Gaus mean : 37.2816
Gaus Width: 3.7885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

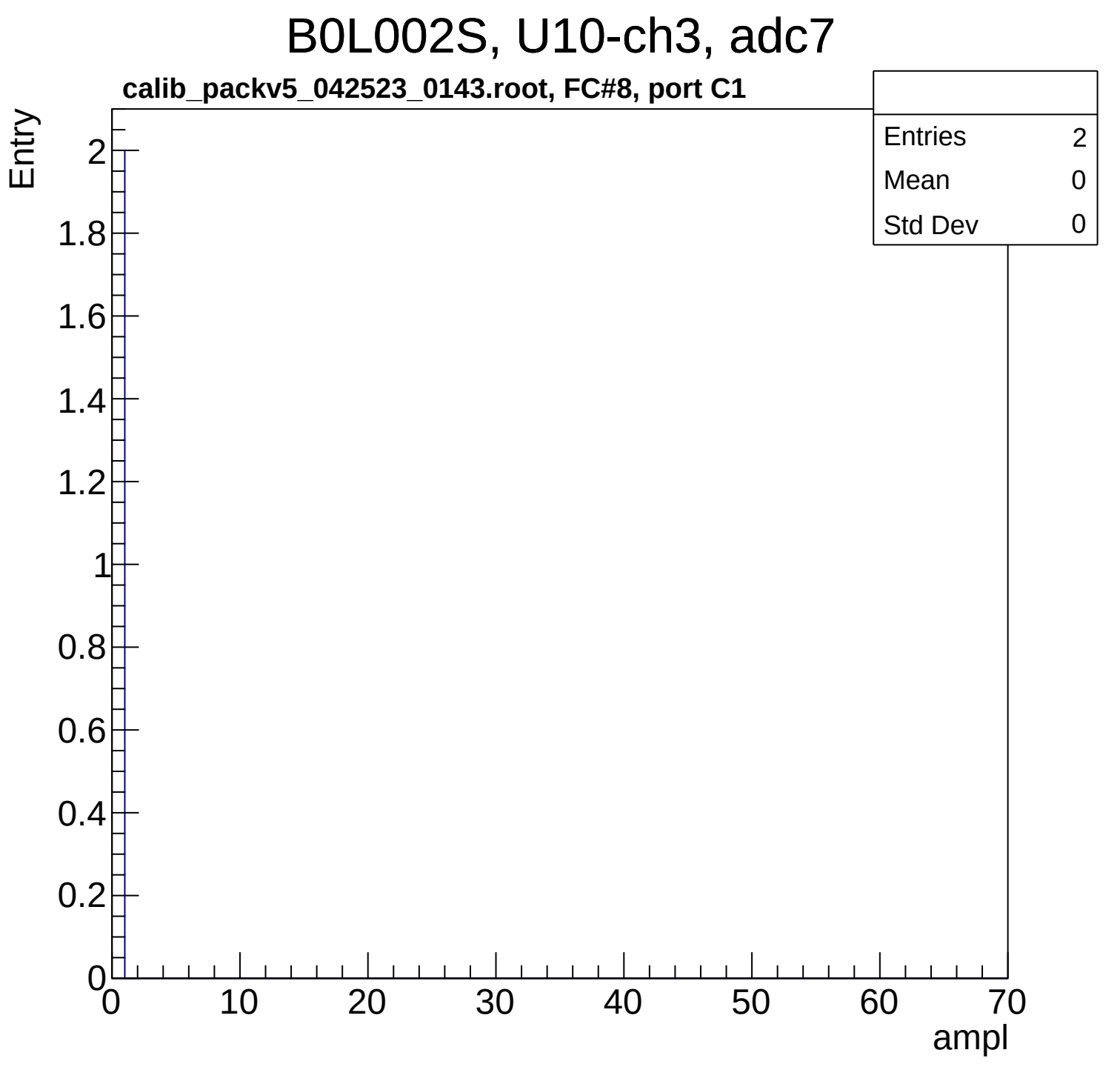

B0L002S, U10-ch3, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 43.71
Std Dev 3.428
Gaus mean : 44.0620
Gaus Width: 3.6365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

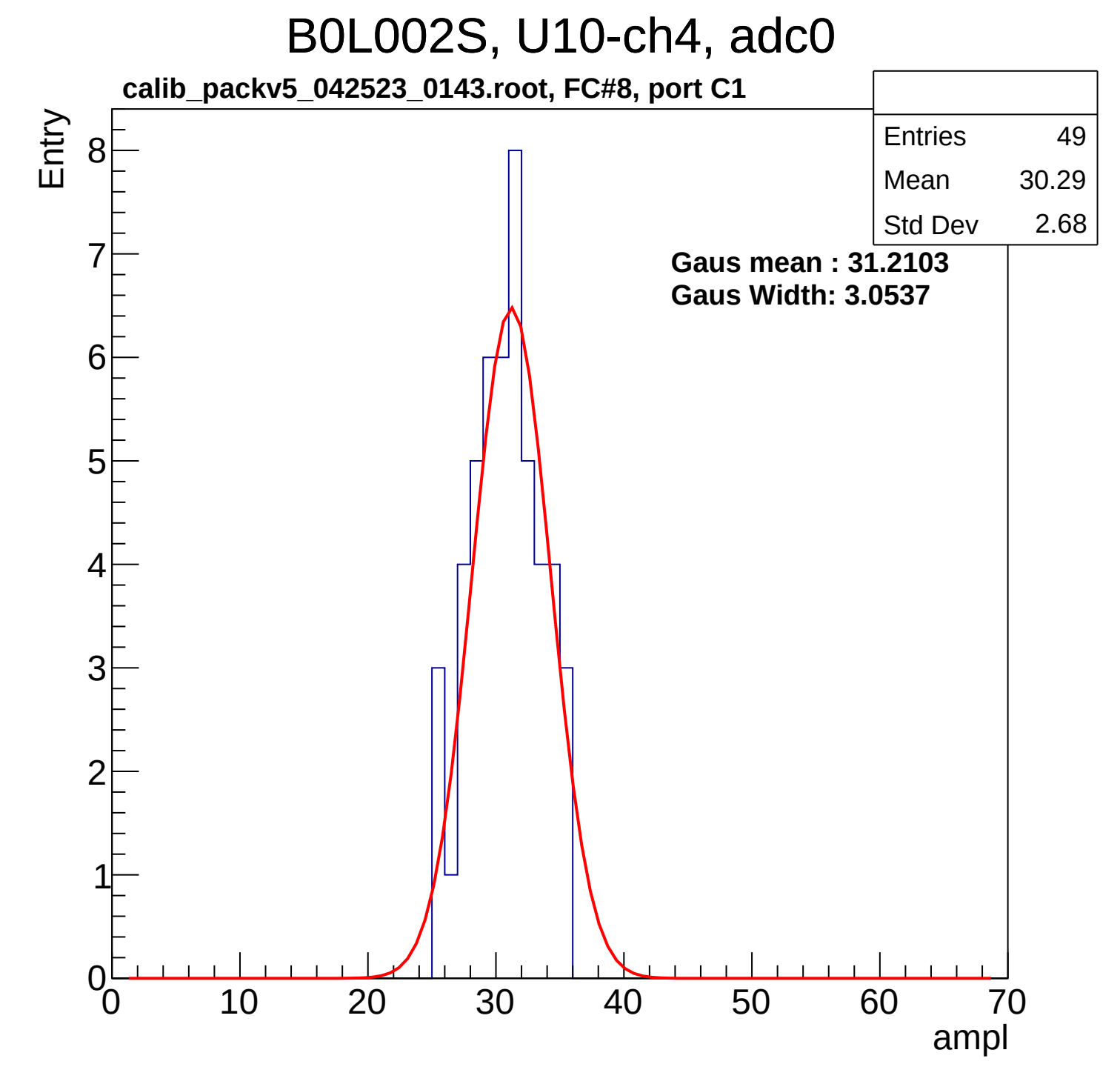

B0L002S, U10-ch3, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.12
Std Dev 3.588
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

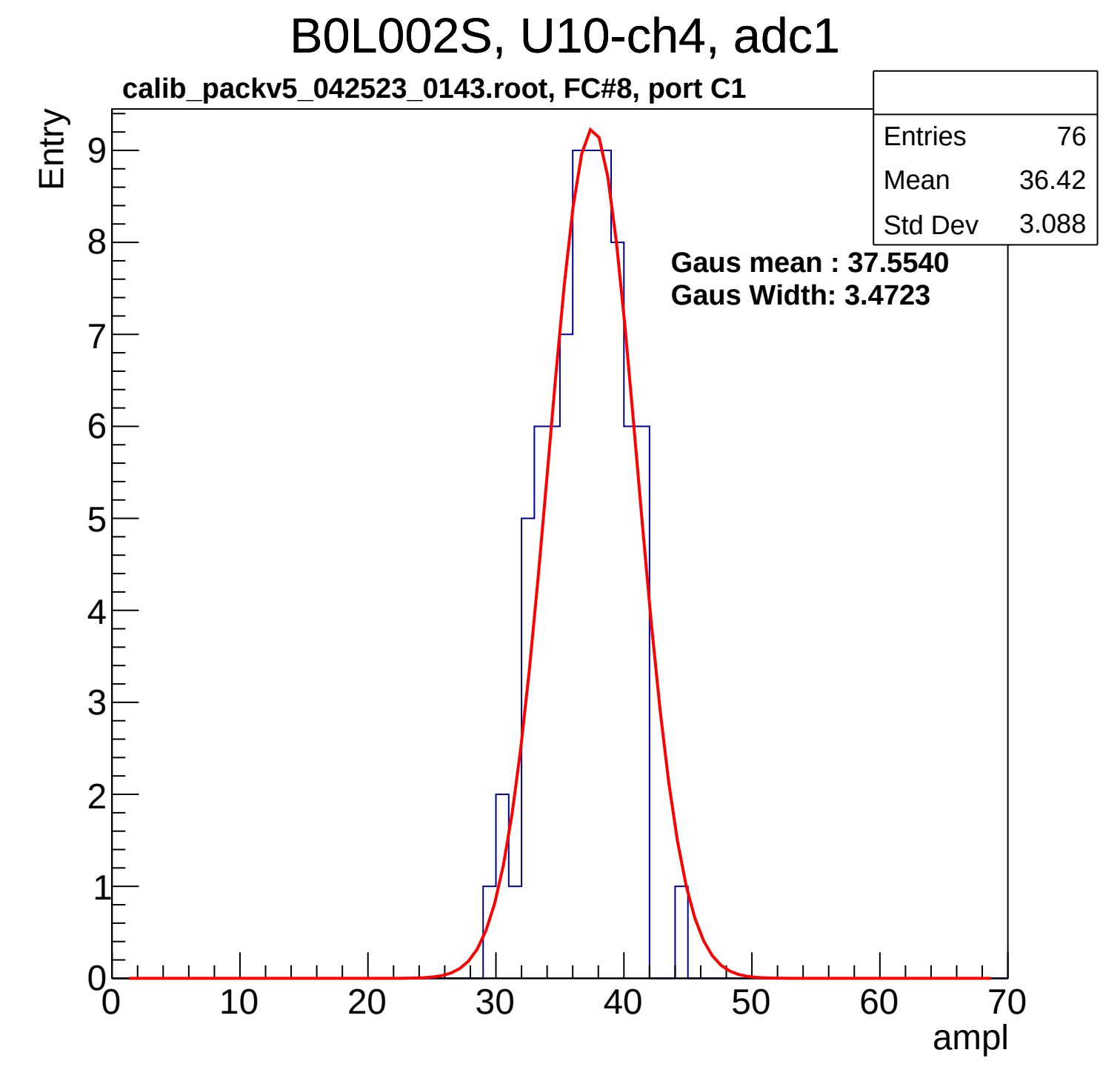

B0L002S, U10-ch3, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.16
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

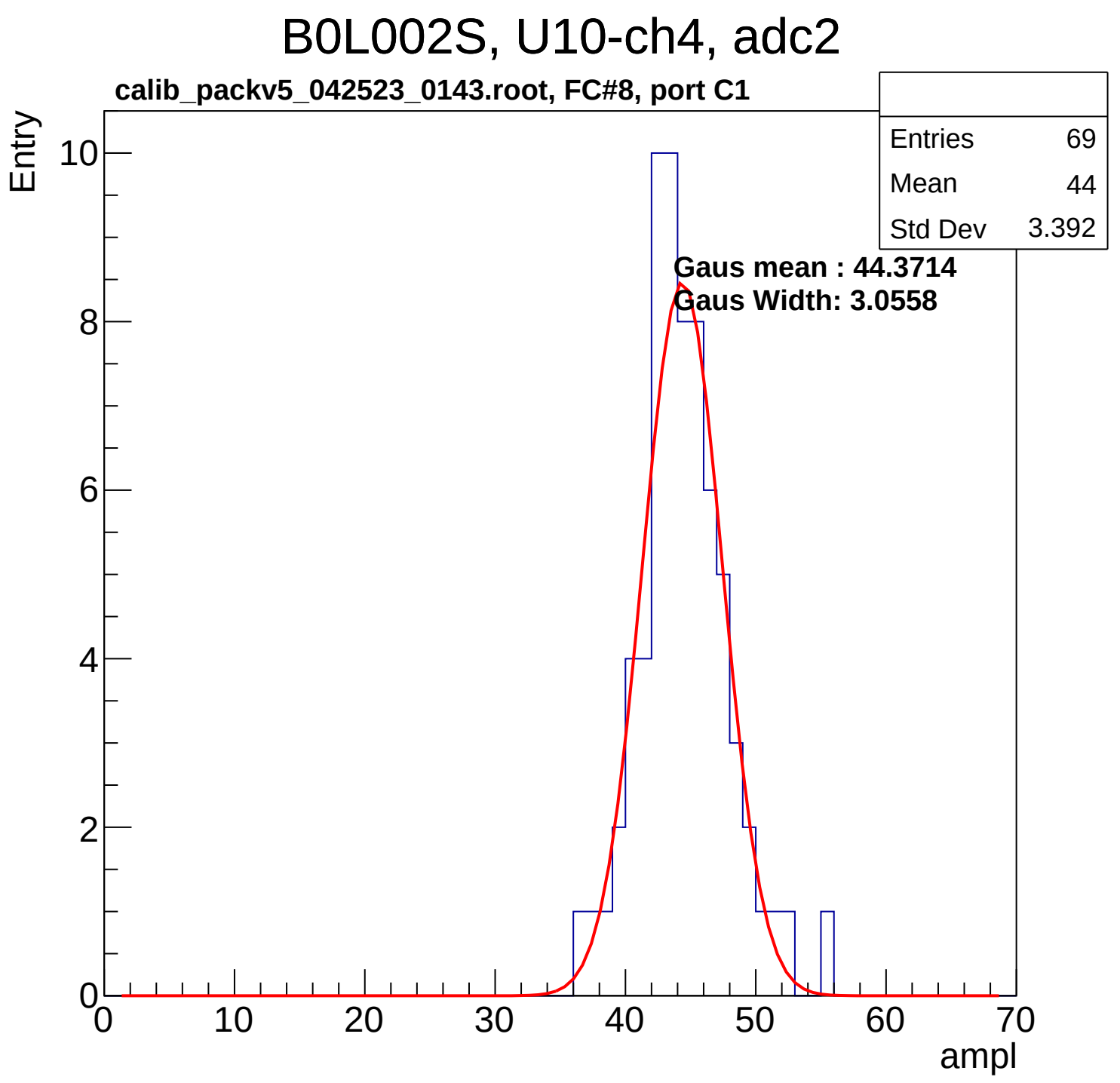

B0L002S, U10-ch3, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.19
Std Dev 10.75
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

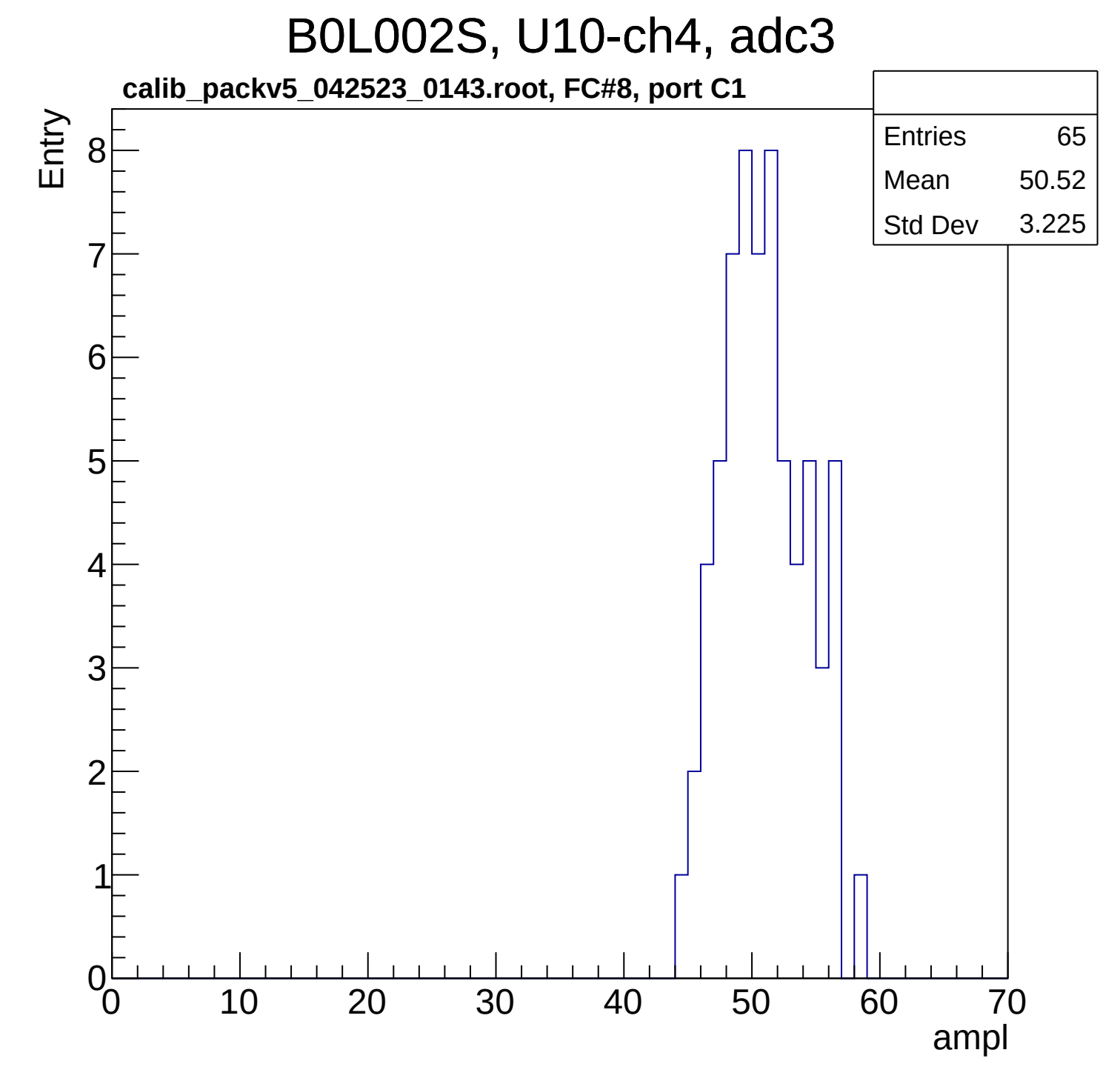

B0L002S, U10-ch3, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

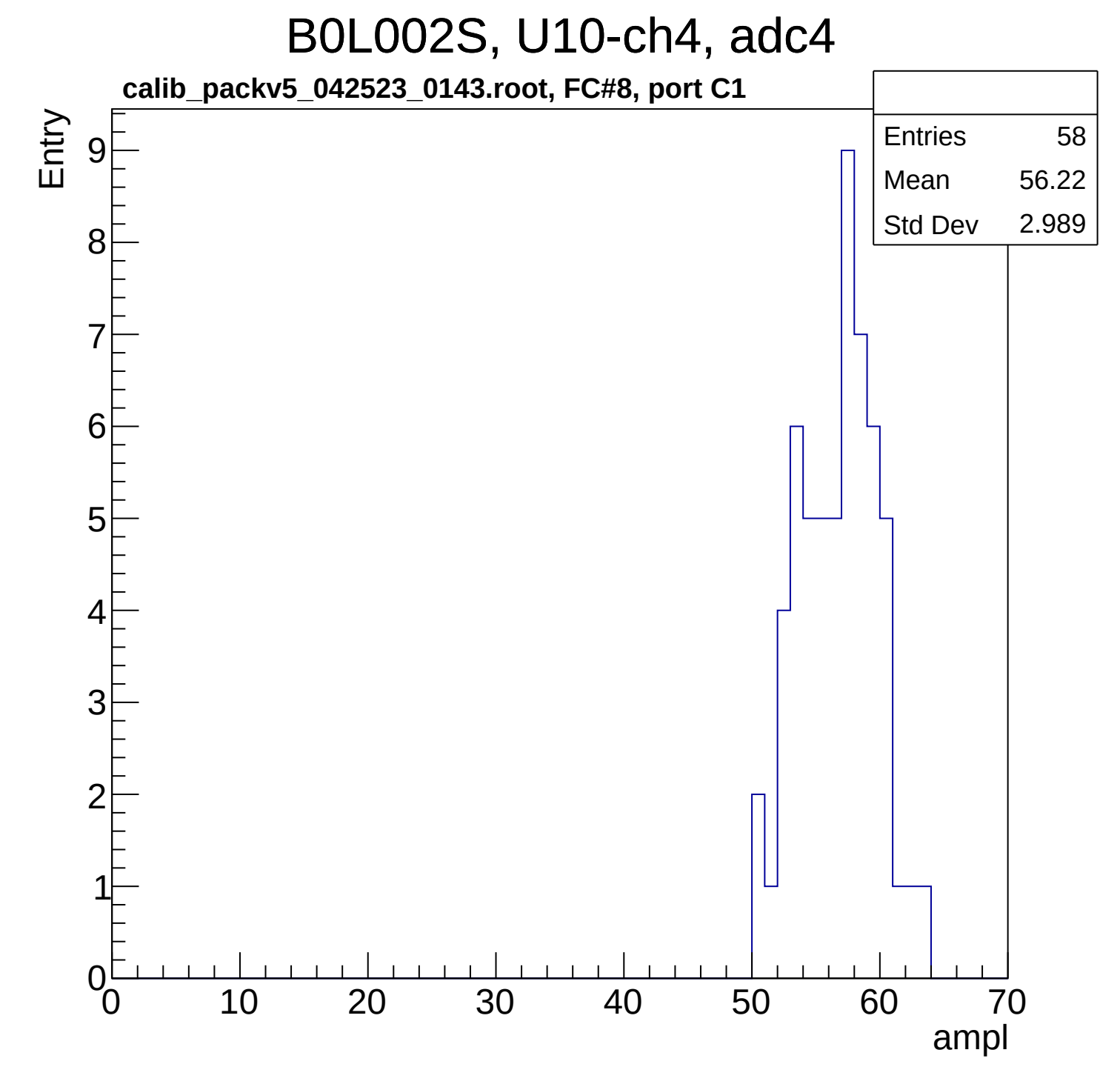

B0L002S, U10-ch3, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

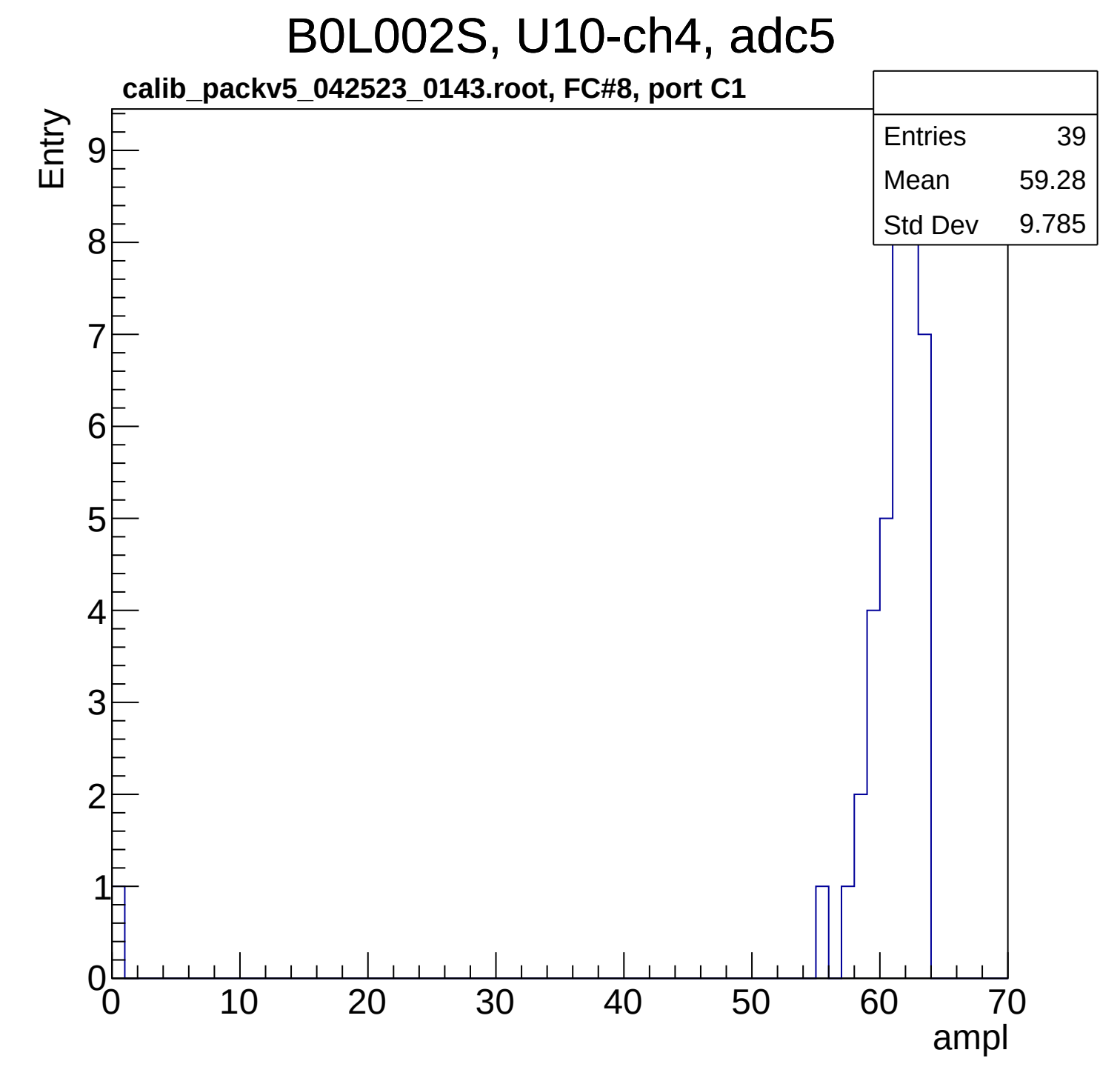

B0L002S, U10-ch4, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 30.29
Std Dev 2.68
Gaus mean : 31.2103
Gaus Width: 3.0537
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

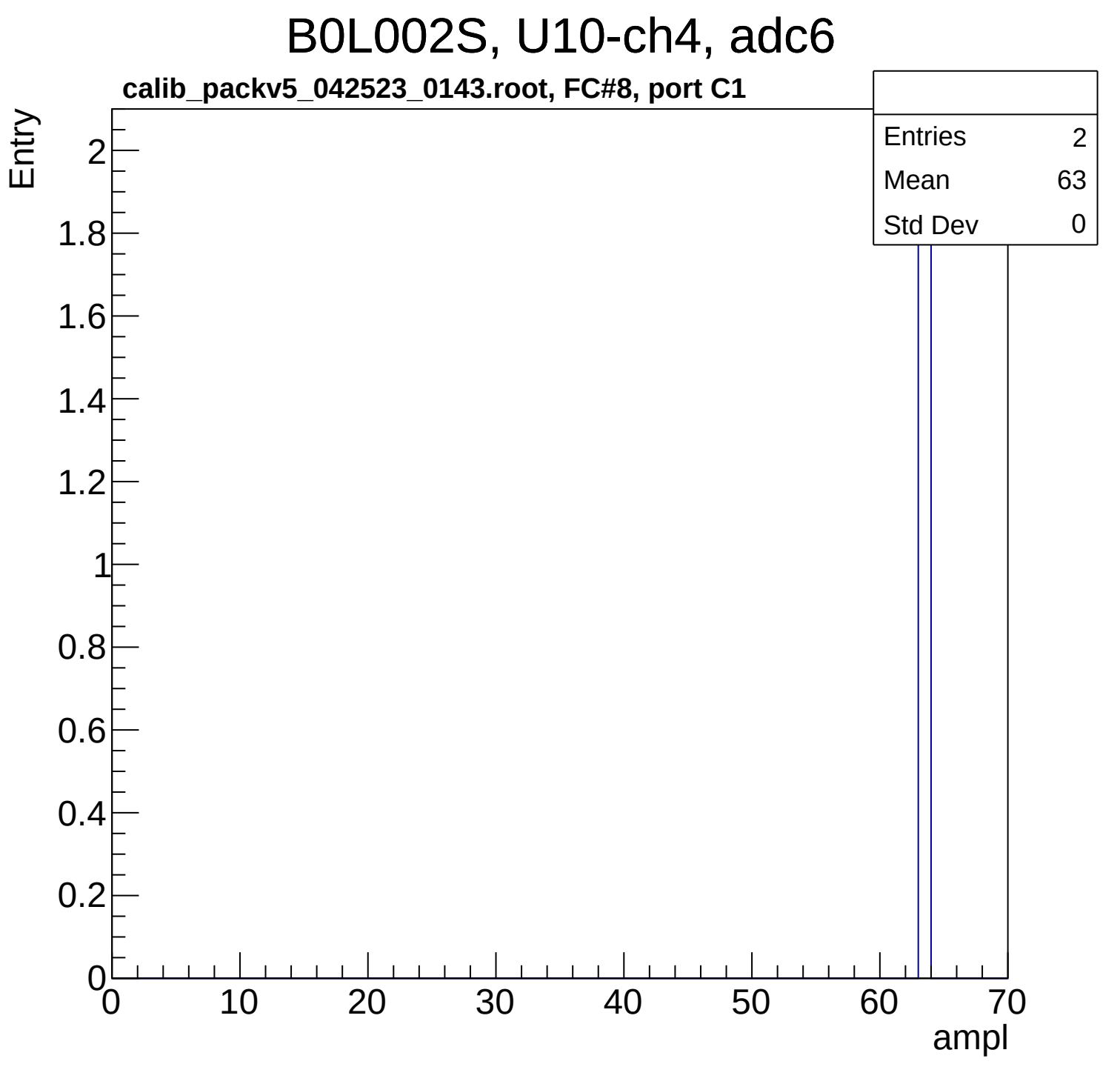

B0L002S, U10-ch4, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.42
Std Dev 3.088
Gaus mean : 37.5540
Gaus Width: 3.4723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

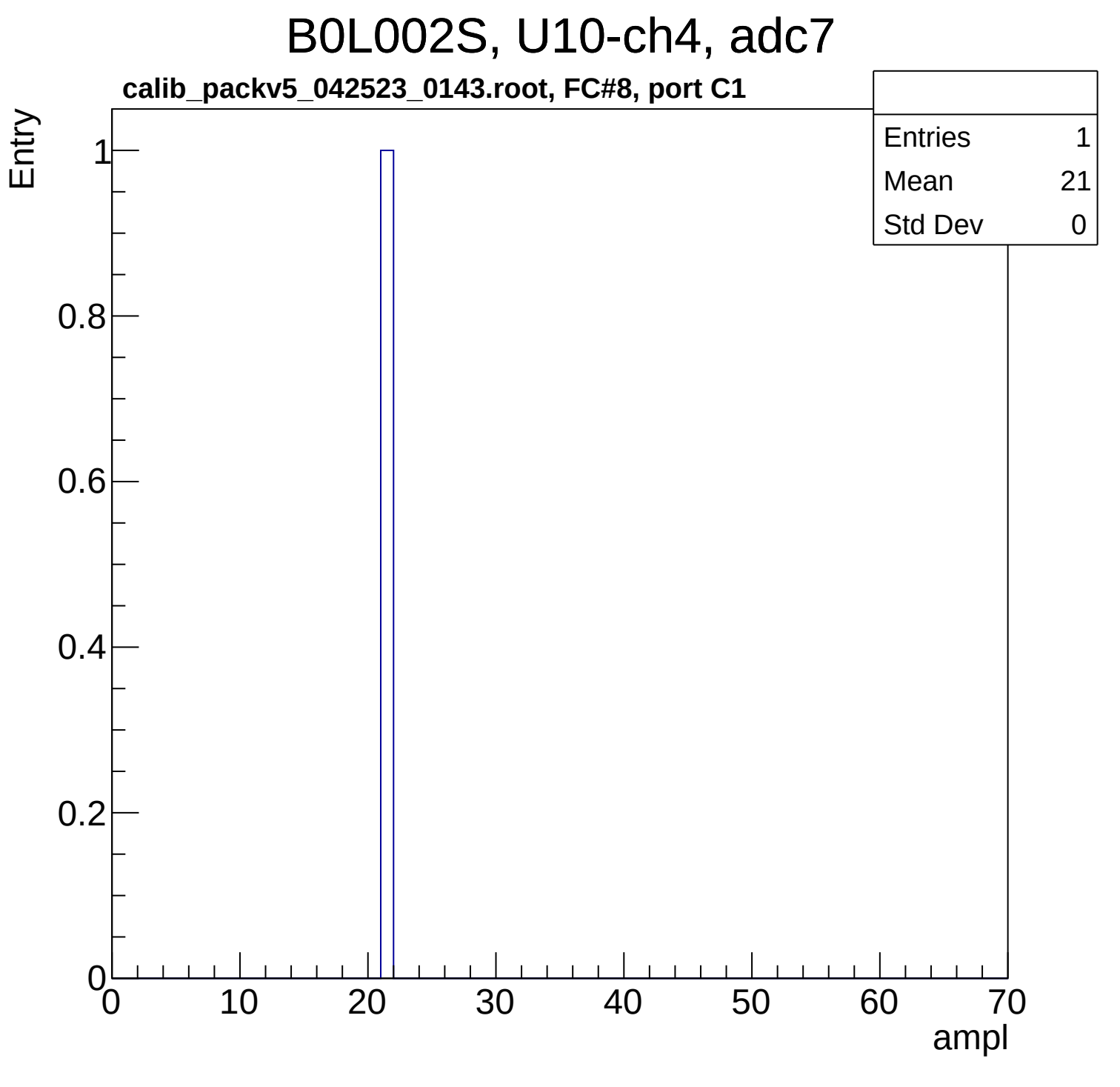

B0L002S, U10-ch4, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 44
Std Dev 3.392
Gaus mean : 44.3714
Gaus Width: 3.0558
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

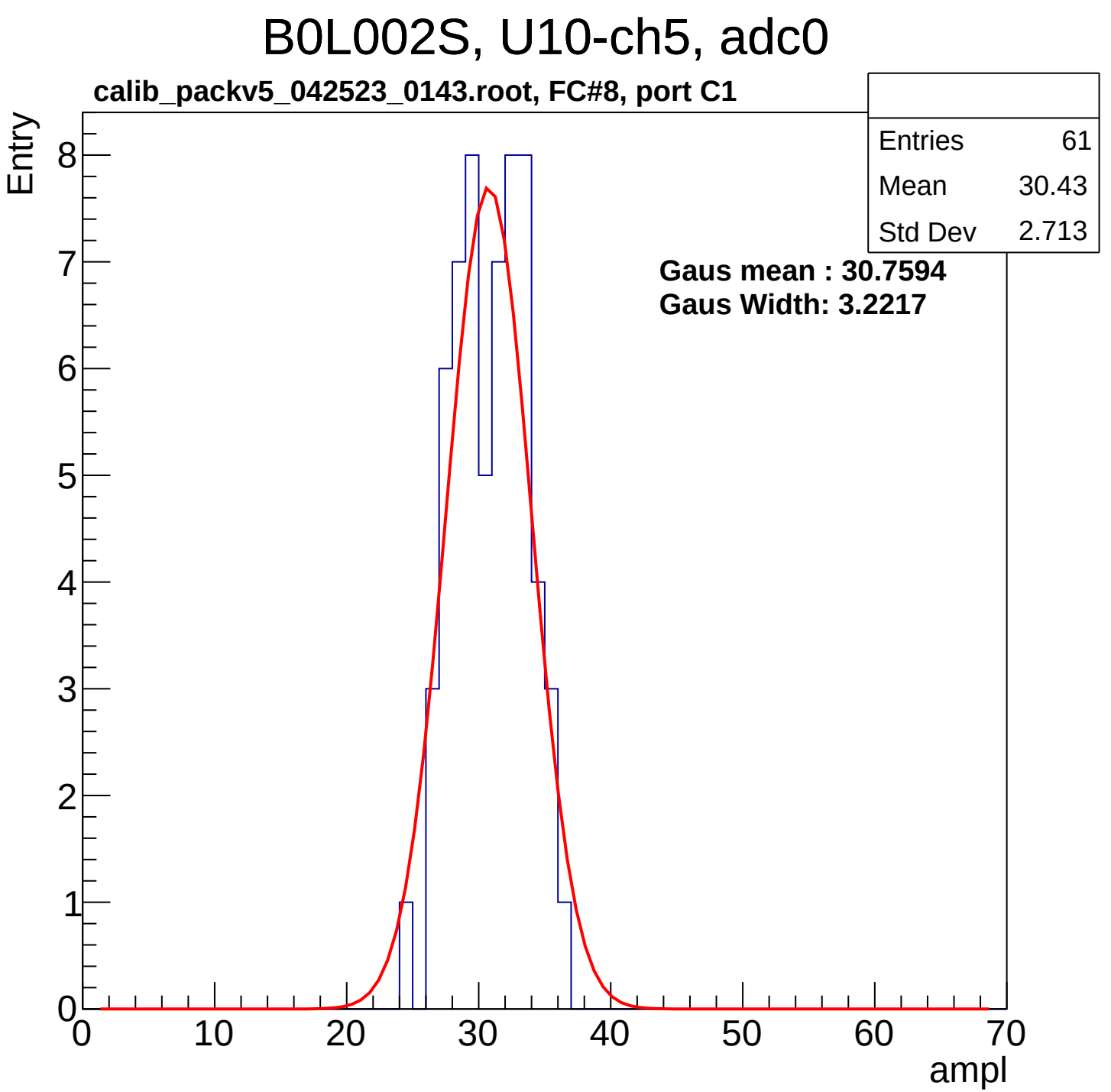

B0L002S, U10-ch4, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.52
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

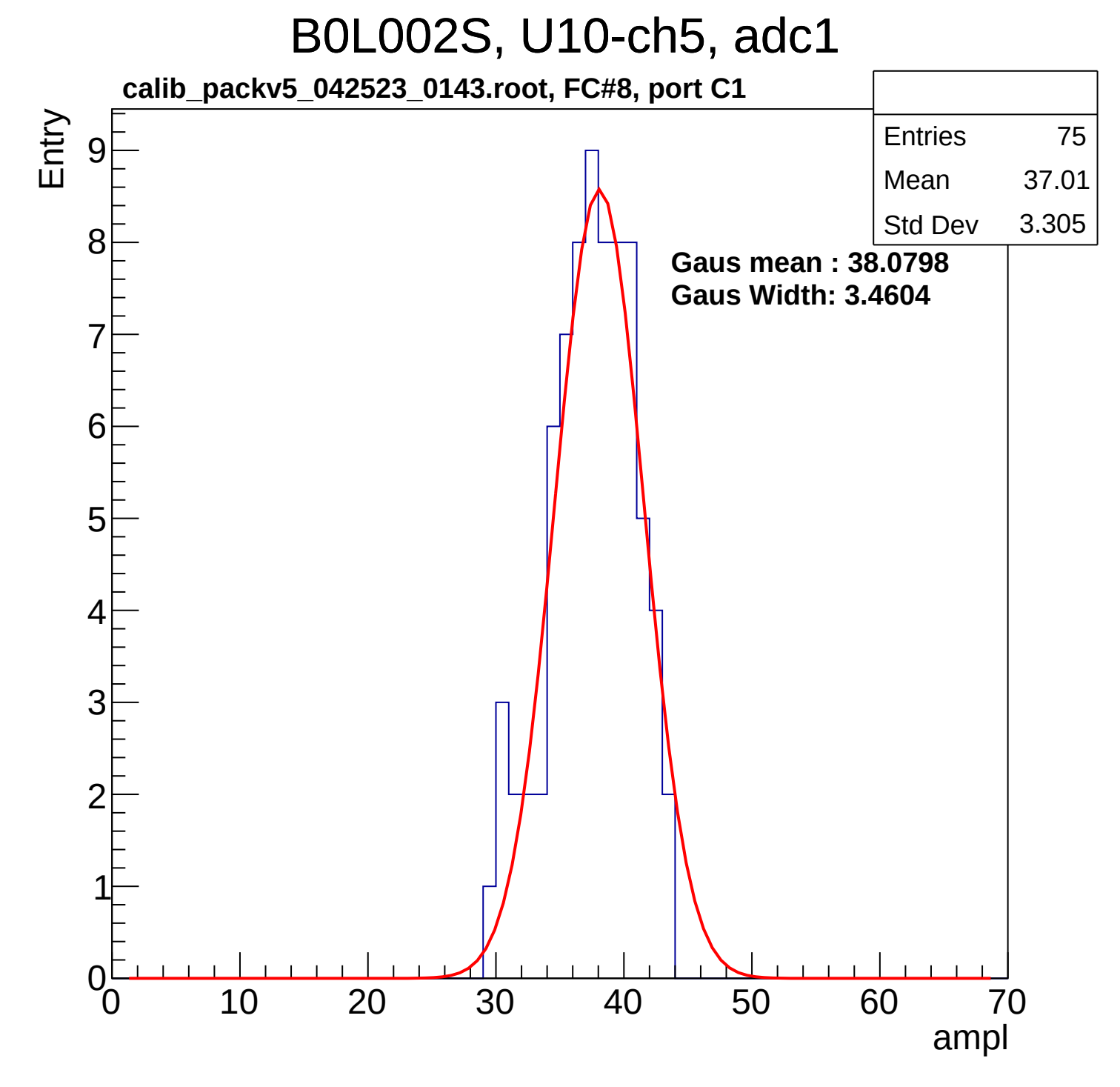

B0L002S, U10-ch4, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 56.22
Std Dev 2.989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

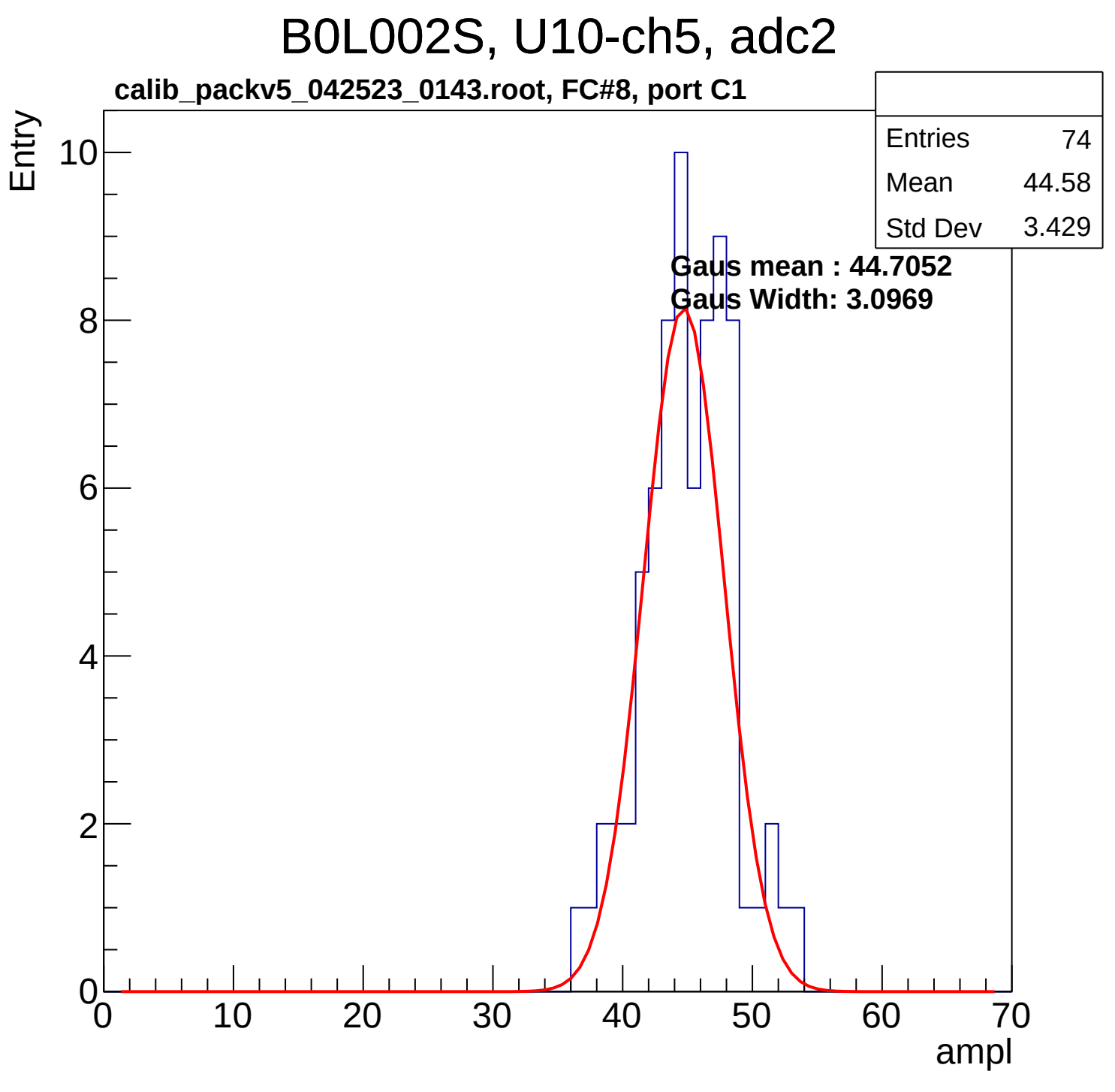

B0L002S, U10-ch4, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.28
Std Dev 9.785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

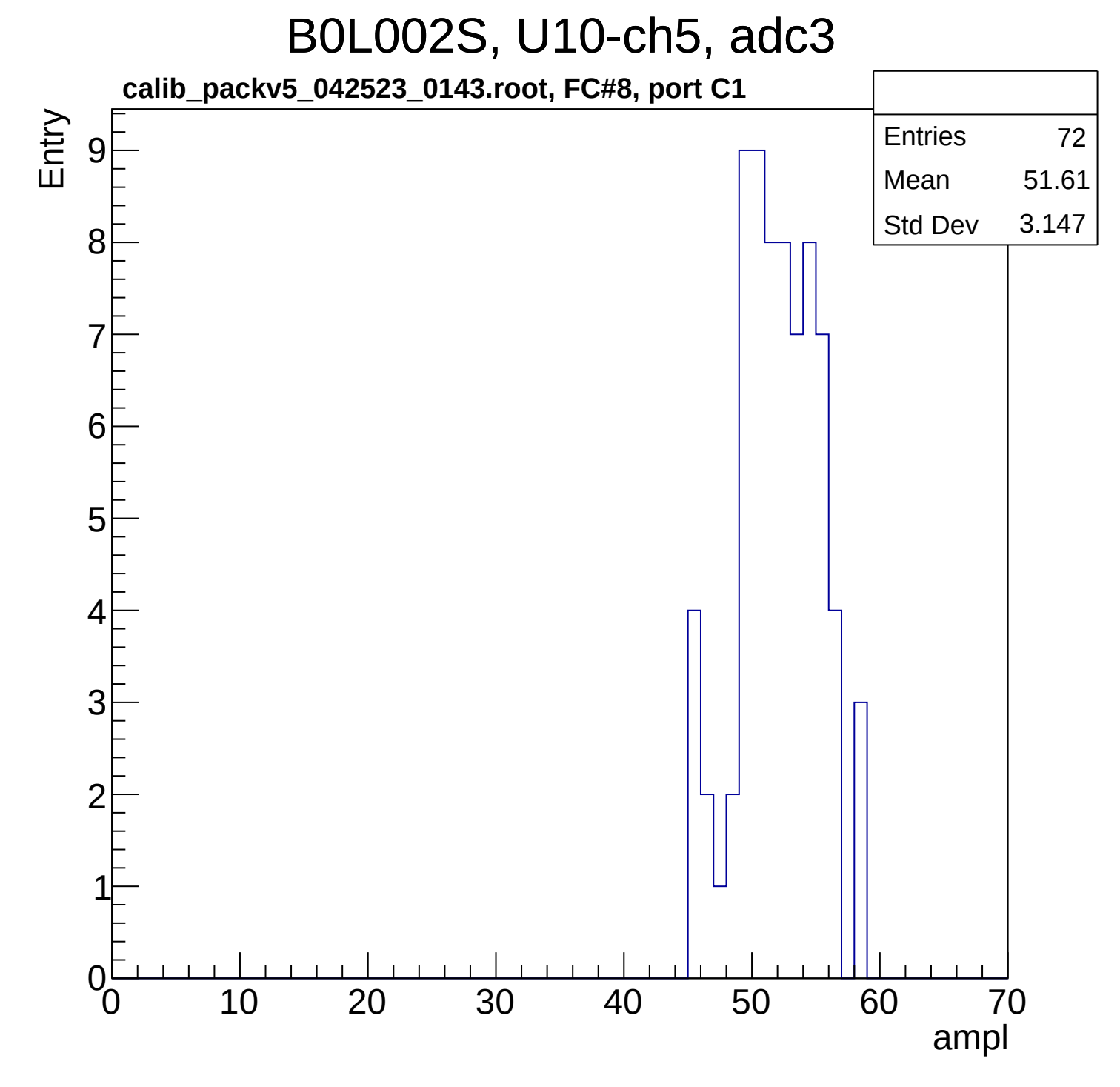

B0L002S, U10-ch4, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

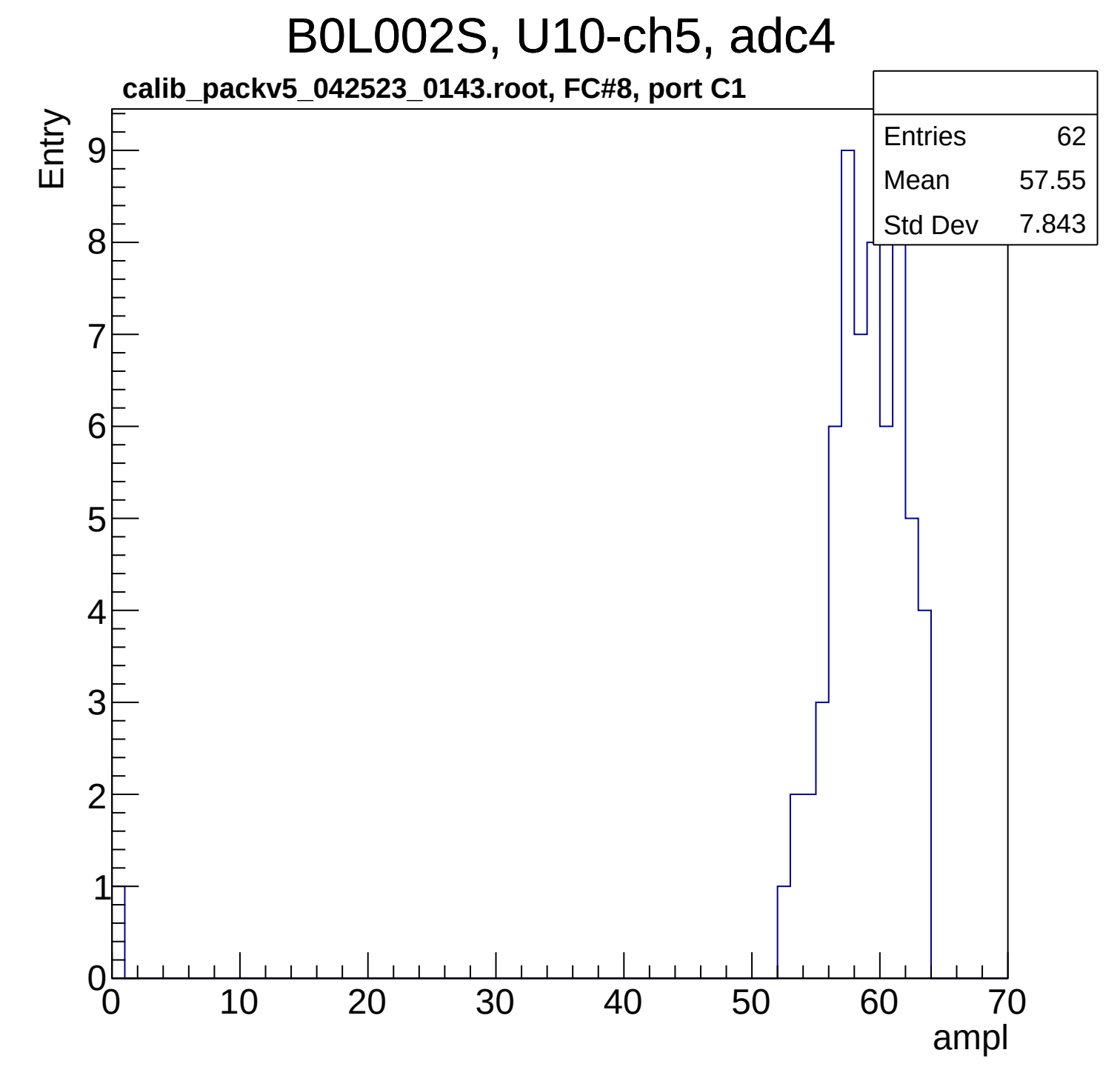

B0L002S, U10-ch4, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

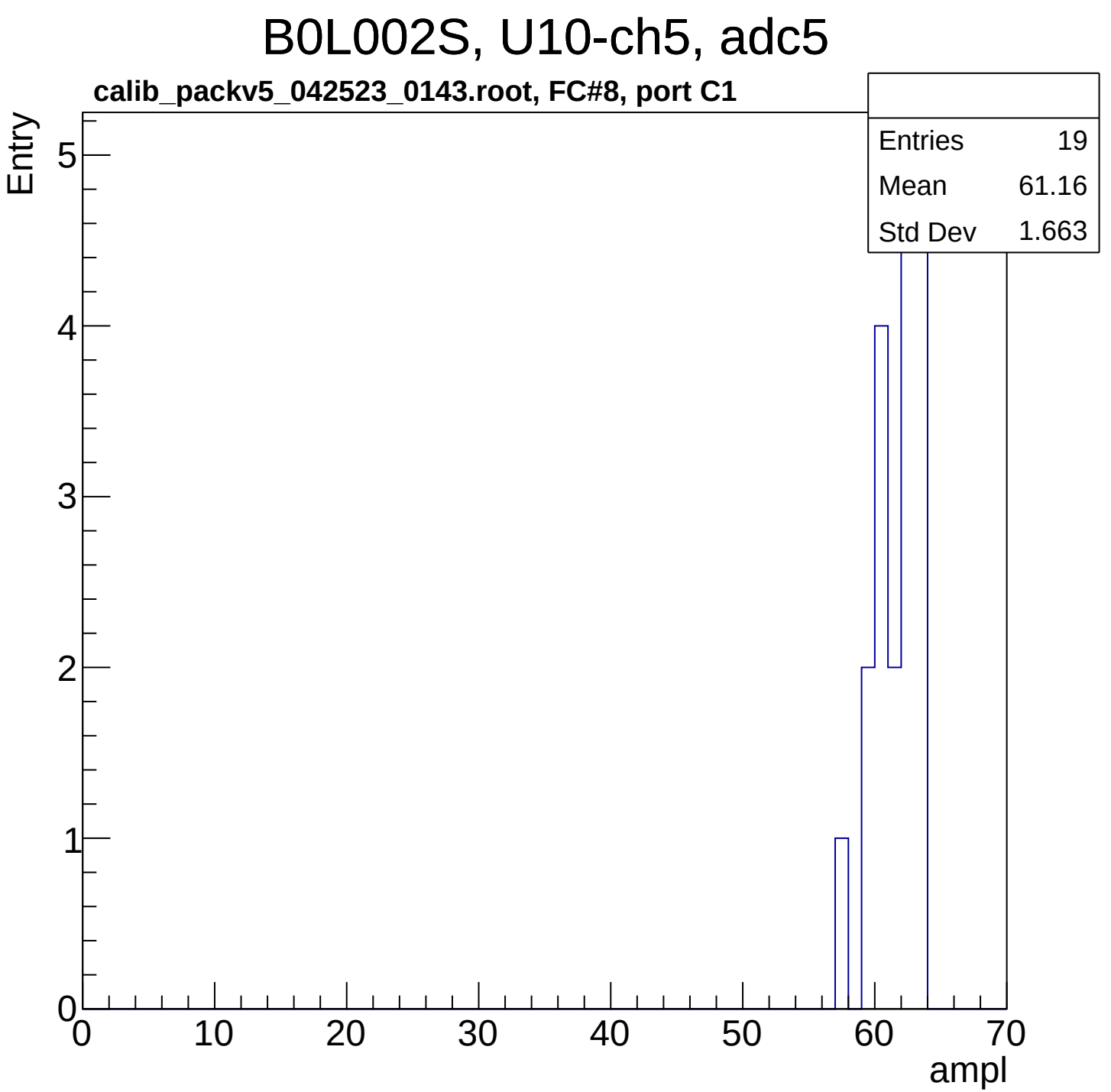

B0L002S, U10-ch5, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.43
Std Dev 2.713
Gaus mean : 30.7594
Gaus Width: 3.2217
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

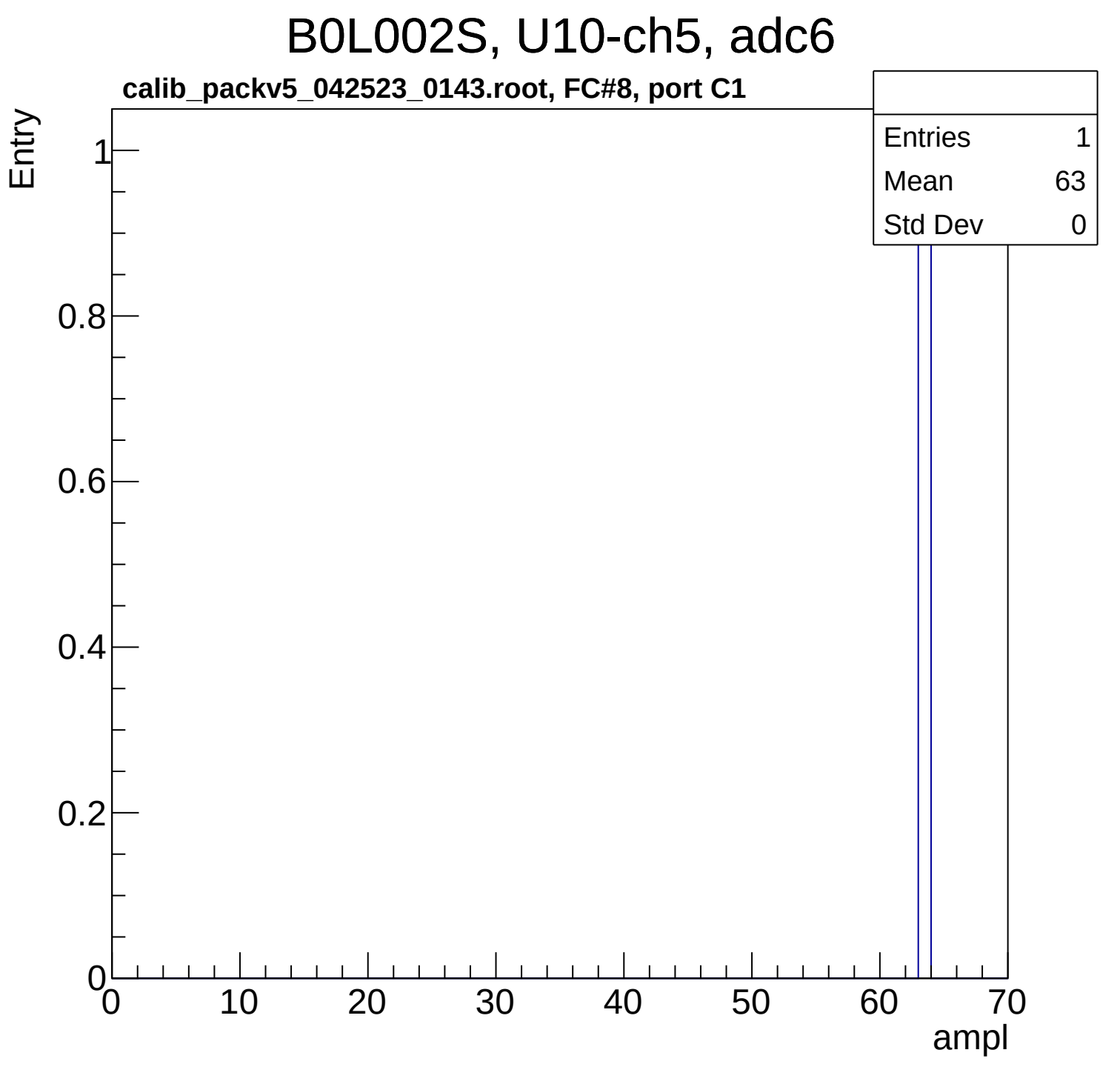

B0L002S, U10-ch5, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.01
Std Dev 3.305
Gaus mean : 38.0798
Gaus Width: 3.4604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

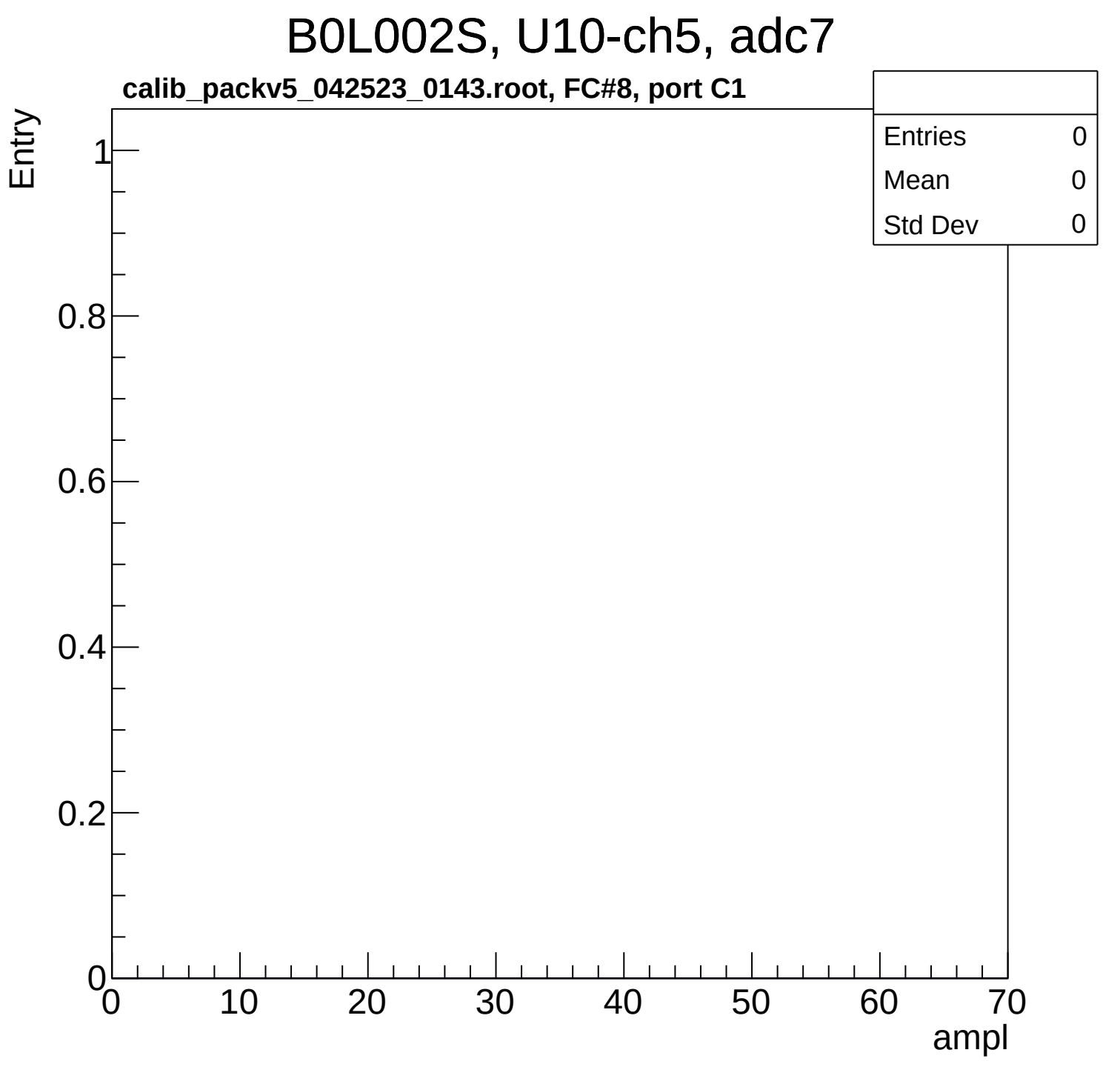

B0L002S, U10-ch5, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 44.58
Std Dev 3.429
Gaus mean : 44.7052
Gaus Width: 3.0969
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

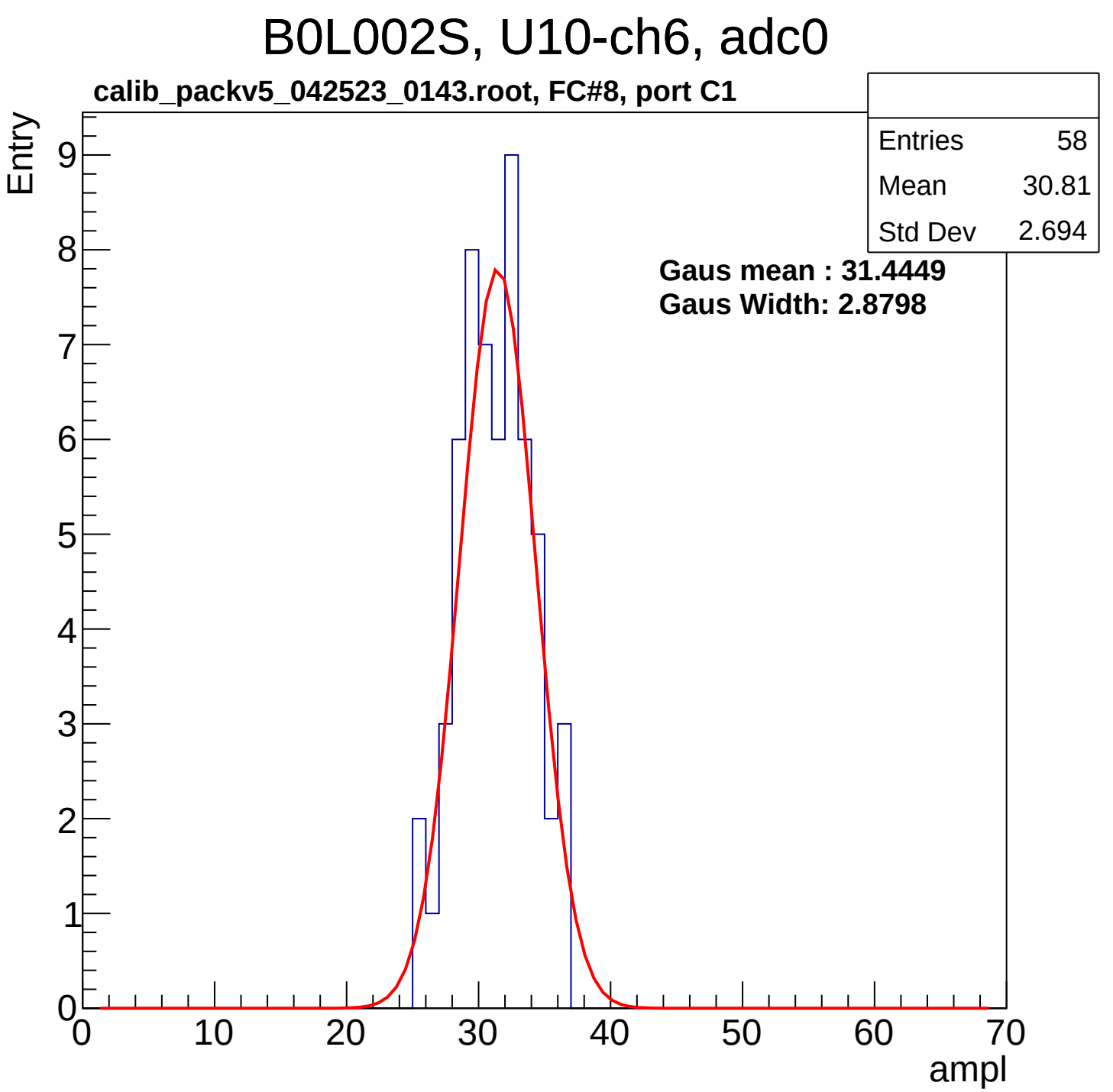

B0L002S, U10-ch5, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 51.61
Std Dev 3.147
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

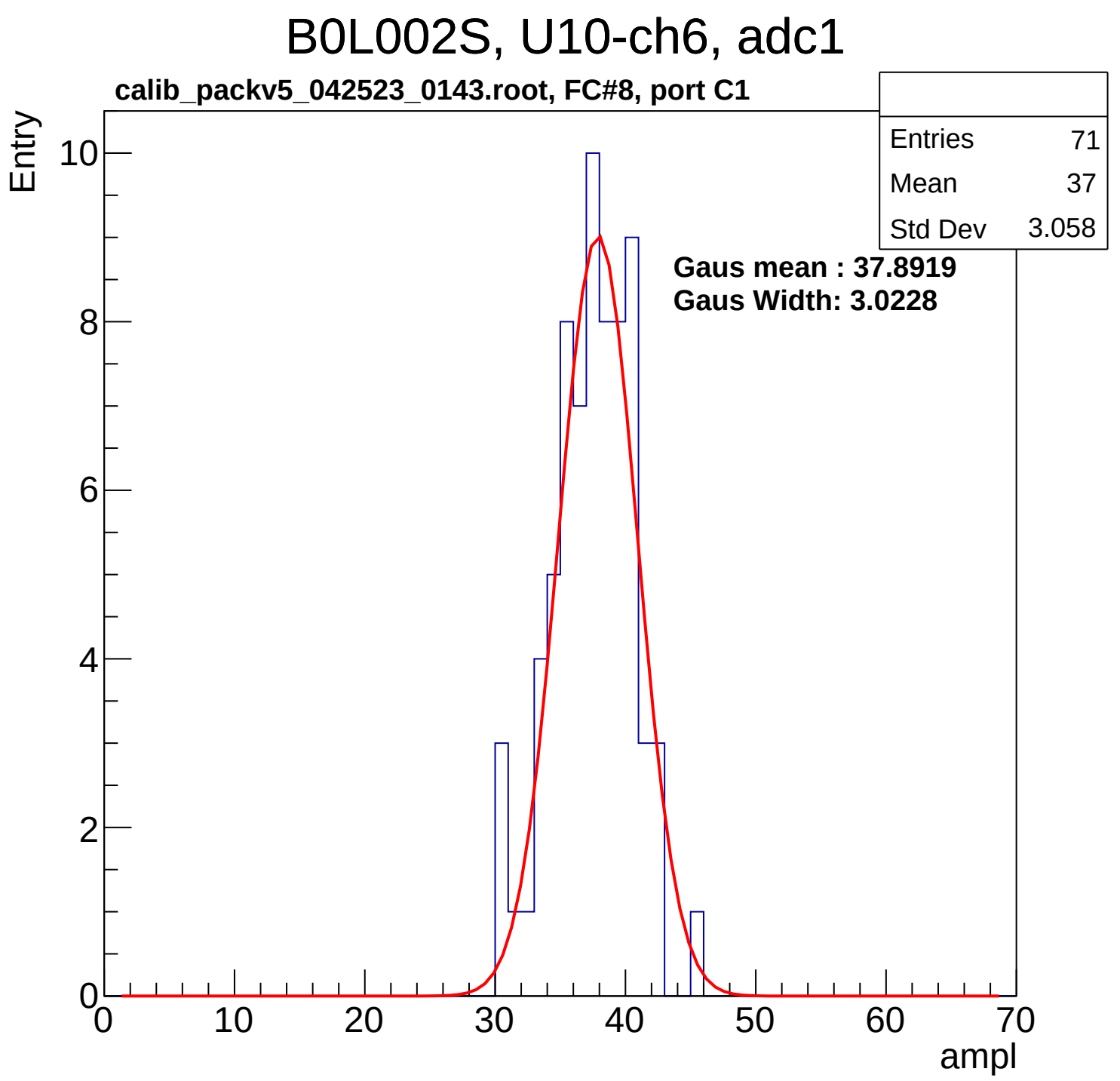

B0L002S, U10-ch5, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.55
Std Dev 7.843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

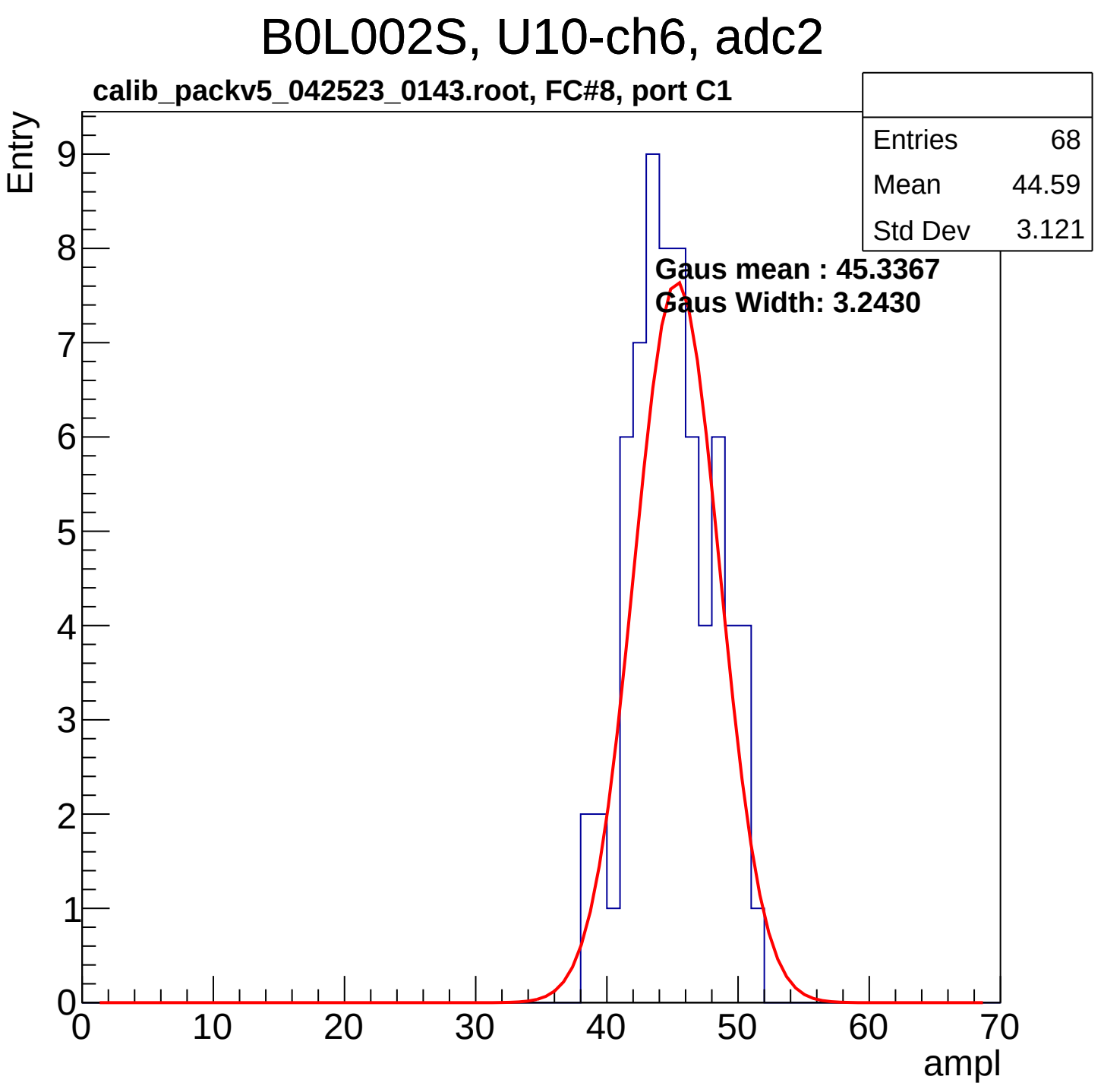

B0L002S, U10-ch5, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.16
Std Dev 1.663
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

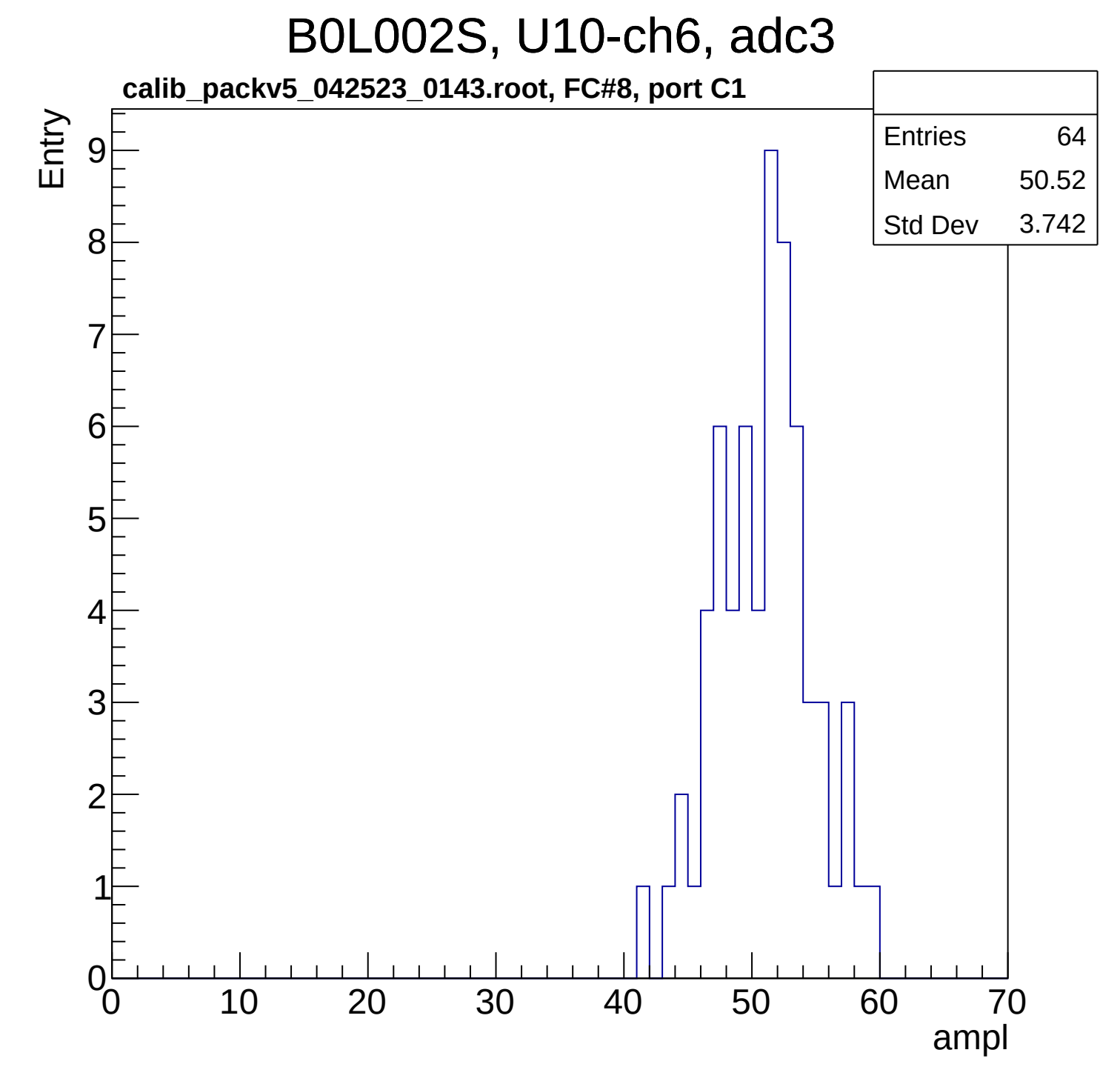

B0L002S, U10-ch5, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

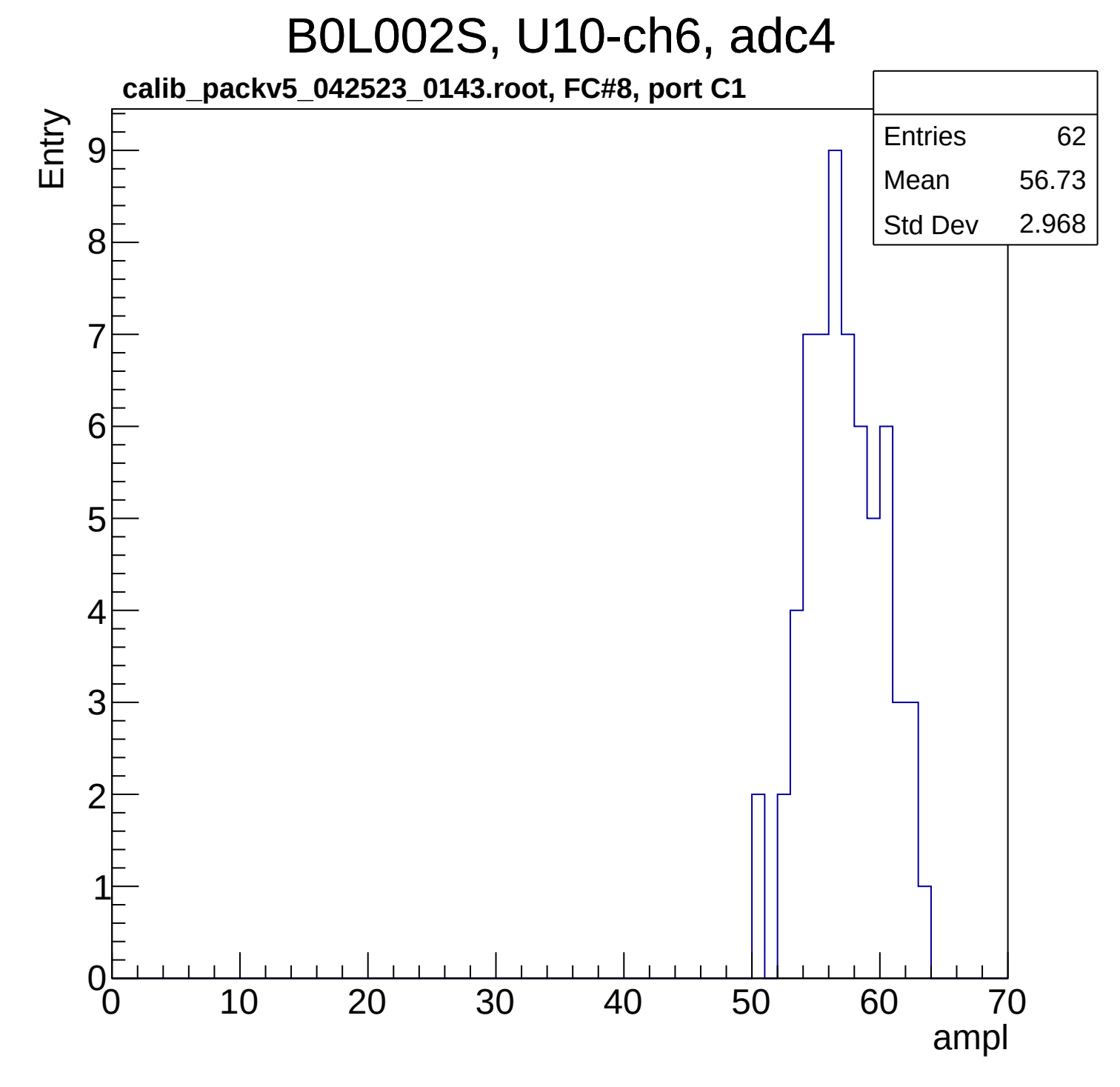

B0L002S, U10-ch5, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

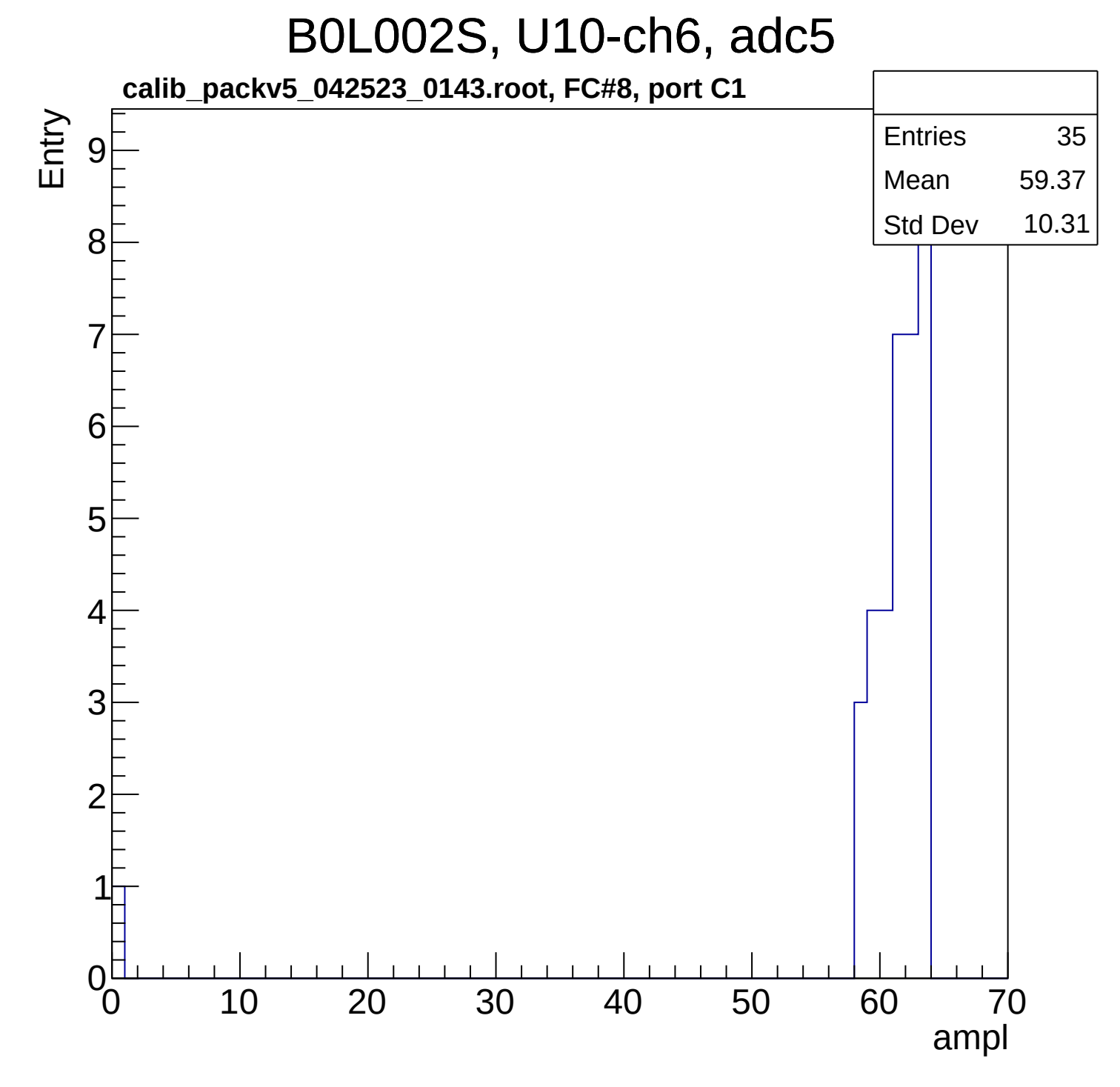

B0L002S, U10-ch6, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.81
Std Dev 2.694
Gaus mean : 31.4449
Gaus Width: 2.8798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

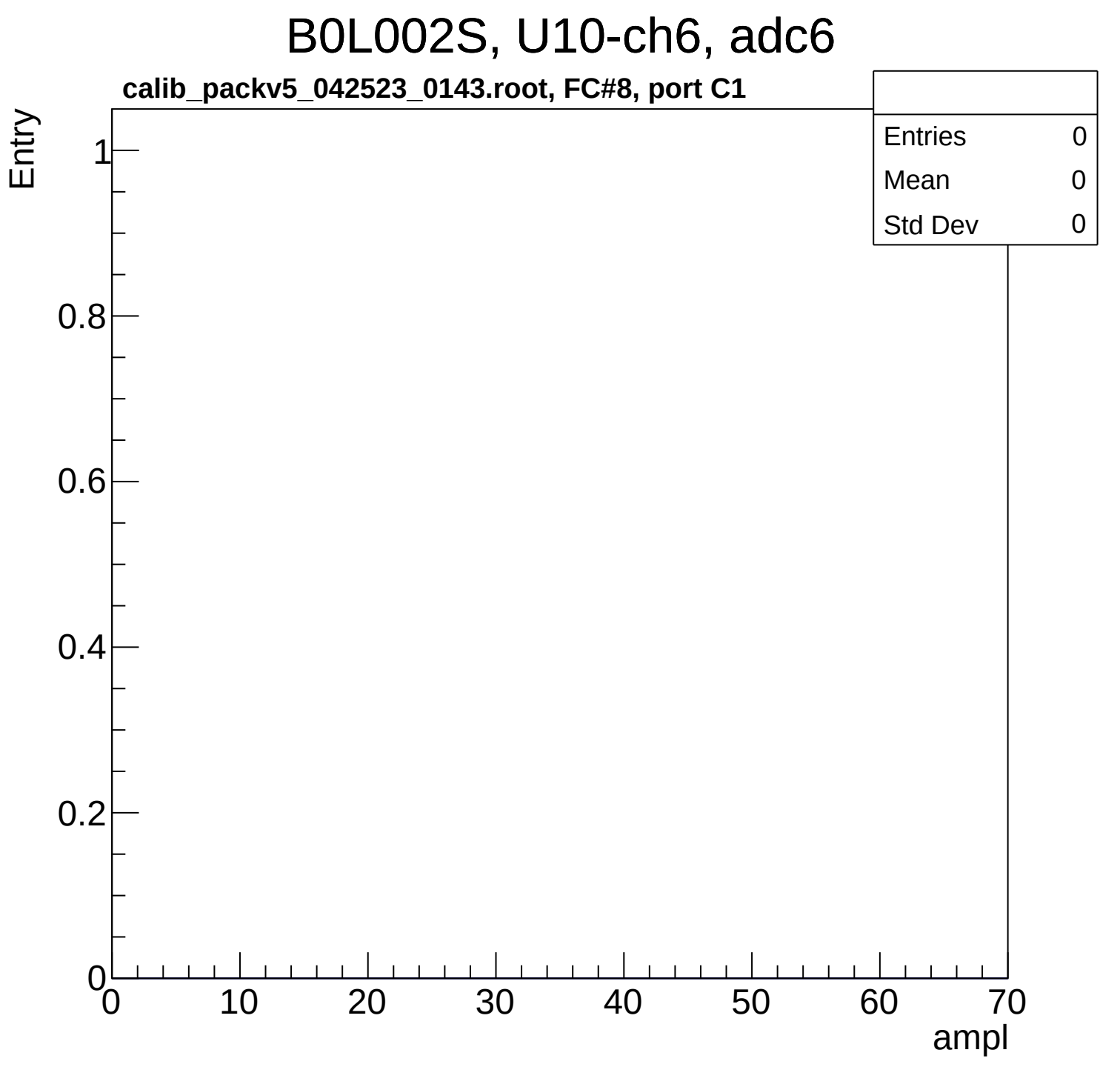

B0L002S, U10-ch6, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37
Std Dev 3.058
Gaus mean : 37.8919
Gaus Width: 3.0228
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

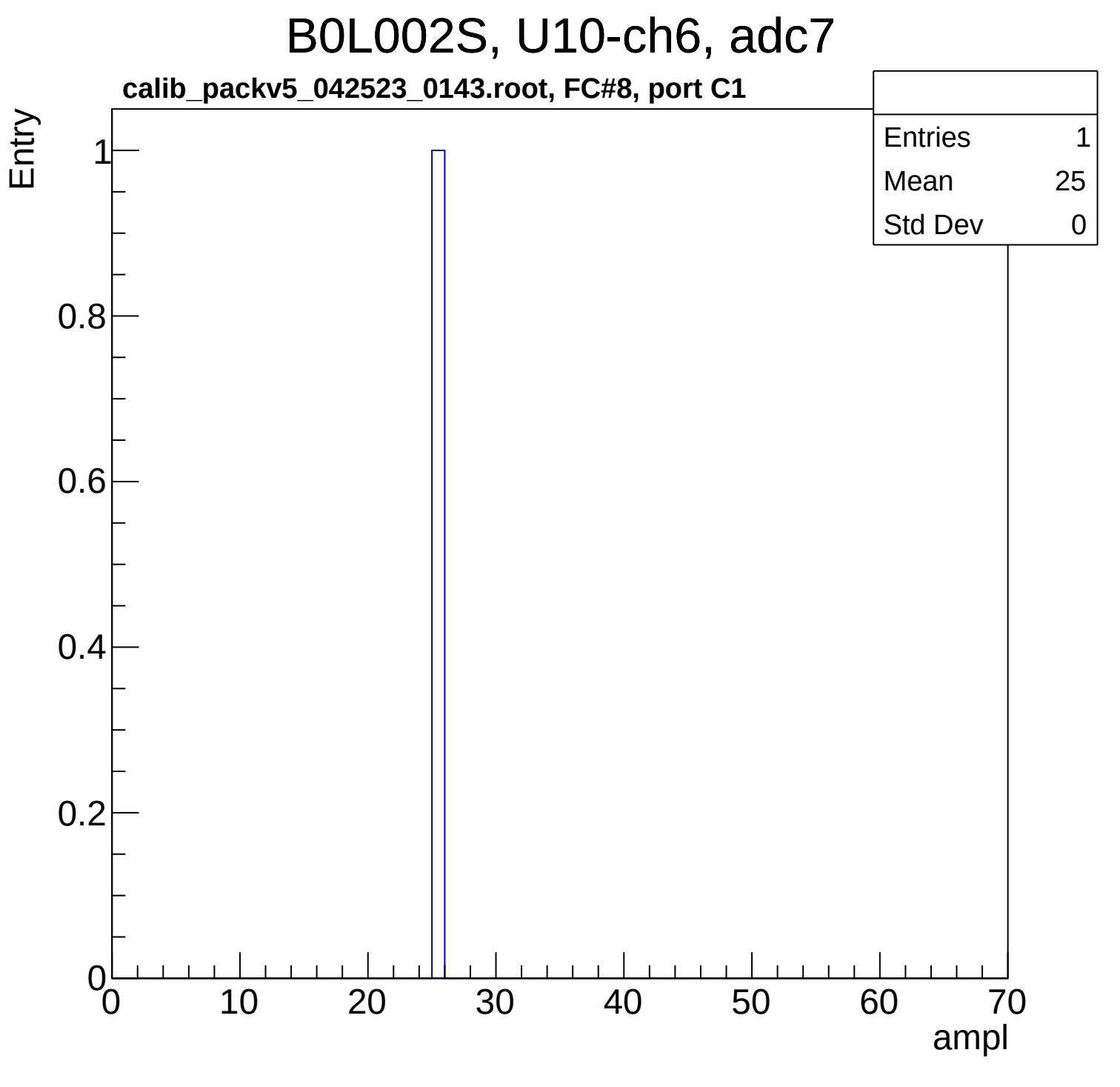

B0L002S, U10-ch6, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.59
Std Dev 3.121
Gaus mean : 45.3367
Gaus Width: 3.2430
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

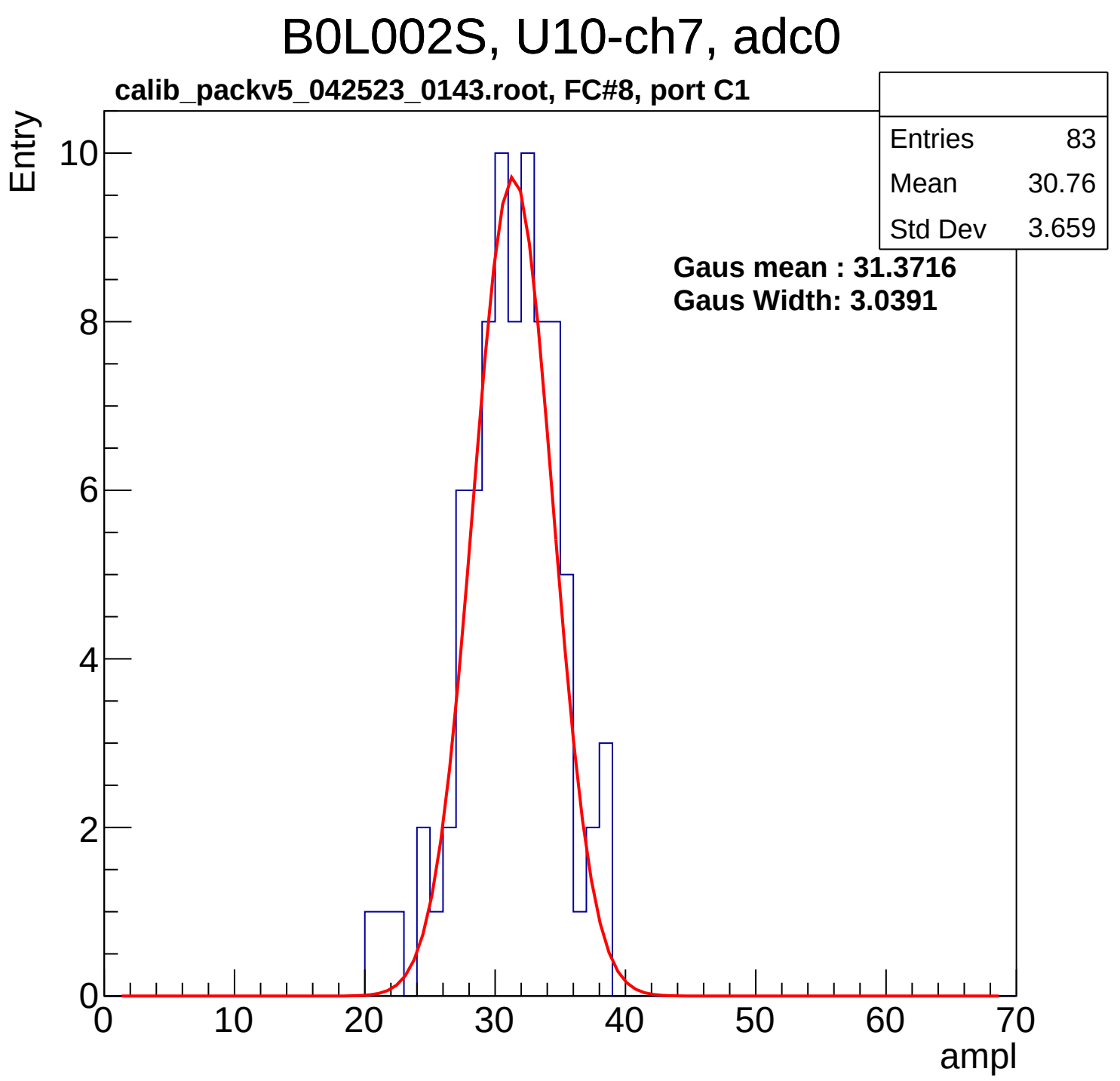

B0L002S, U10-ch6, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.52
Std Dev 3.742
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

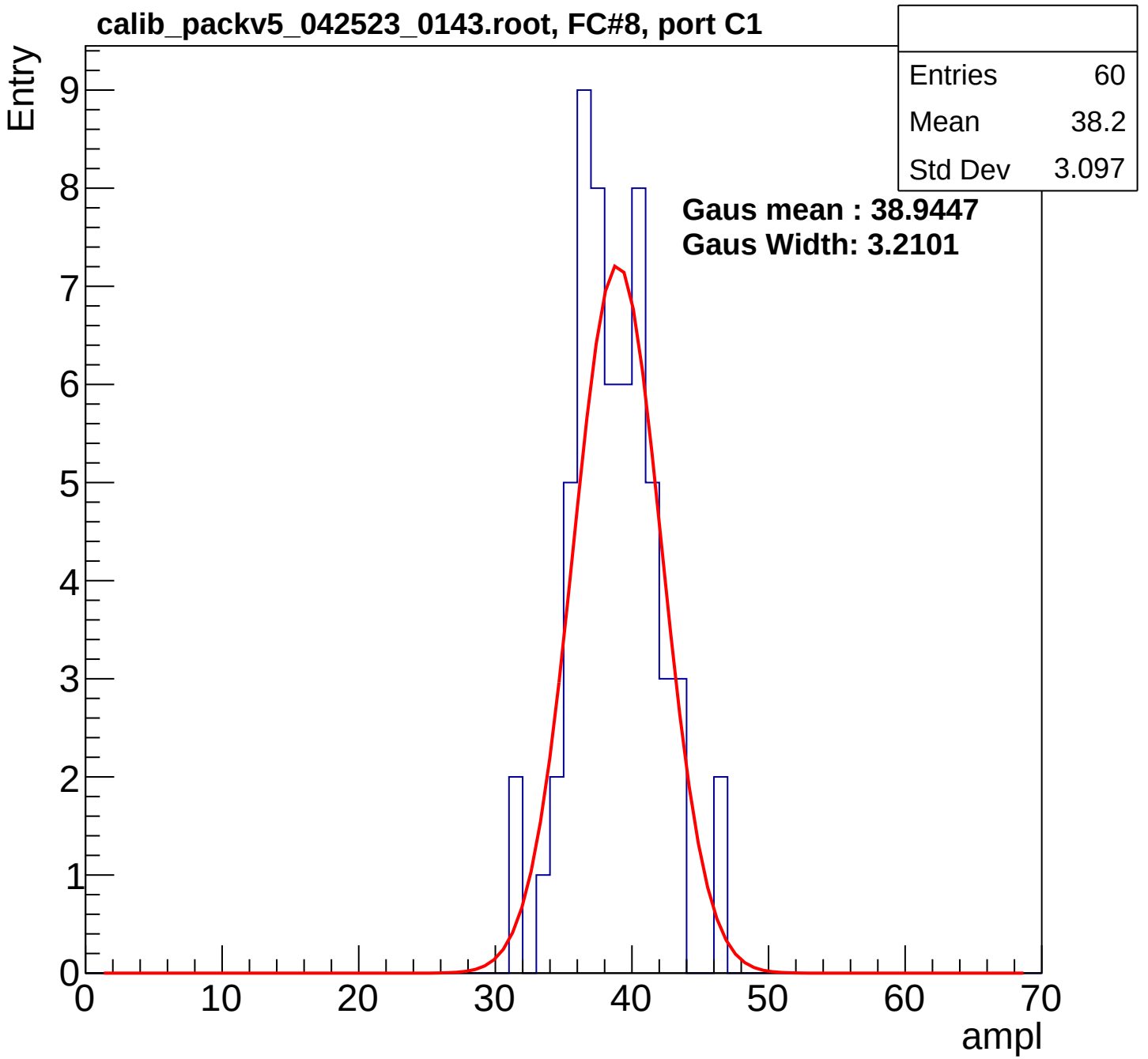

B0L002S, U10-ch6, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 56.73
Std Dev 2.968
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

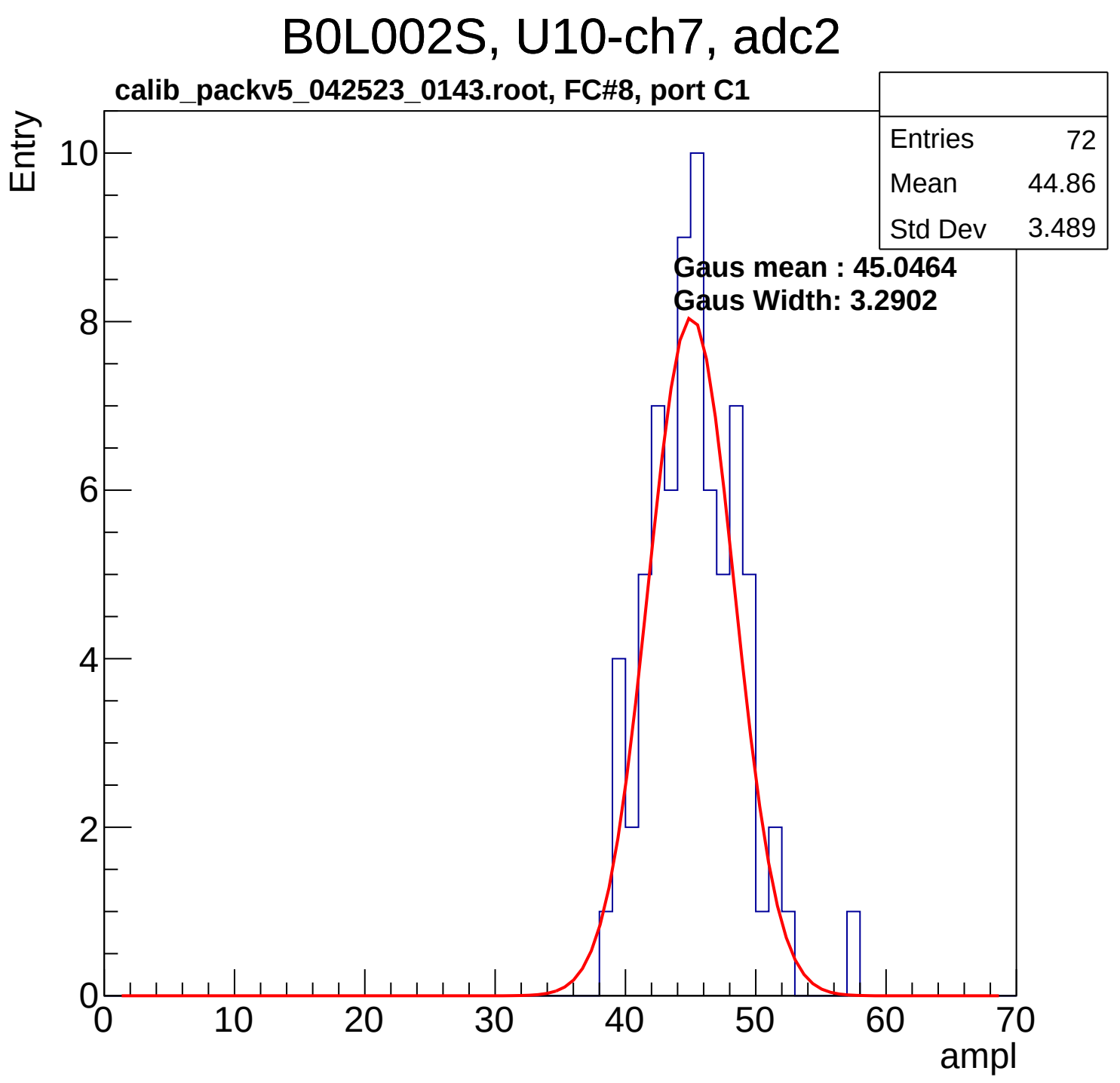

B0L002S, U10-ch6, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.37
Std Dev 10.31
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

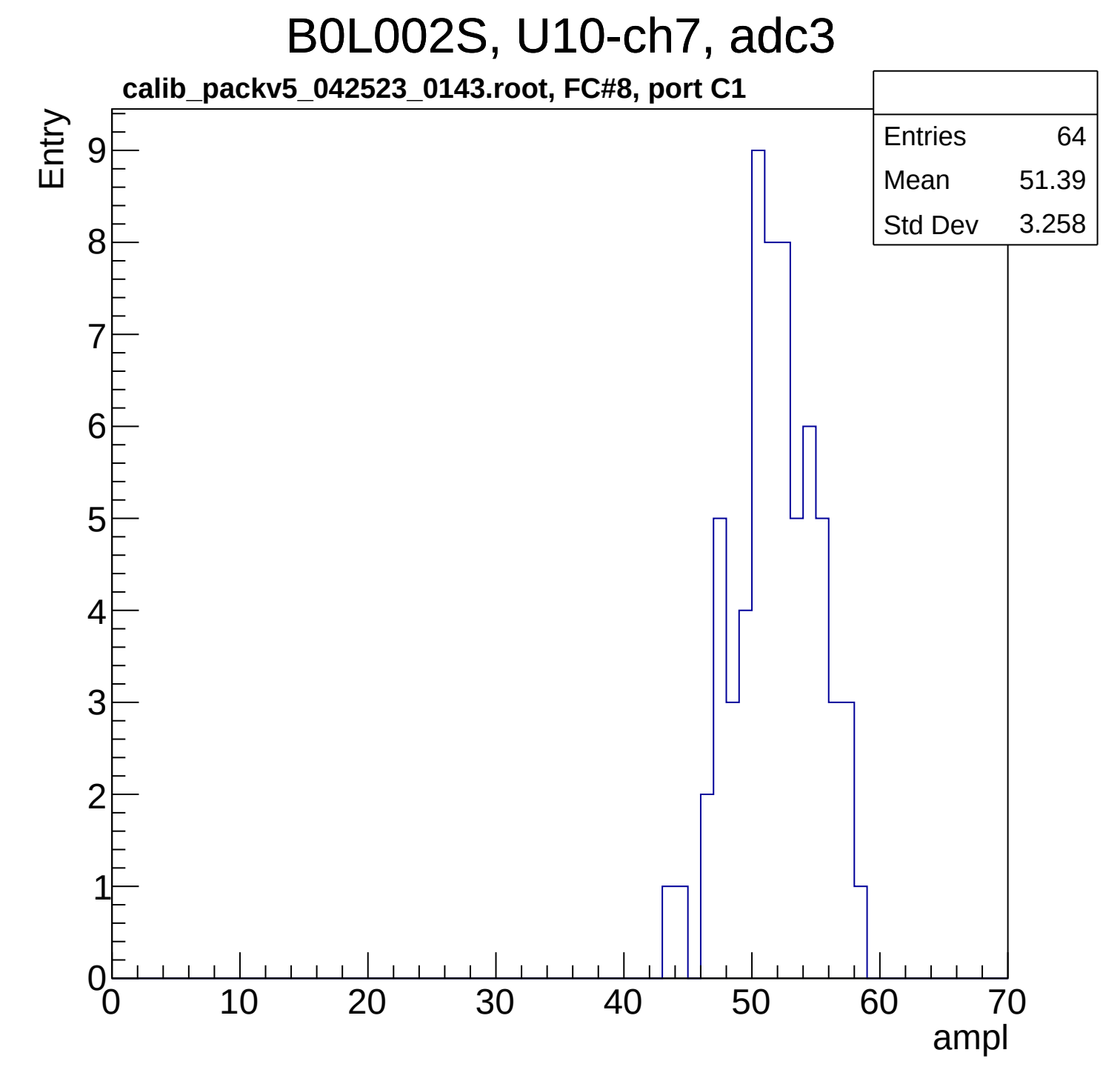

B0L002S, U10-ch6, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

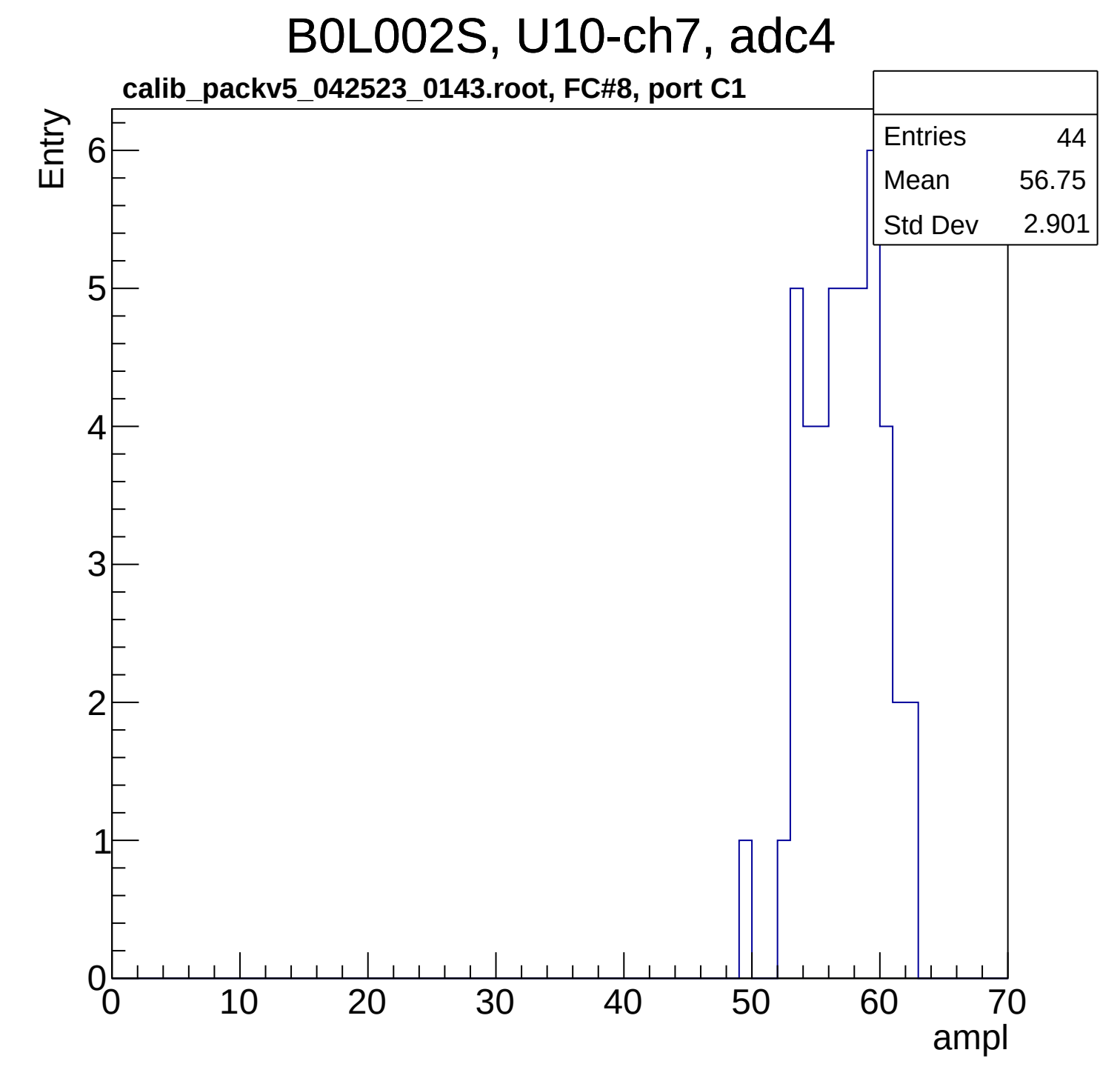

B0L002S, U10-ch6, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

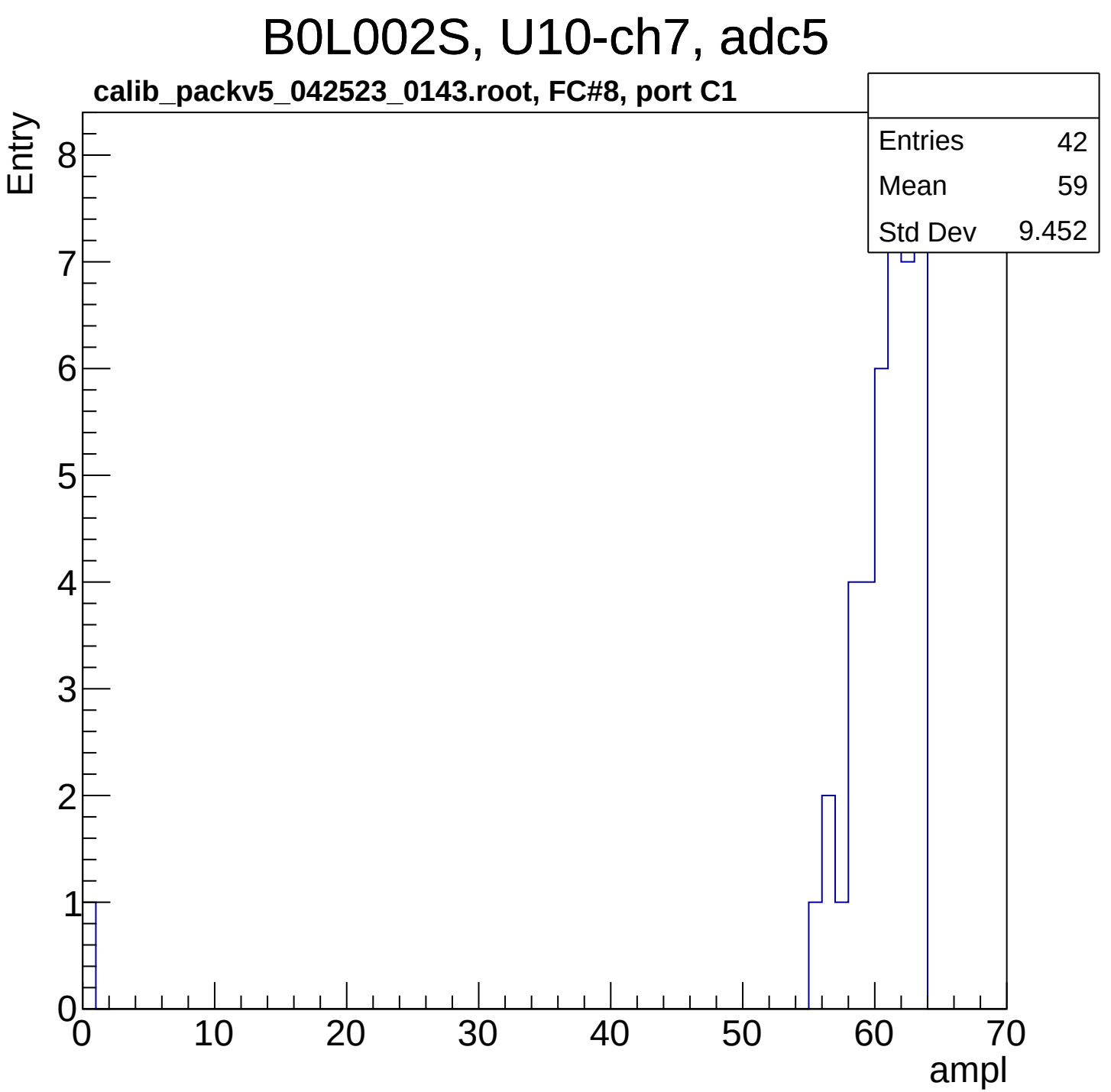

B0L002S, U10-ch7, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 30.76
Std Dev 3.659
Gaus mean : 31.3716
Gaus Width: 3.0391
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

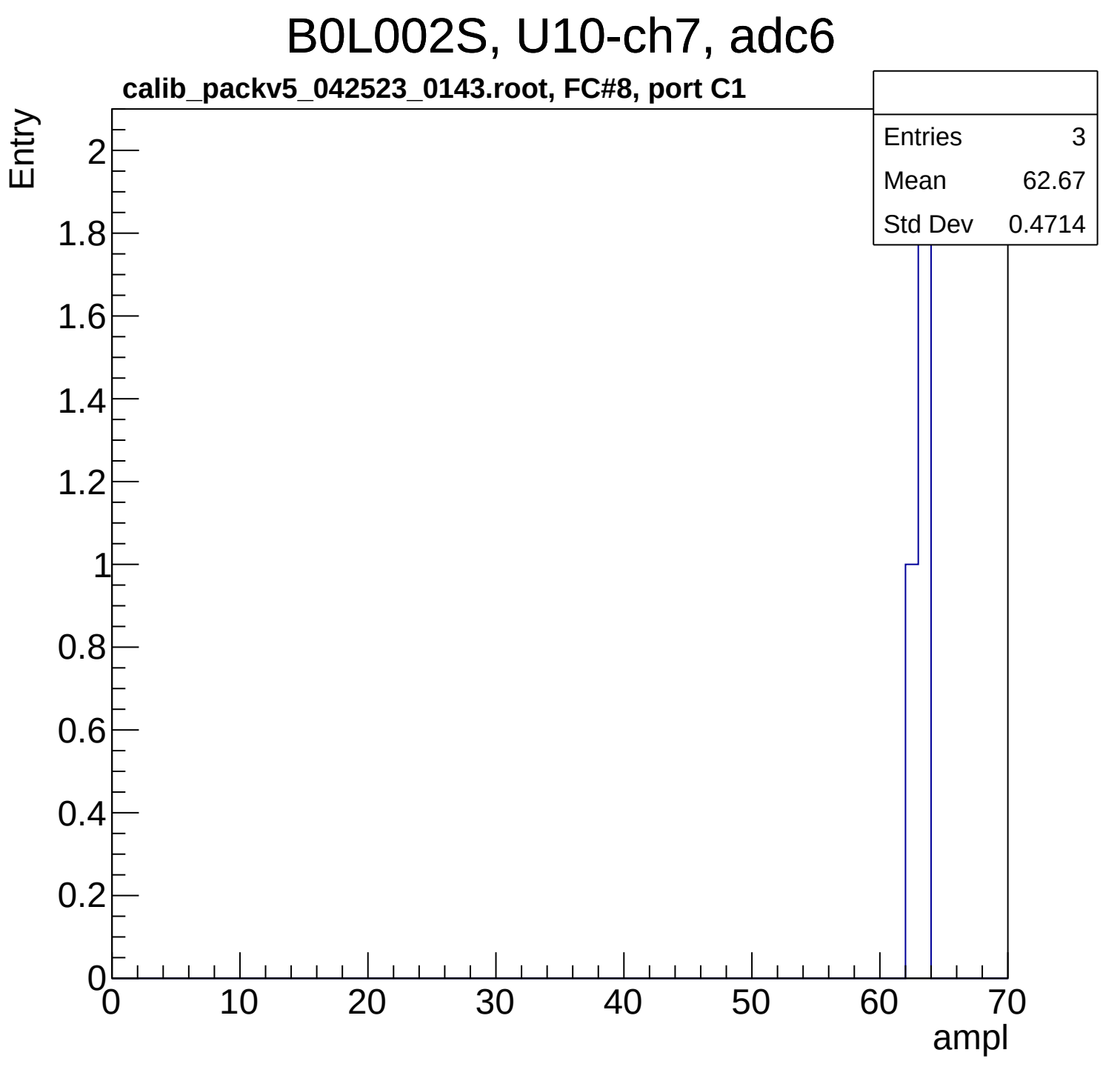

B0L002S, U10-ch7, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.2
Std Dev 3.097
Gaus mean : 38.9447
Gaus Width: 3.2101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

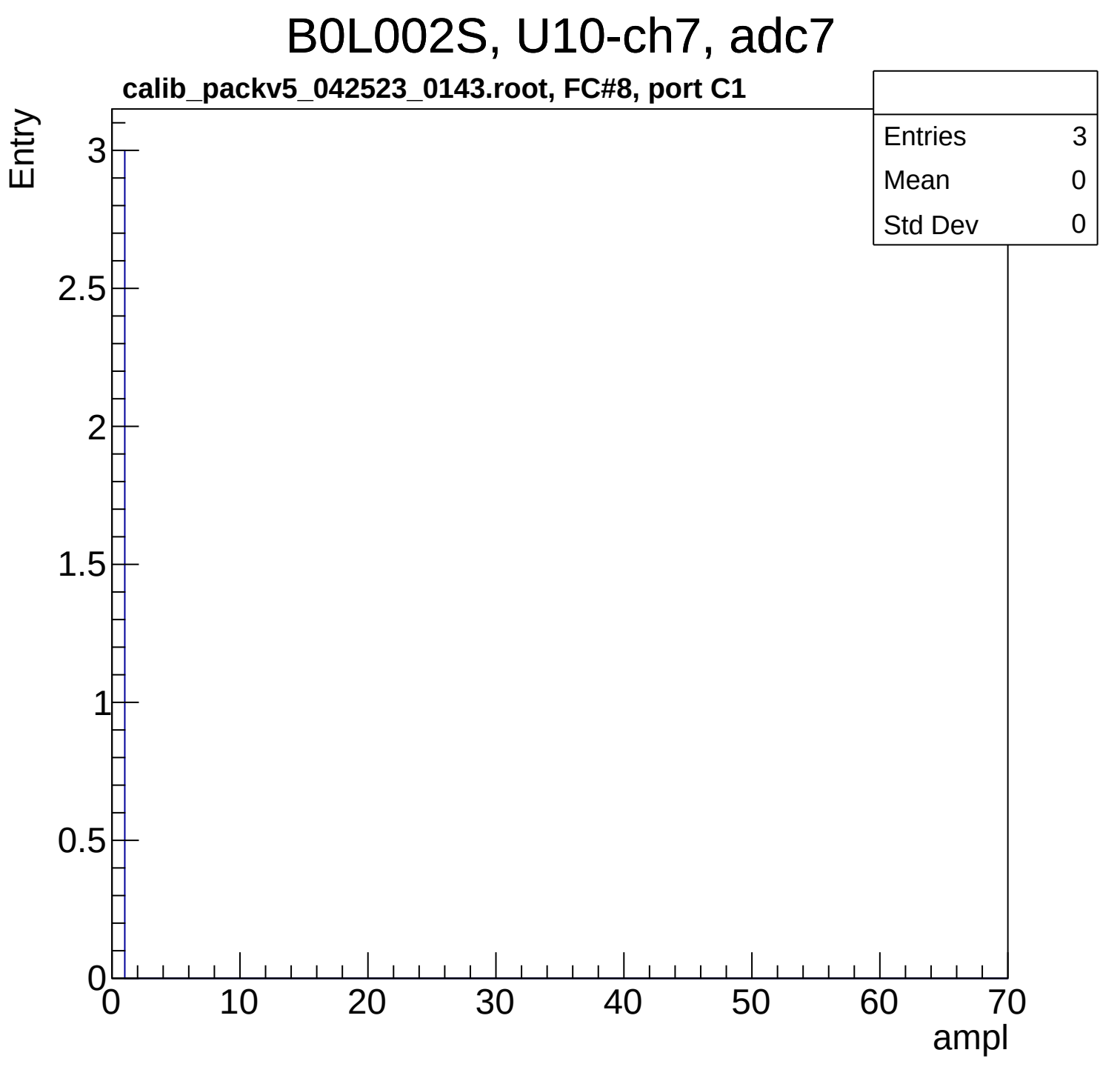

B0L002S, U10-ch7, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.86
Std Dev 3.489
Gaus mean : 45.0464
Gaus Width: 3.2902
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

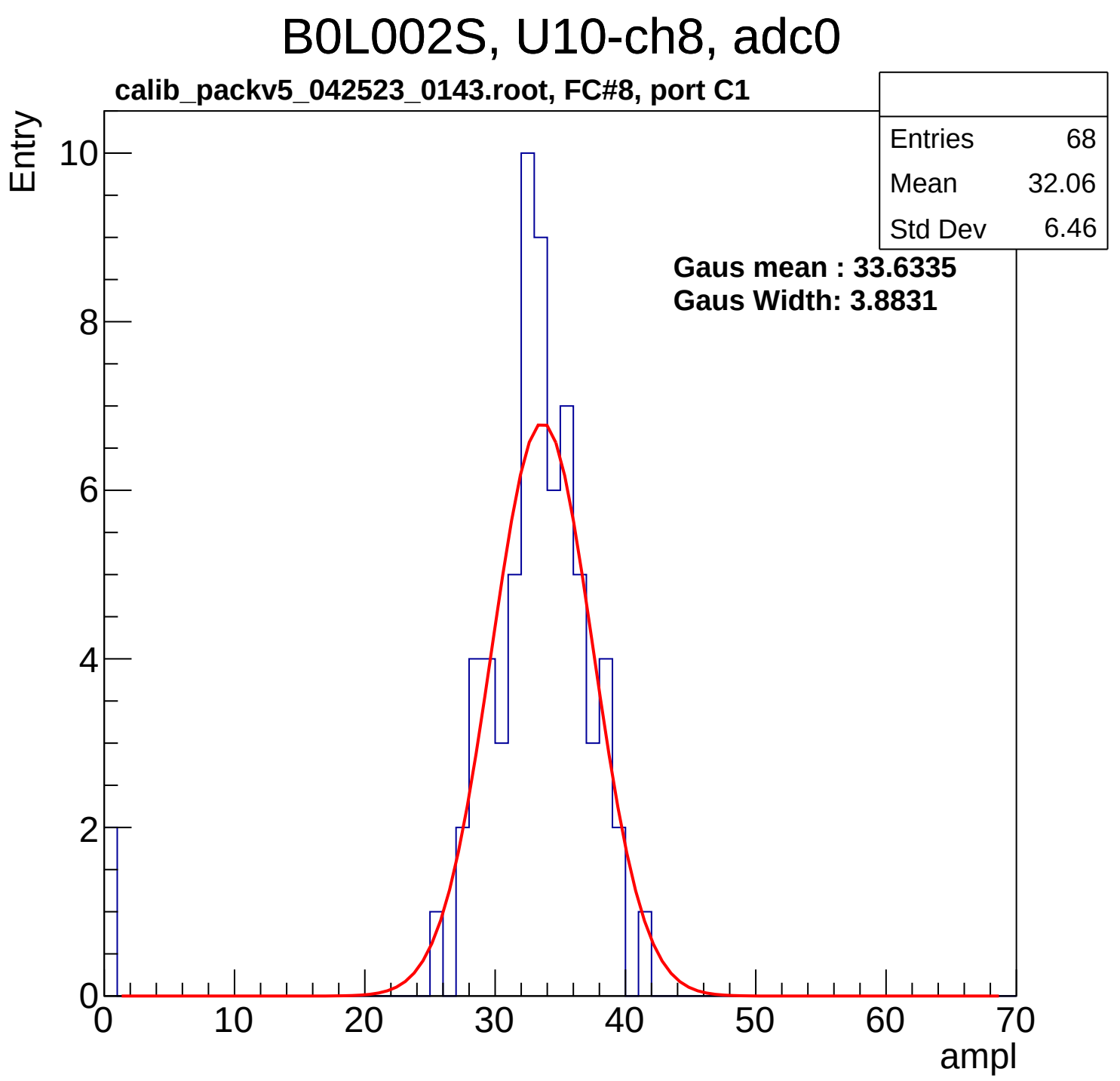

B0L002S, U10-ch7, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.39
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

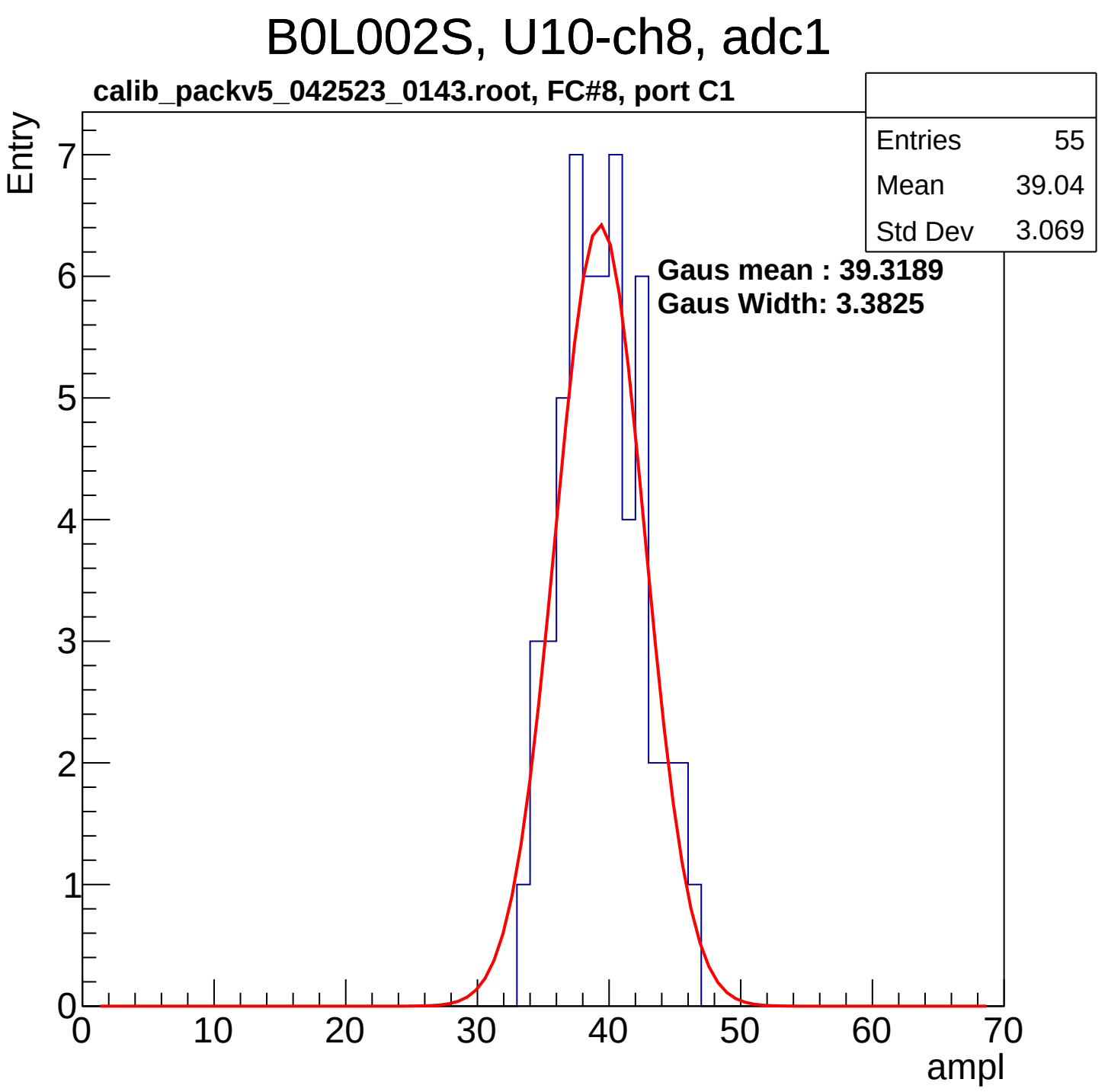

B0L002S, U10-ch7, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 56.75
Std Dev 2.901
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

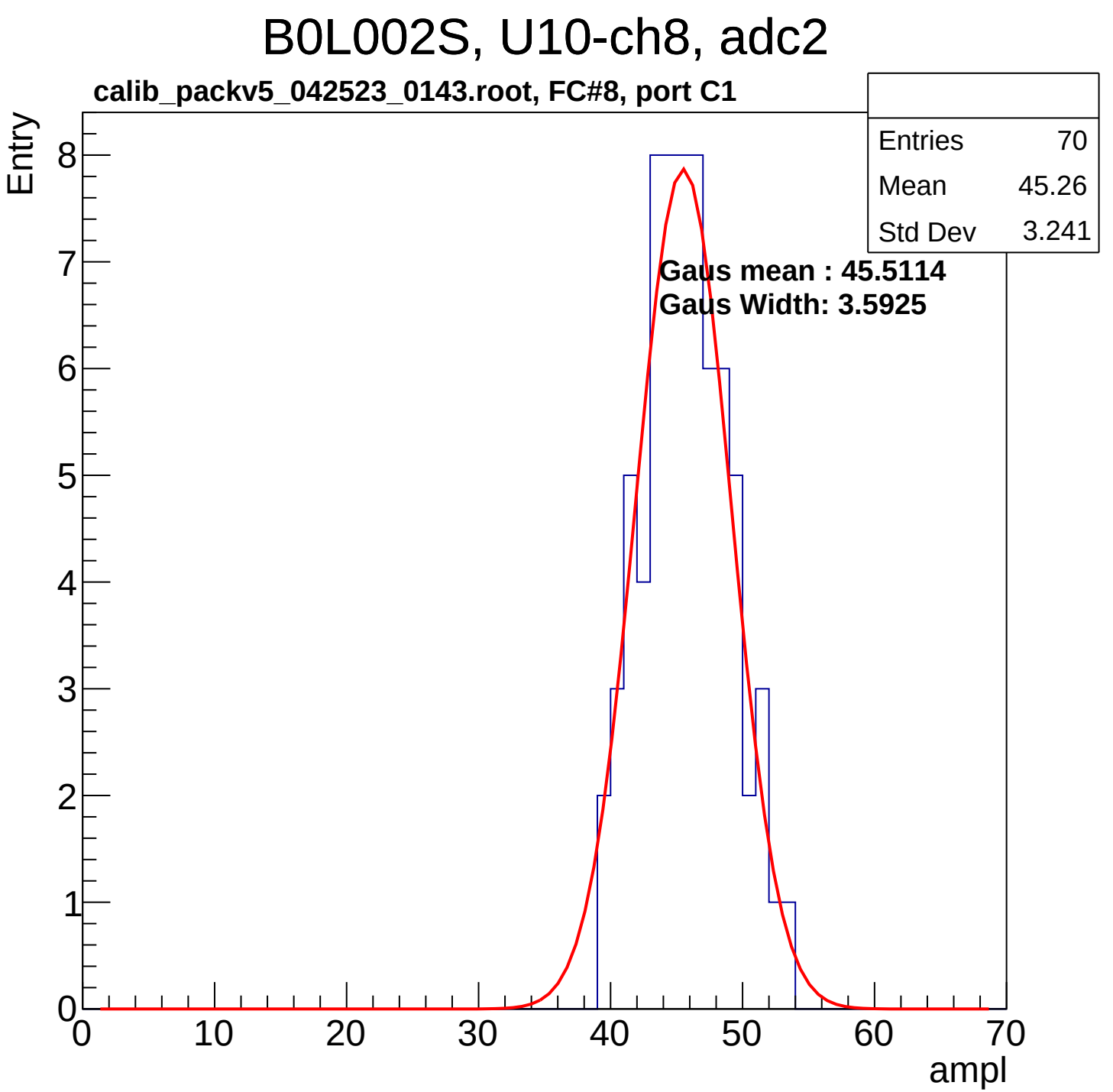

B0L002S, U10-ch7, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59
Std Dev 9.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

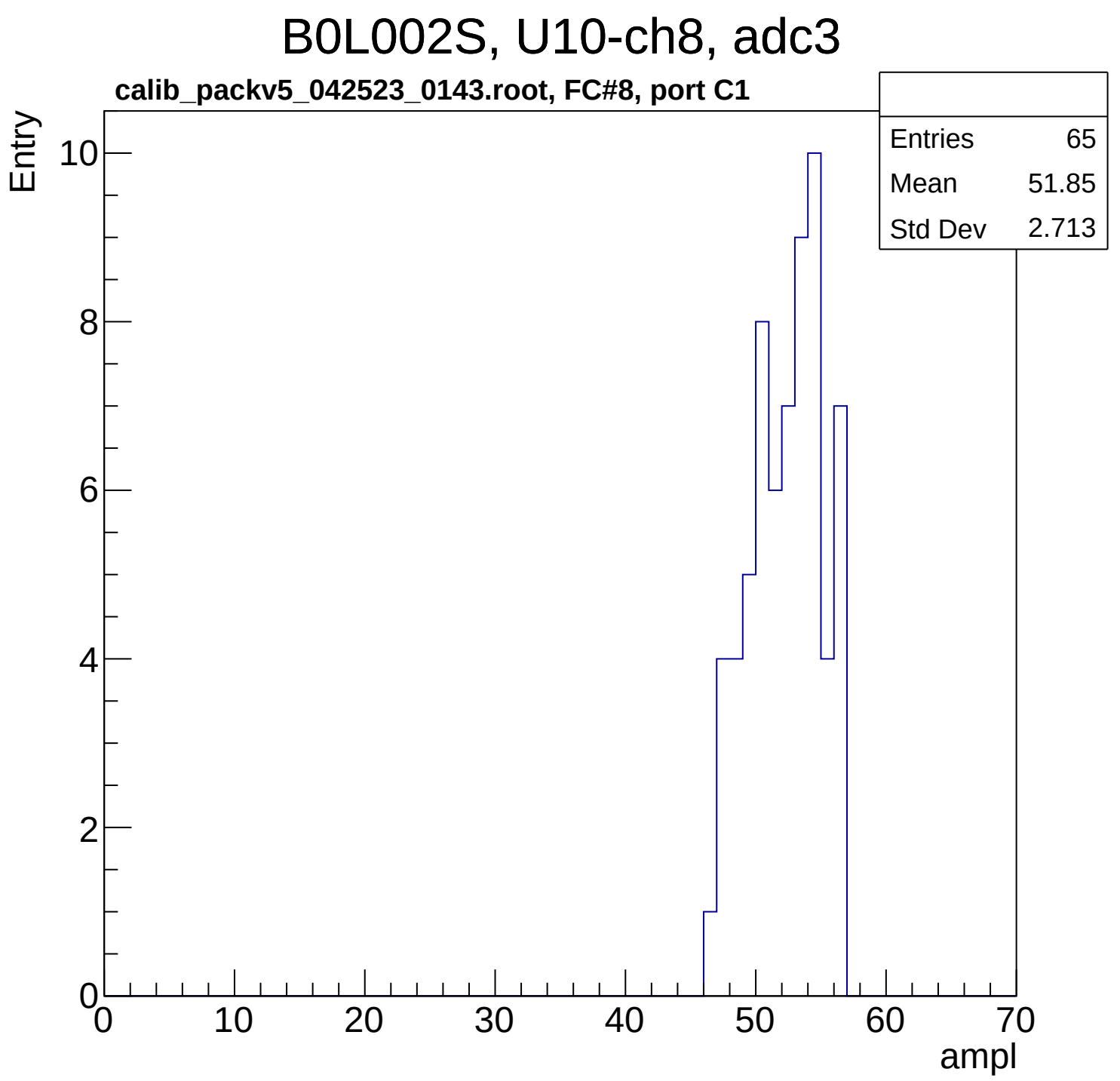

B0L002S, U10-ch7, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

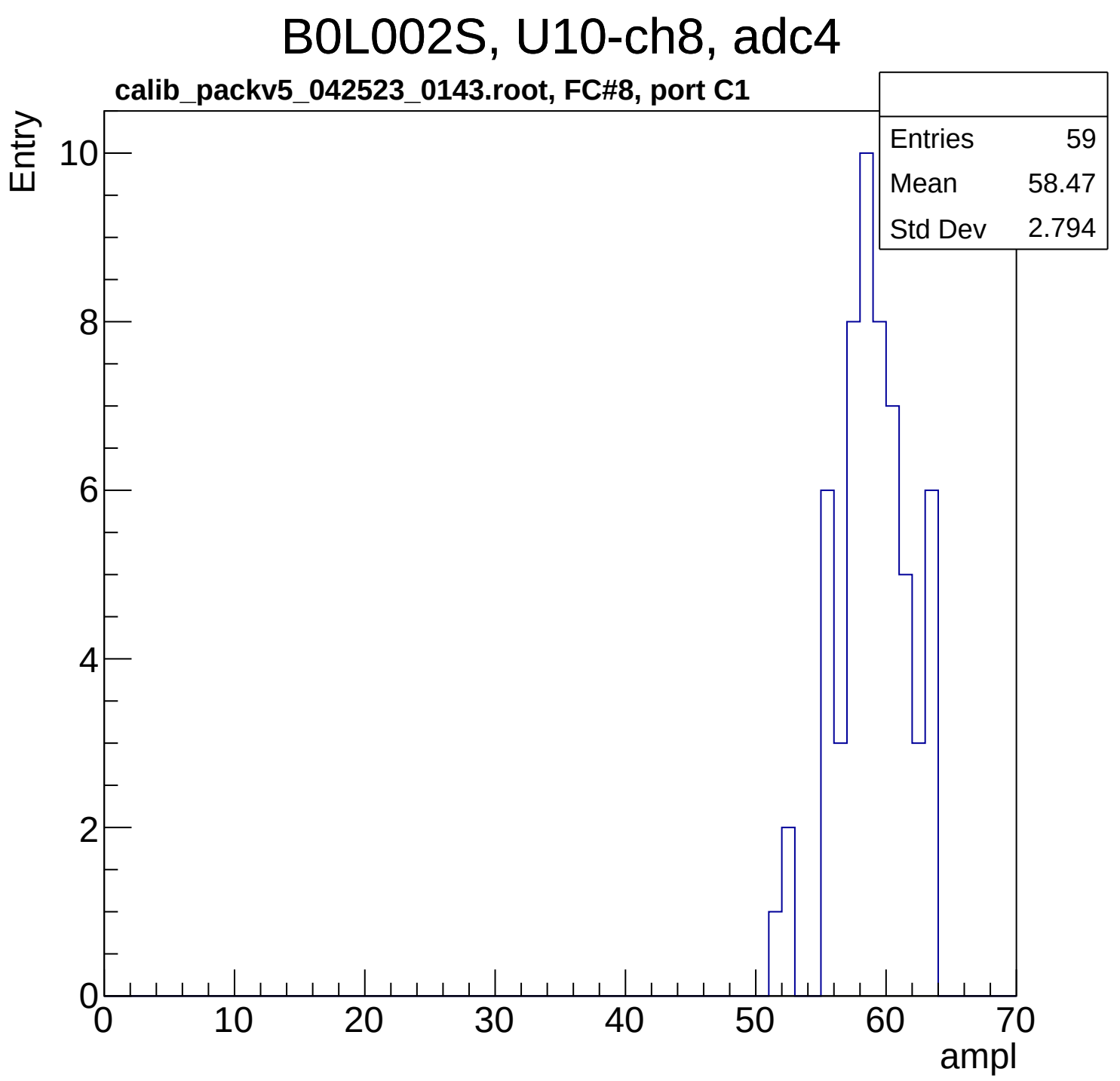

B0L002S, U10-ch7, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

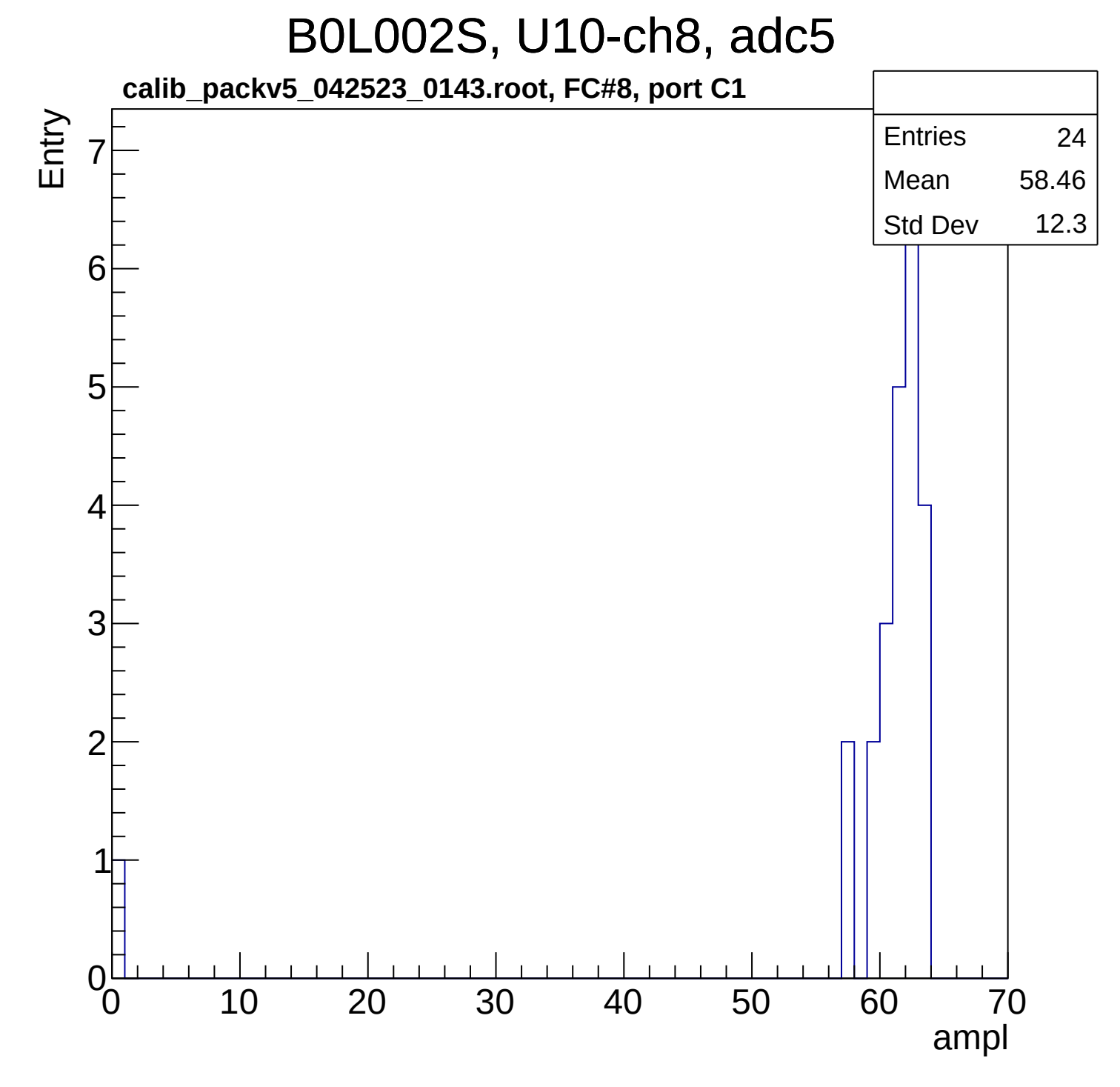

B0L002S, U10-ch8, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 32.06
Std Dev 6.46
Gaus mean : 33.6335
Gaus Width: 3.8831
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

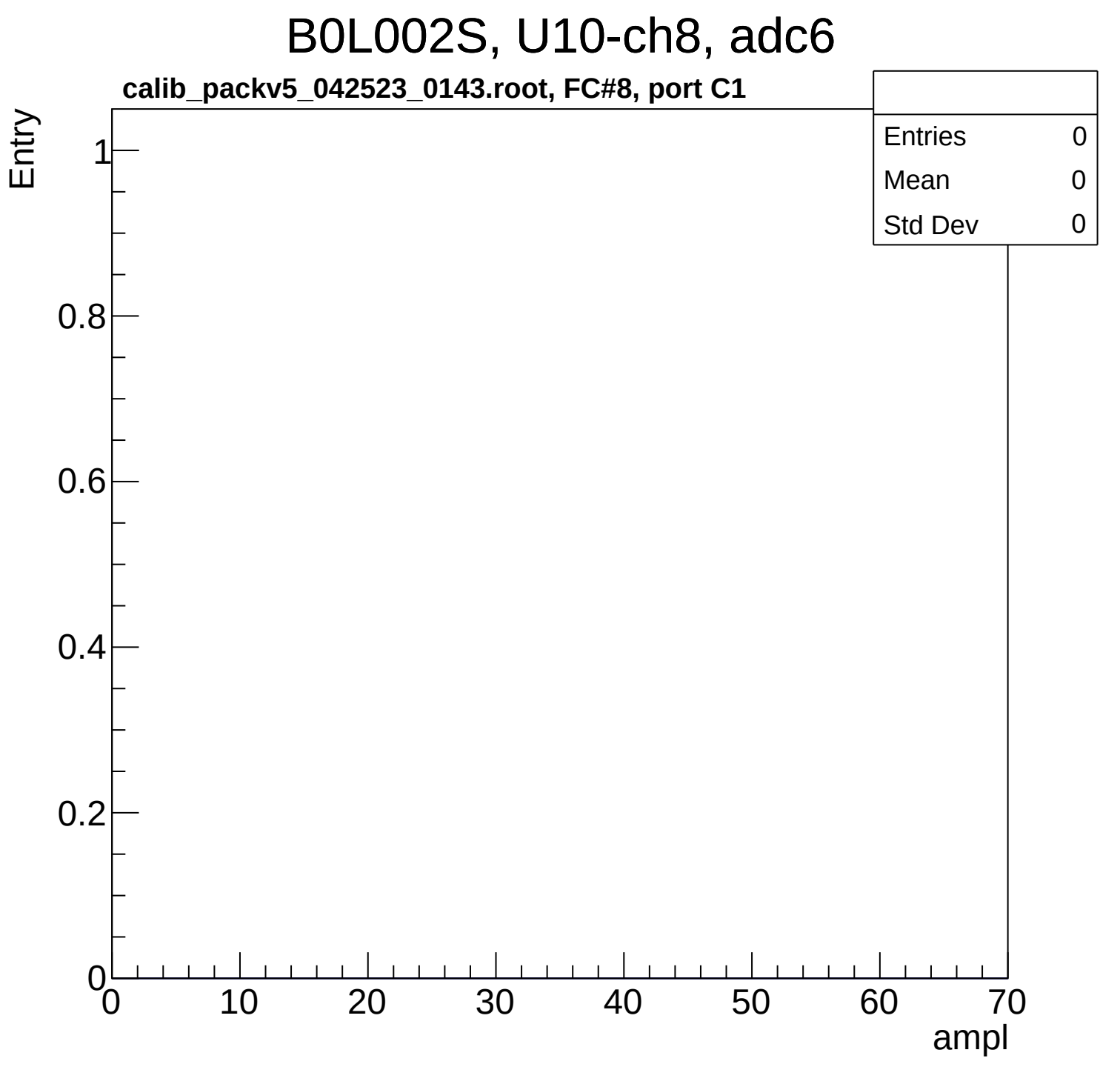

B0L002S, U10-ch8, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 39.04
Std Dev 3.069
Gaus mean : 39.3189
Gaus Width: 3.3825
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

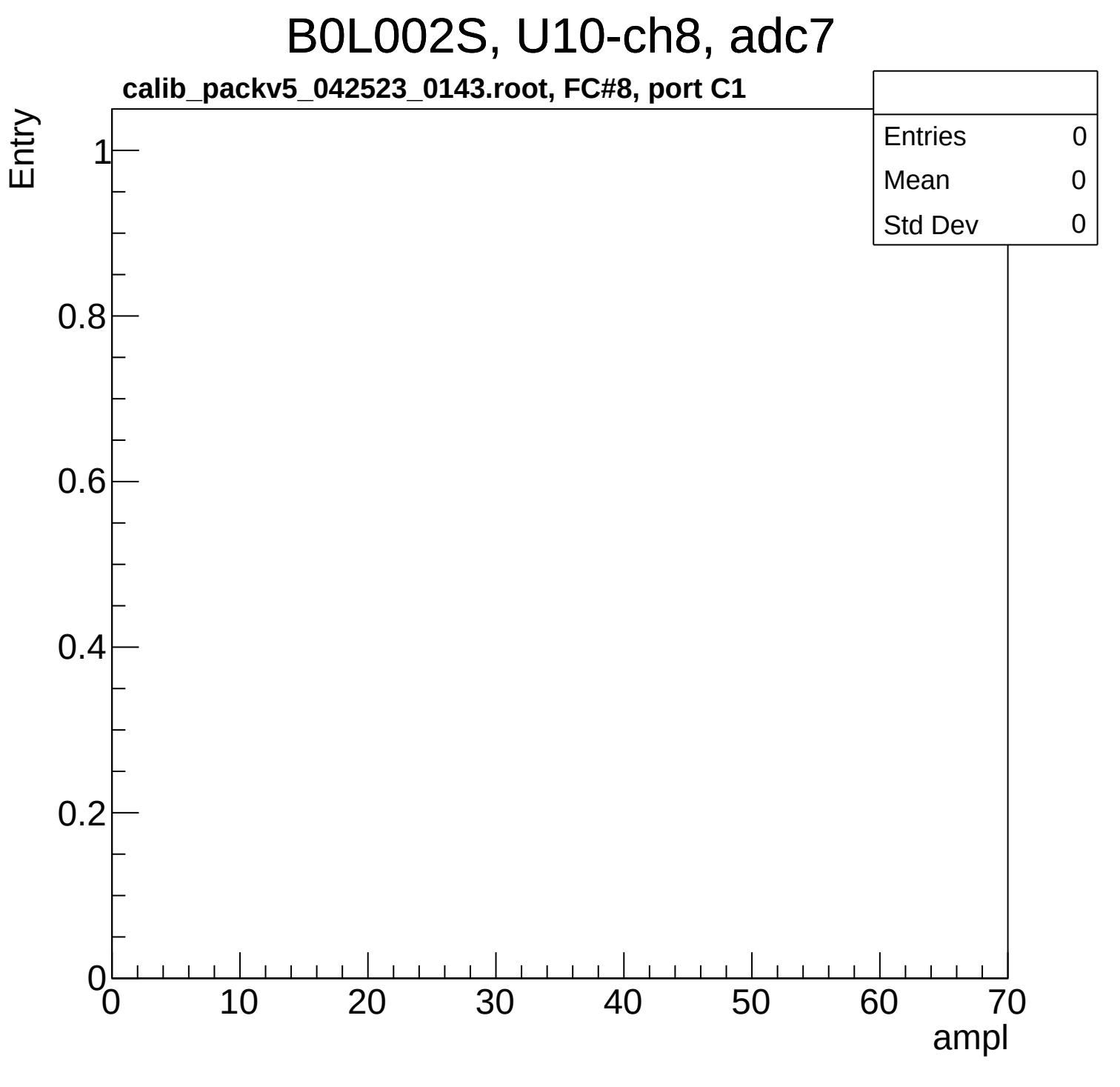

B0L002S, U10-ch8, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 45.26
Std Dev 3.241
Gaus mean : 45.5114
Gaus Width: 3.5925
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

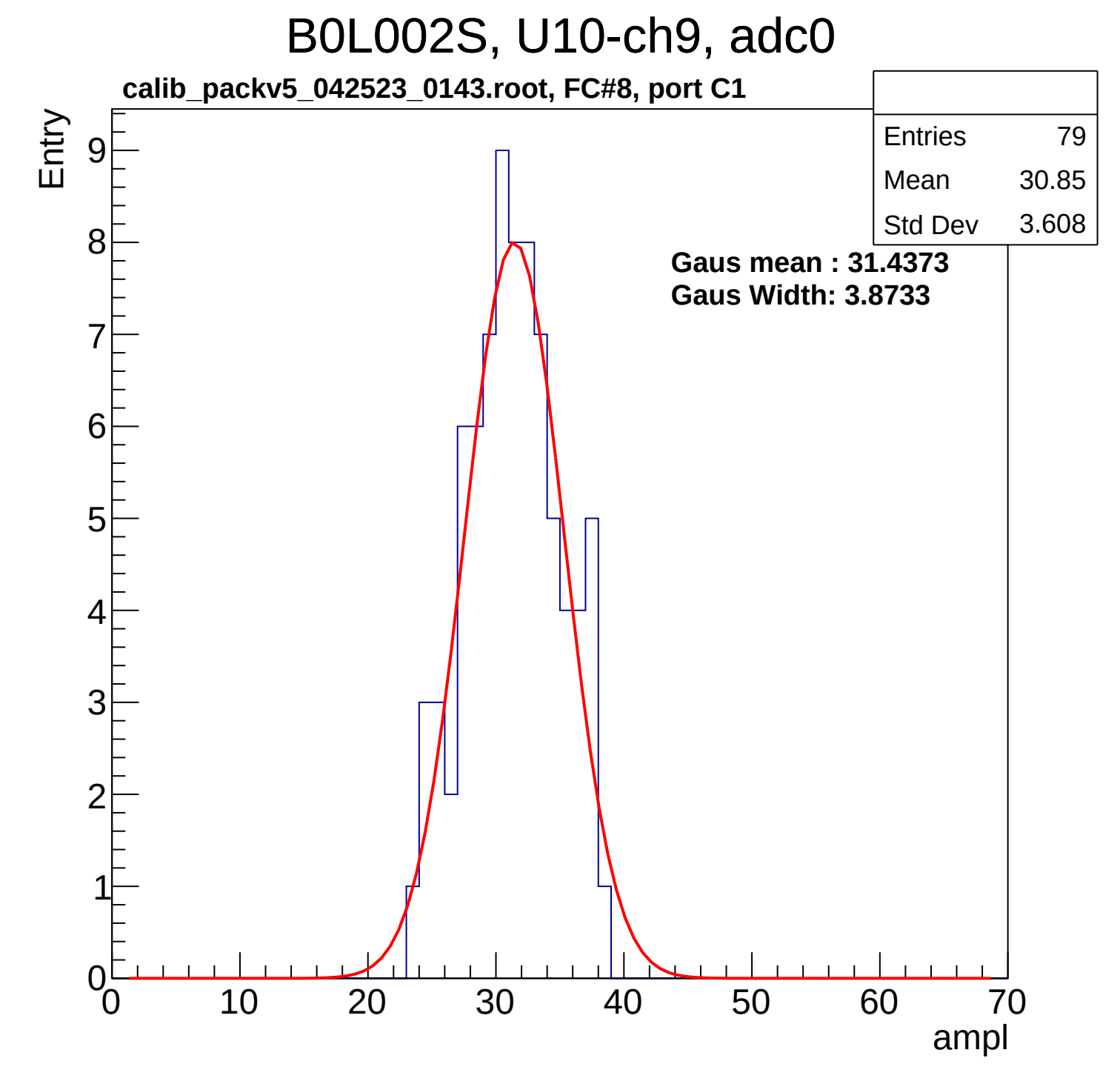

B0L002S, U10-ch8, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 51.85
Std Dev 2.713
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

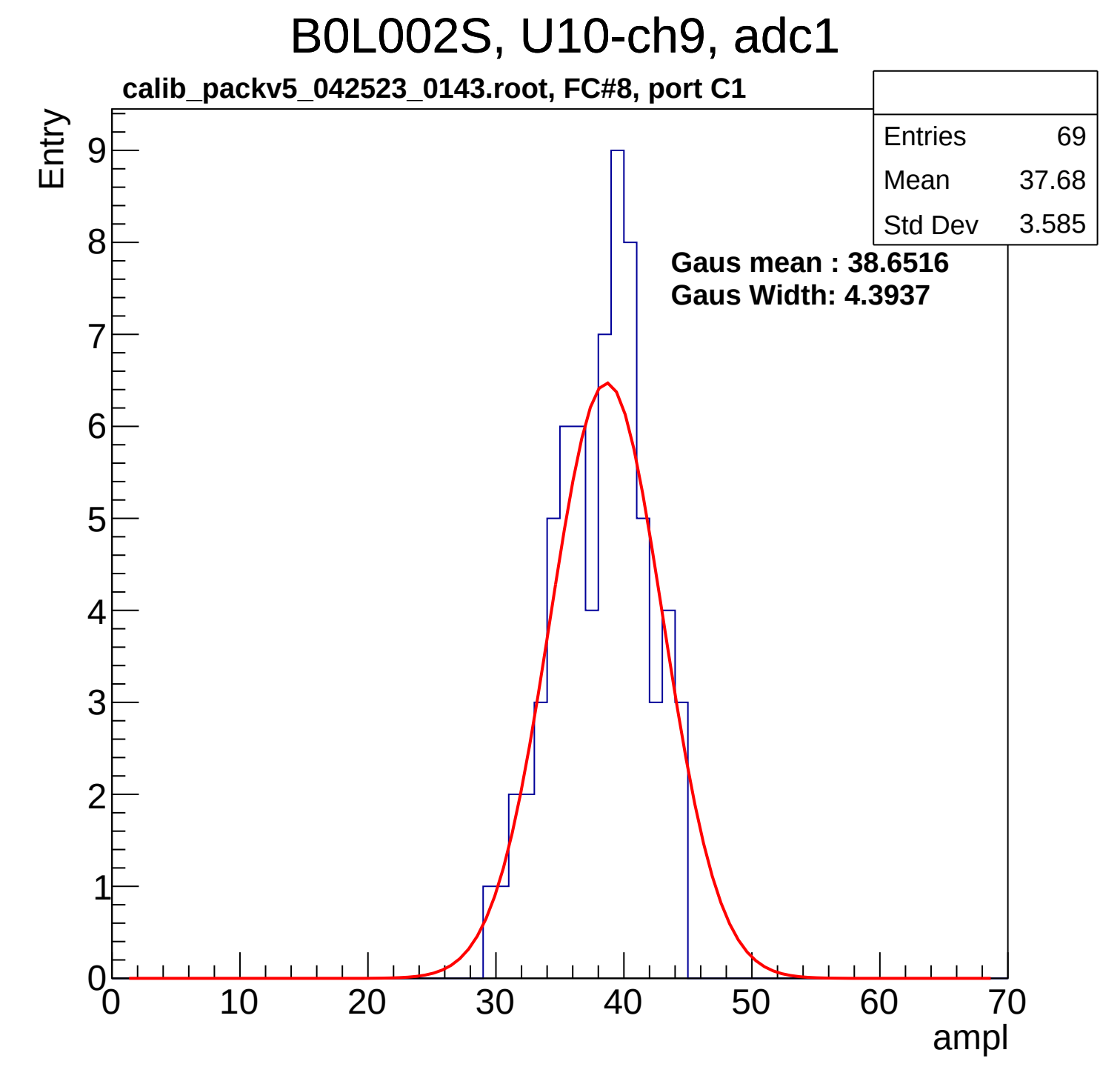

B0L002S, U10-ch8, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 58.47
Std Dev 2.794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

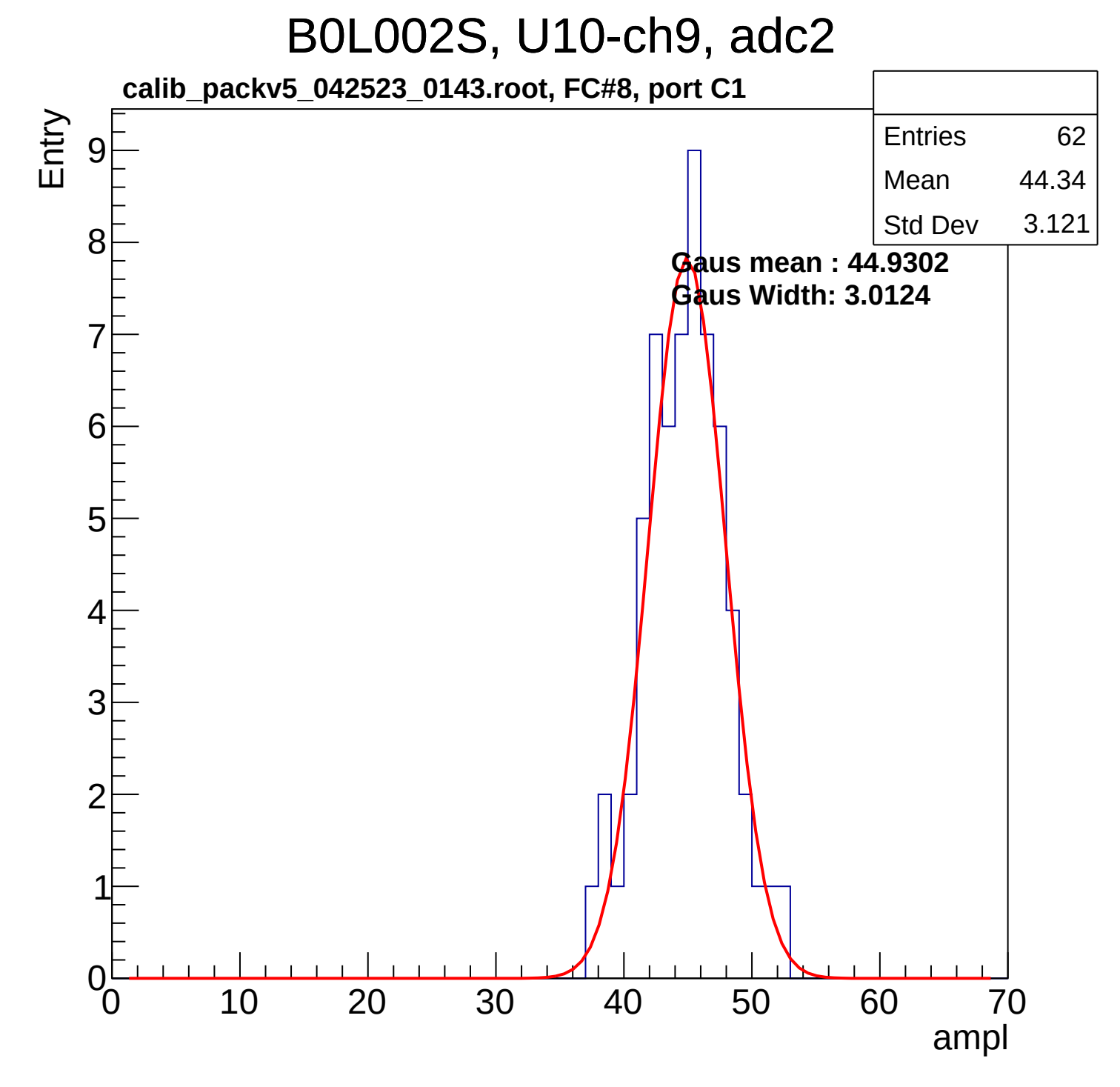

B0L002S, U10-ch8, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 58.46
Std Dev 12.3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

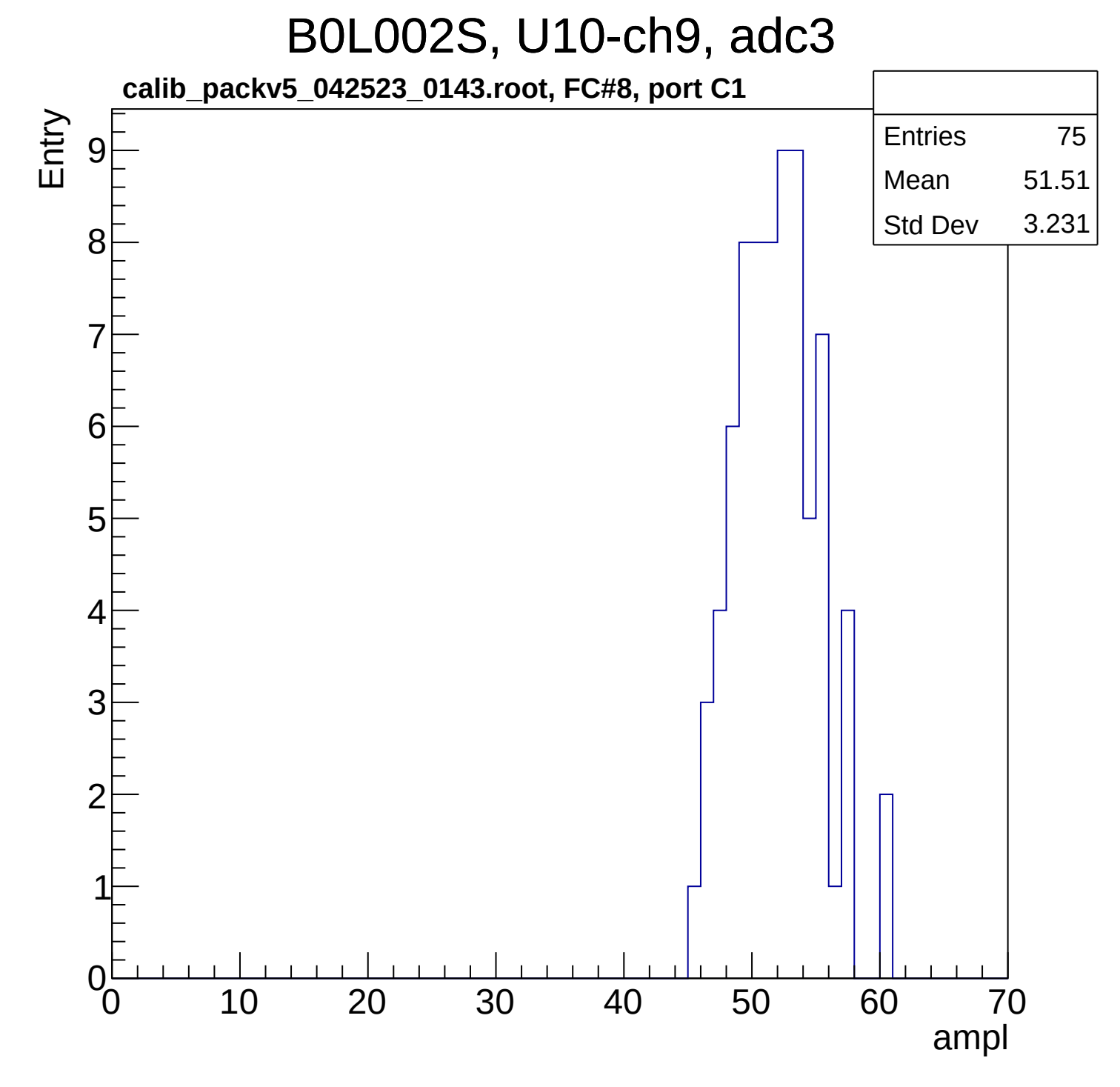

B0L002S, U10-ch8, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

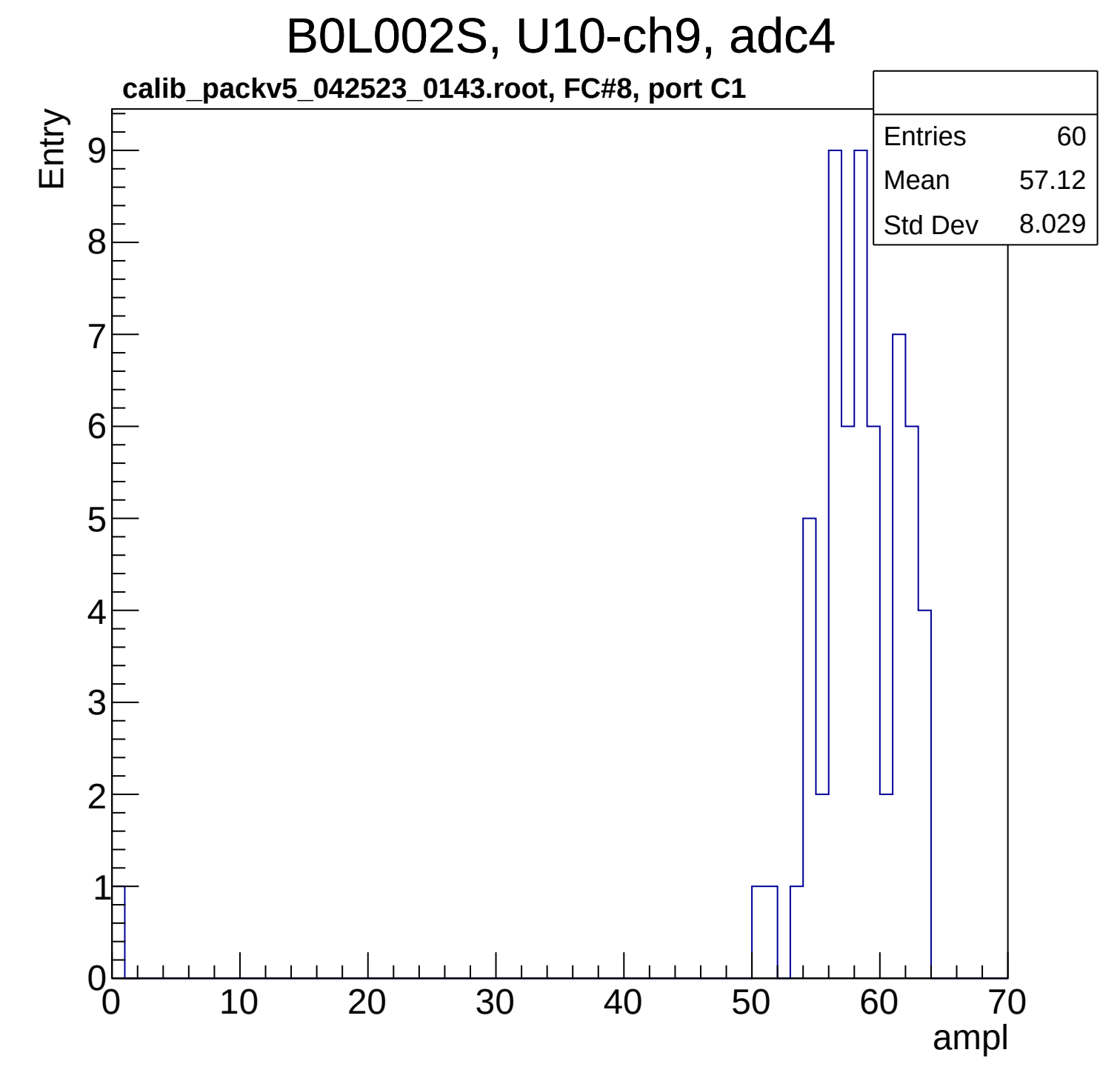

B0L002S, U10-ch8, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

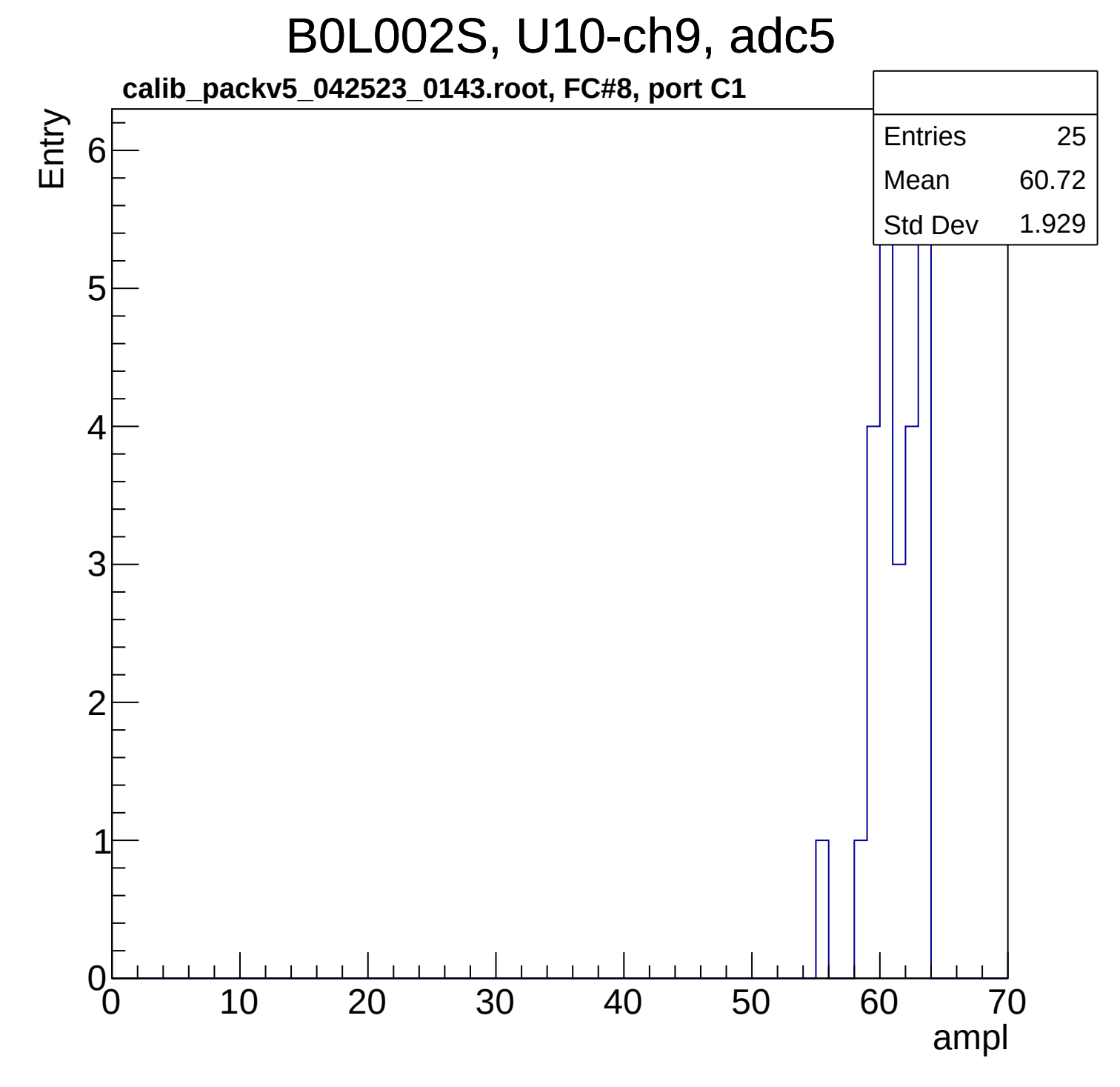

B0L002S, U10-ch9, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 30.85
Std Dev 3.608
Gaus mean : 31.4373
Gaus Width: 3.8733
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

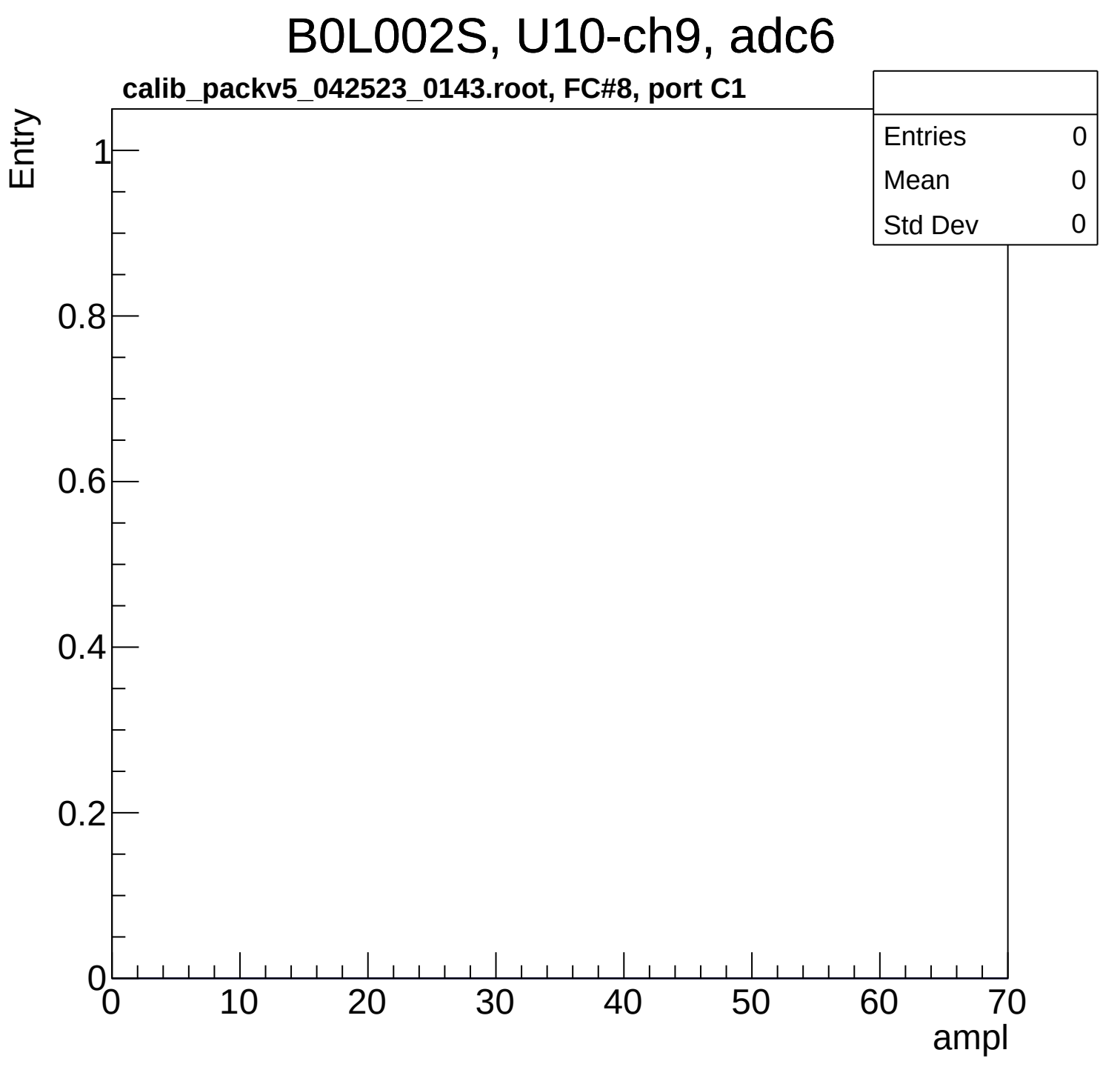

B0L002S, U10-ch9, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.68
Std Dev 3.585
Gaus mean : 38.6516
Gaus Width: 4.3937
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

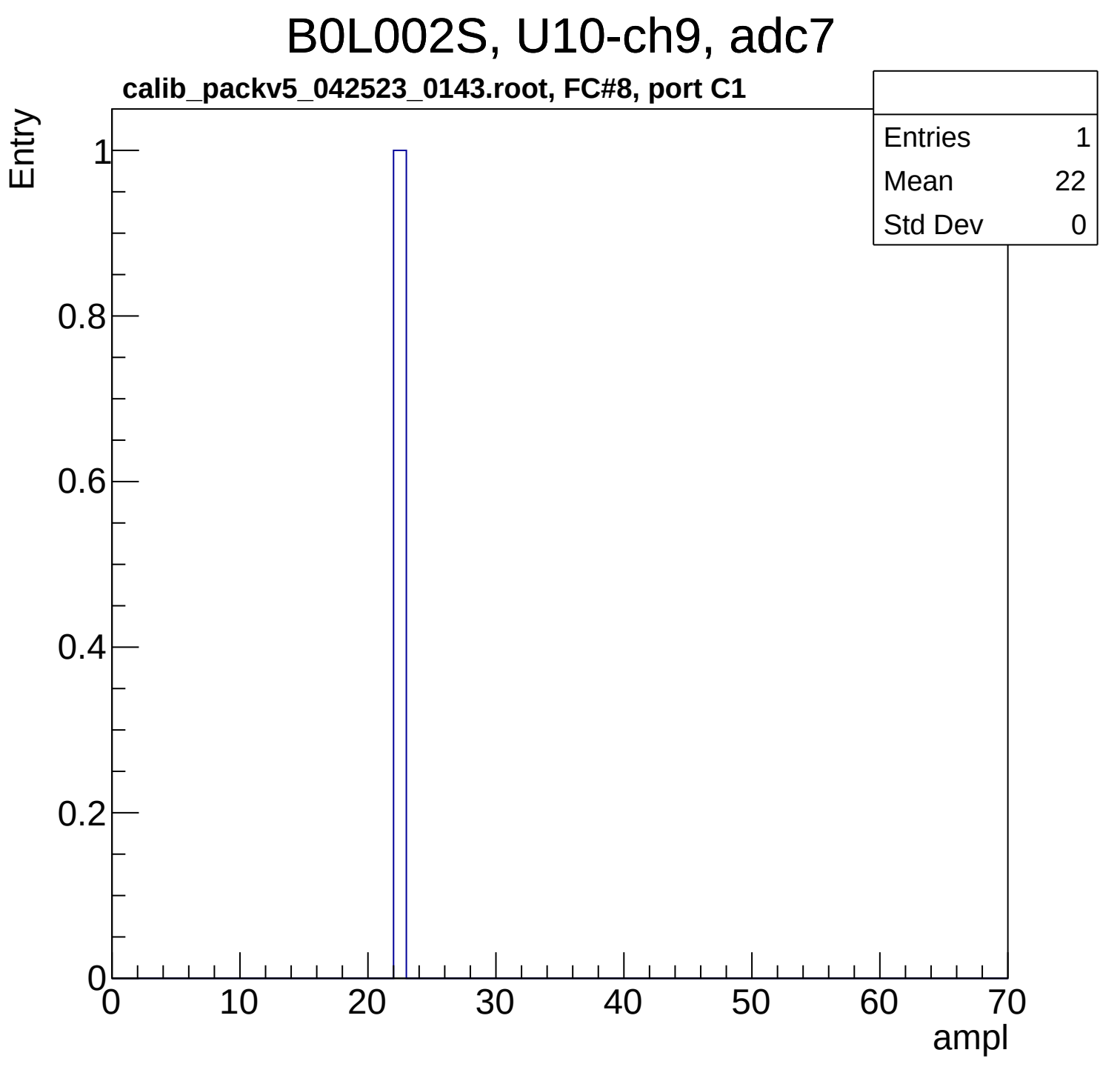

B0L002S, U10-ch9, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.34
Std Dev 3.121
Gaus mean : 44.9302
Gaus Width: 3.0124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

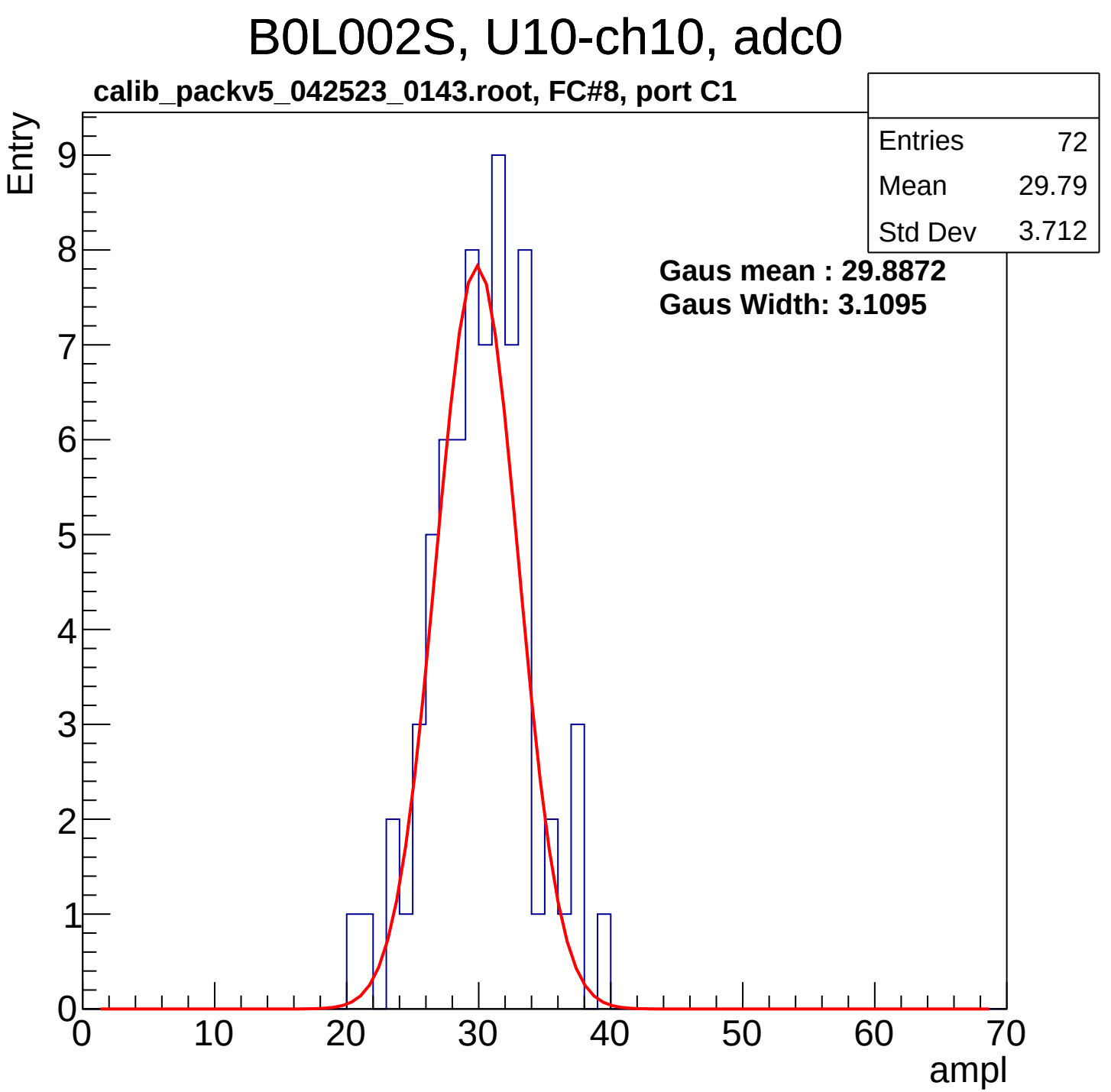

B0L002S, U10-ch9, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.51
Std Dev 3.231
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

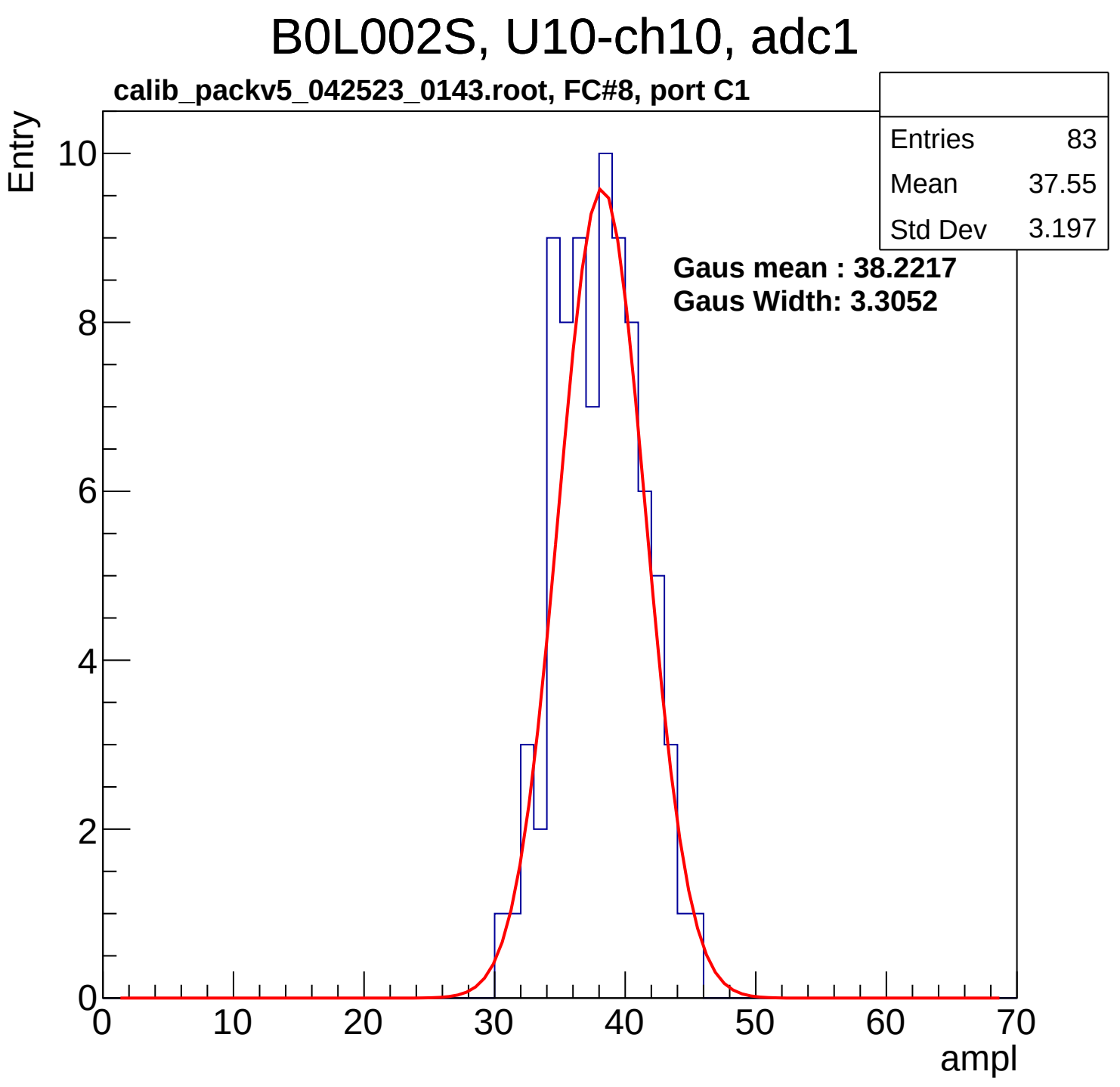

B0L002S, U10-ch9, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57.12
Std Dev 8.029
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

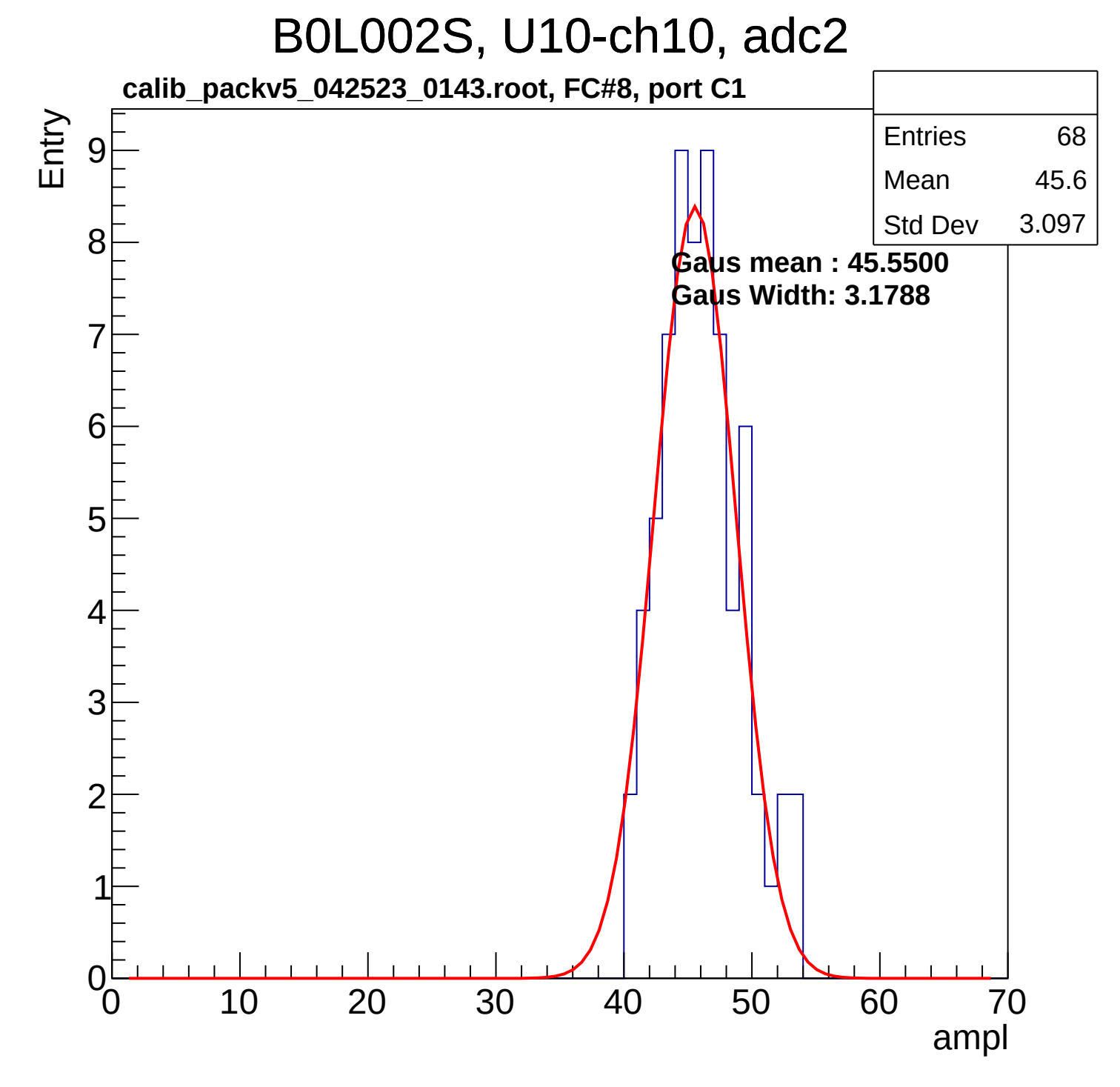

B0L002S, U10-ch9, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 60.72
Std Dev 1.929
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

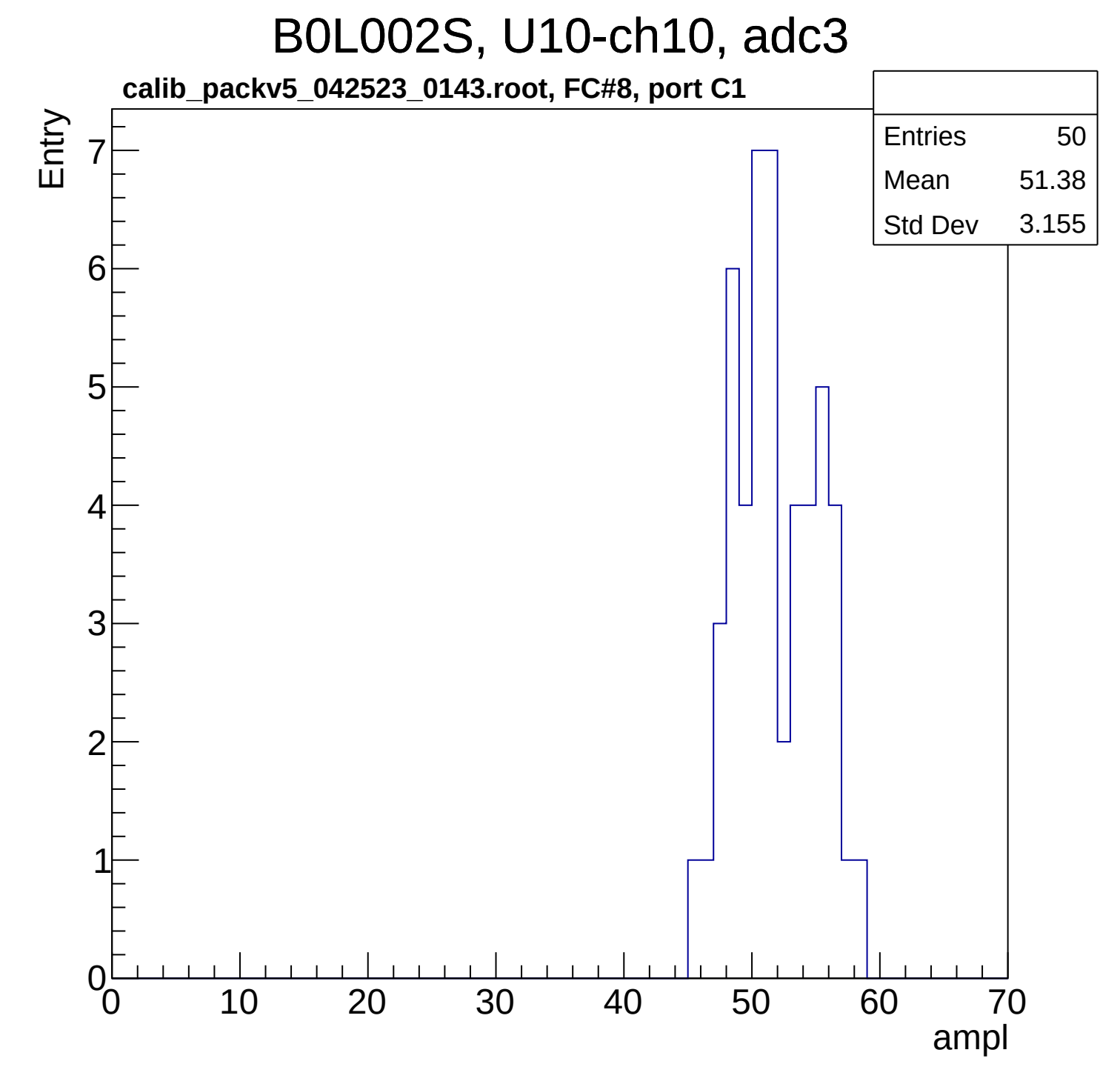

B0L002S, U10-ch9, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

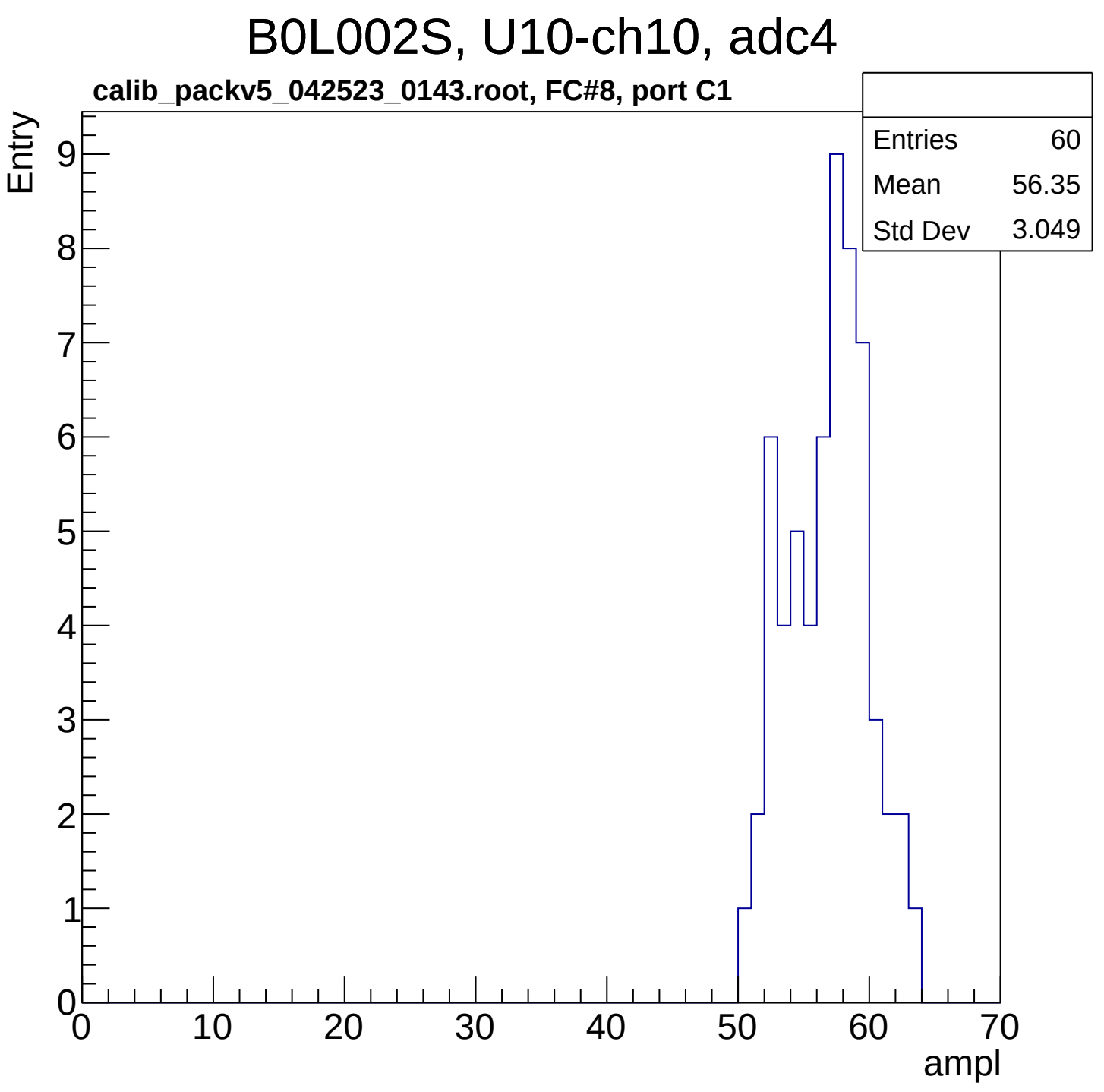

B0L002S, U10-ch9, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

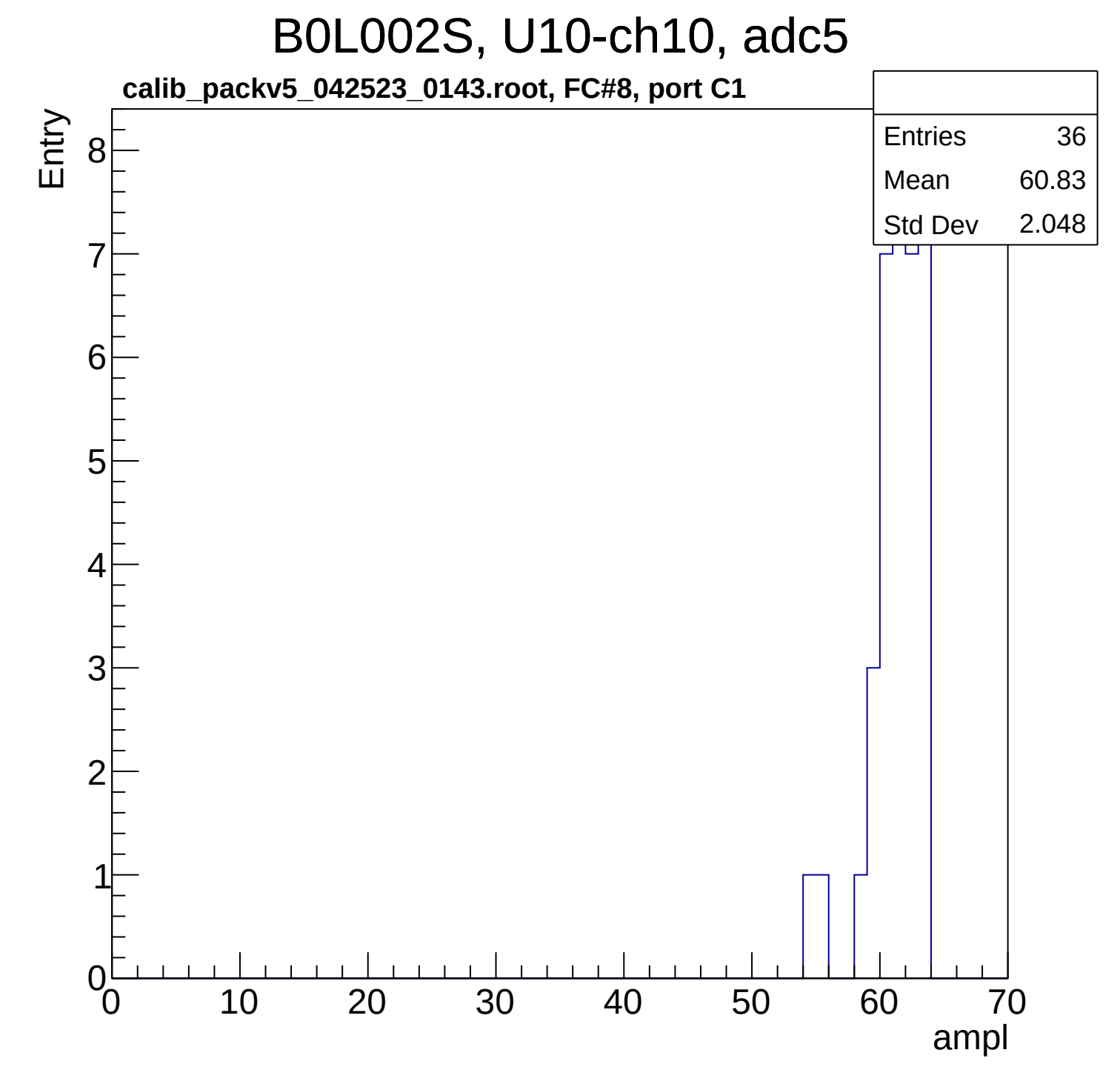

B0L002S, U10-ch10, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 29.79
Std Dev 3.712
Gaus mean : 29.8872
Gaus Width: 3.1095
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

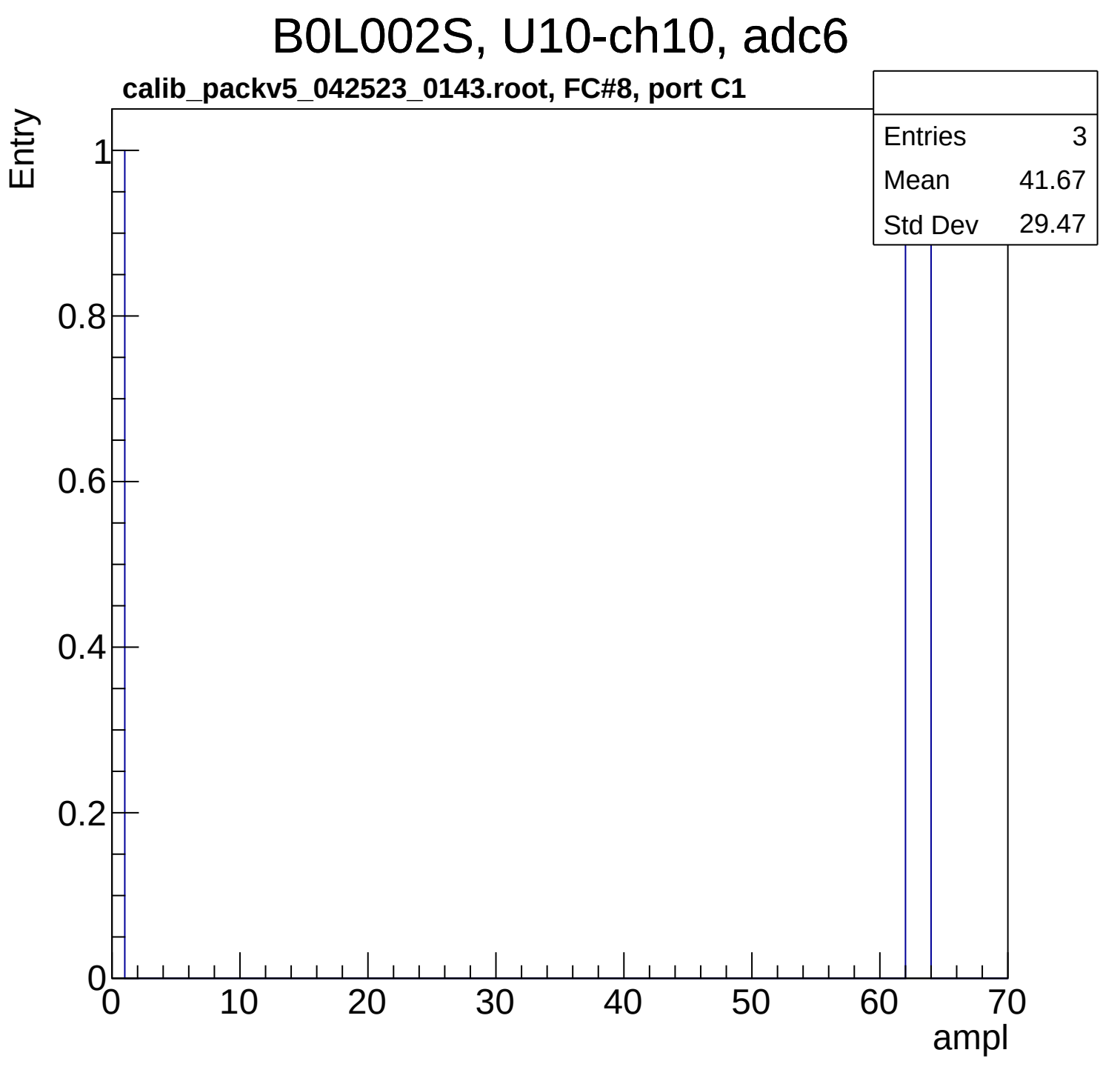

B0L002S, U10-ch10, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 37.55
Std Dev 3.197
Gaus mean : 38.2217
Gaus Width: 3.3052
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

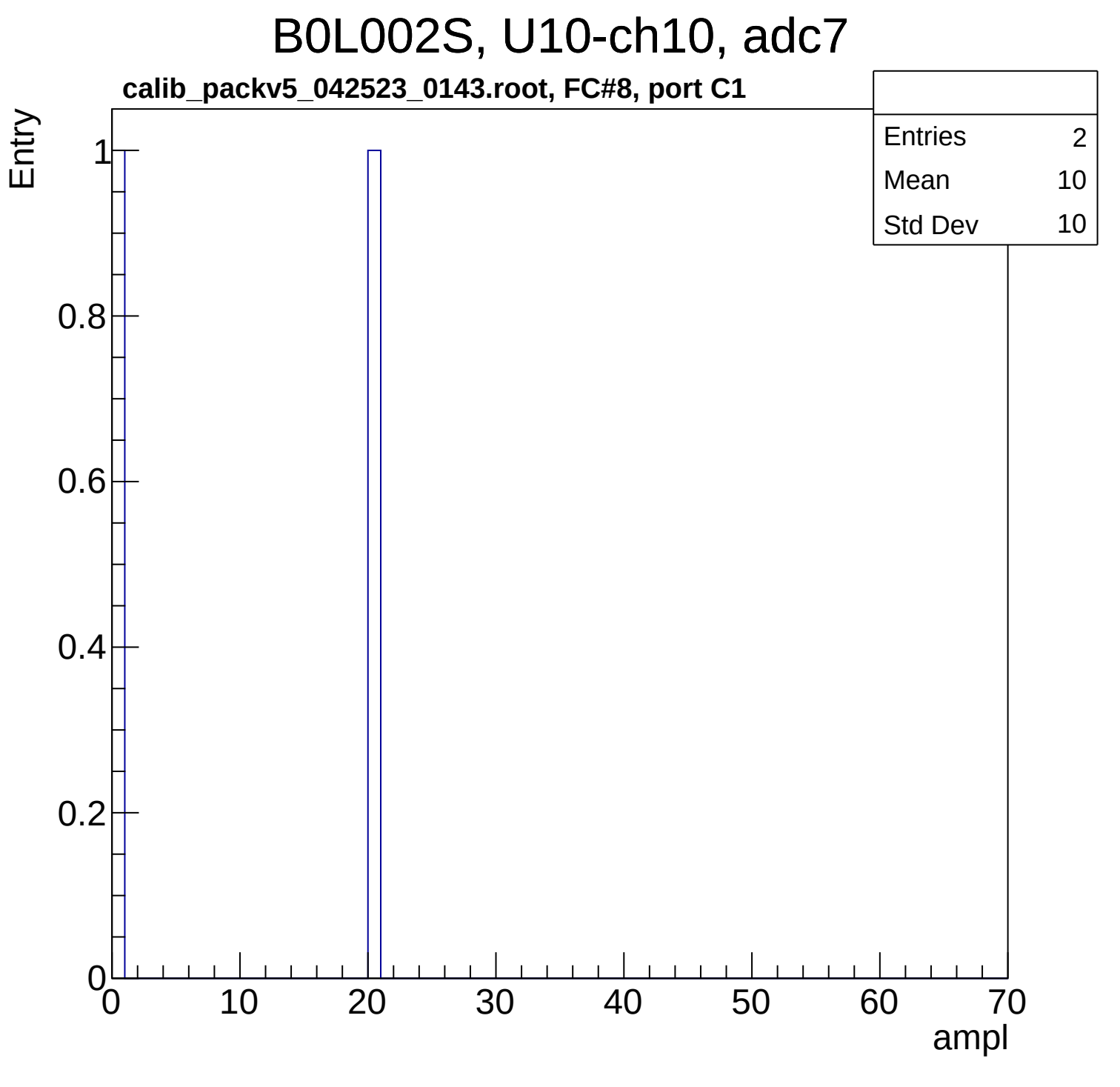

B0L002S, U10-ch10, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 45.6
Std Dev 3.097
Gaus mean : 45.5500
Gaus Width: 3.1788
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

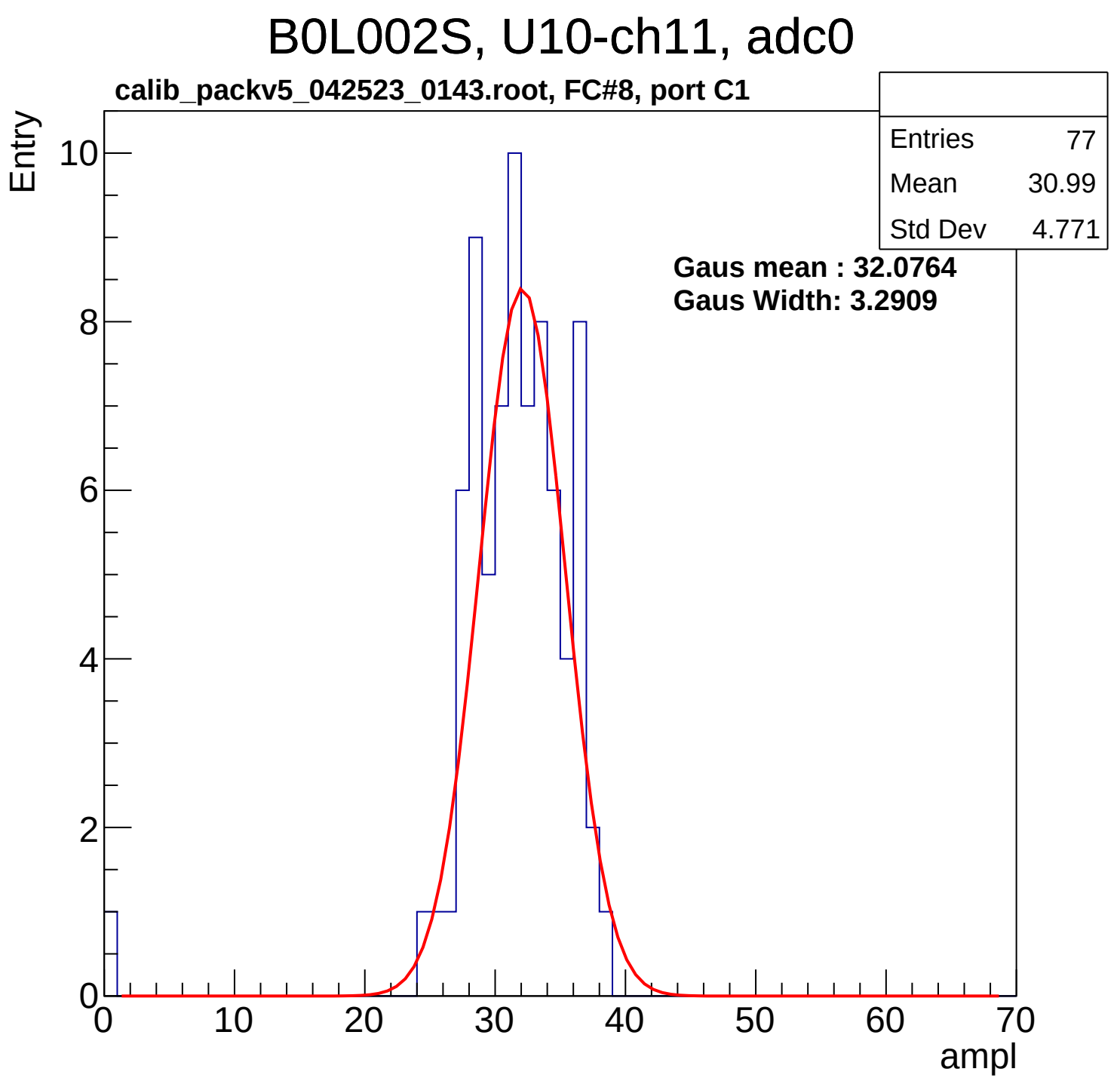

B0L002S, U10-ch10, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 51.38
Std Dev 3.155
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

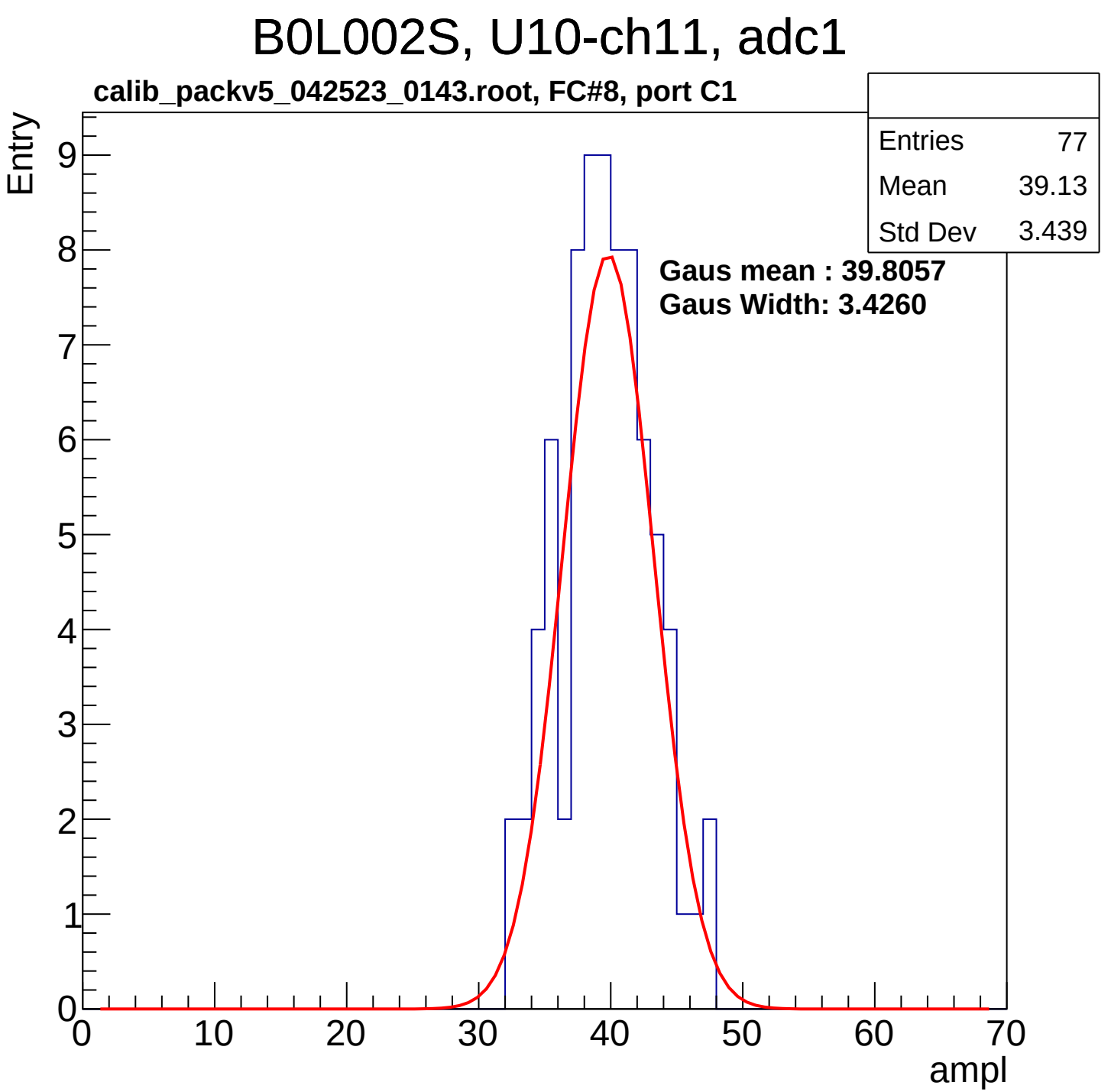

B0L002S, U10-ch10, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.35
Std Dev 3.049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

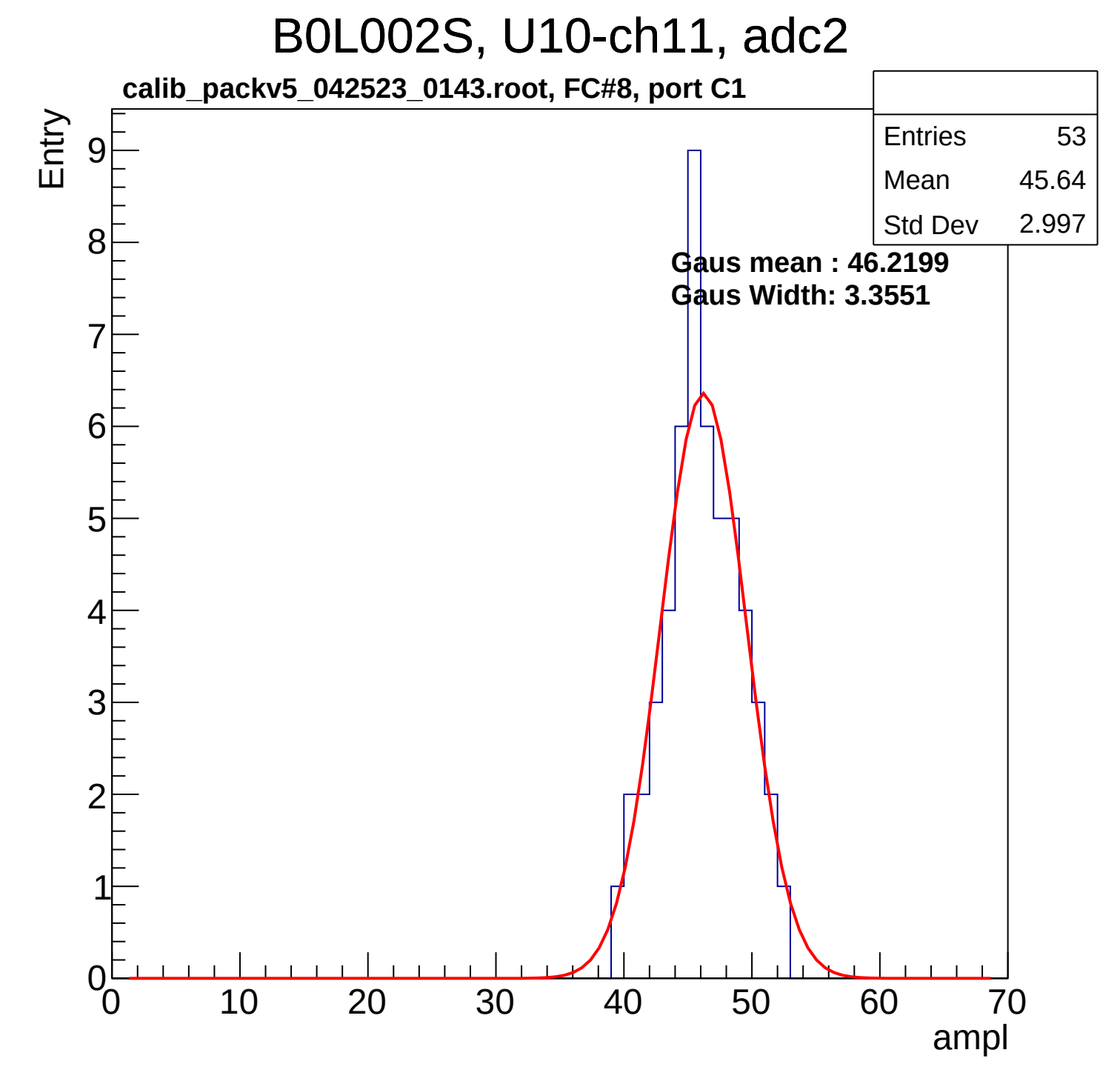

B0L002S, U10-ch10, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 60.83
Std Dev 2.048
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

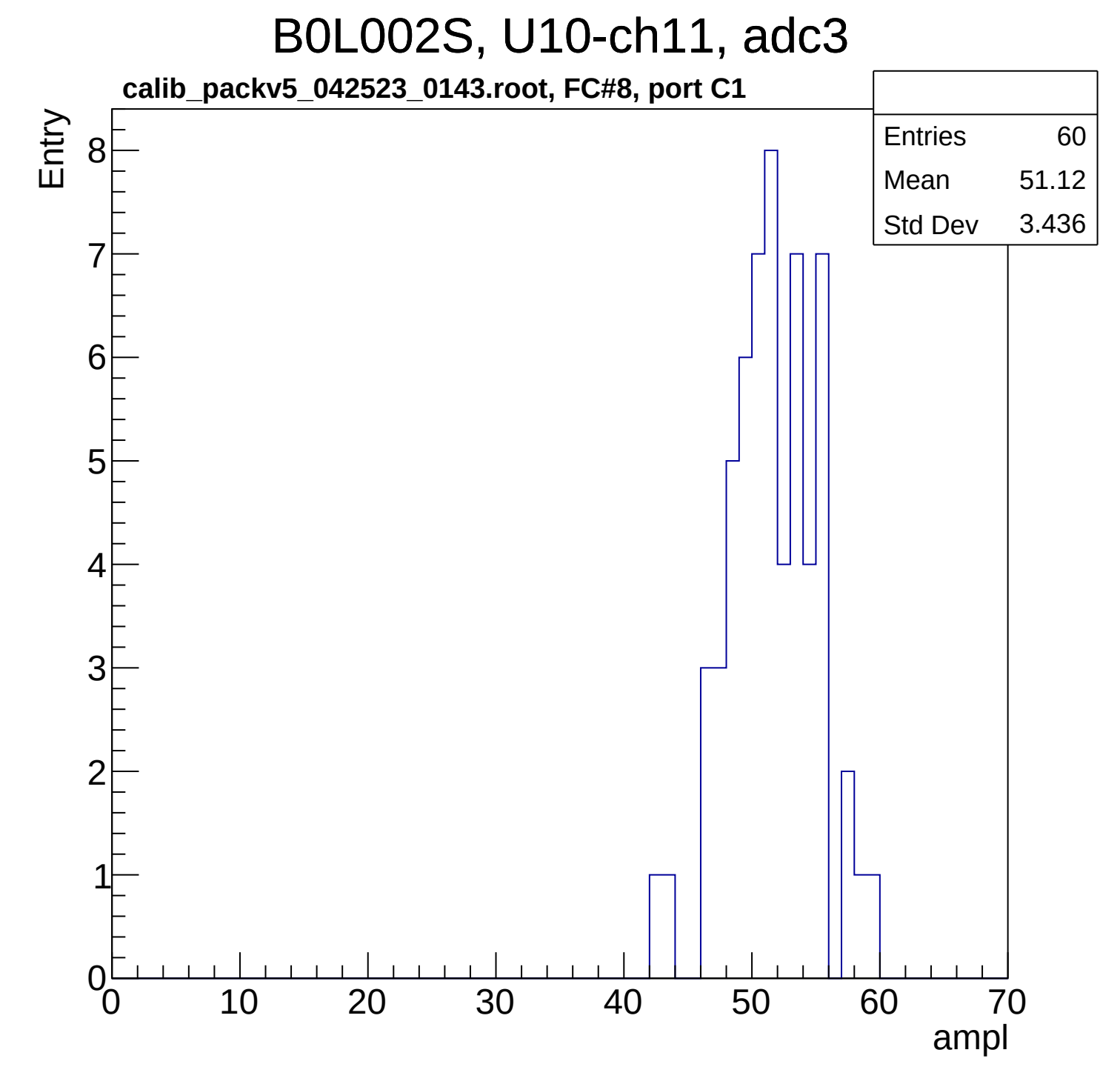

B0L002S, U10-ch10, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

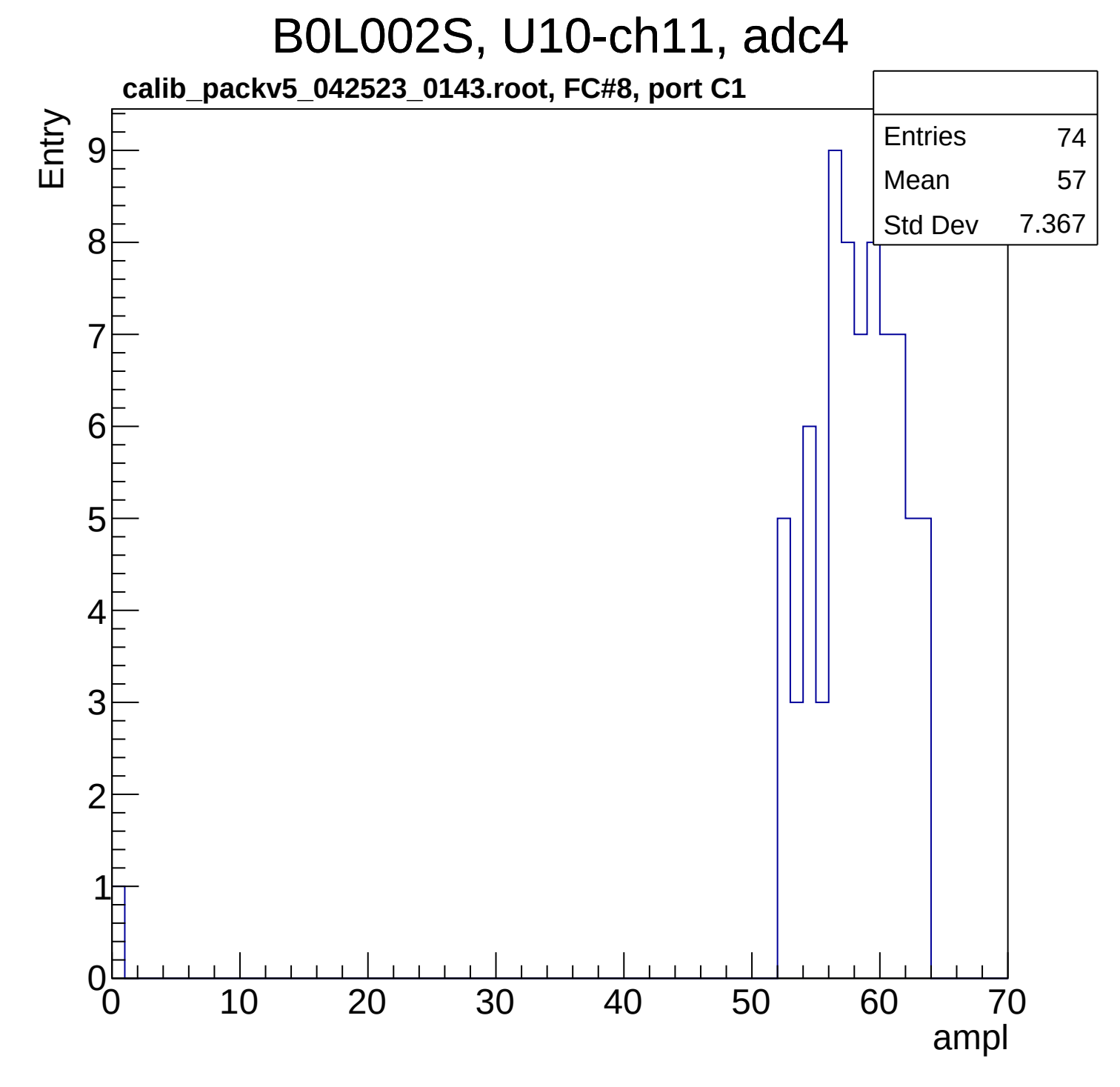

B0L002S, U10-ch10, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

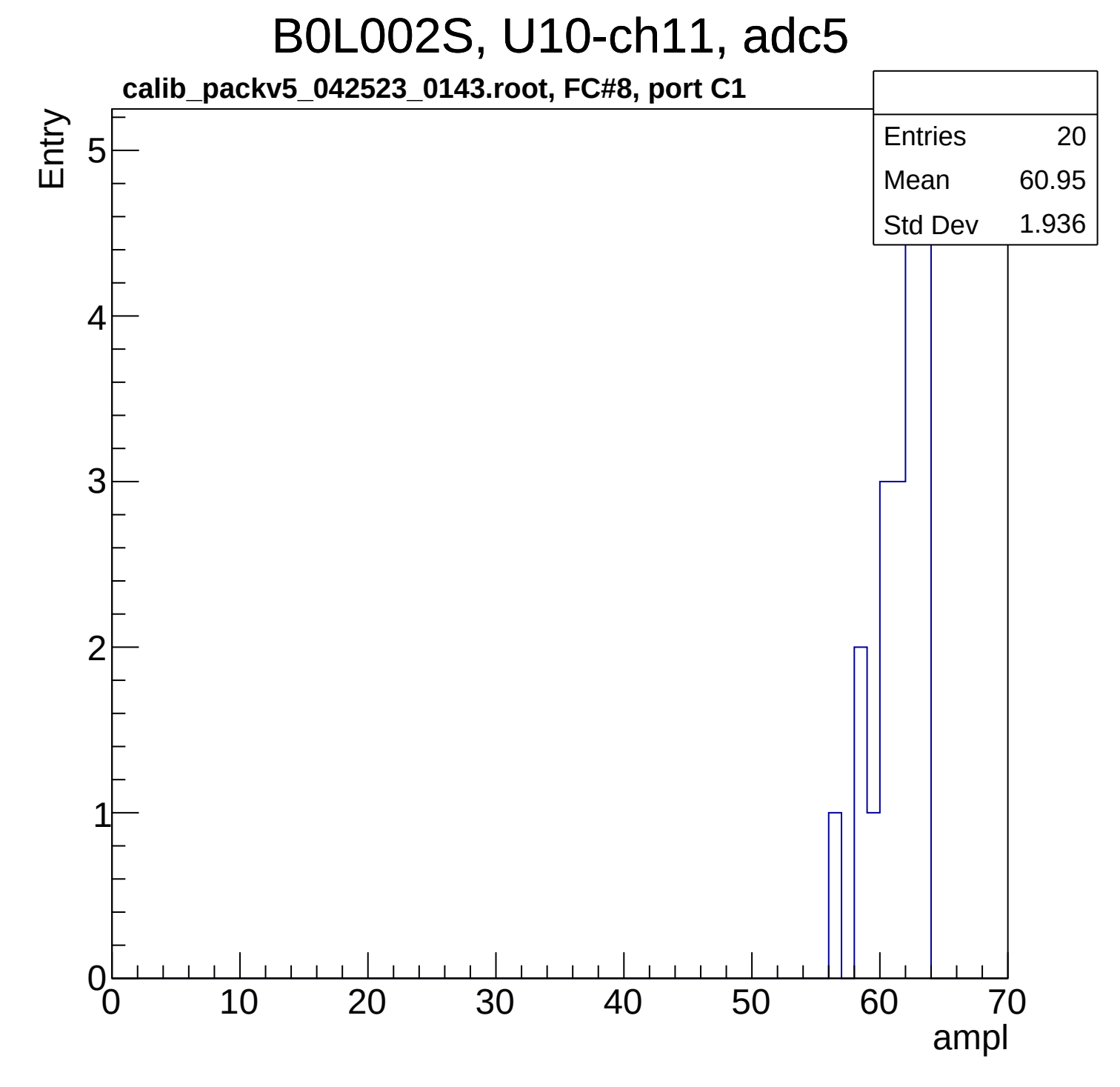

B0L002S, U10-ch11, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 30.99
Std Dev 4.771
Gaus mean : 32.0764
Gaus Width: 3.2909
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

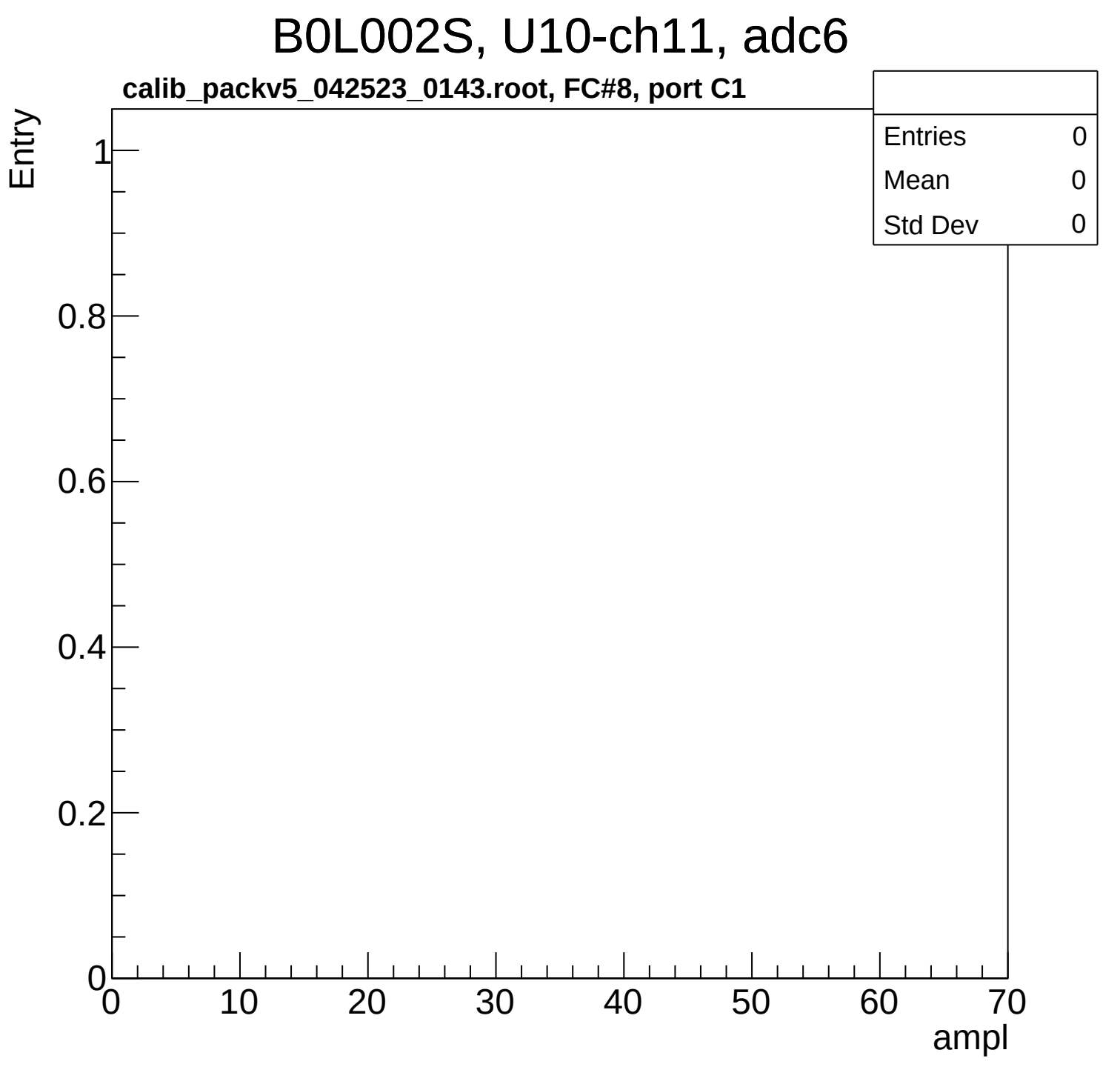

B0L002S, U10-ch11, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.13
Std Dev 3.439
Gaus mean : 39.8057
Gaus Width: 3.4260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

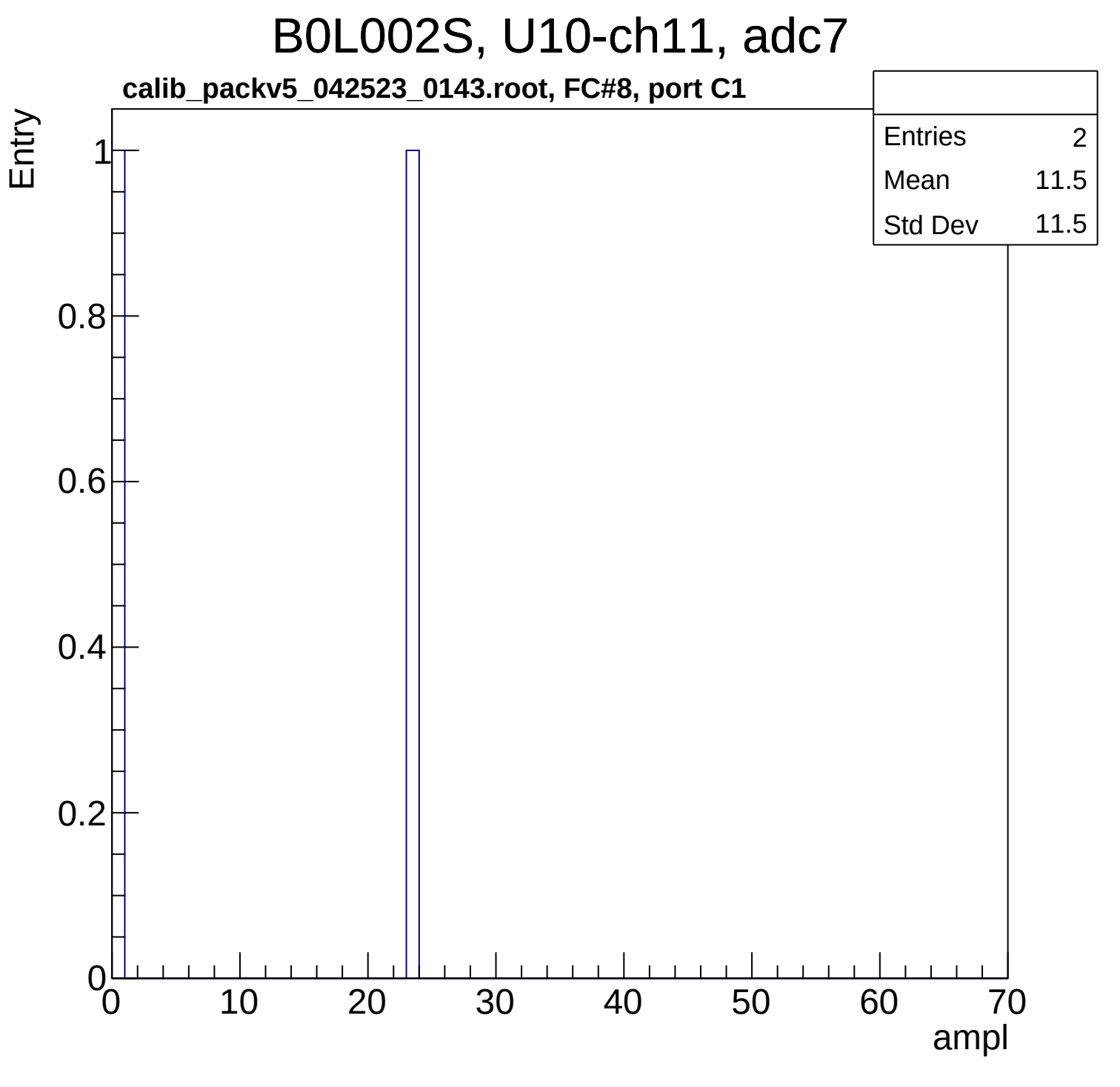

B0L002S, U10-ch11, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 45.64
Std Dev 2.997
Gaus mean : 46.2199
Gaus Width: 3.3551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

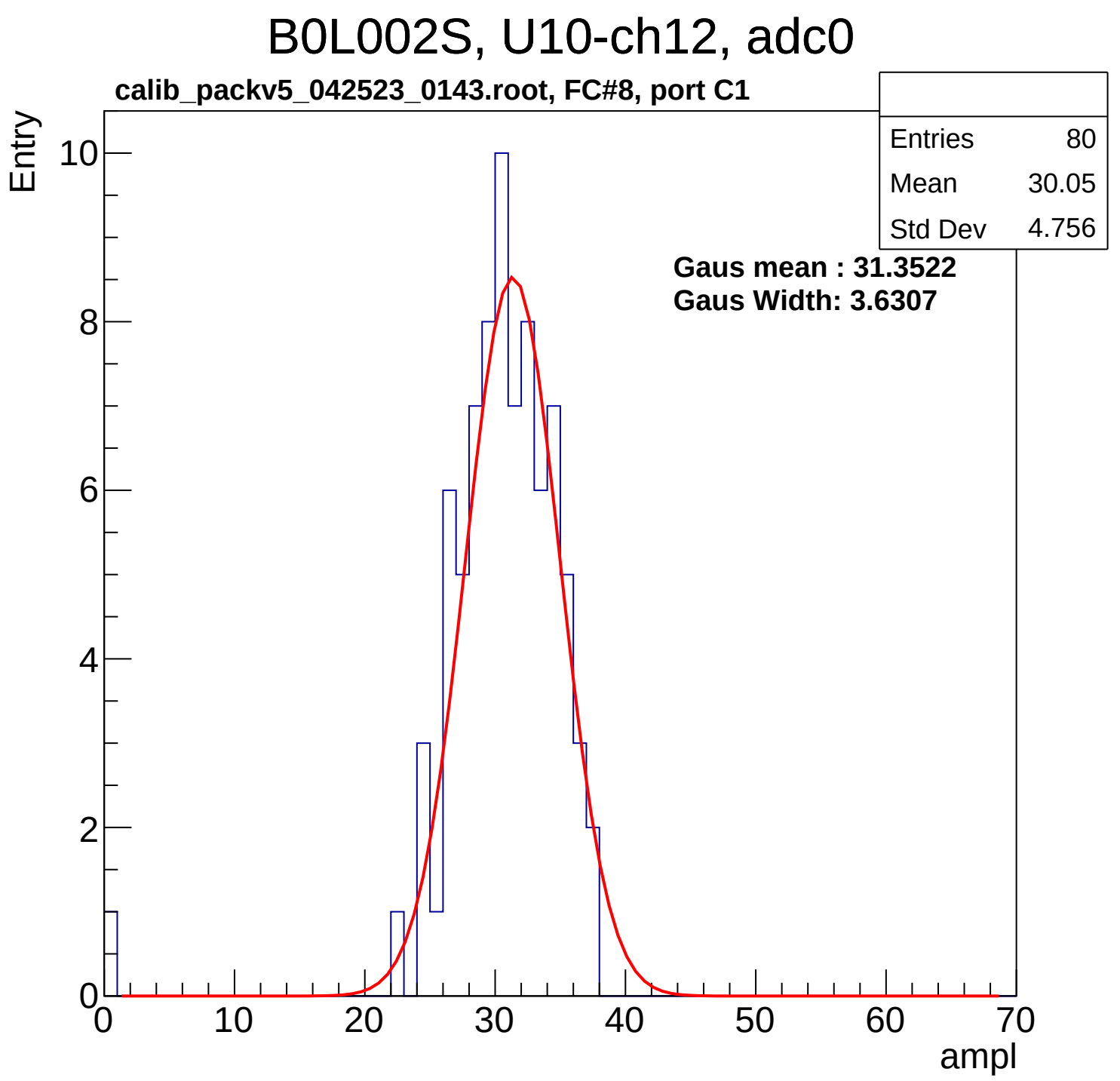

B0L002S, U10-ch11, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.12
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

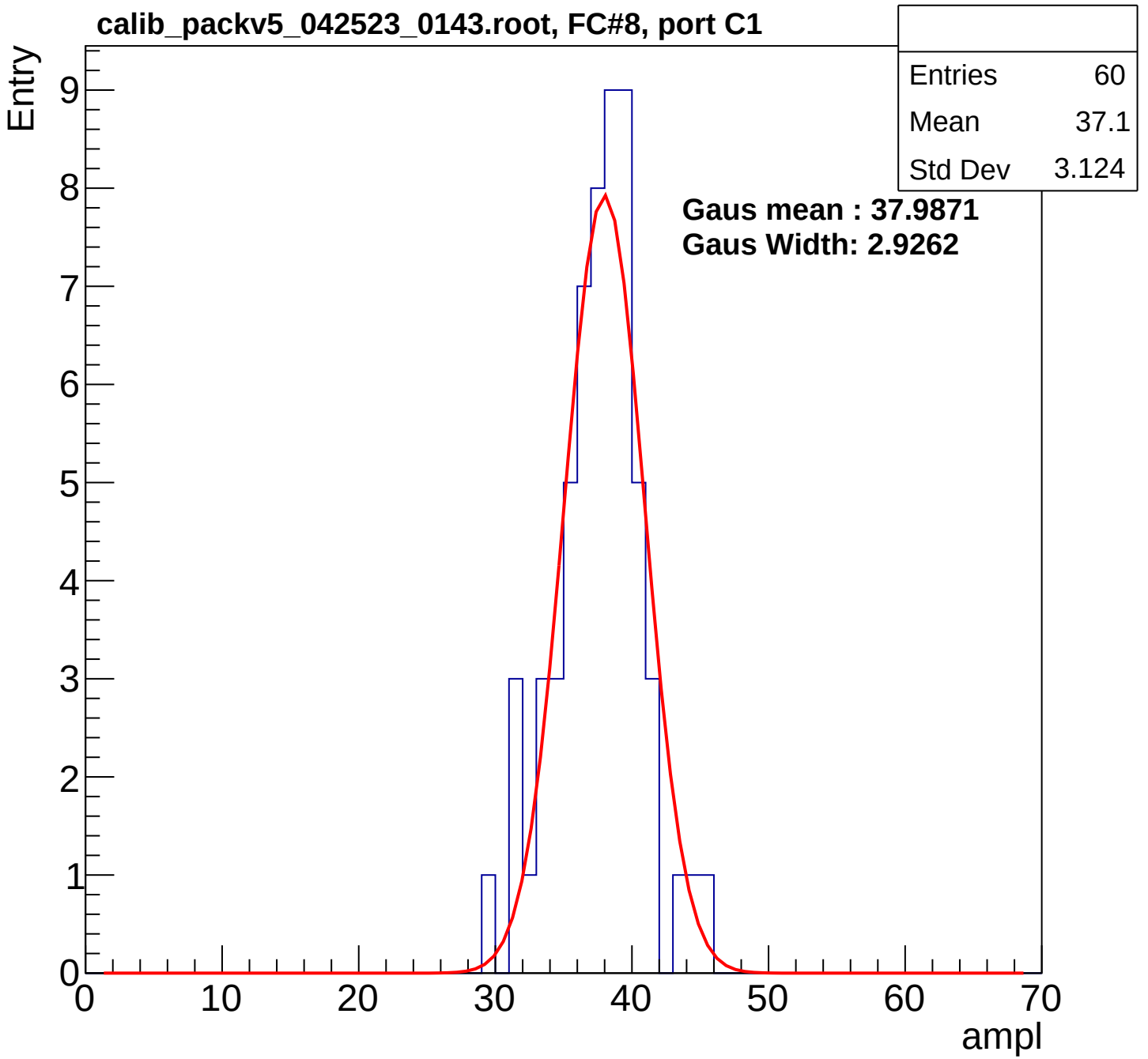

B0L002S, U10-ch11, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 57
Std Dev 7.367
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

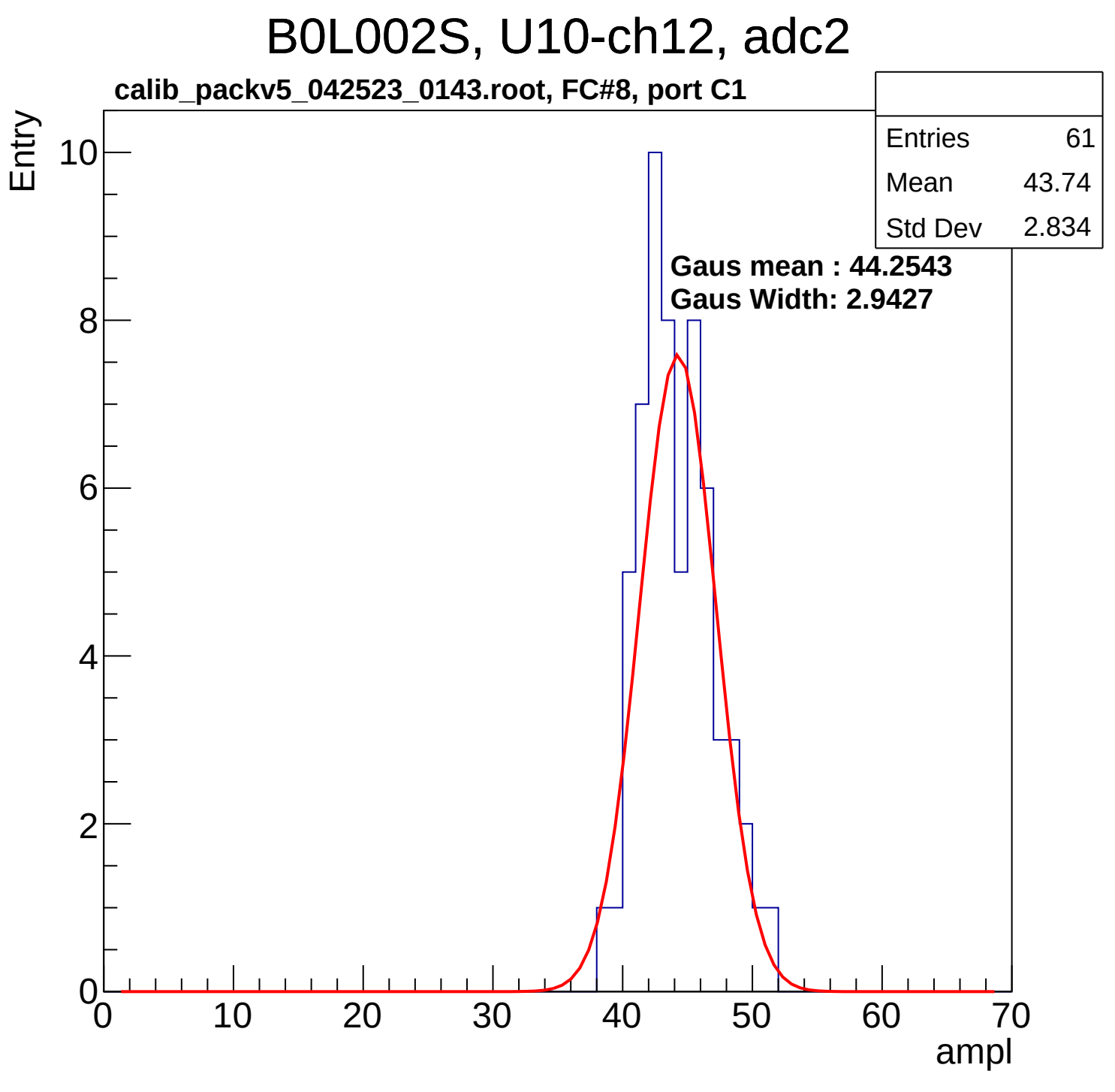

B0L002S, U10-ch11, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 60.95
Std Dev 1.936
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

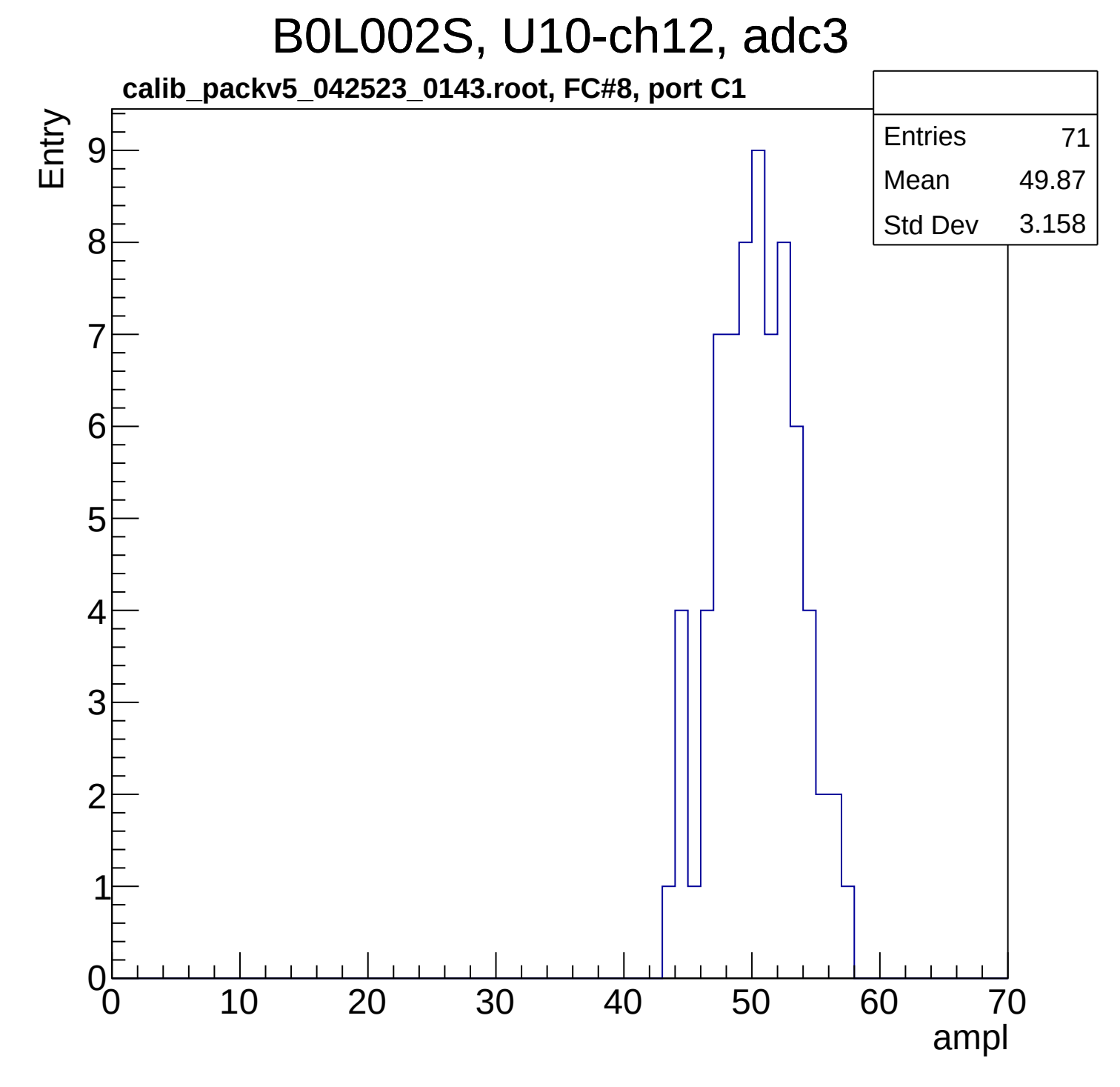

B0L002S, U10-ch11, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

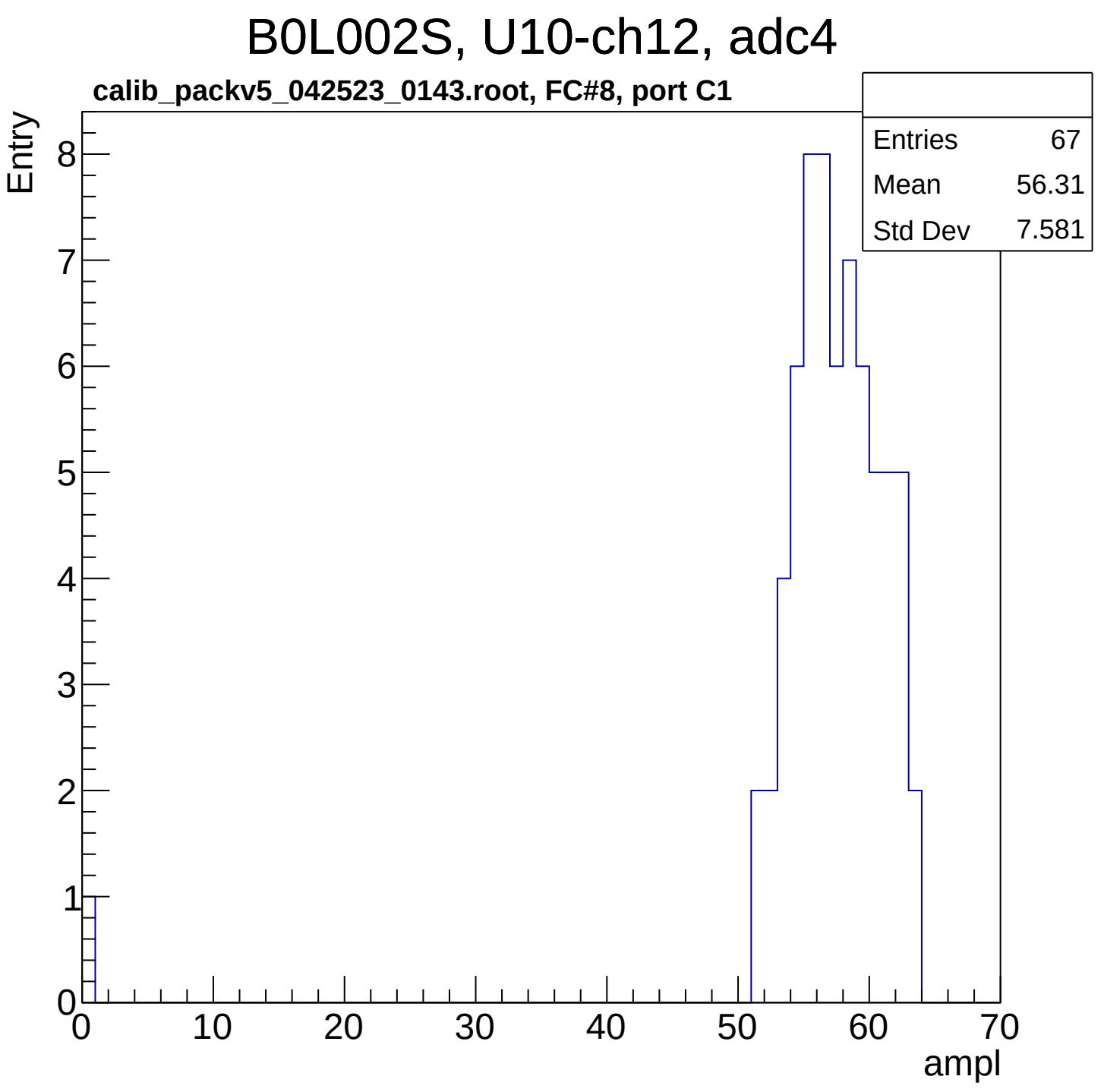

B0L002S, U10-ch11, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

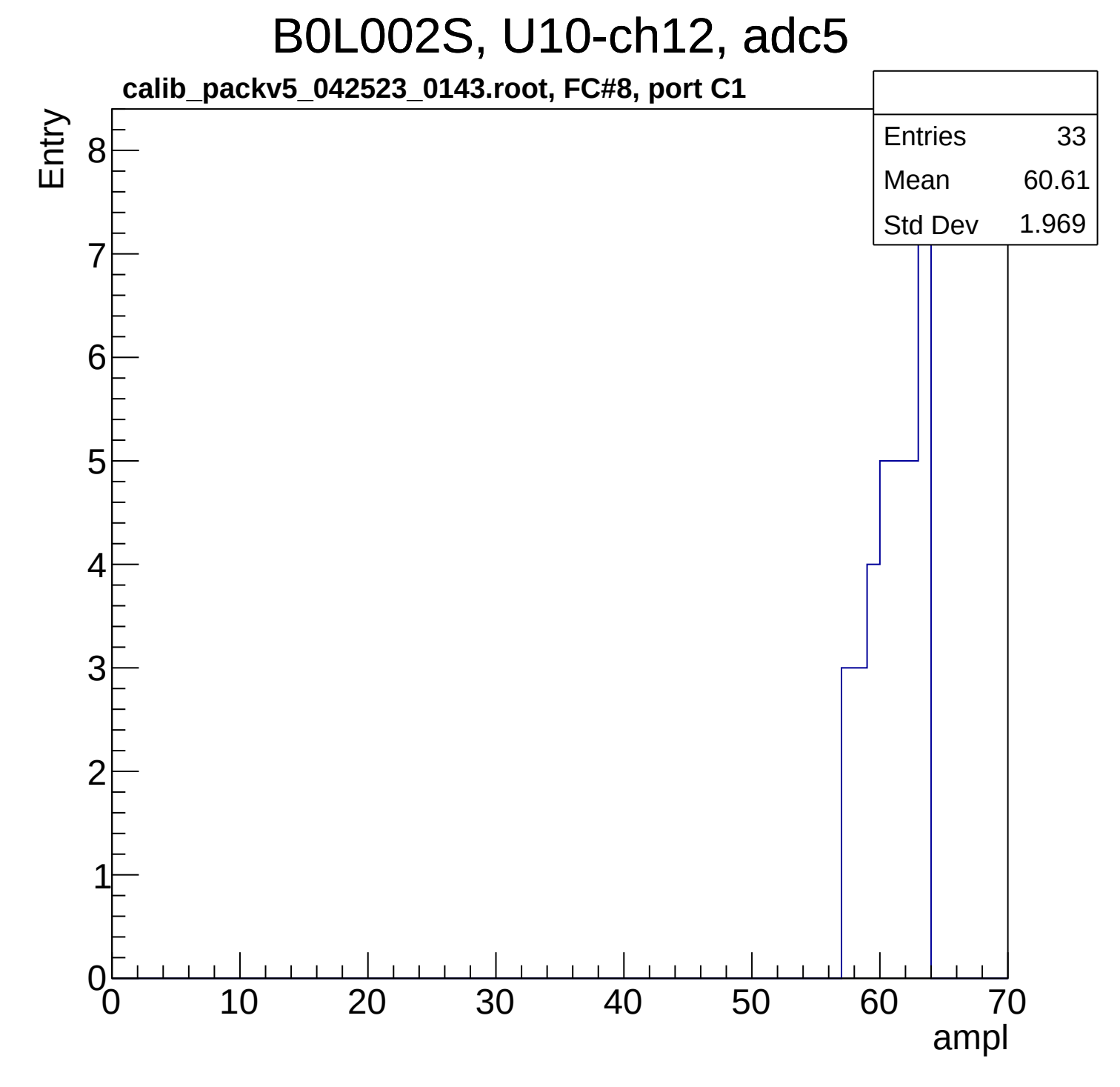

B0L002S, U10-ch12, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 30.05
Std Dev 4.756
Gaus mean : 31.3522
Gaus Width: 3.6307
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

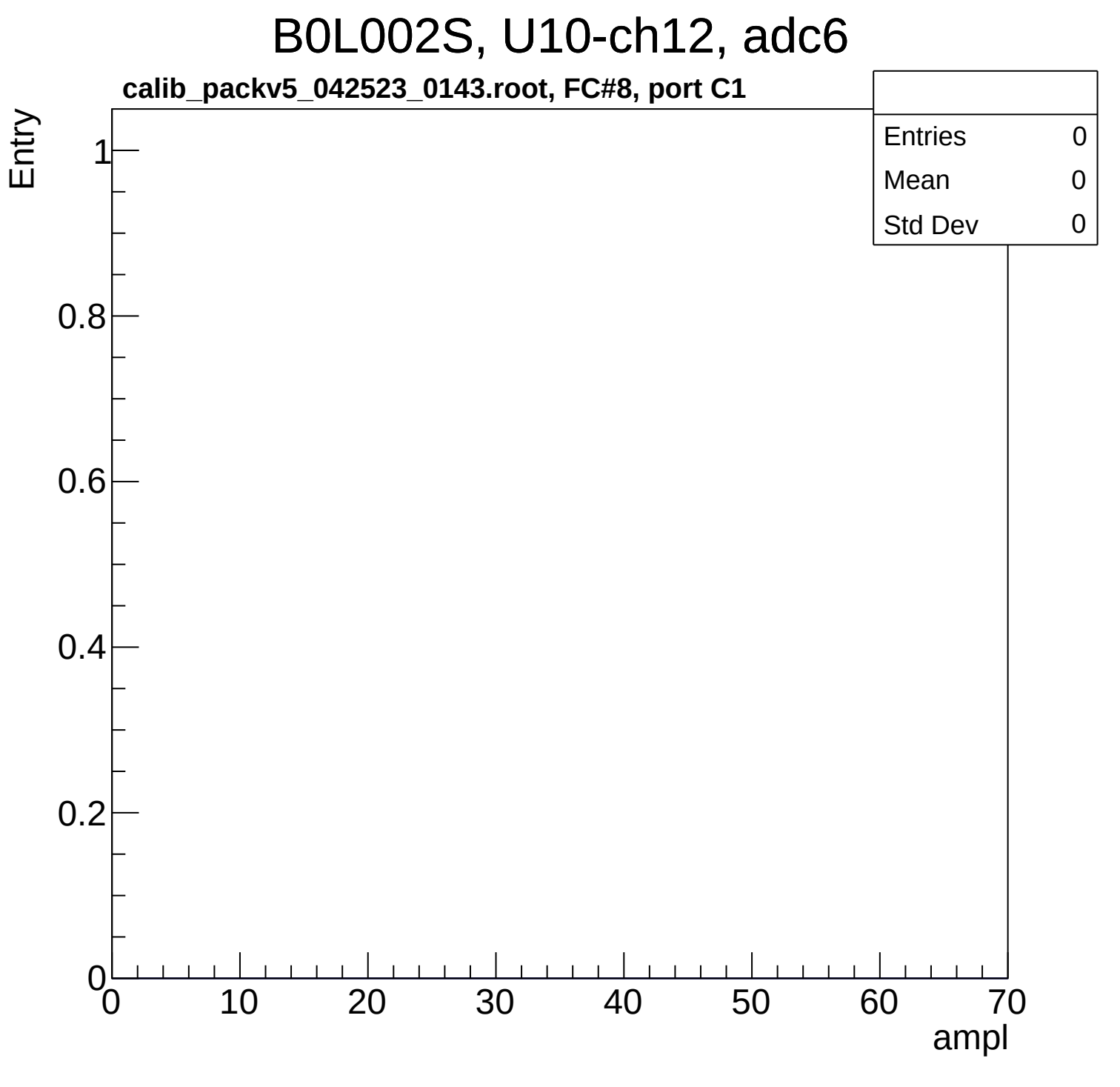

B0L002S, U10-ch12, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 37.1
Std Dev 3.124
Gaus mean : 37.9871
Gaus Width: 2.9262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

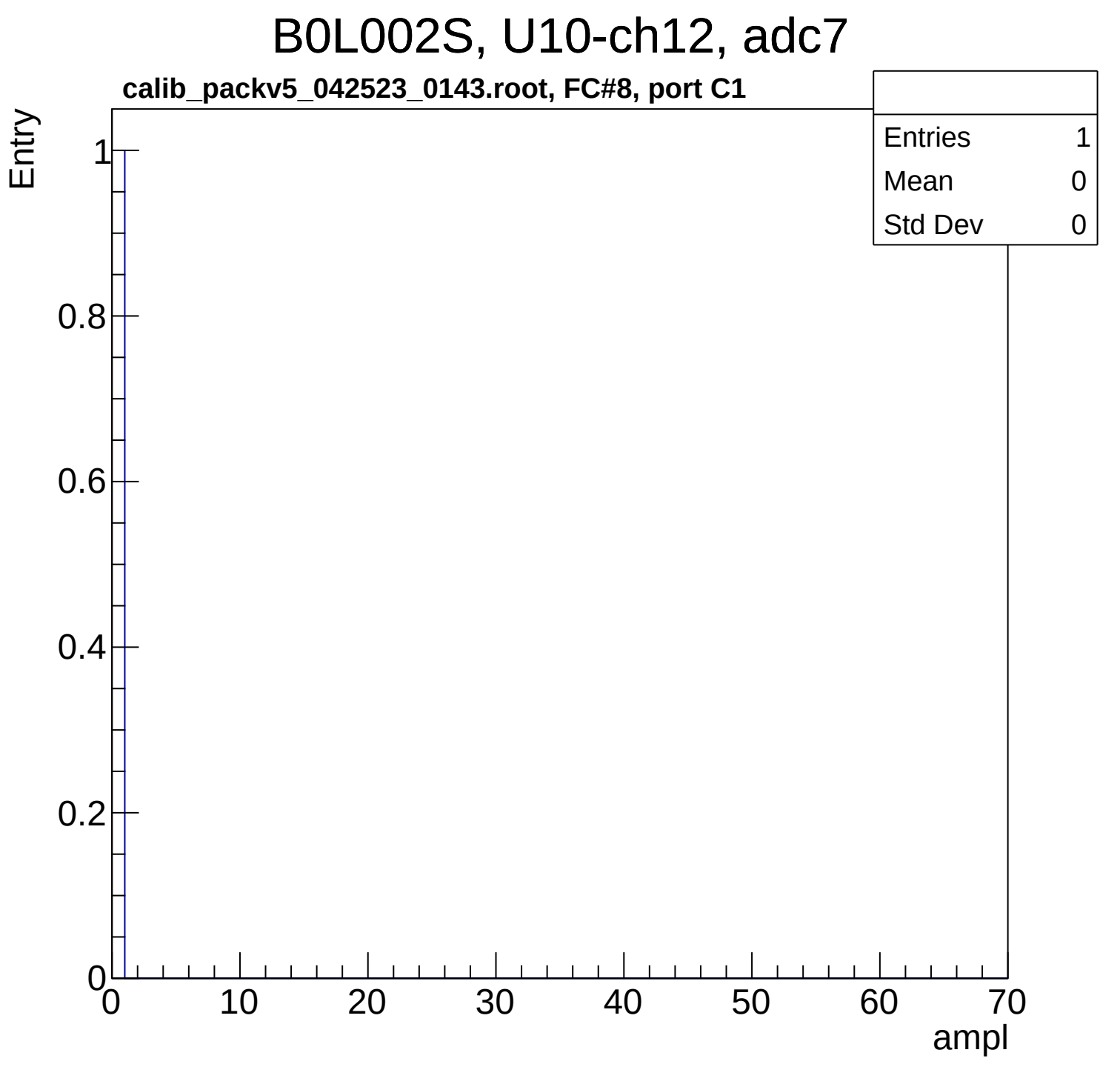

B0L002S, U10-ch12, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.74
Std Dev 2.834
Gaus mean : 44.2543
Gaus Width: 2.9427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

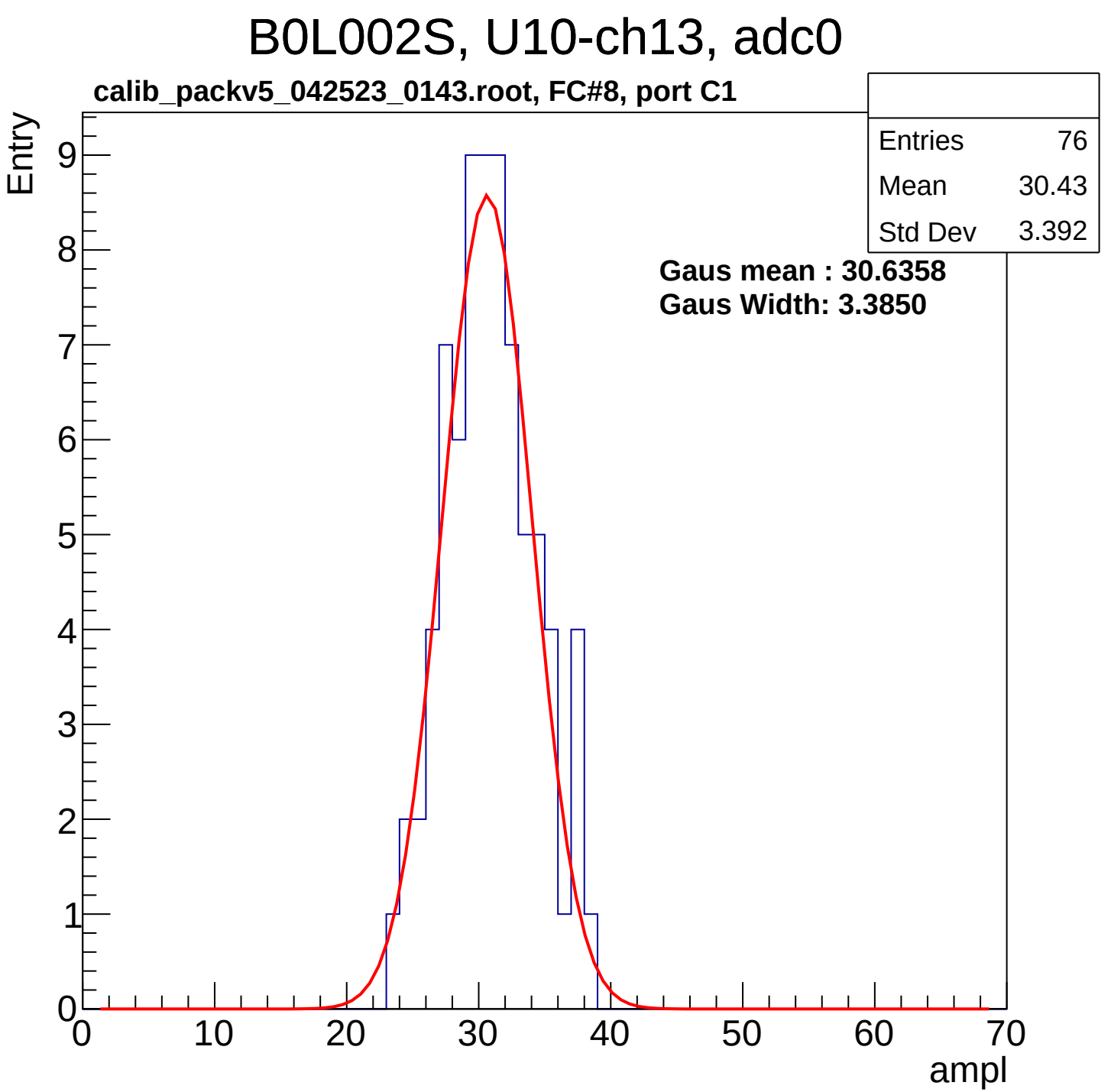

B0L002S, U10-ch12, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 49.87
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

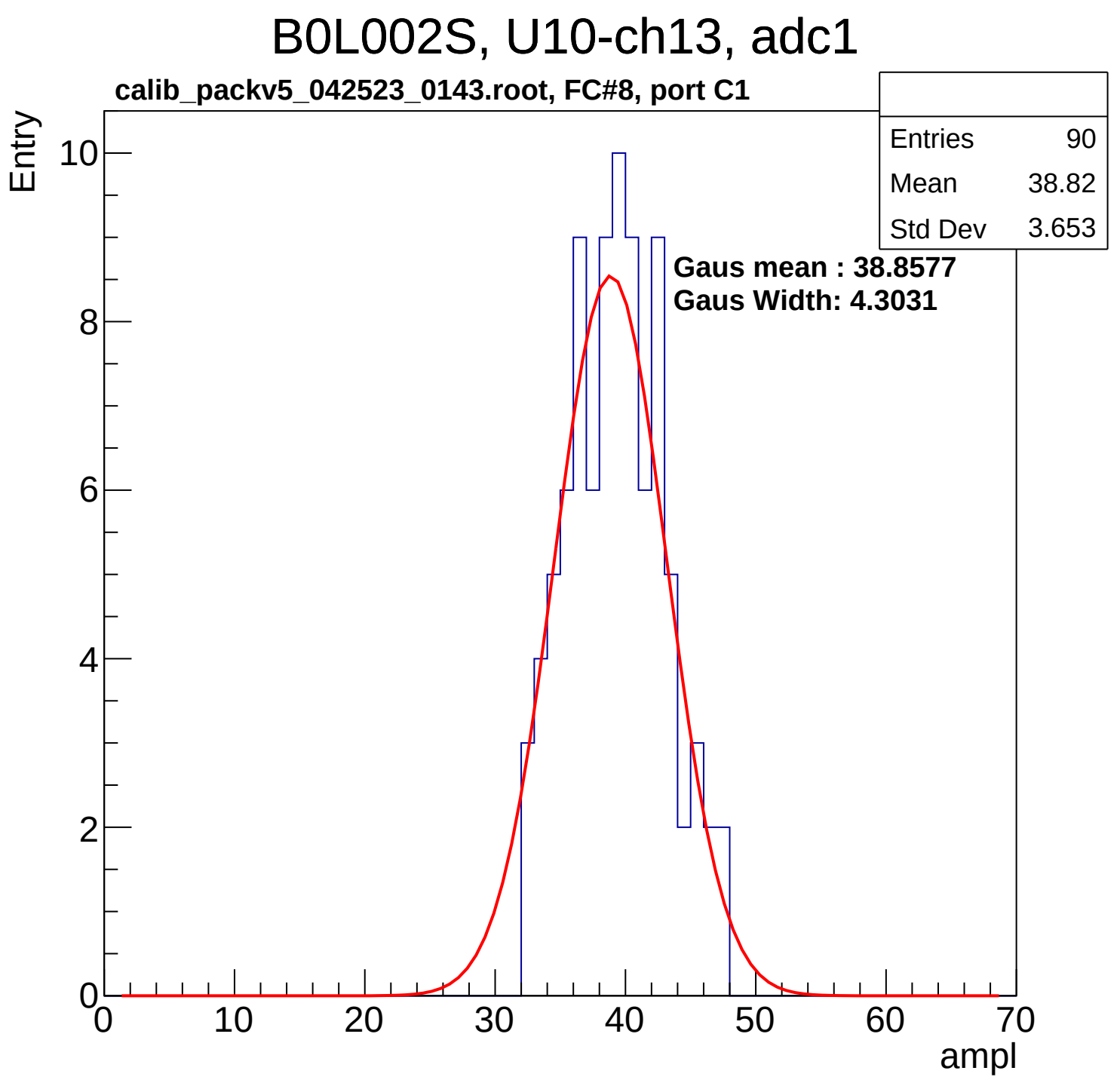

B0L002S, U10-ch12, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.31
Std Dev 7.581
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

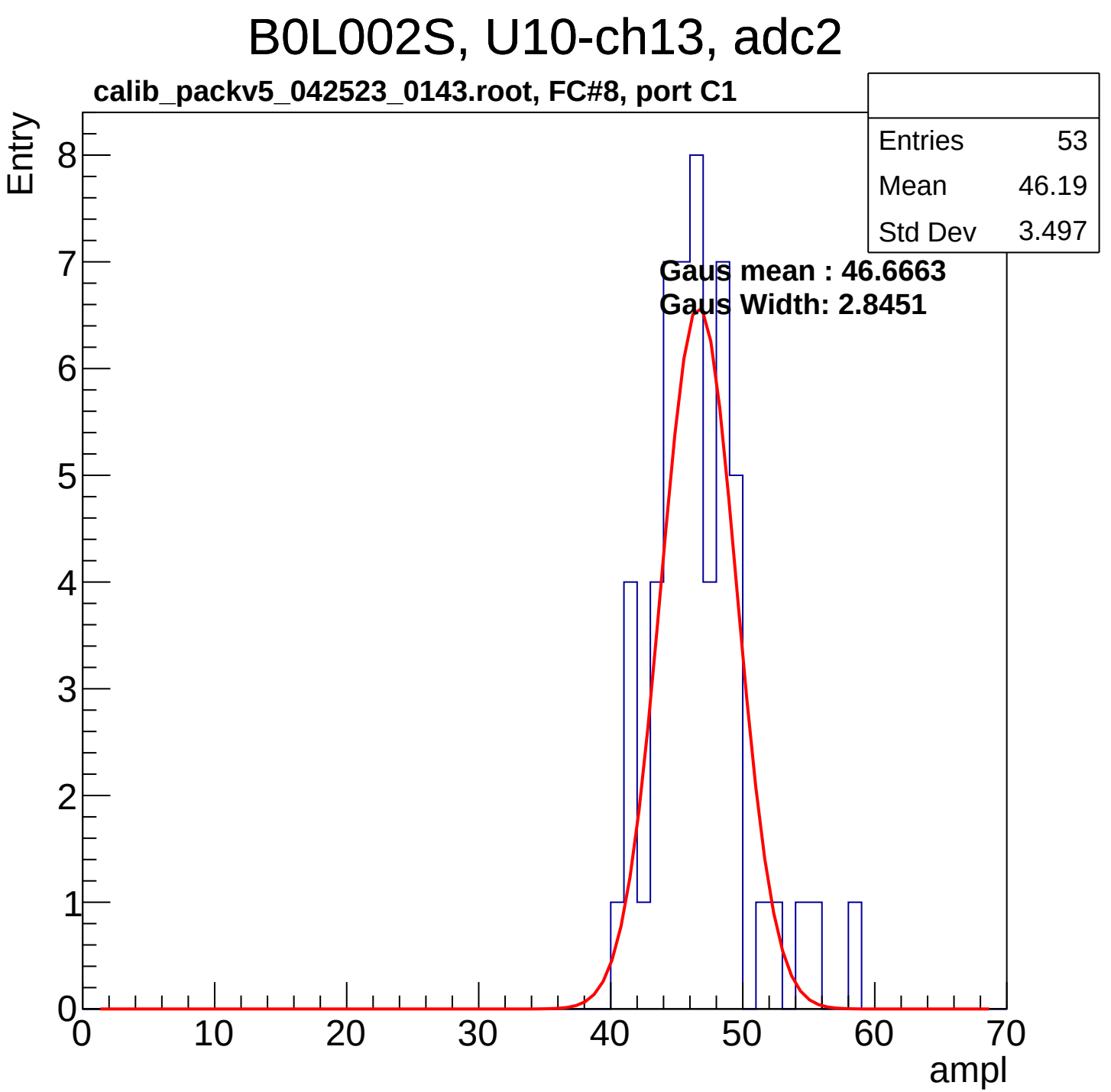

B0L002S, U10-ch12, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 60.61
Std Dev 1.969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

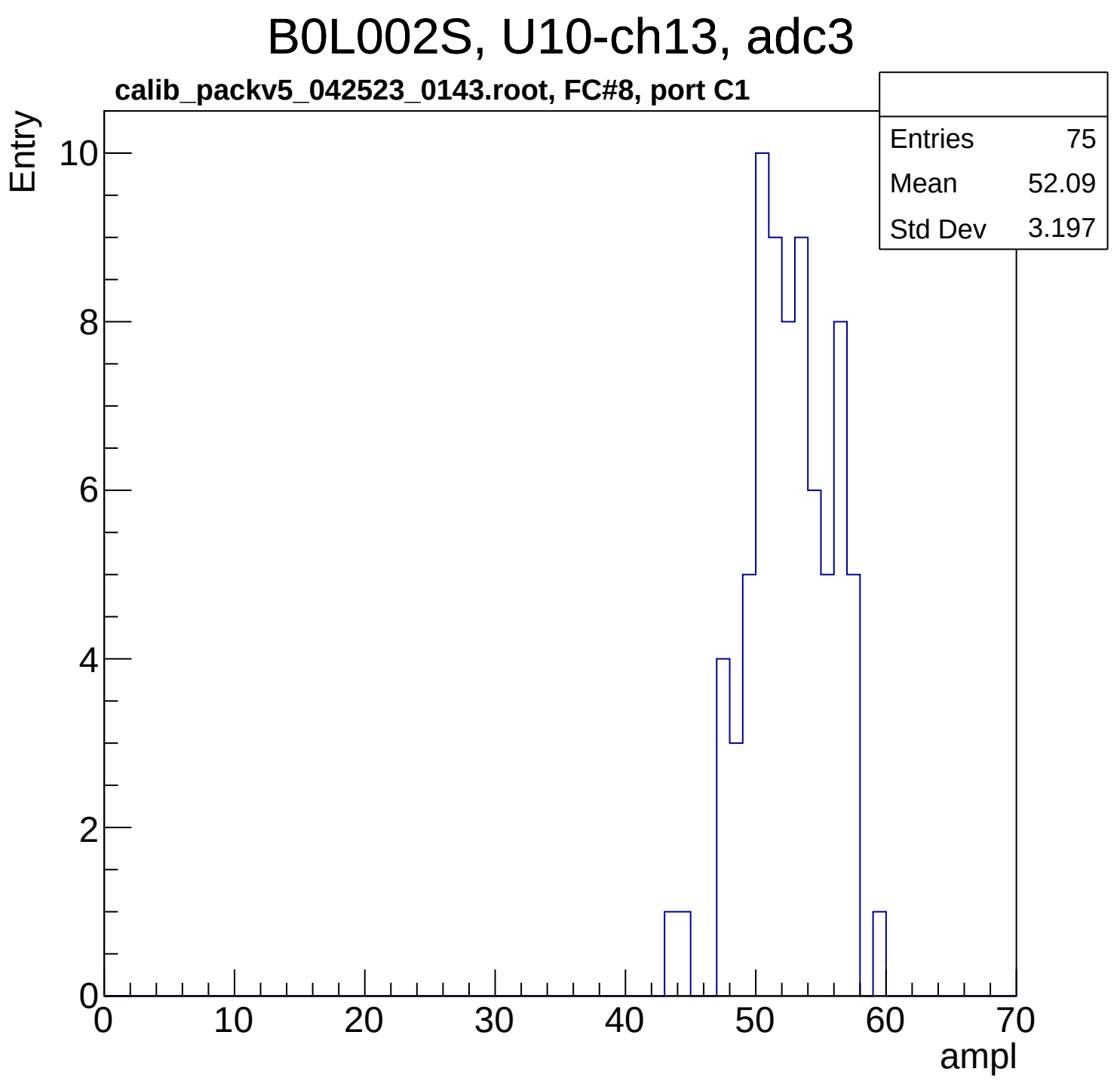

B0L002S, U10-ch12, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

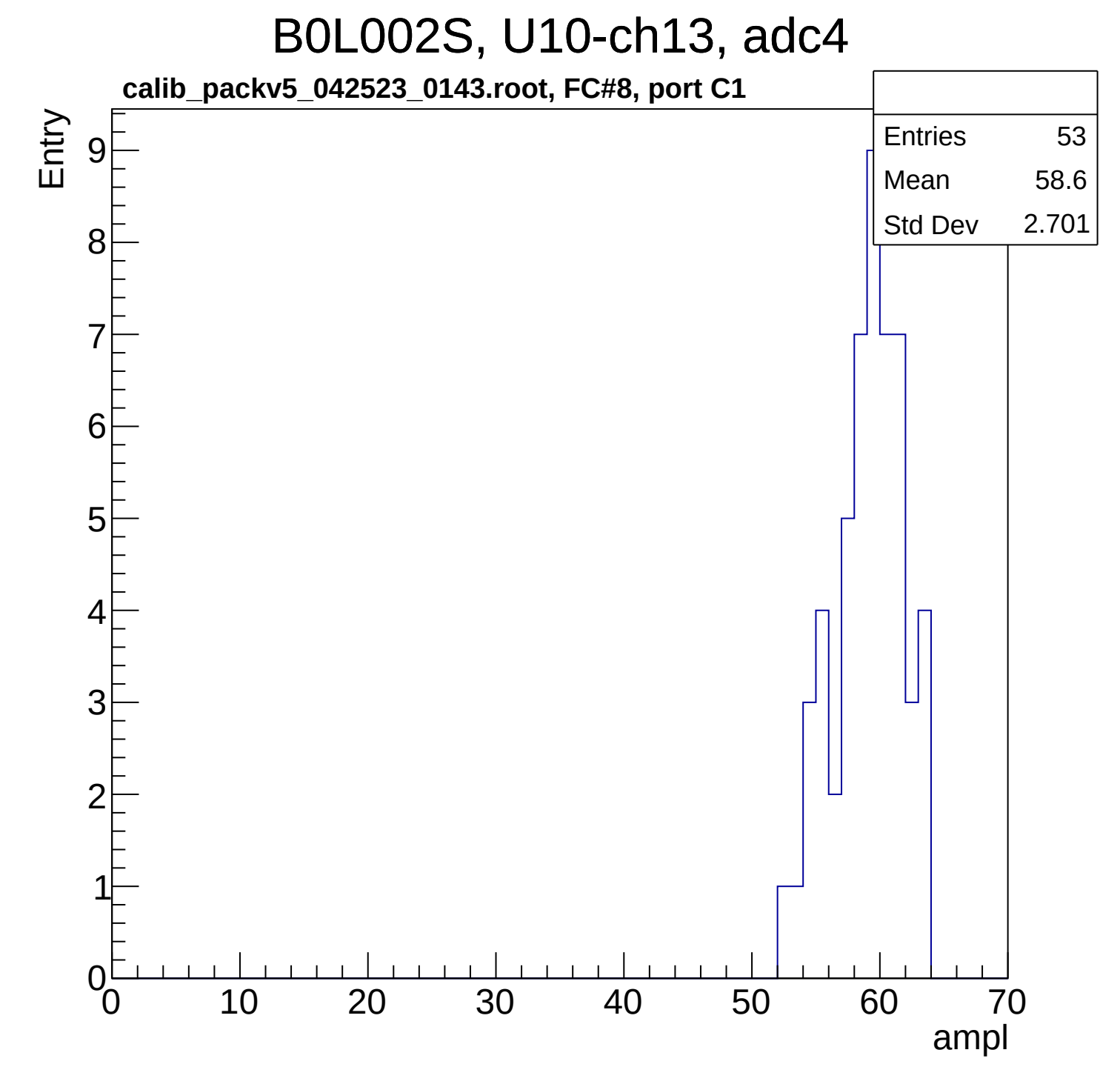

B0L002S, U10-ch12, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

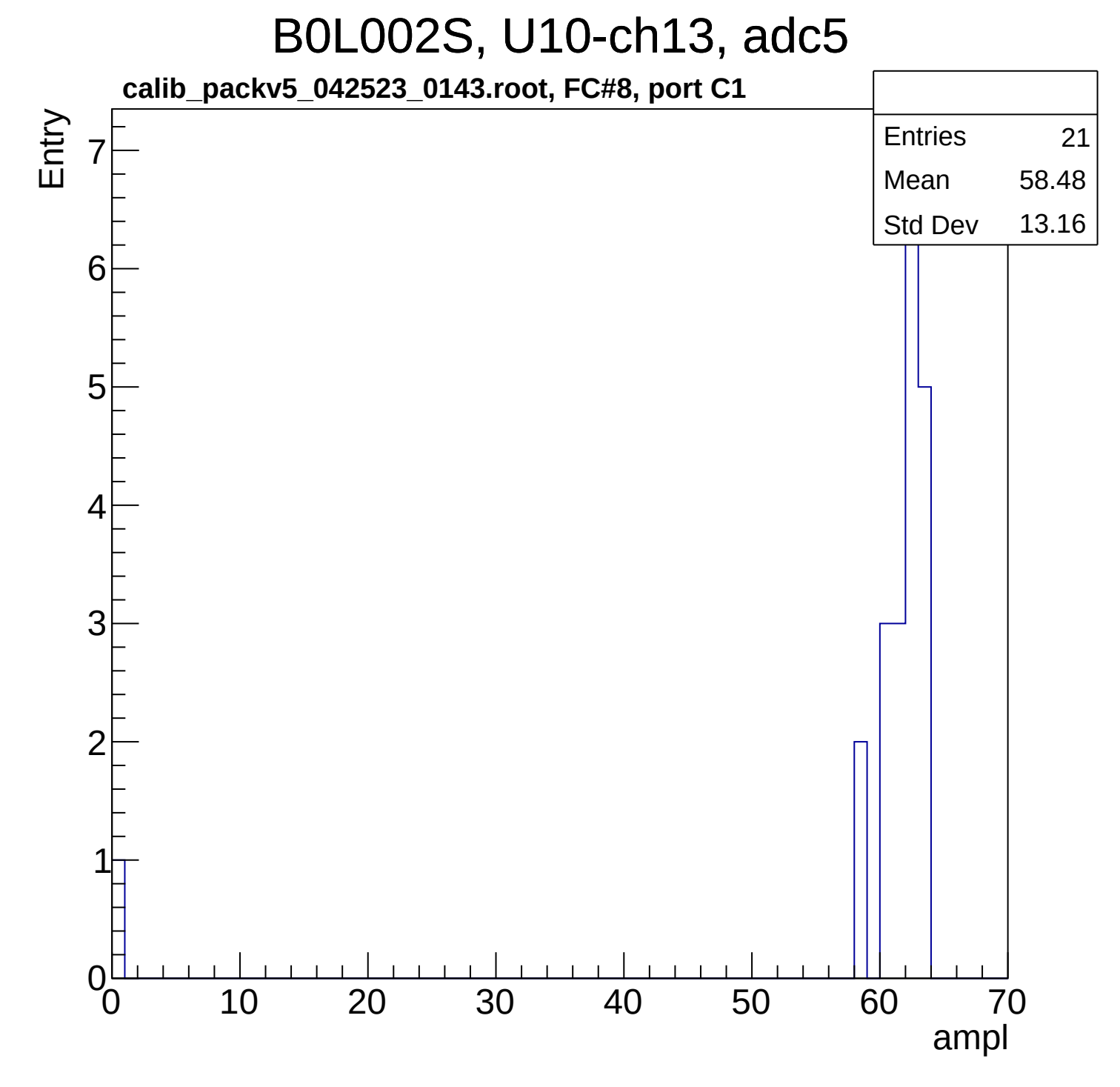

B0L002S, U10-ch13, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.43
Std Dev 3.392
Gaus mean : 30.6358
Gaus Width: 3.3850
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

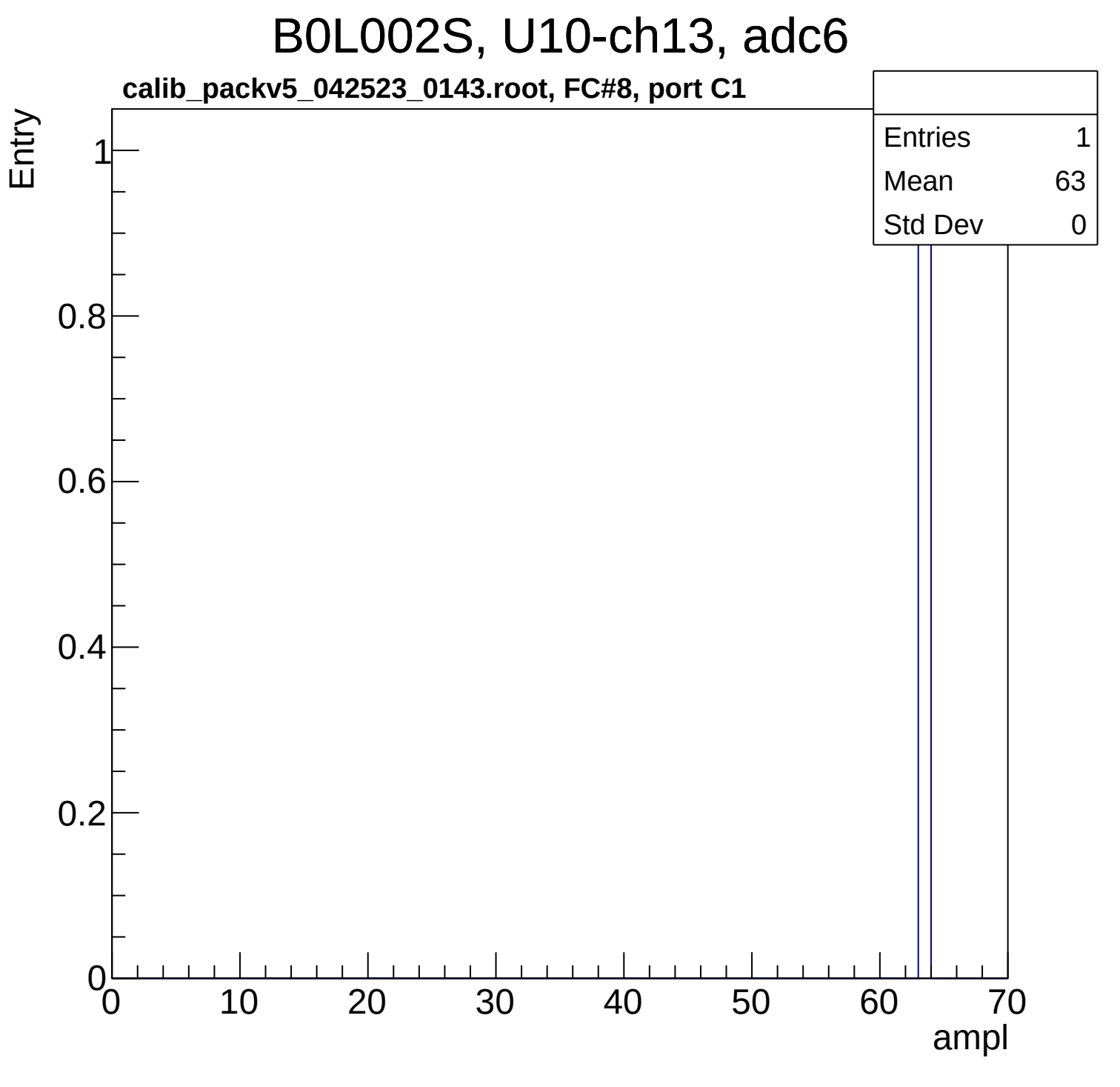

B0L002S, U10-ch13, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 38.82
Std Dev 3.653
Gaus mean : 38.8577
Gaus Width: 4.3031
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

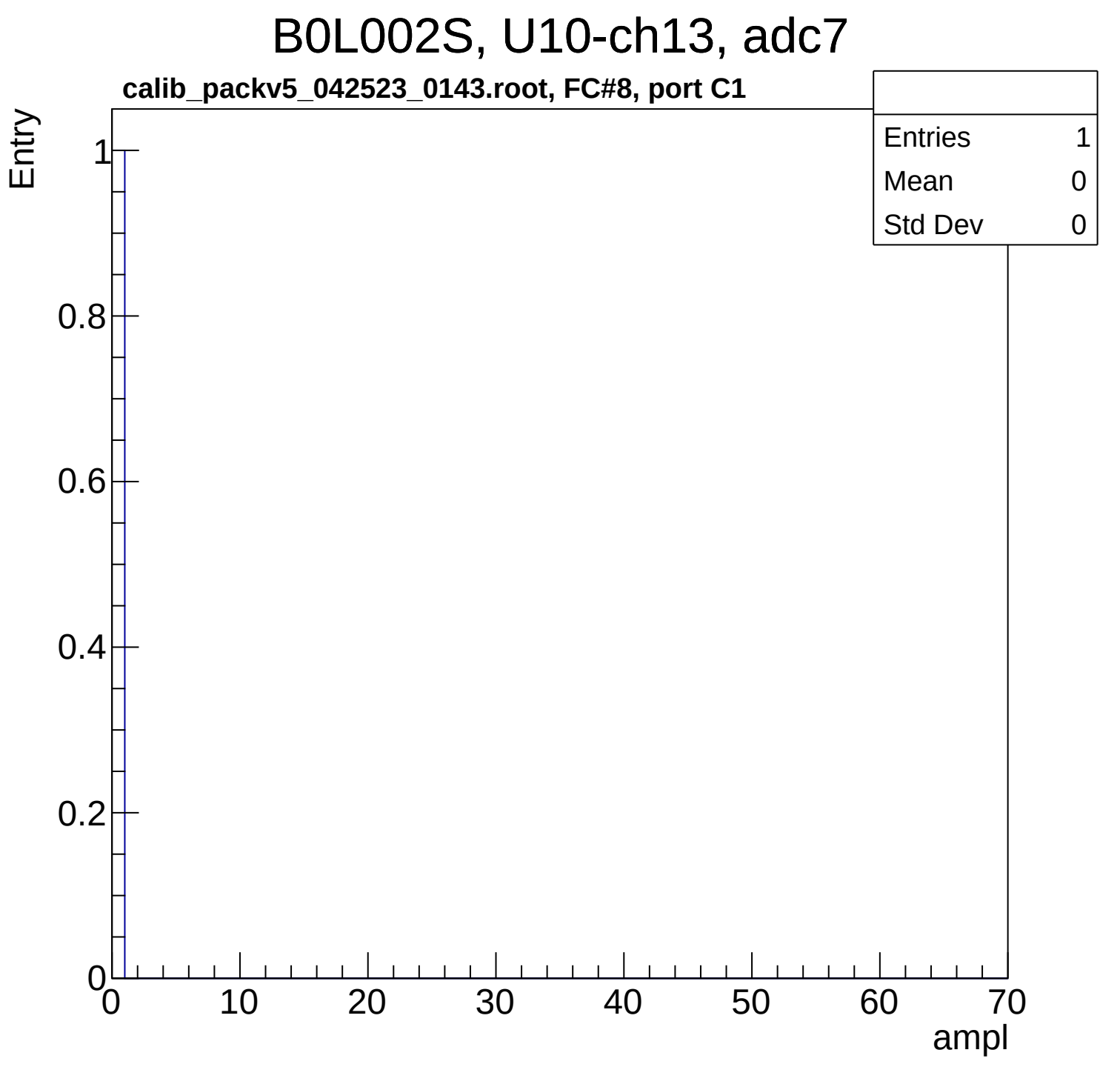

B0L002S, U10-ch13, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 46.19
Std Dev 3.497
Gaus mean : 46.6663
Gaus Width: 2.8451
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

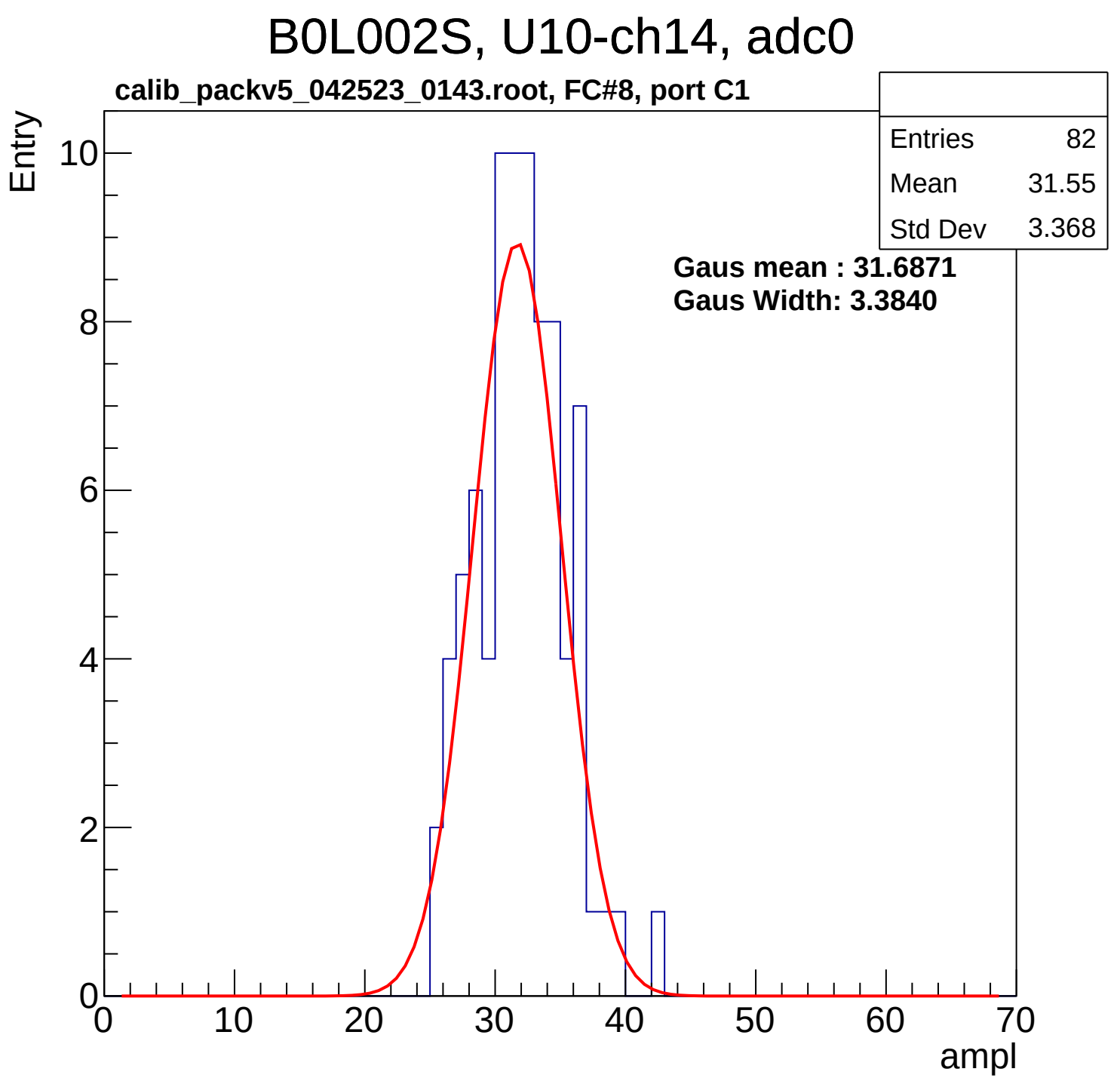

B0L002S, U10-ch13, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 52.09
Std Dev 3.197
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

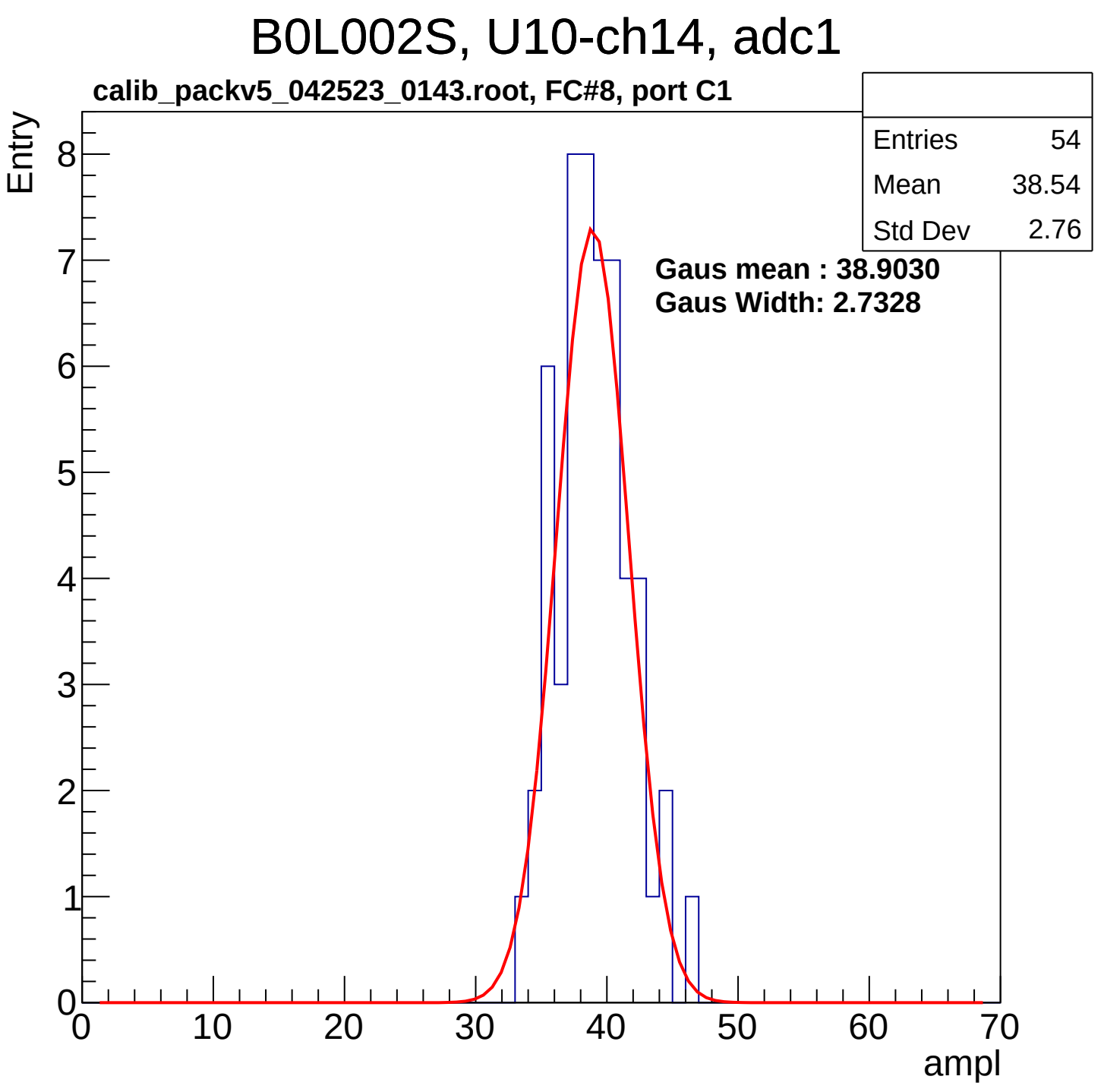

B0L002S, U10-ch13, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 58.6
Std Dev 2.701
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

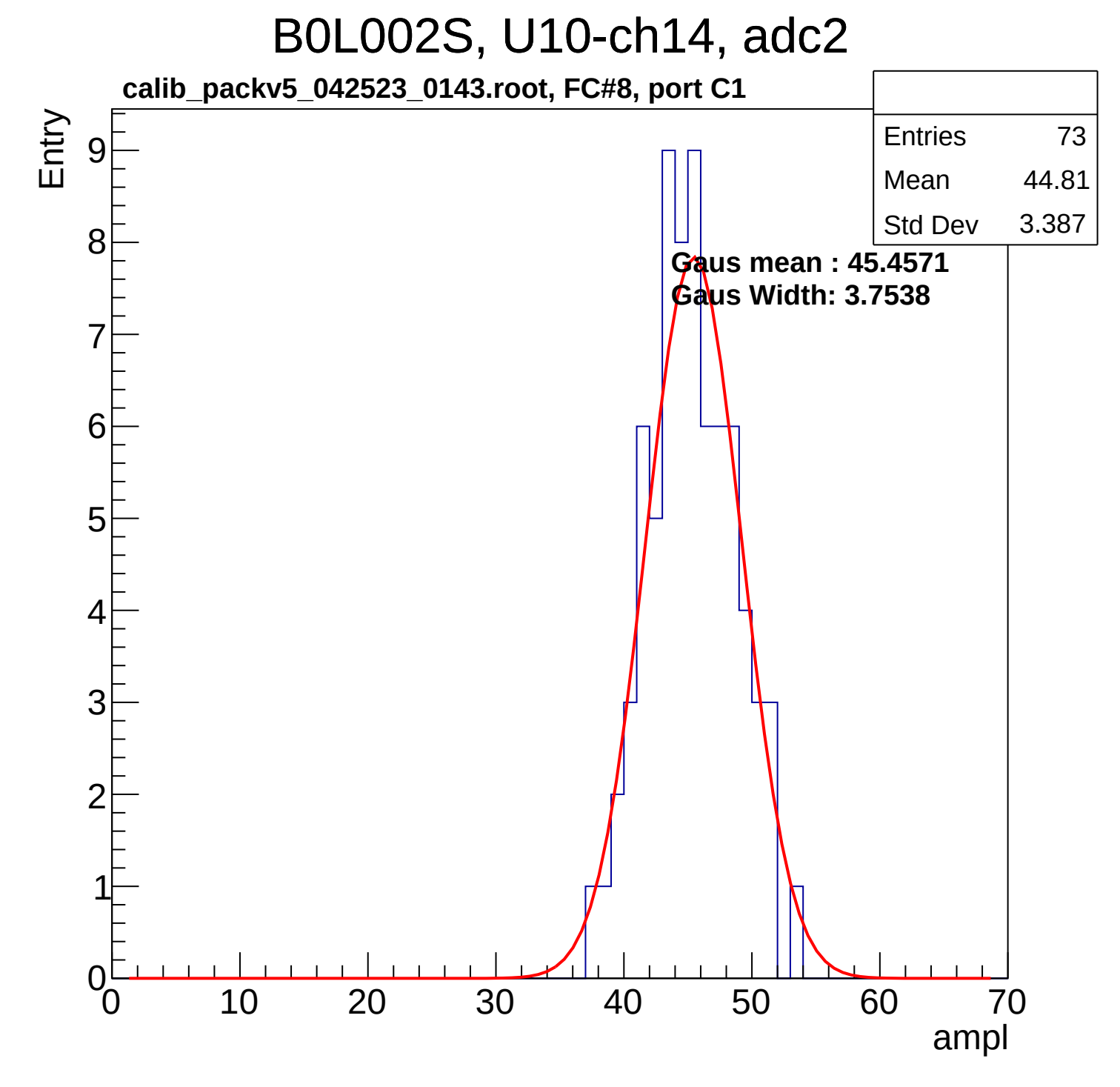

B0L002S, U10-ch13, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.48
Std Dev 13.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

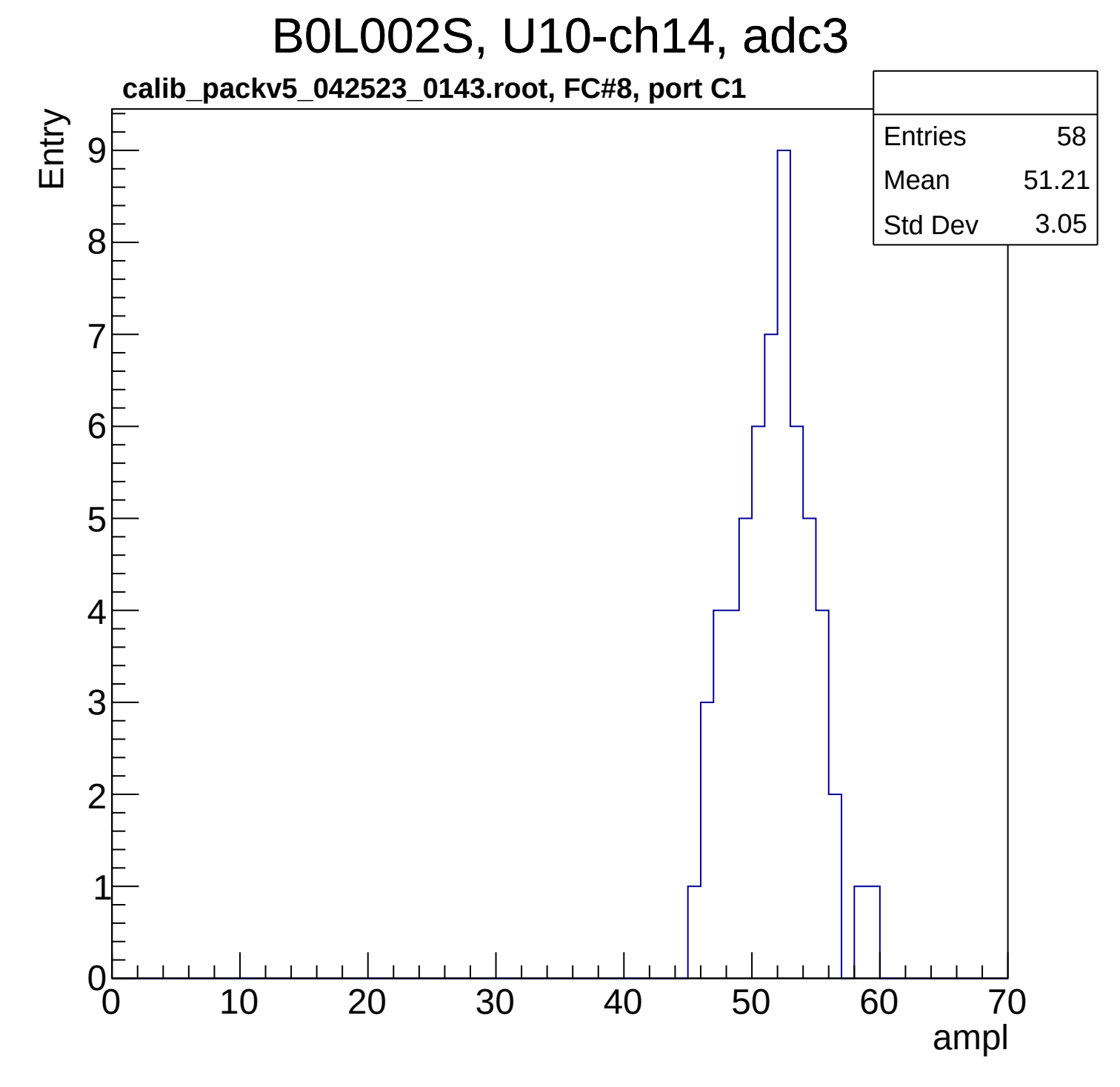

B0L002S, U10-ch13, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

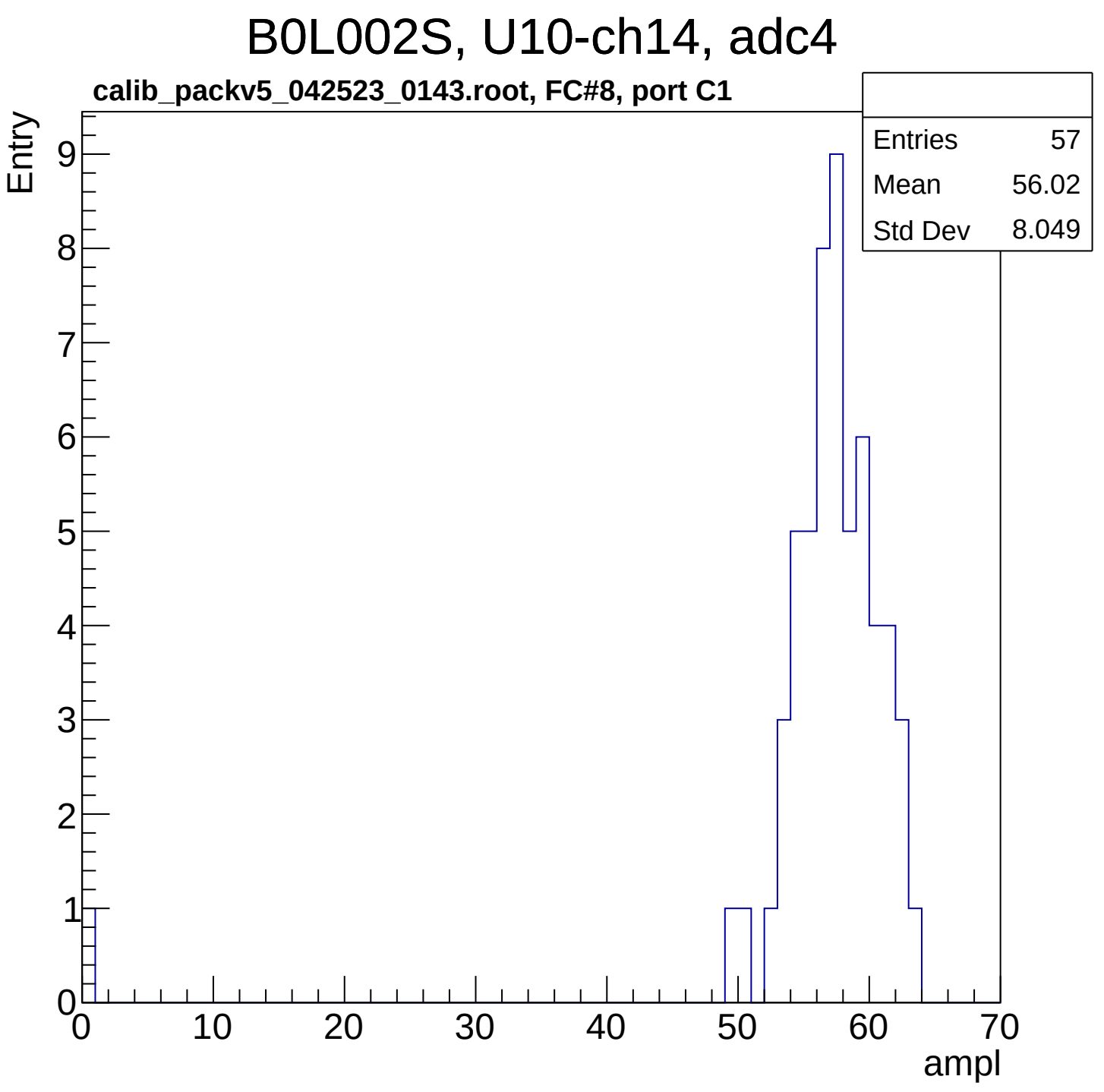

B0L002S, U10-ch13, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

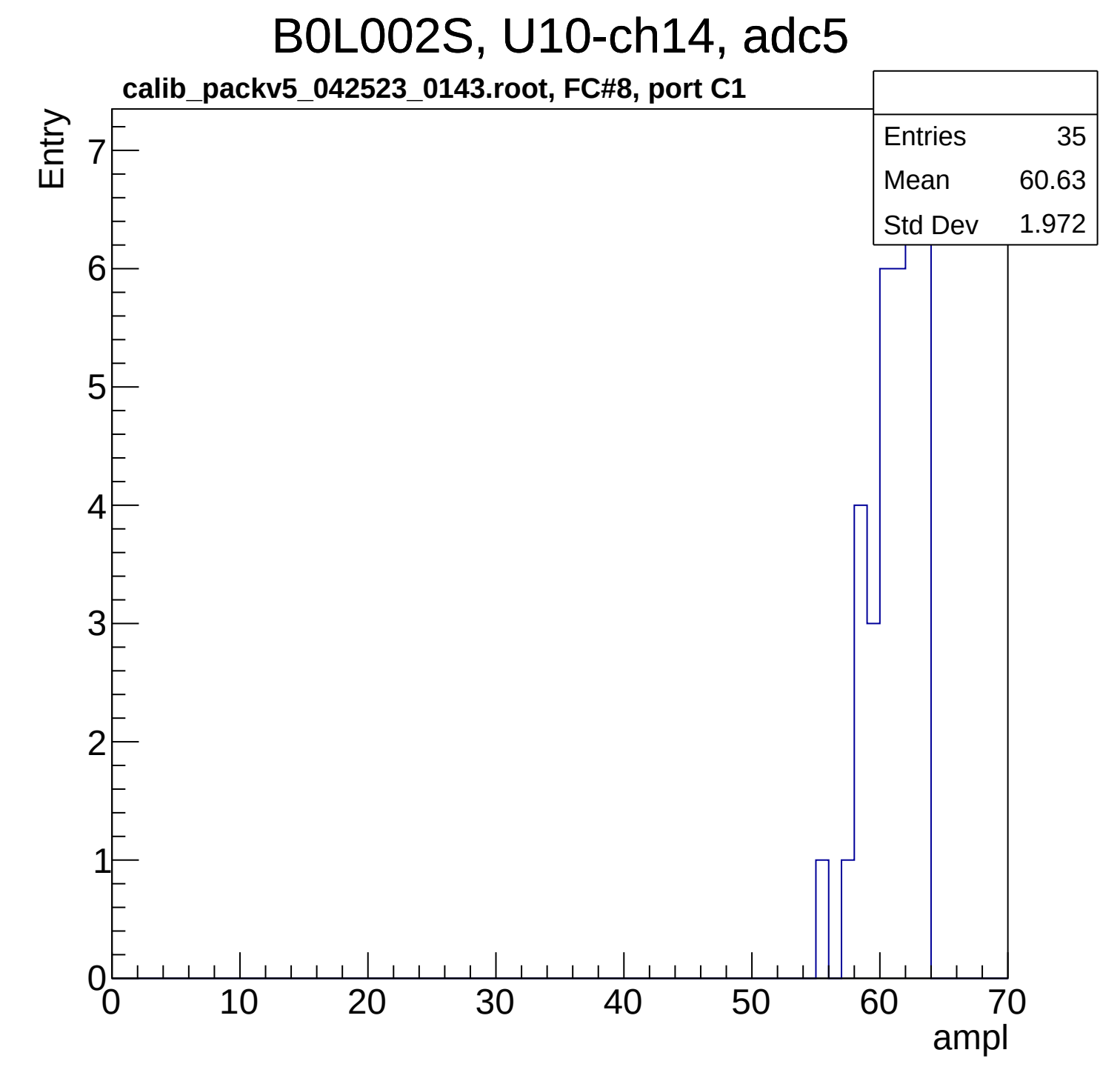

B0L002S, U10-ch14, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 31.55
Std Dev 3.368
Gaus mean : 31.6871
Gaus Width: 3.3840
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

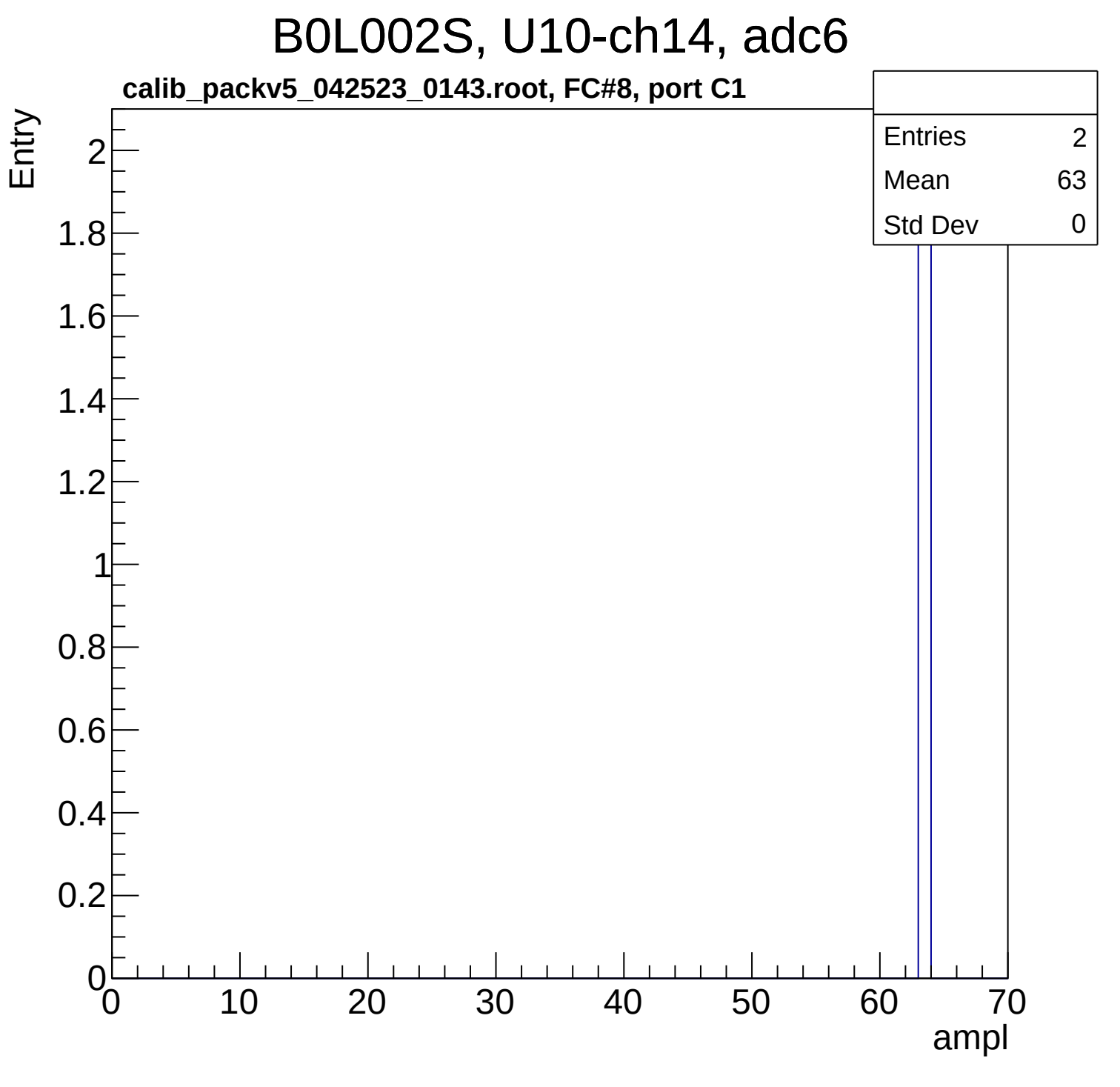

B0L002S, U10-ch14, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 38.54
Std Dev 2.76
Gaus mean : 38.9030
Gaus Width: 2.7328
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

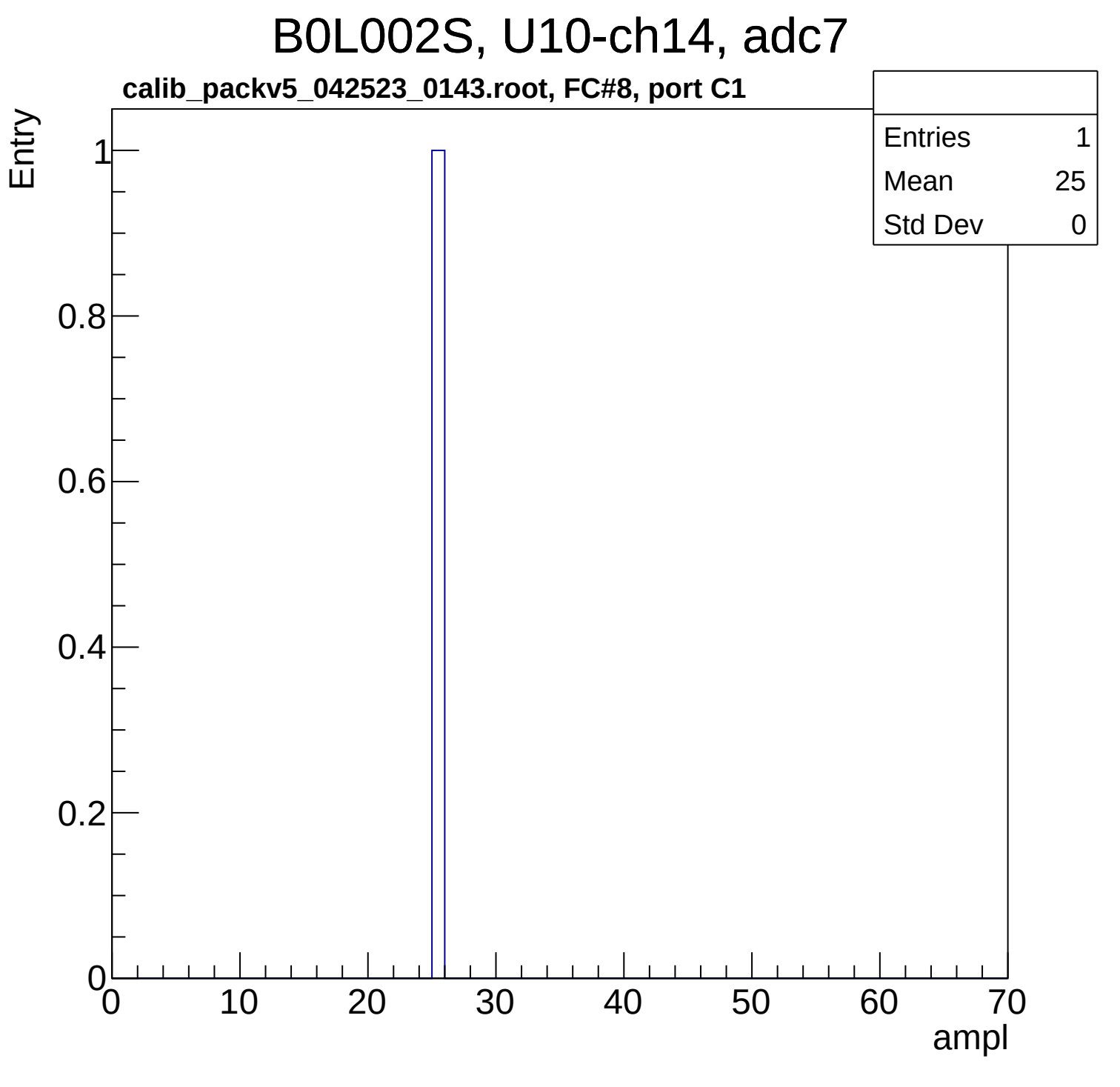

B0L002S, U10-ch14, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 44.81
Std Dev 3.387
Gaus mean : 45.4571
Gaus Width: 3.7538
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

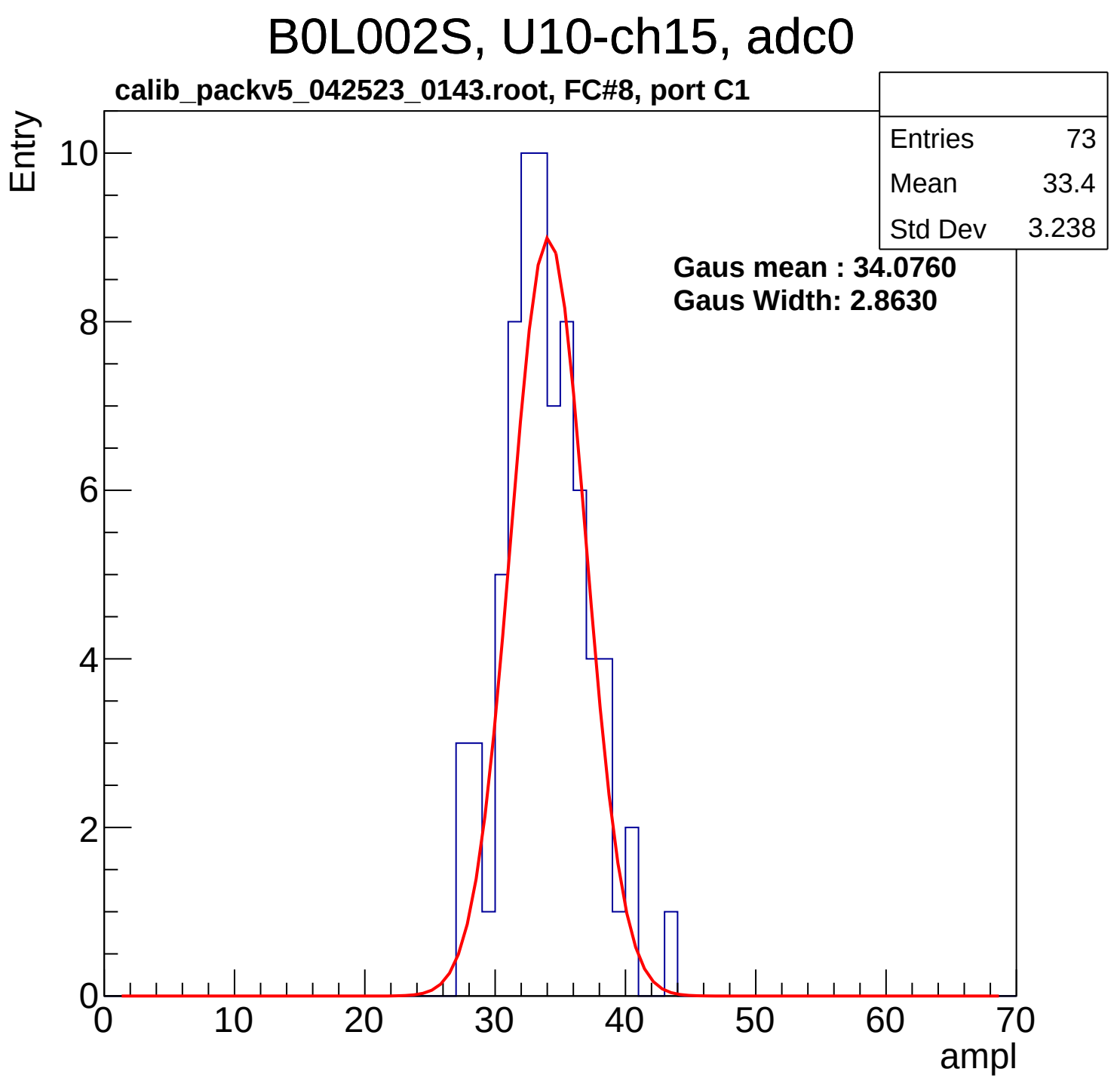

B0L002S, U10-ch14, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.21
Std Dev 3.05
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

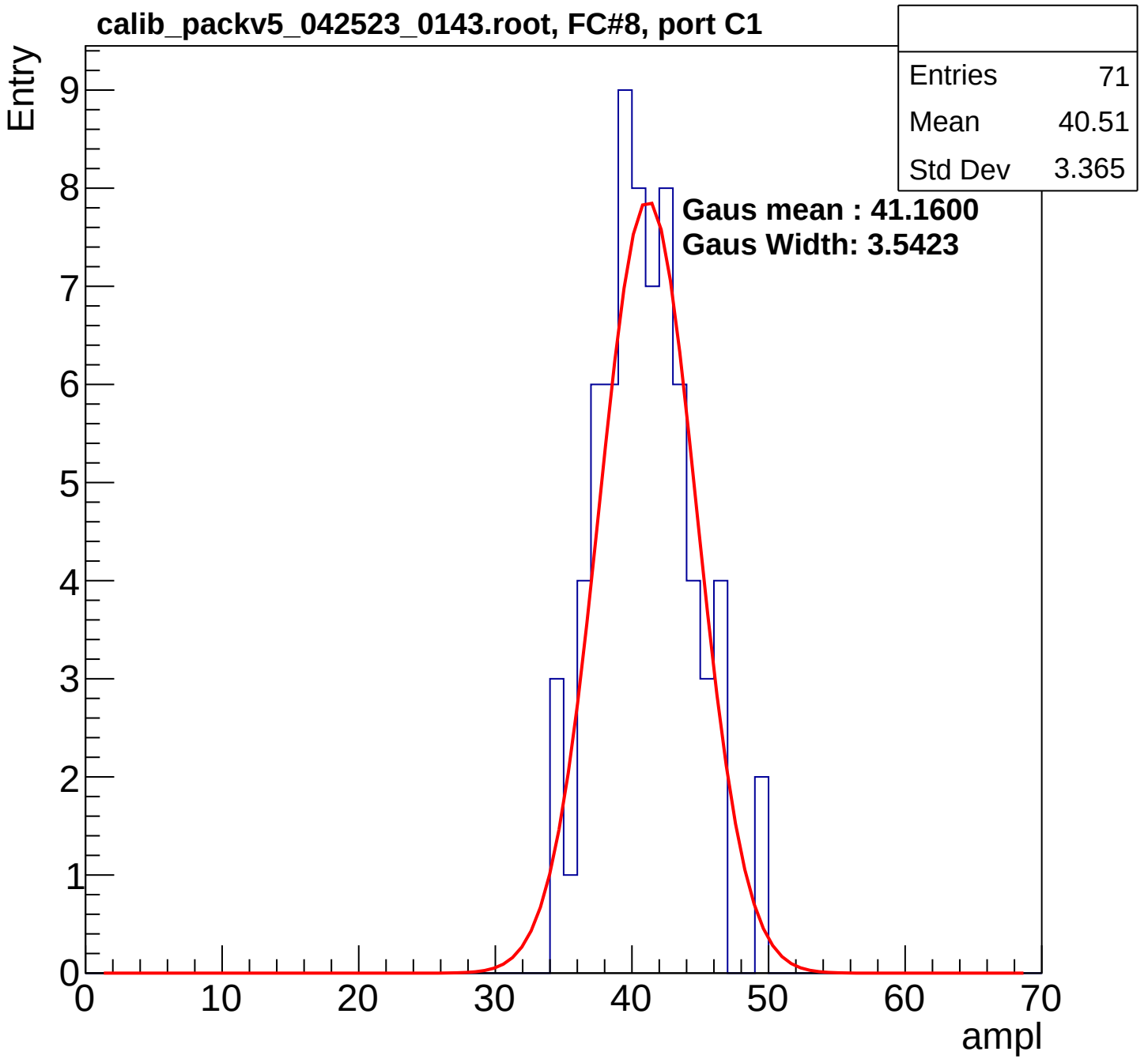

B0L002S, U10-ch14, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.02
Std Dev 8.049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

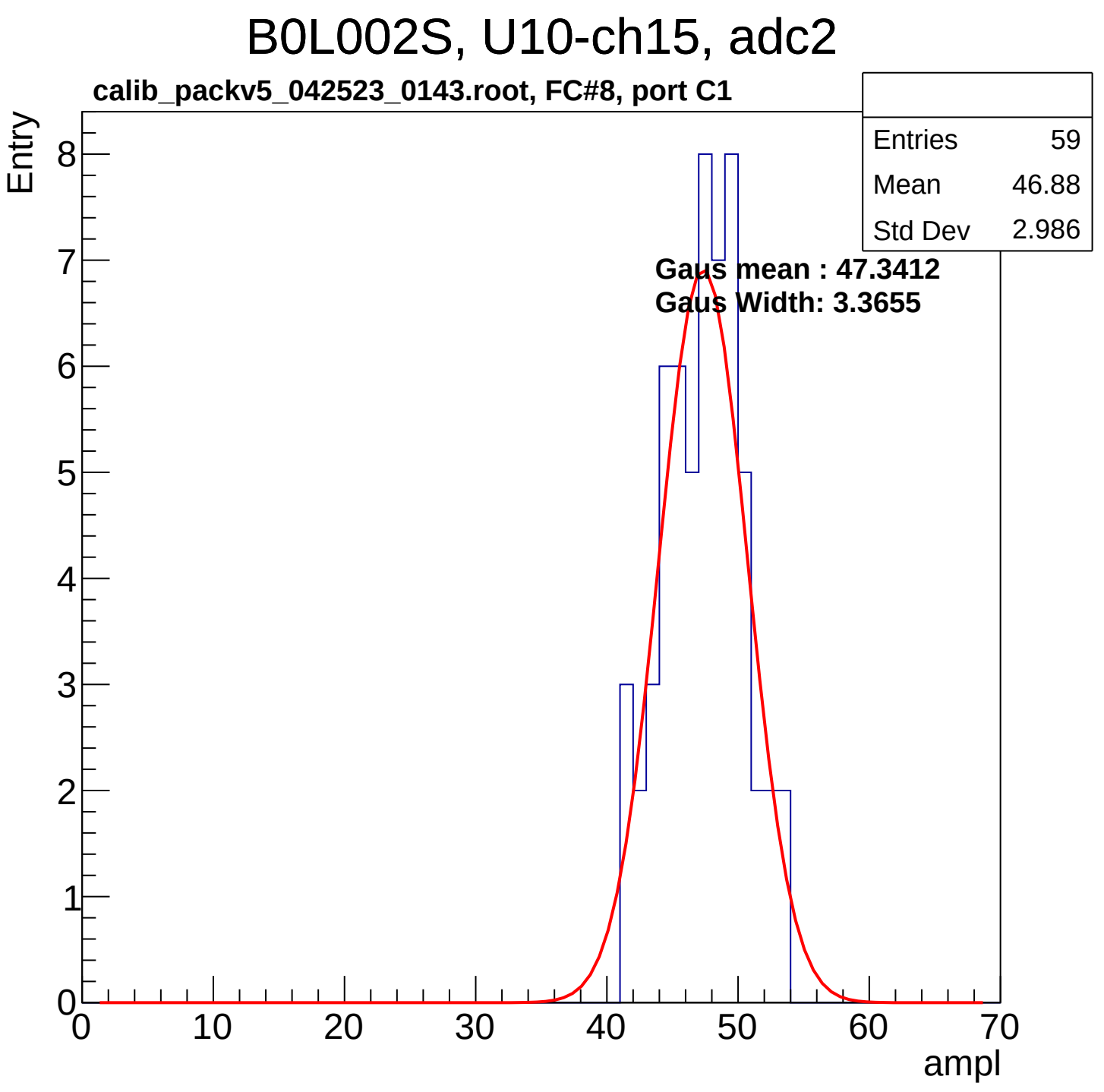

B0L002S, U10-ch14, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.63
Std Dev 1.972
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

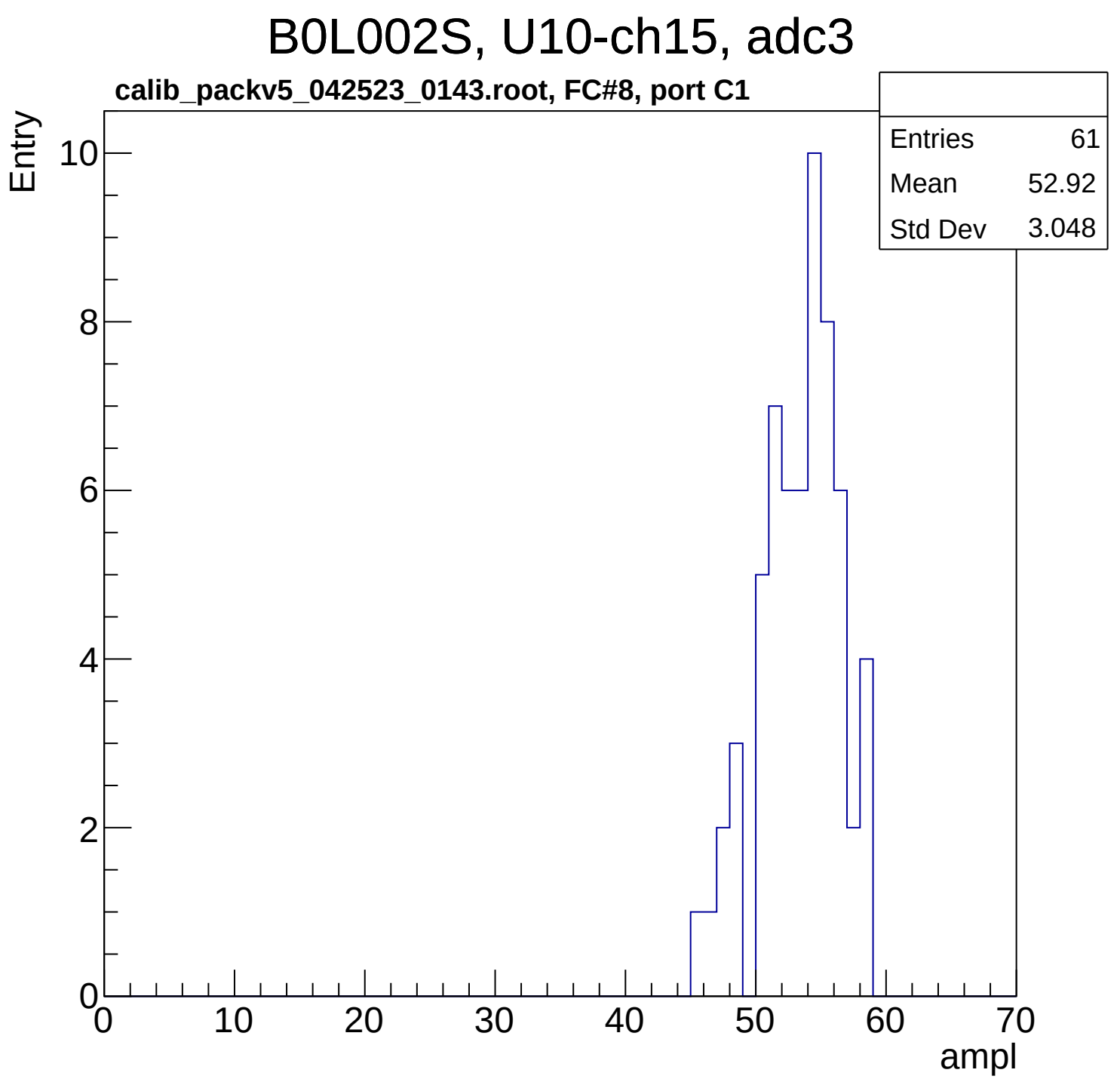

B0L002S, U10-ch14, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

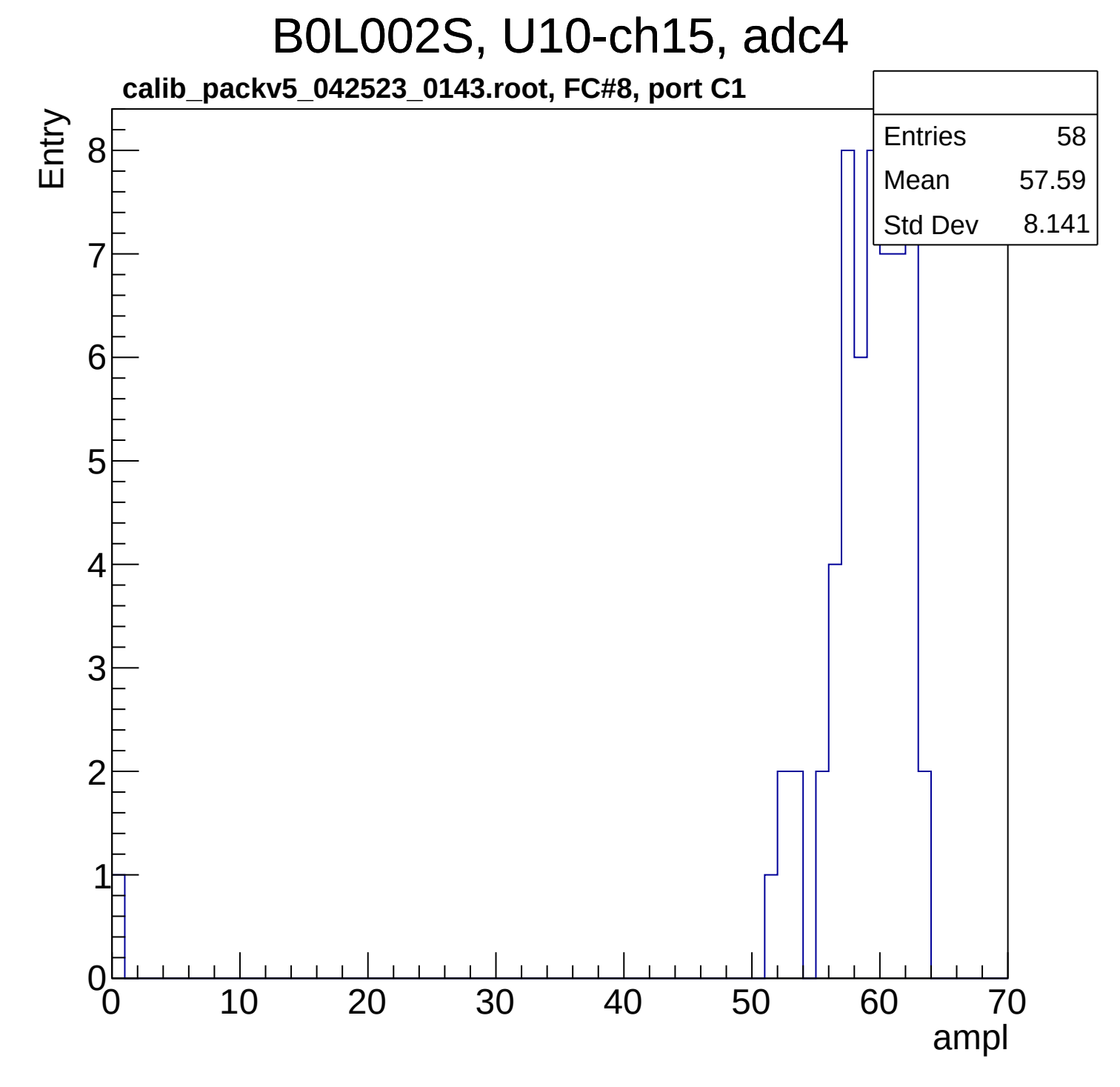

B0L002S, U10-ch14, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

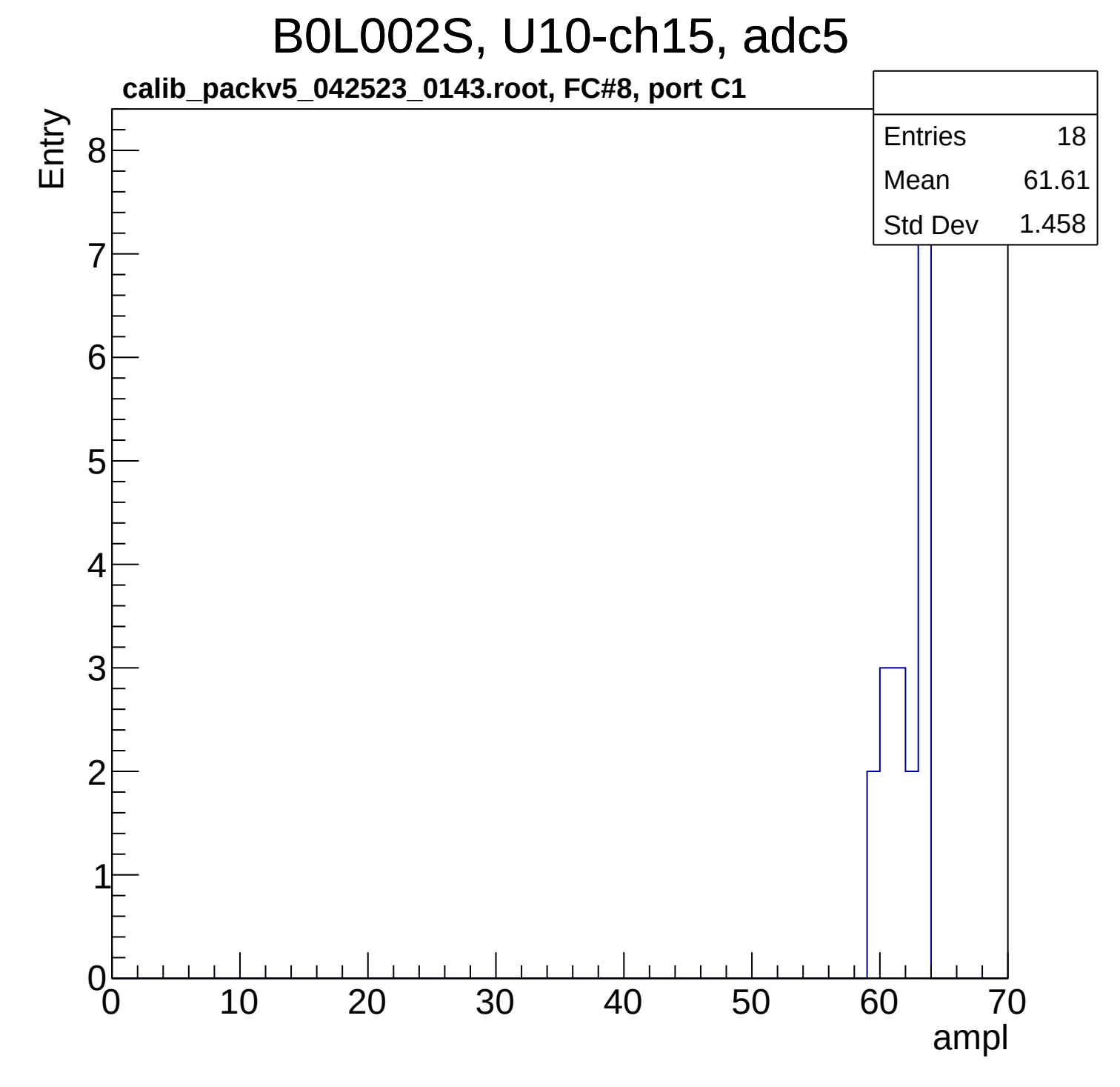

B0L002S, U10-ch15, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 33.4
Std Dev 3.238
Gaus mean : 34.0760
Gaus Width: 2.8630
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

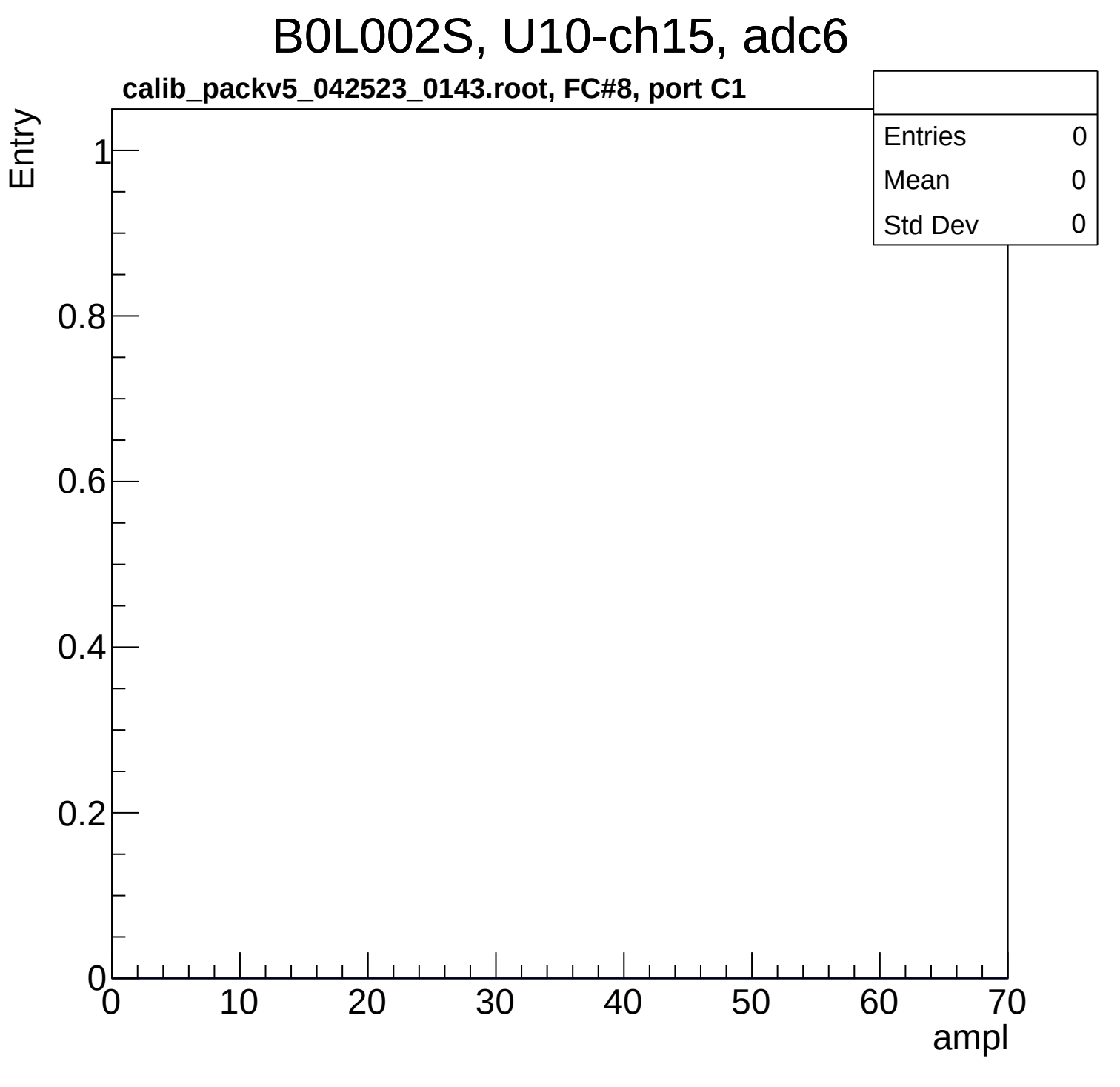

B0L002S, U10-ch15, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 40.51
Std Dev 3.365
Gaus mean : 41.1600
Gaus Width: 3.5423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

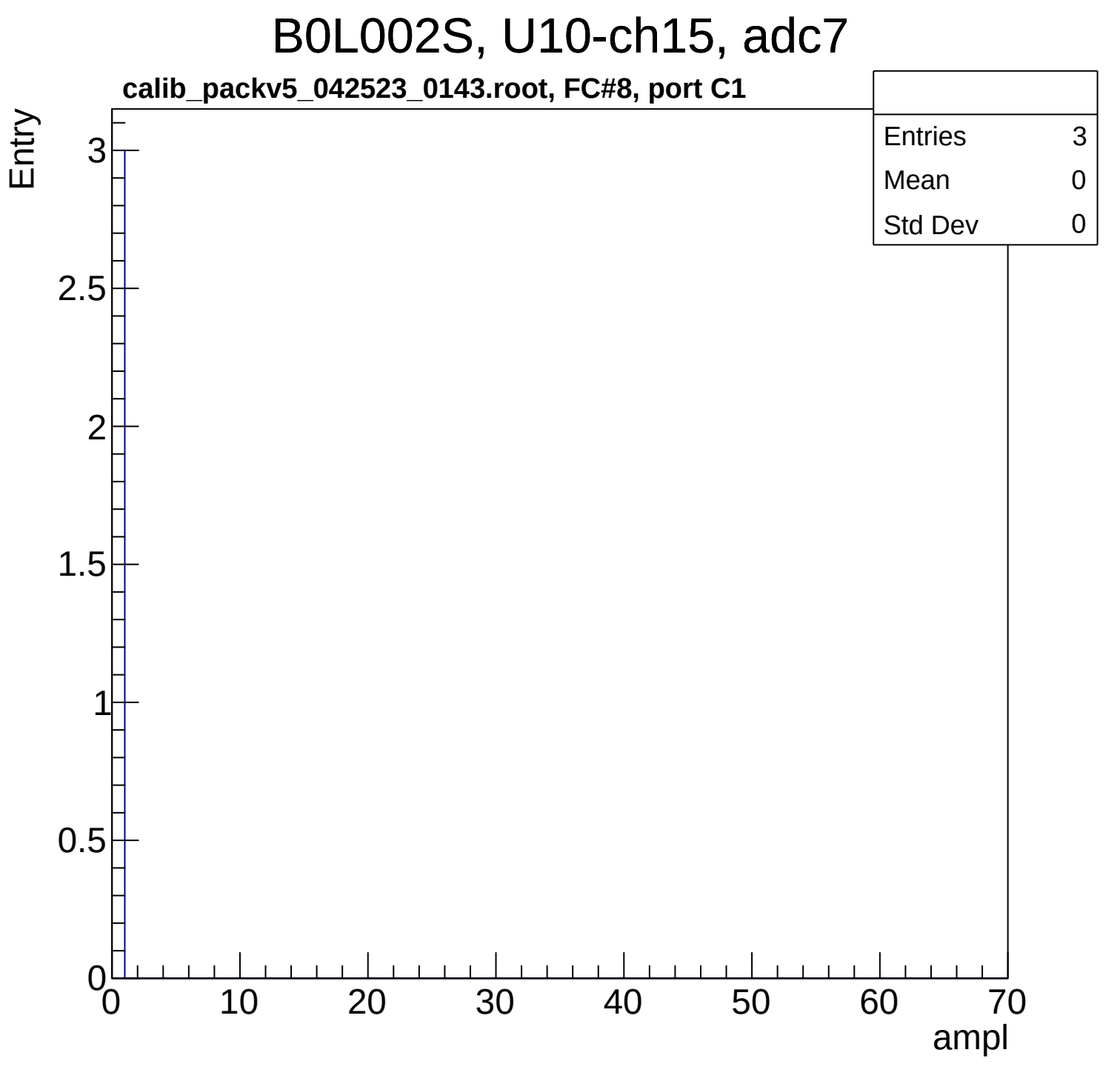

B0L002S, U10-ch15, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 46.88
Std Dev 2.986
Gaus mean : 47.3412
Gaus Width: 3.3655
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

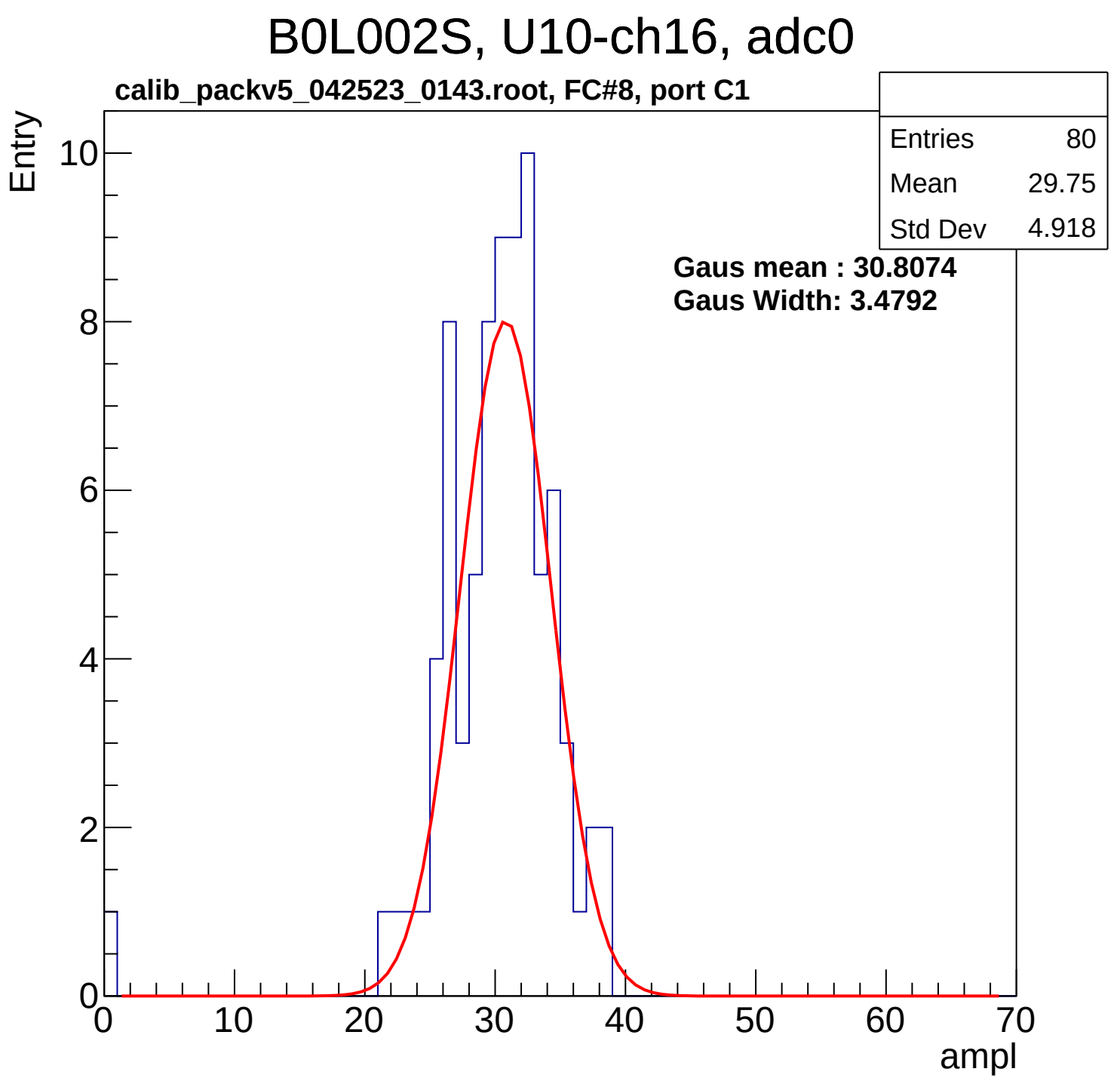

B0L002S, U10-ch15, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 52.92
Std Dev 3.048
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

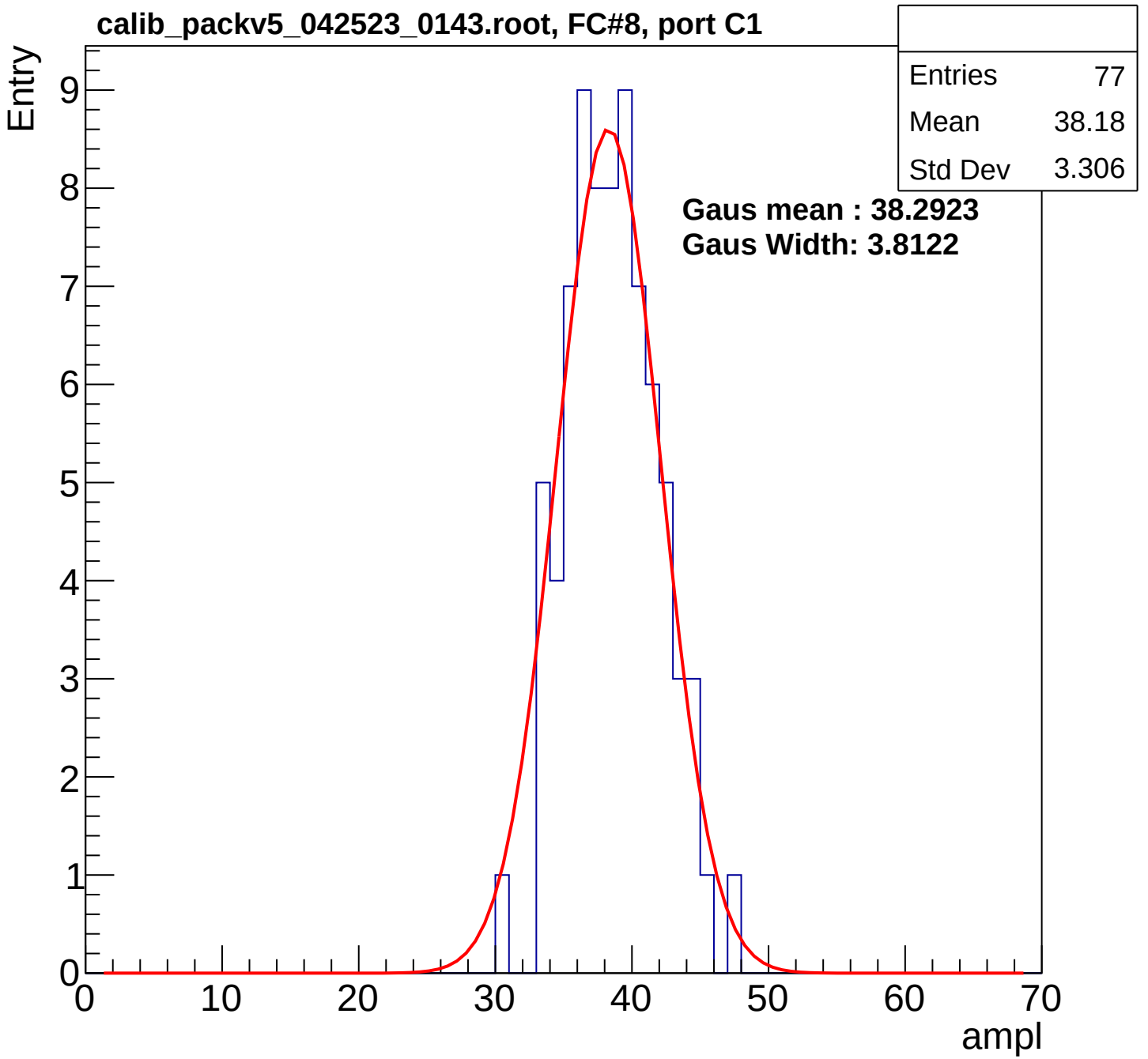

B0L002S, U10-ch15, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.59
Std Dev 8.141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

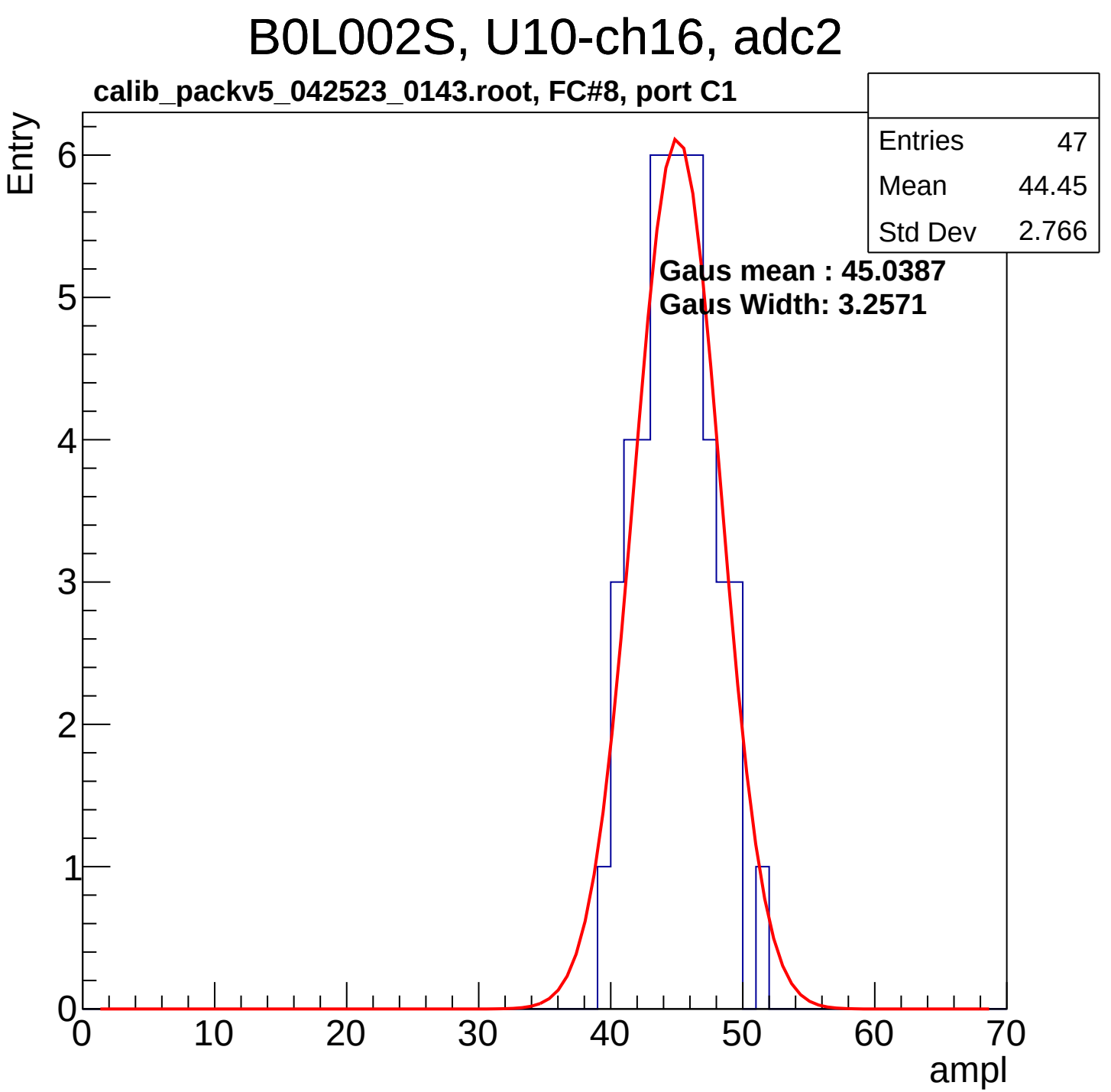

B0L002S, U10-ch15, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 61.61
Std Dev 1.458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

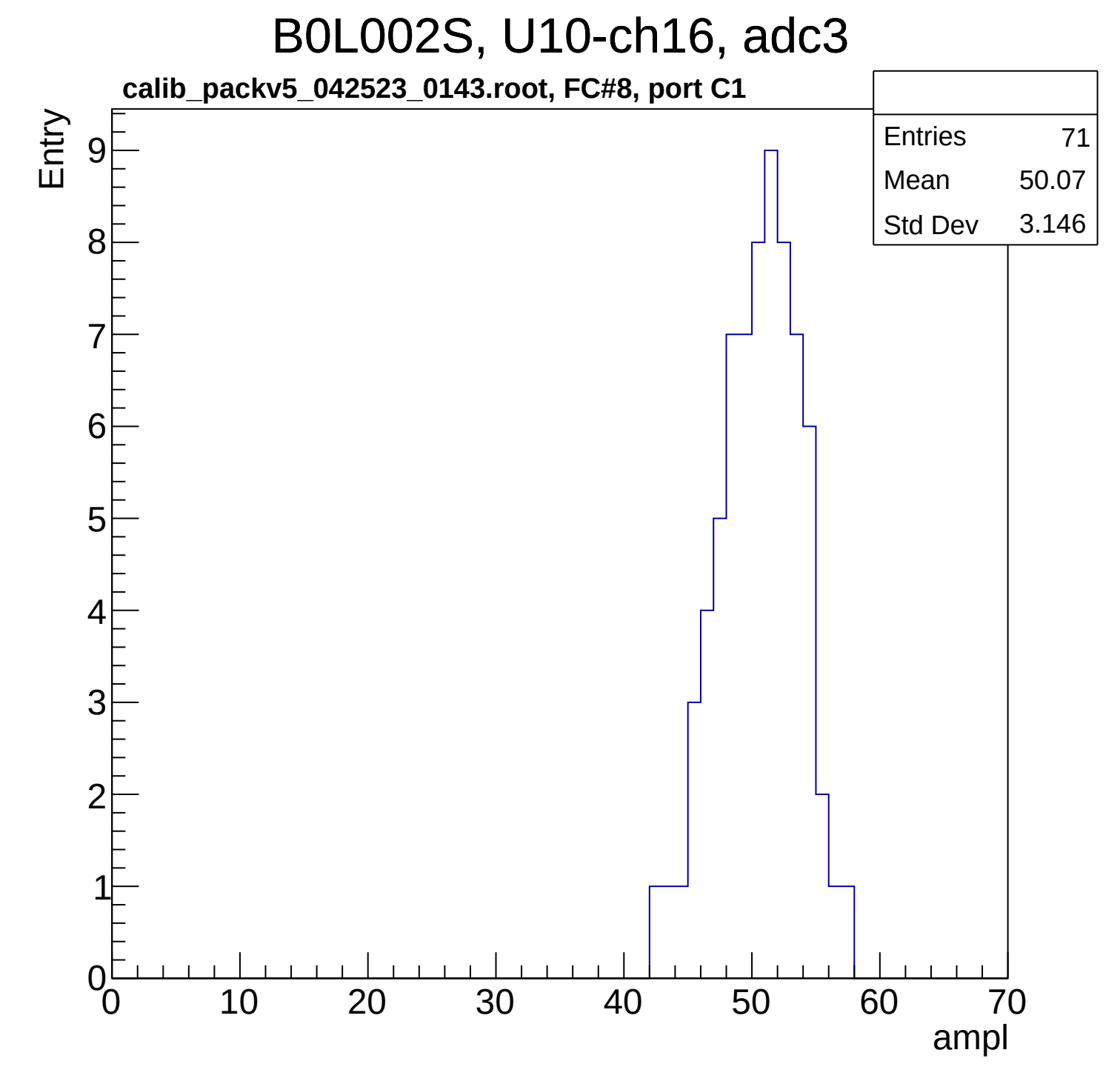

B0L002S, U10-ch15, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

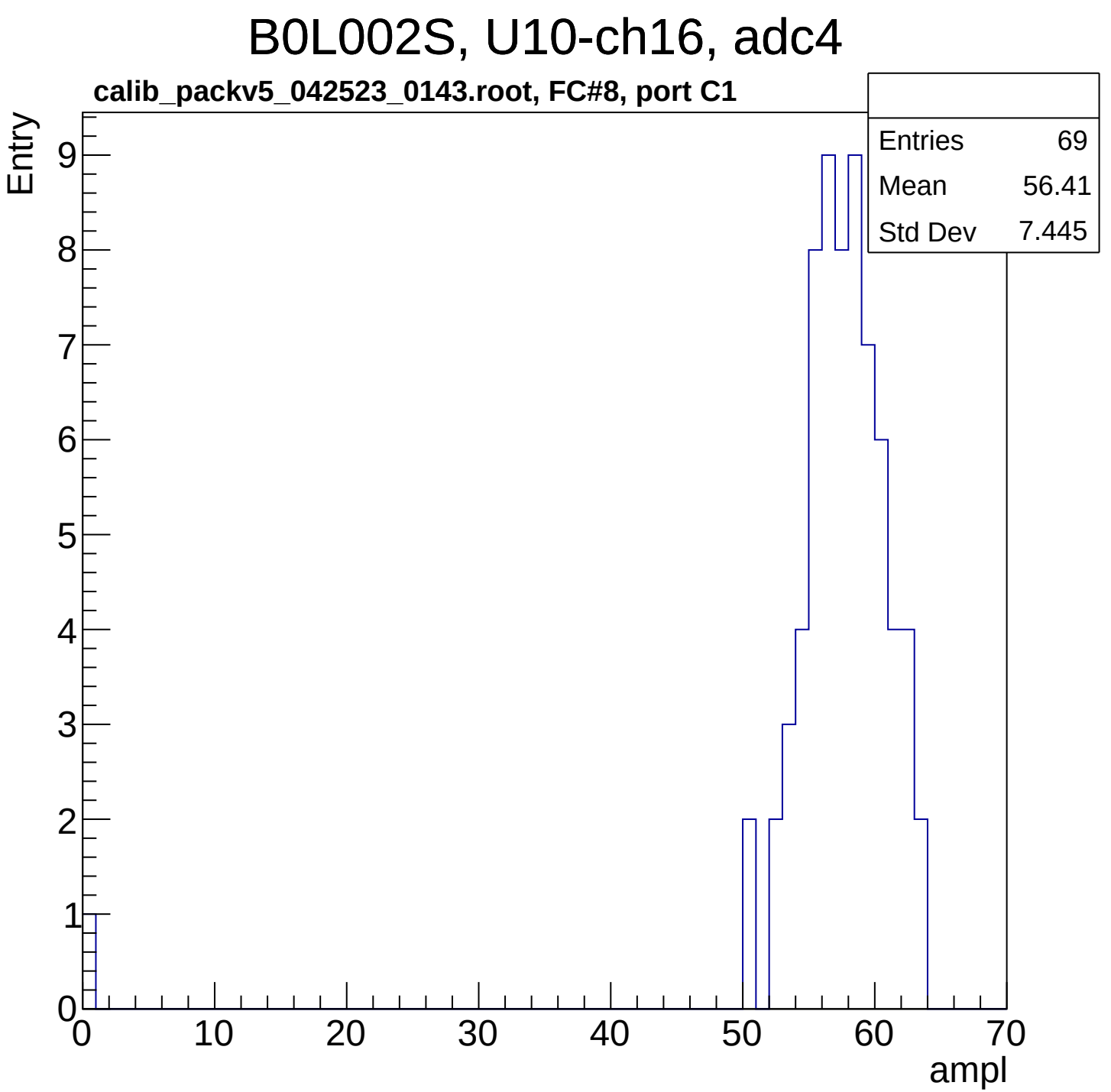

B0L002S, U10-ch15, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

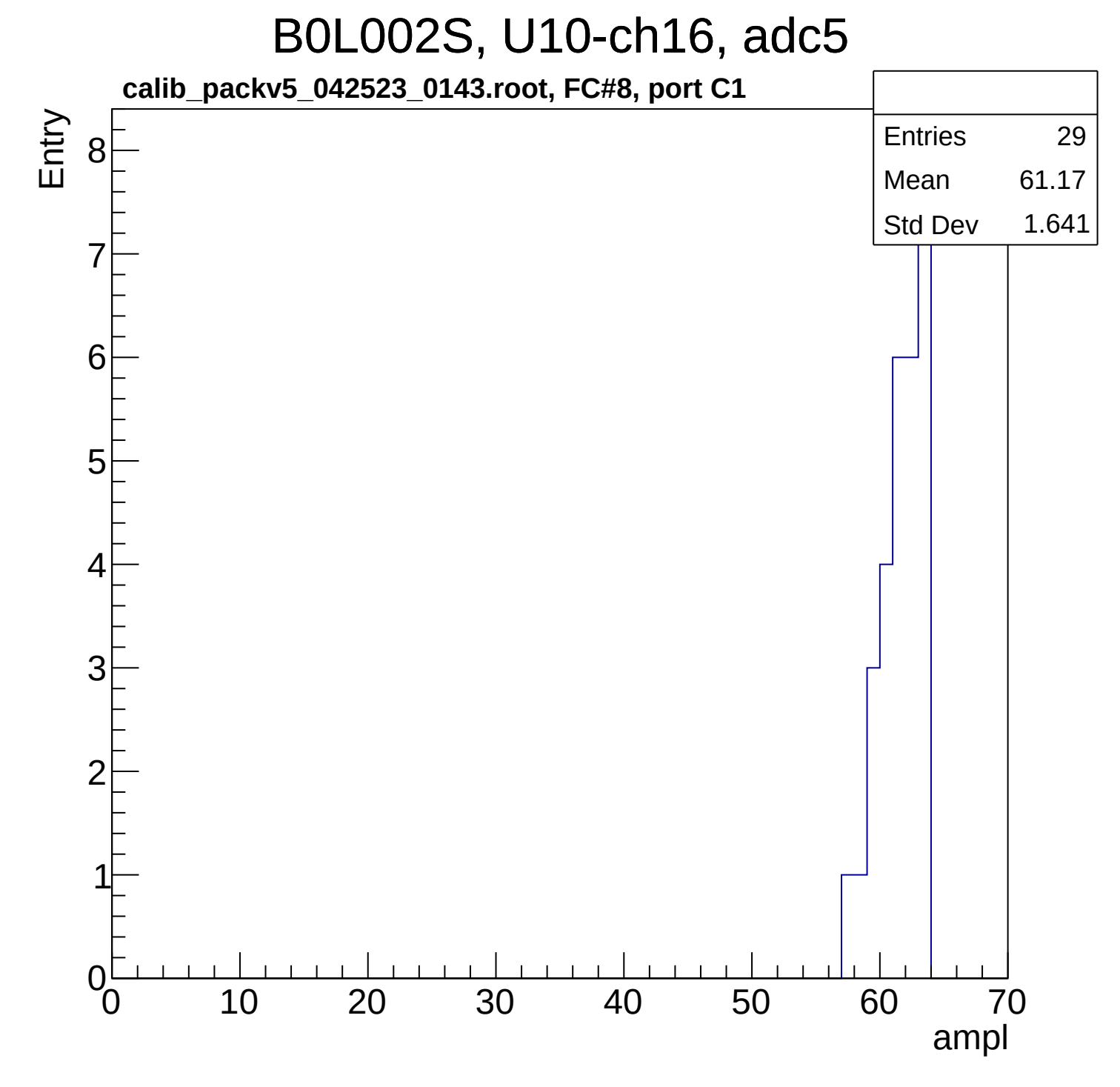

B0L002S, U10-ch16, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 29.75
Std Dev 4.918
Gaus mean : 30.8074
Gaus Width: 3.4792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

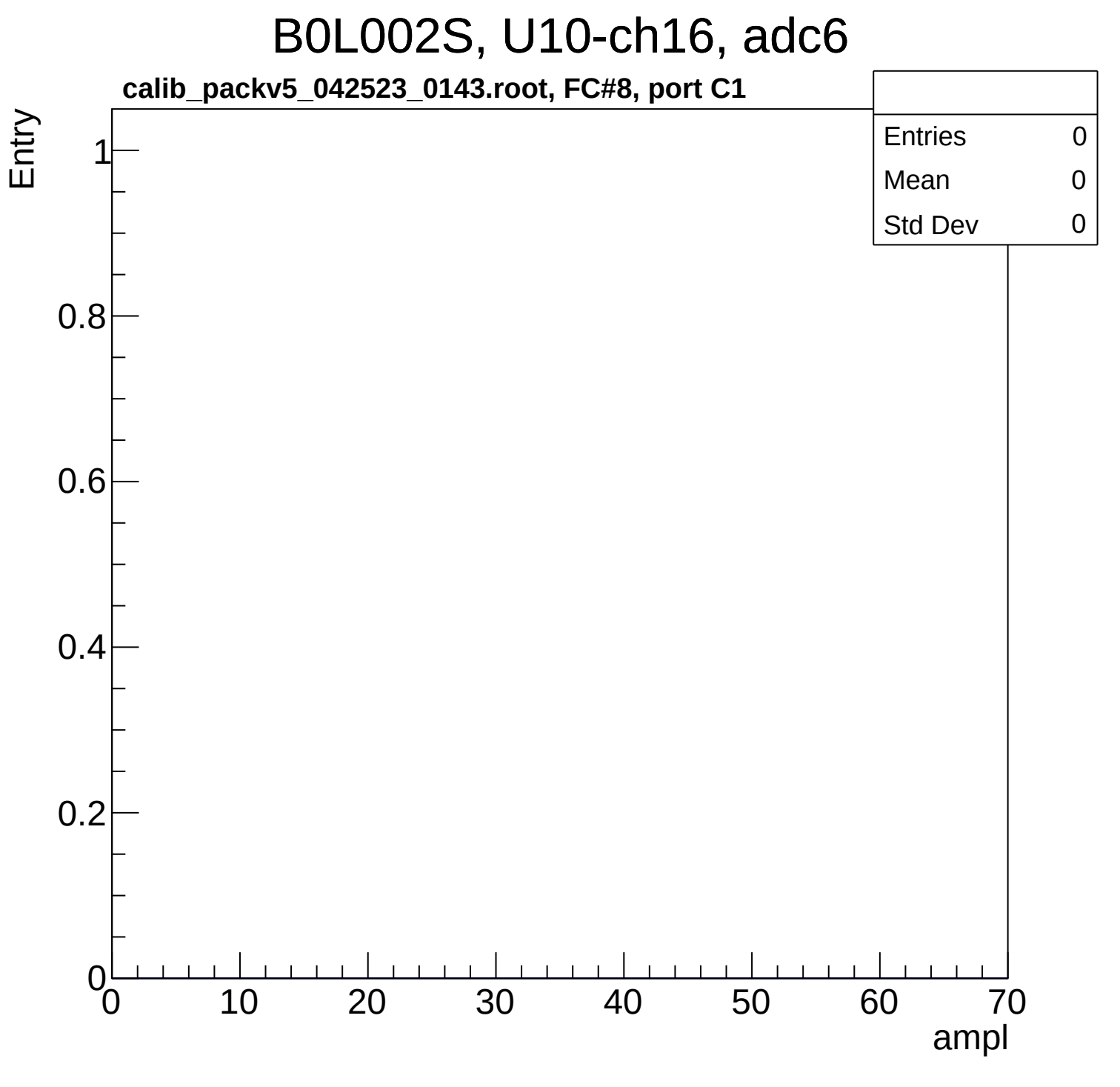

B0L002S, U10-ch16, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 38.18
Std Dev 3.306
Gaus mean : 38.2923
Gaus Width: 3.8122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

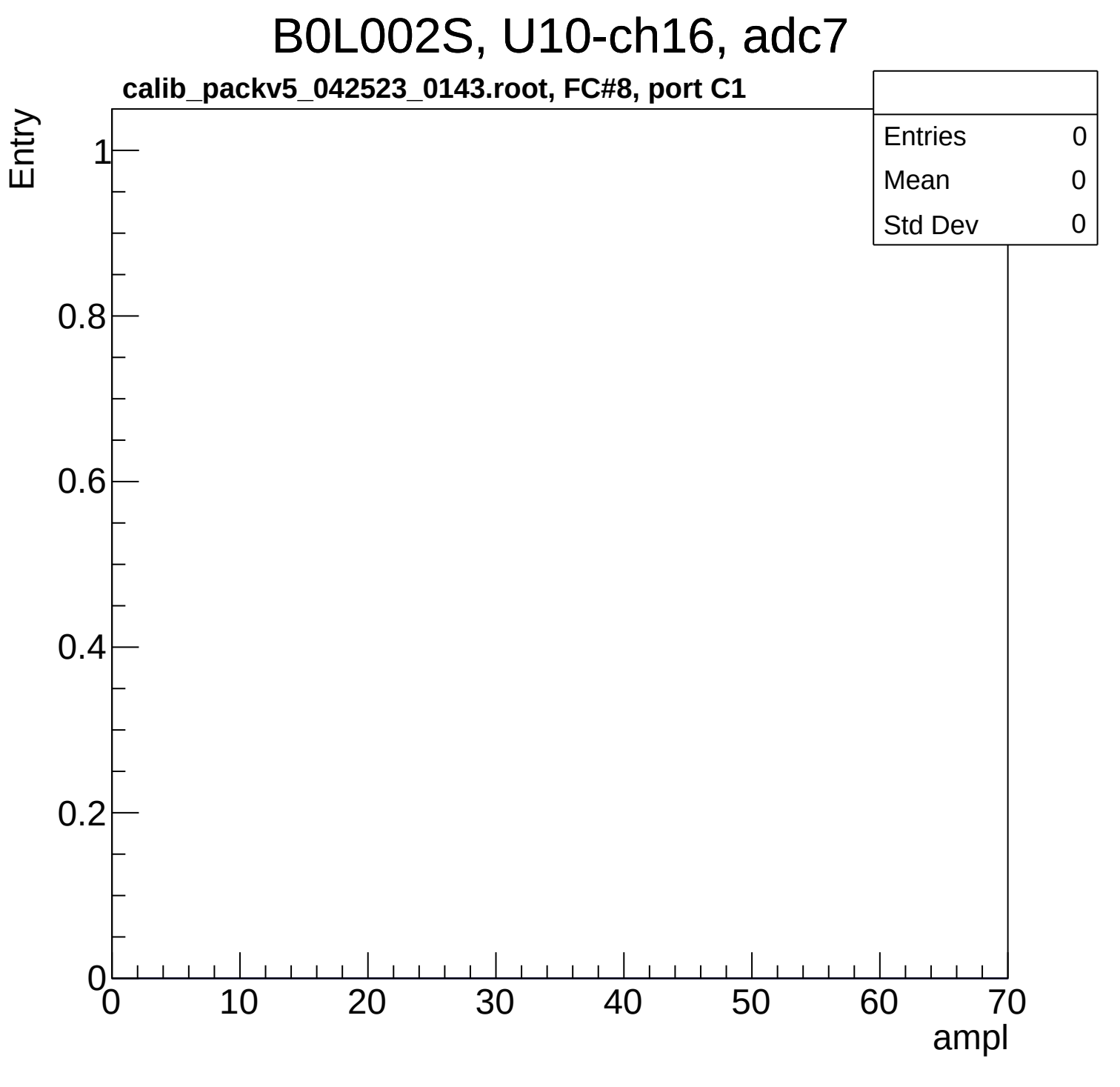

B0L002S, U10-ch16, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 44.45
Std Dev 2.766
Gaus mean : 45.0387
Gaus Width: 3.2571
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

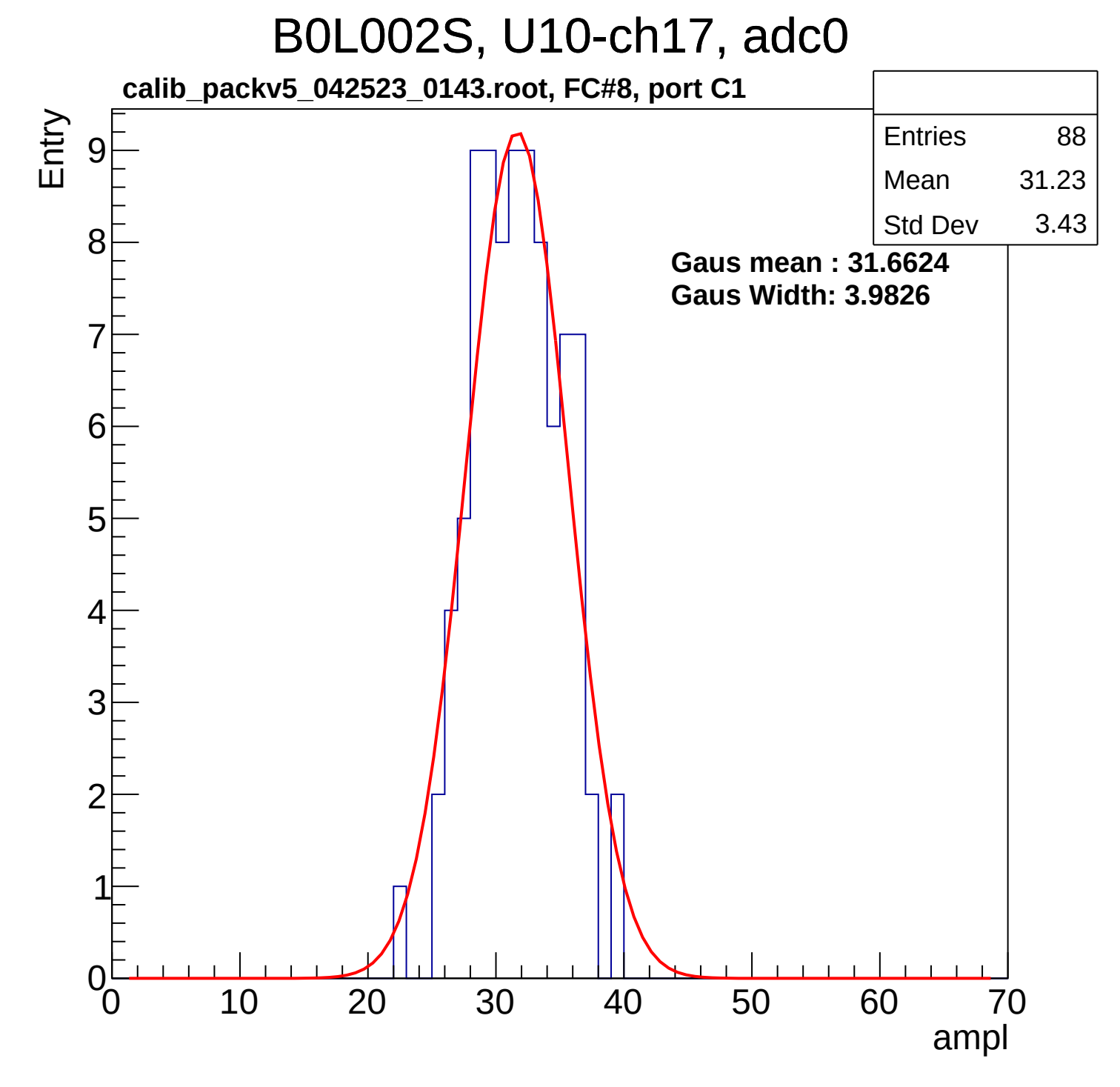

B0L002S, U10-ch16, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 50.07
Std Dev 3.146
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

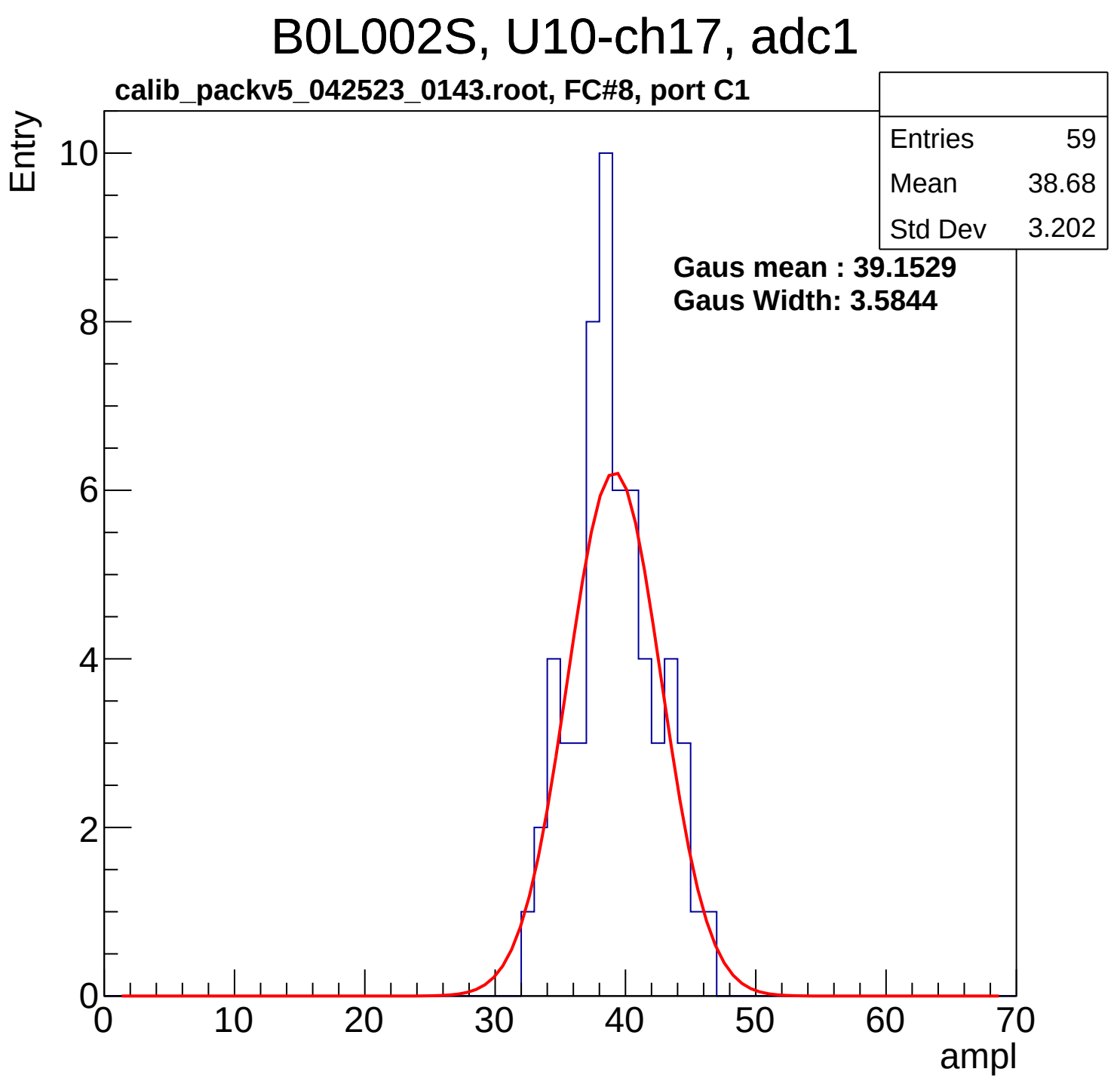

B0L002S, U10-ch16, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 56.41
Std Dev 7.445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

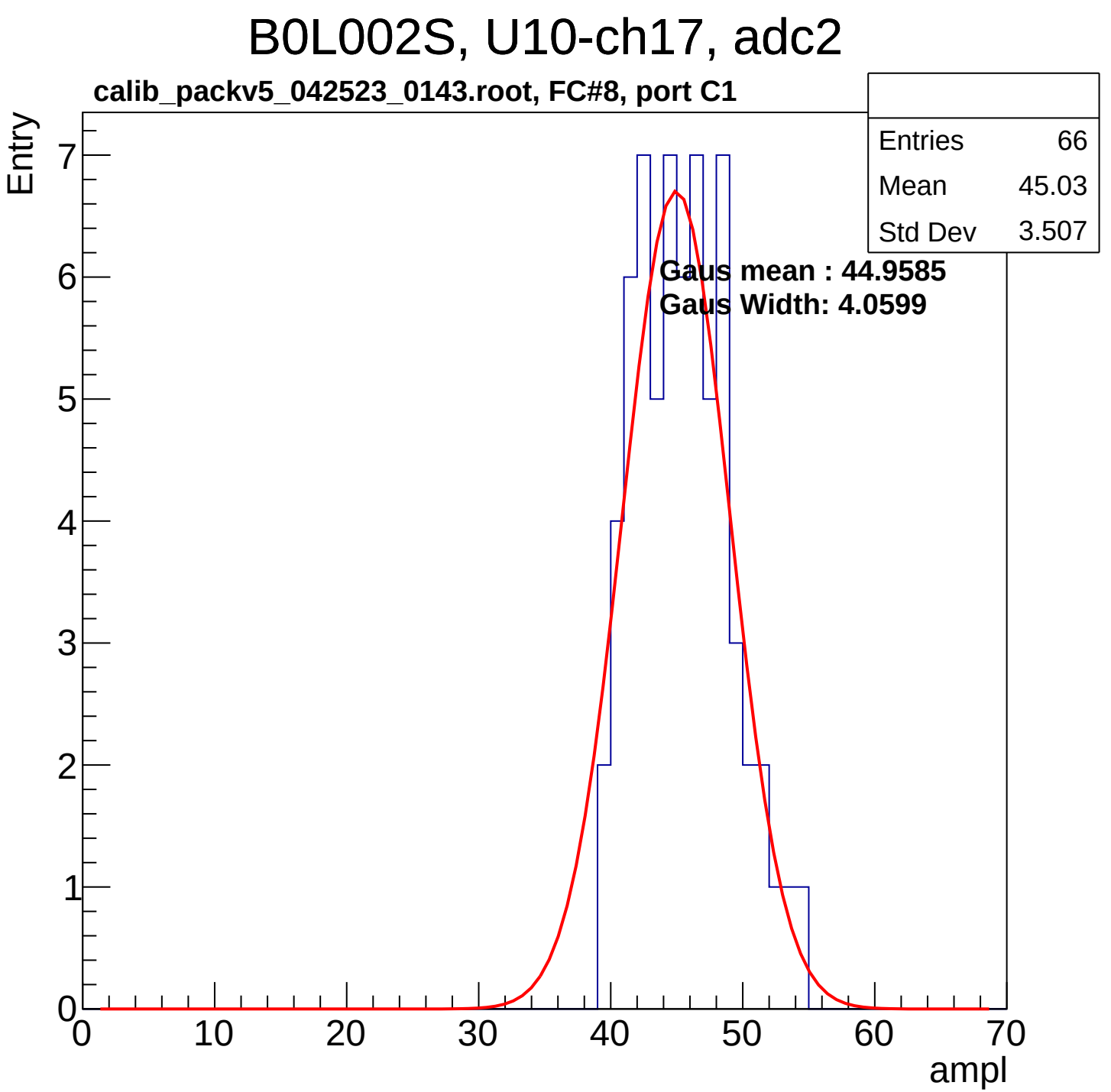

B0L002S, U10-ch16, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 61.17
Std Dev 1.641
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

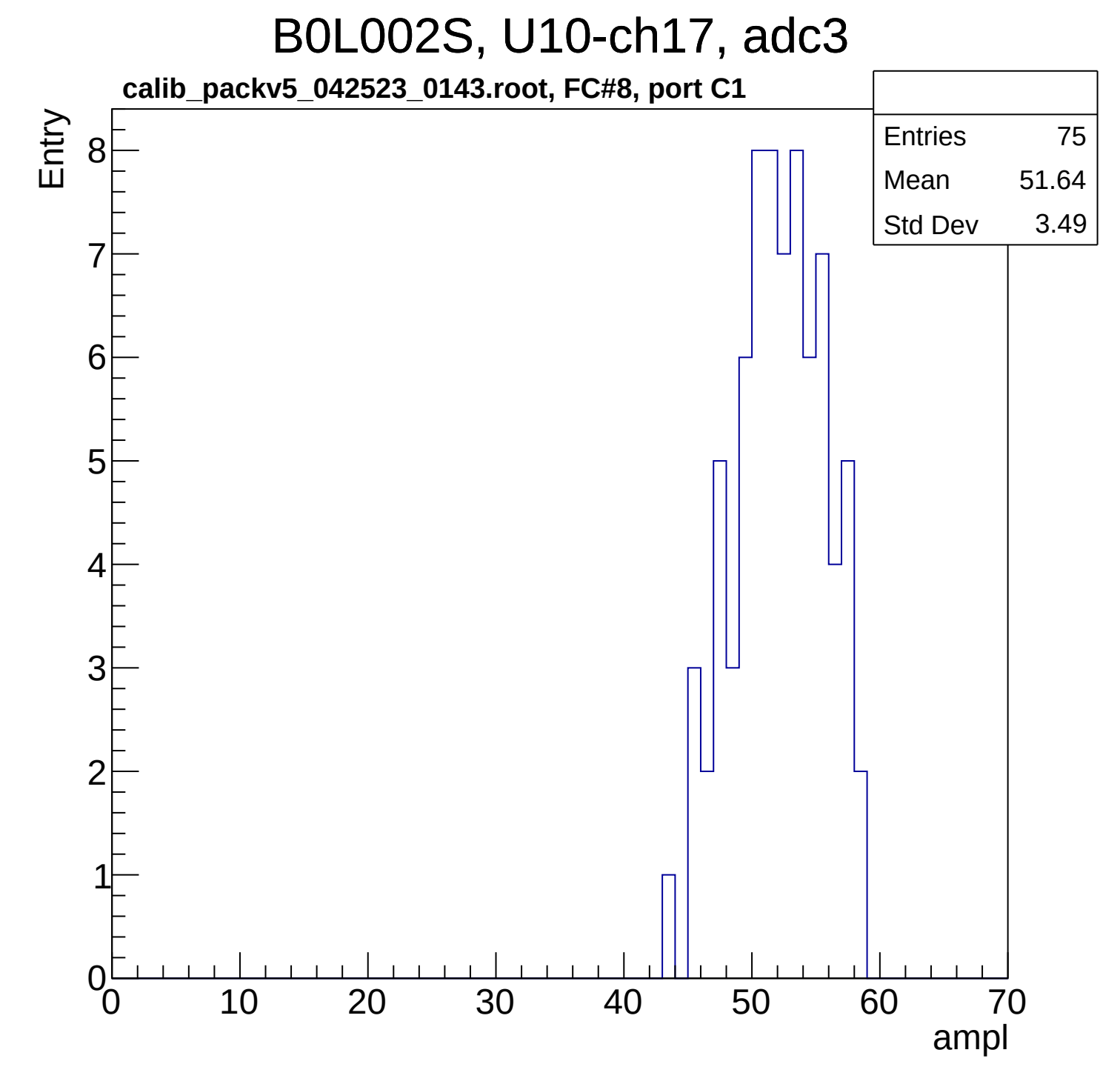

B0L002S, U10-ch16, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

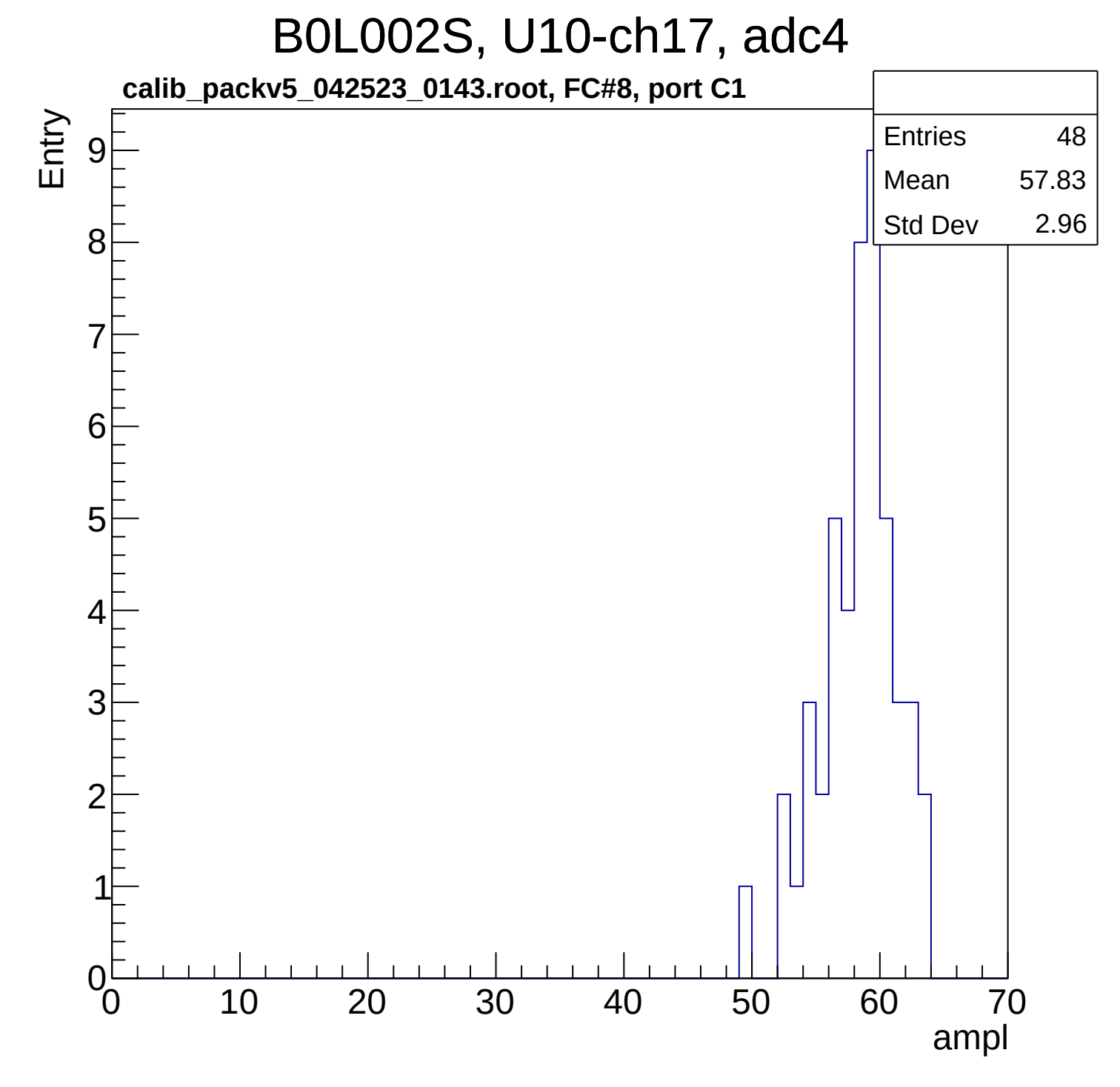

B0L002S, U10-ch16, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

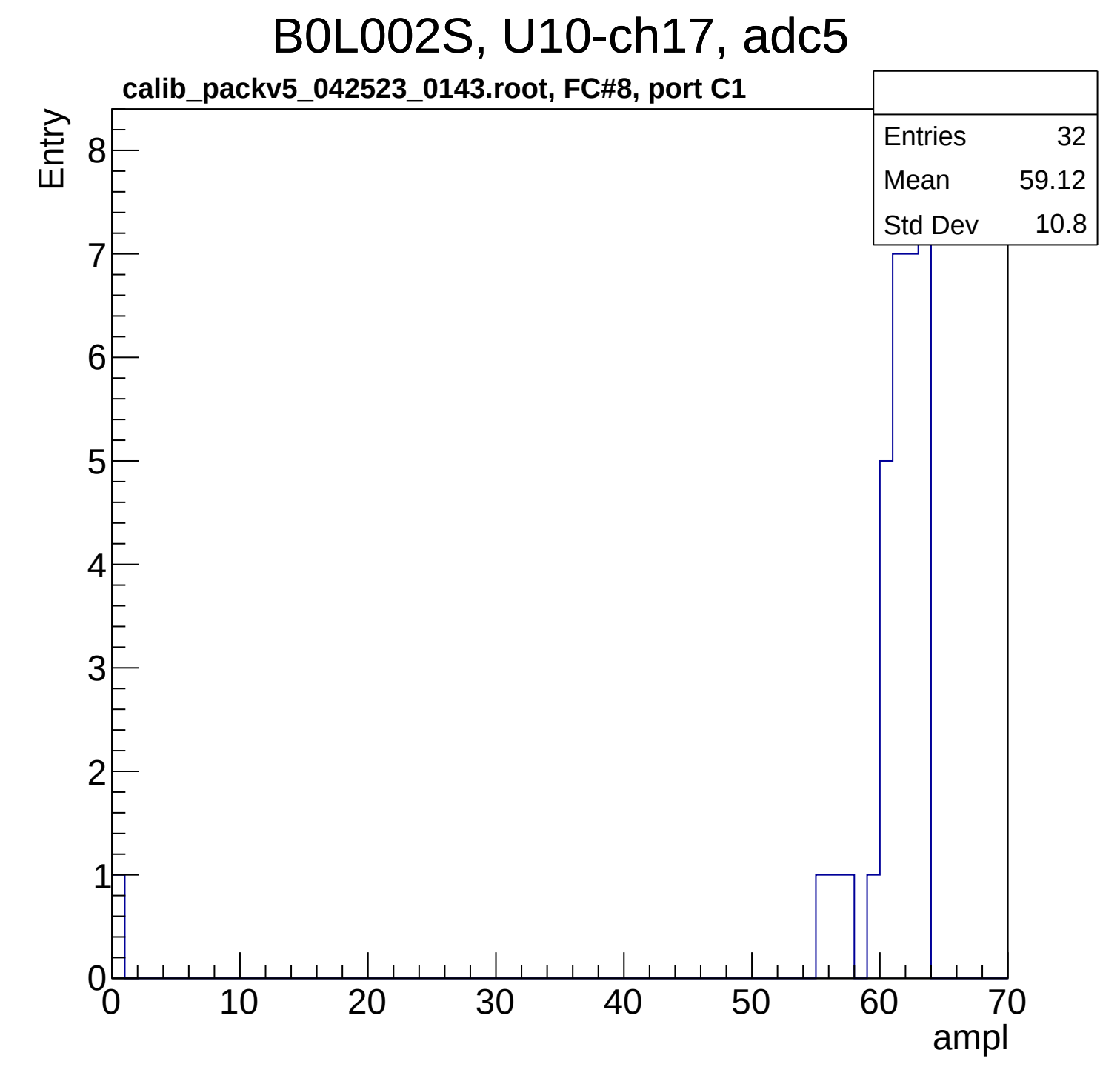

B0L002S, U10-ch17, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 31.23
Std Dev 3.43
Gaus mean : 31.6624
Gaus Width: 3.9826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

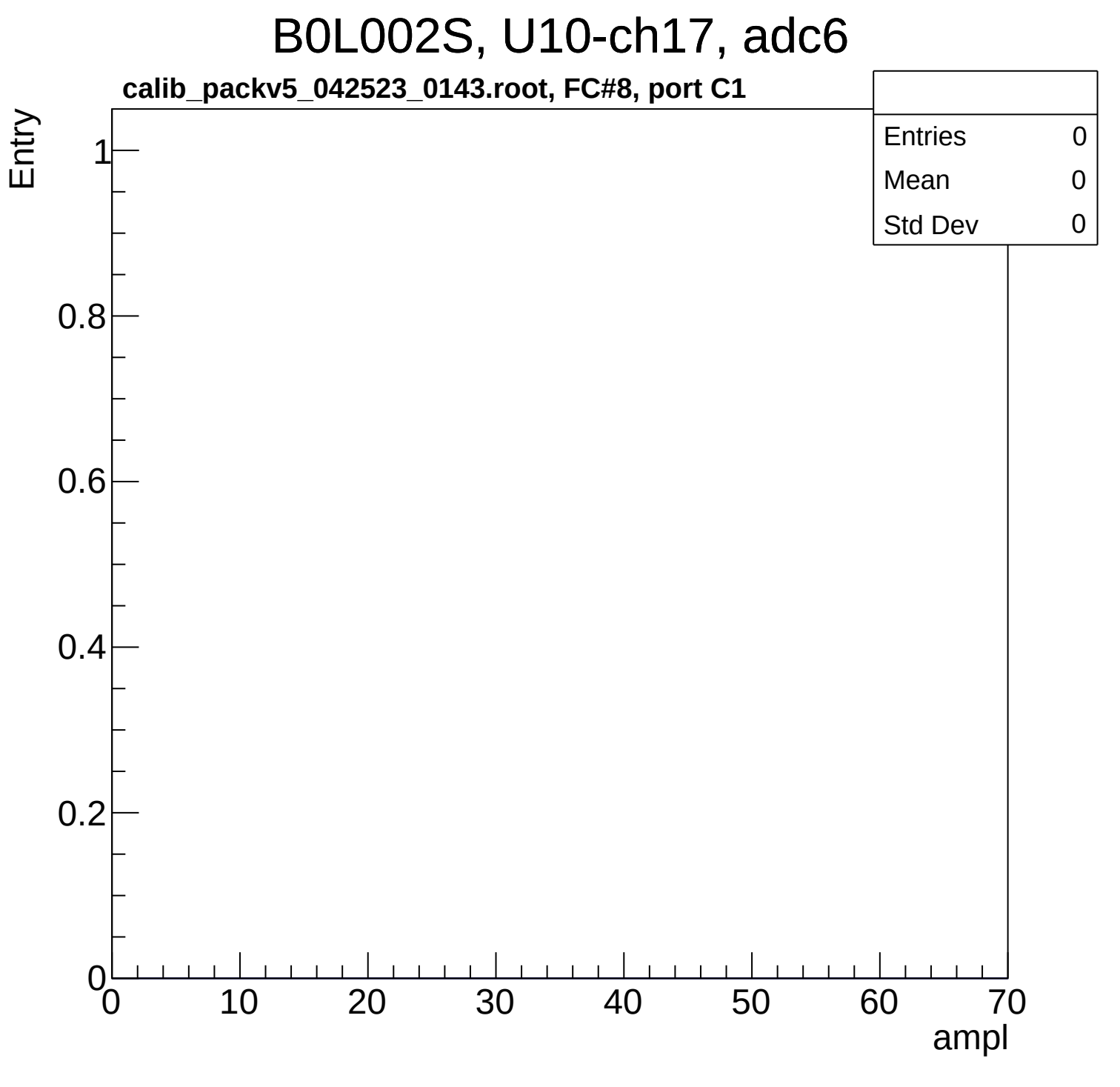

B0L002S, U10-ch17, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 38.68
Std Dev 3.202
Gaus mean : 39.1529
Gaus Width: 3.5844
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

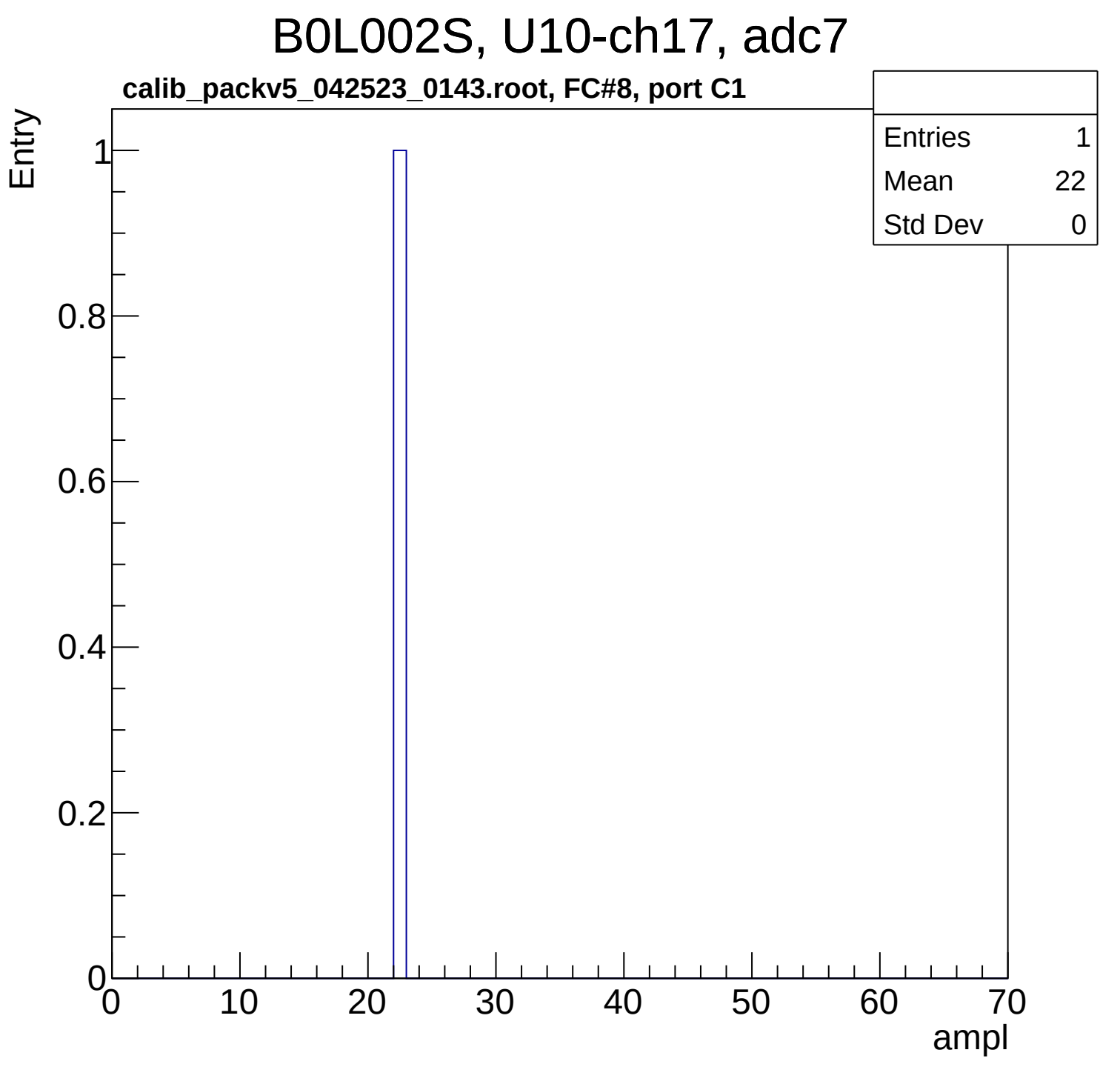

B0L002S, U10-ch17, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.03
Std Dev 3.507
Gaus mean : 44.9585
Gaus Width: 4.0599
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

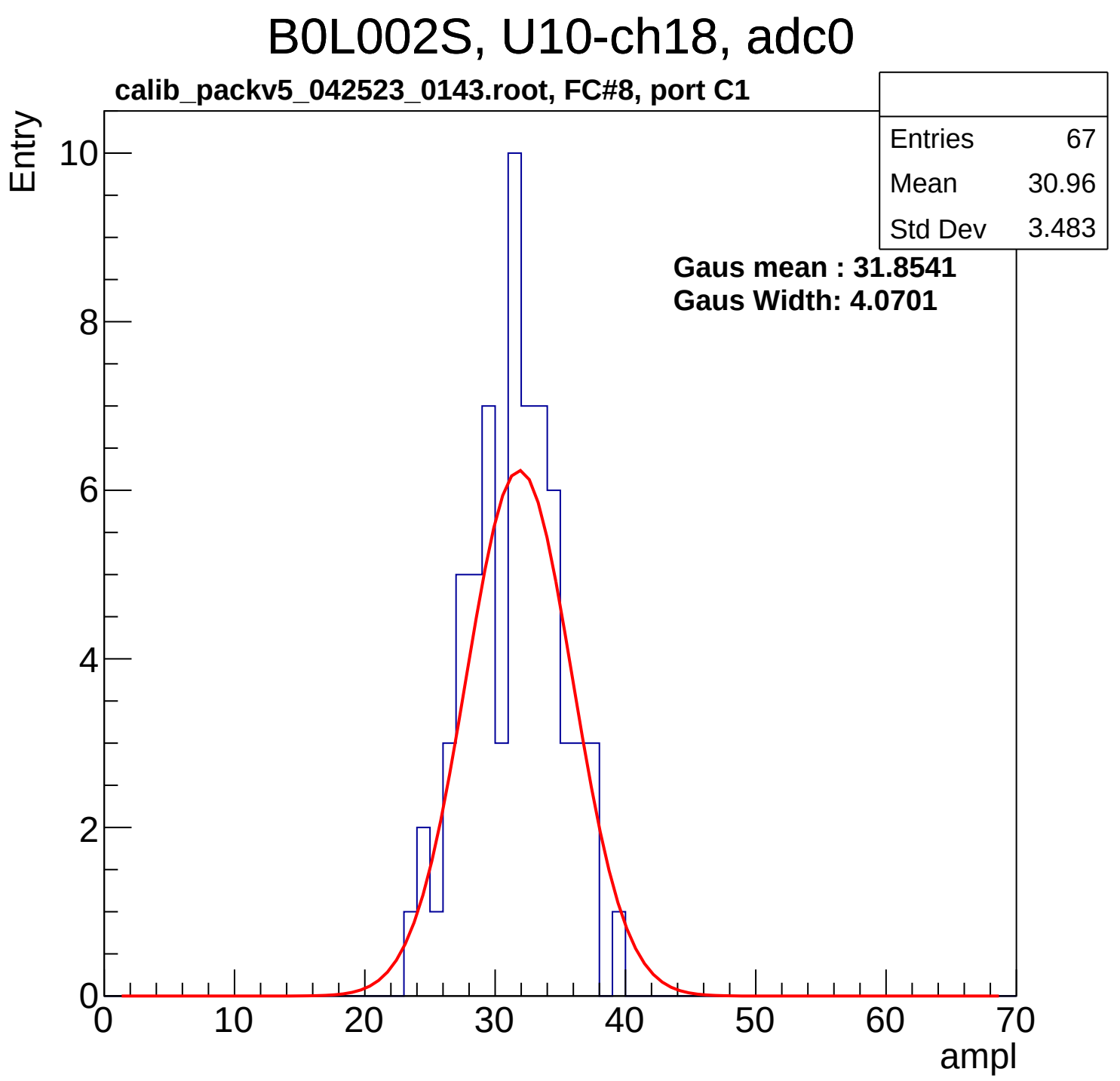

B0L002S, U10-ch17, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.64
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

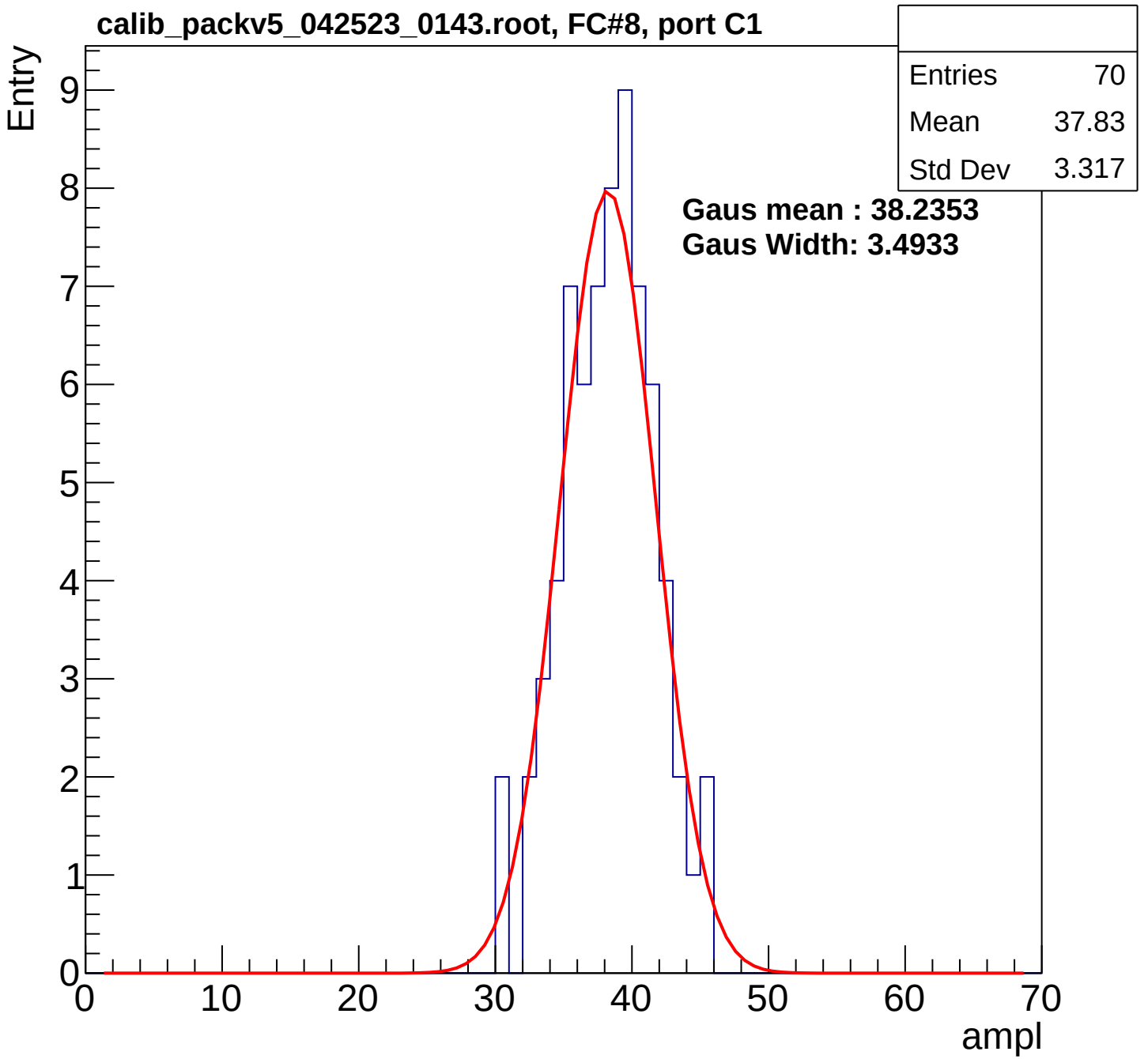

B0L002S, U10-ch17, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.83
Std Dev 2.96
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

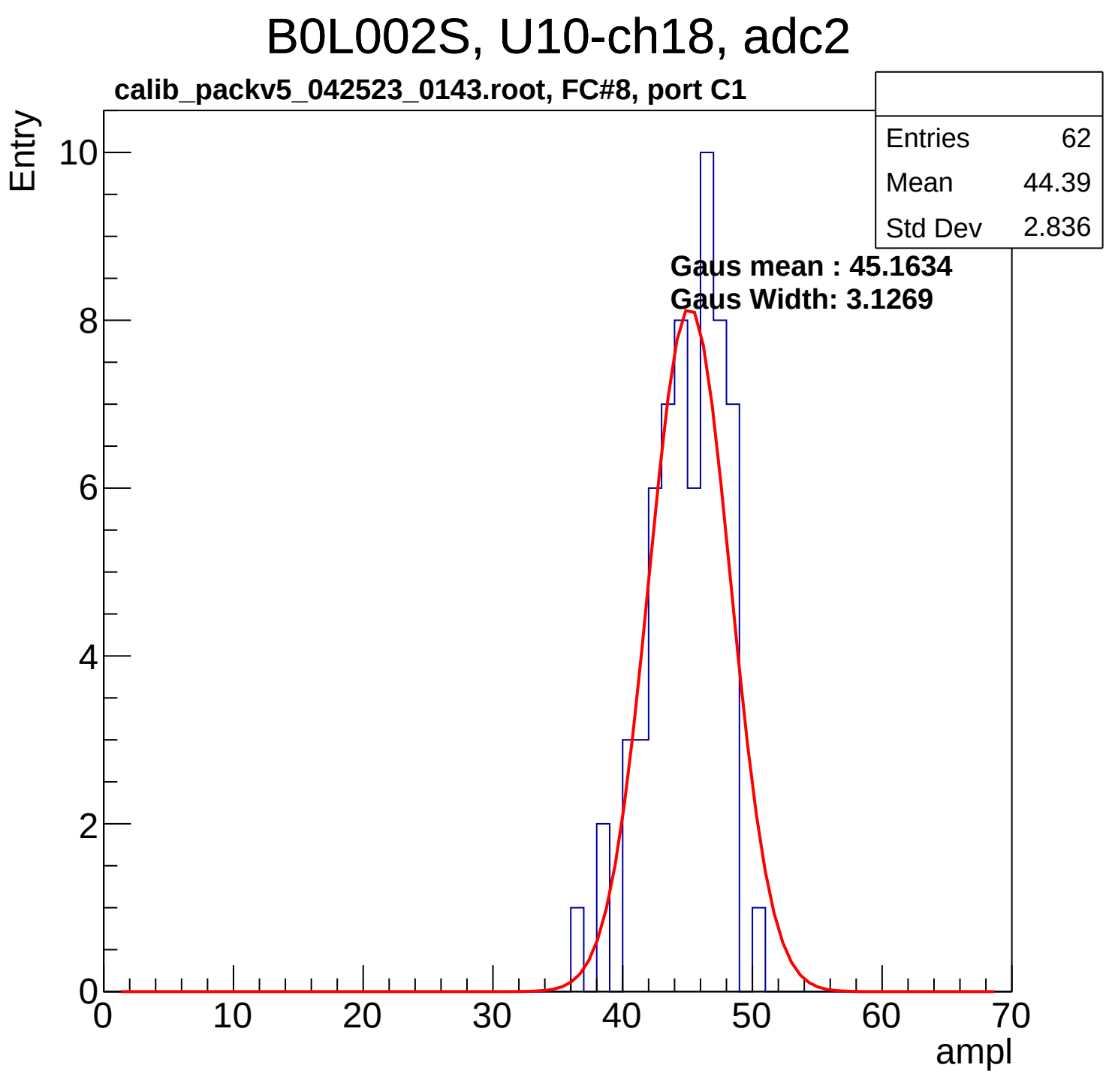

B0L002S, U10-ch17, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 59.12
Std Dev 10.8
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

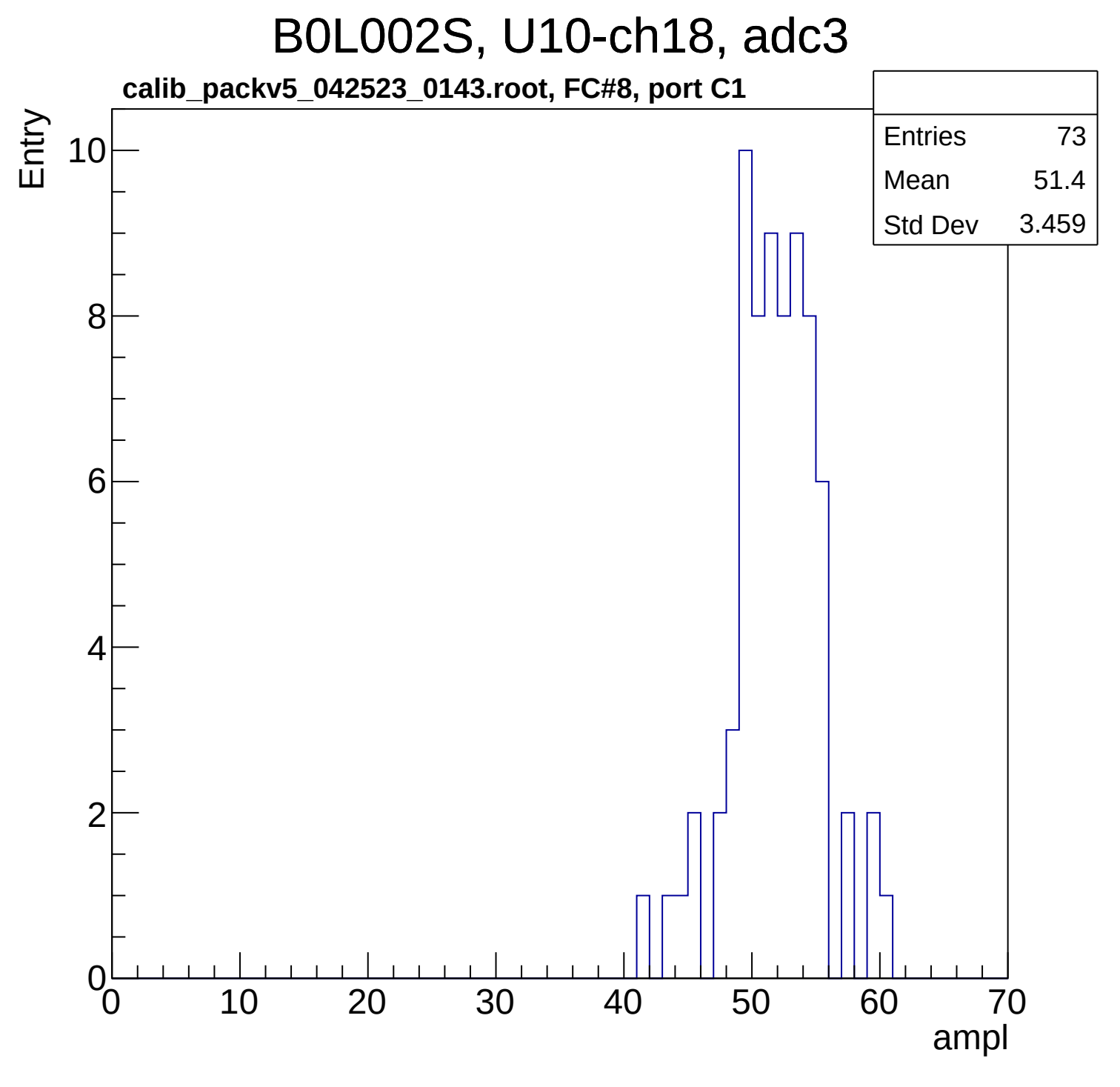

B0L002S, U10-ch17, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

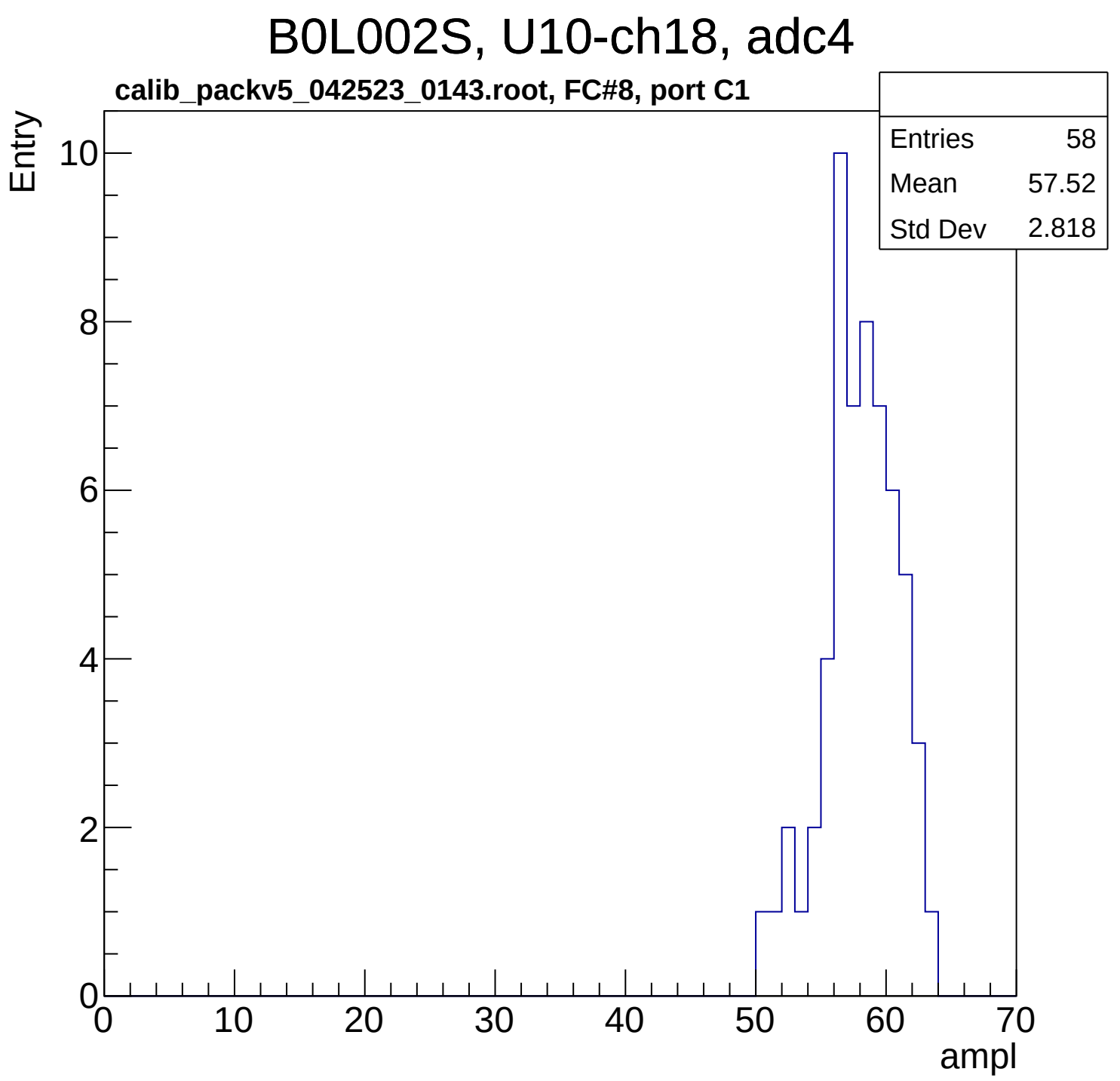

B0L002S, U10-ch17, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

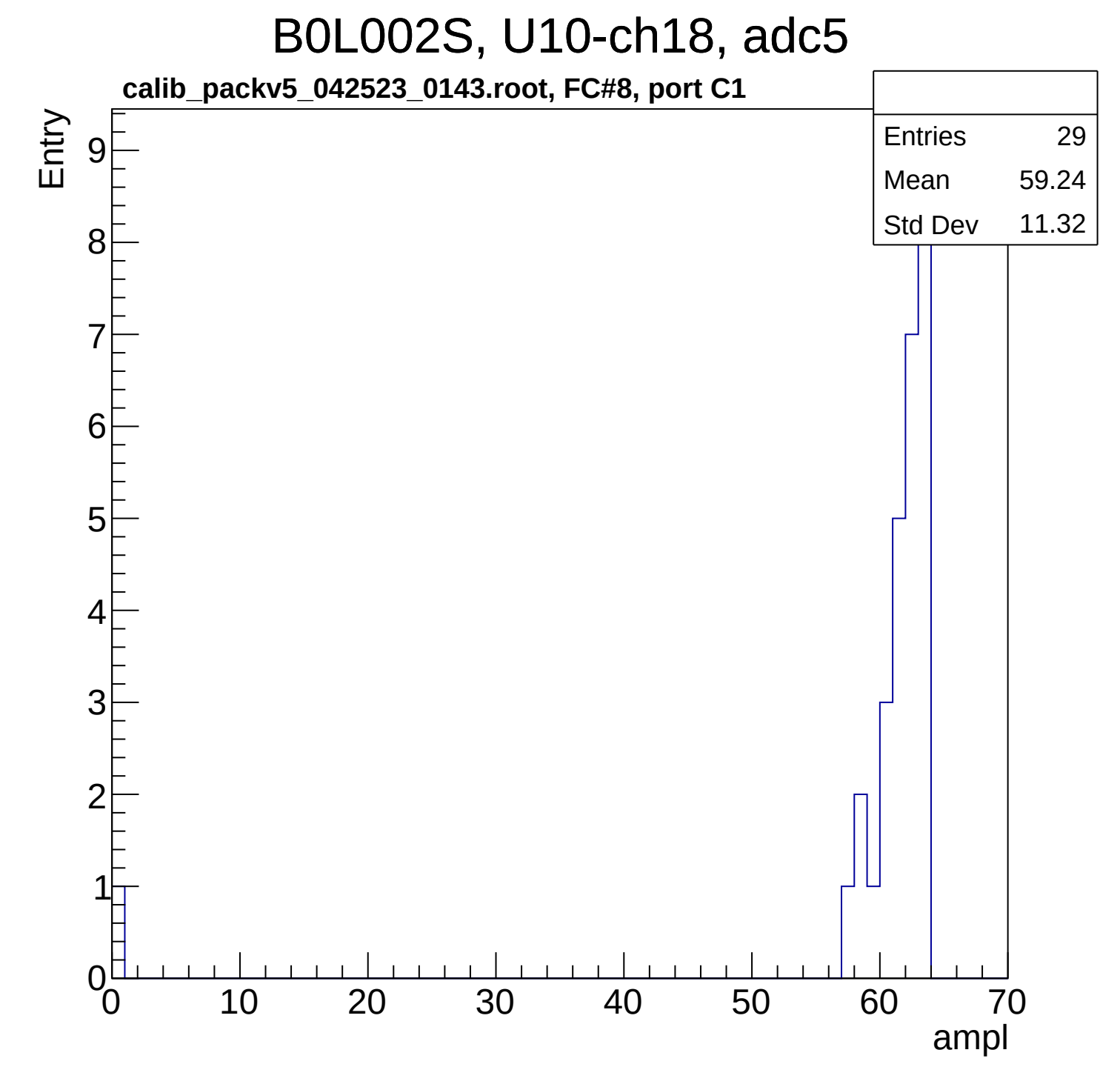

B0L002S, U10-ch18, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 30.96
Std Dev 3.483
Gaus mean : 31.8541
Gaus Width: 4.0701
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

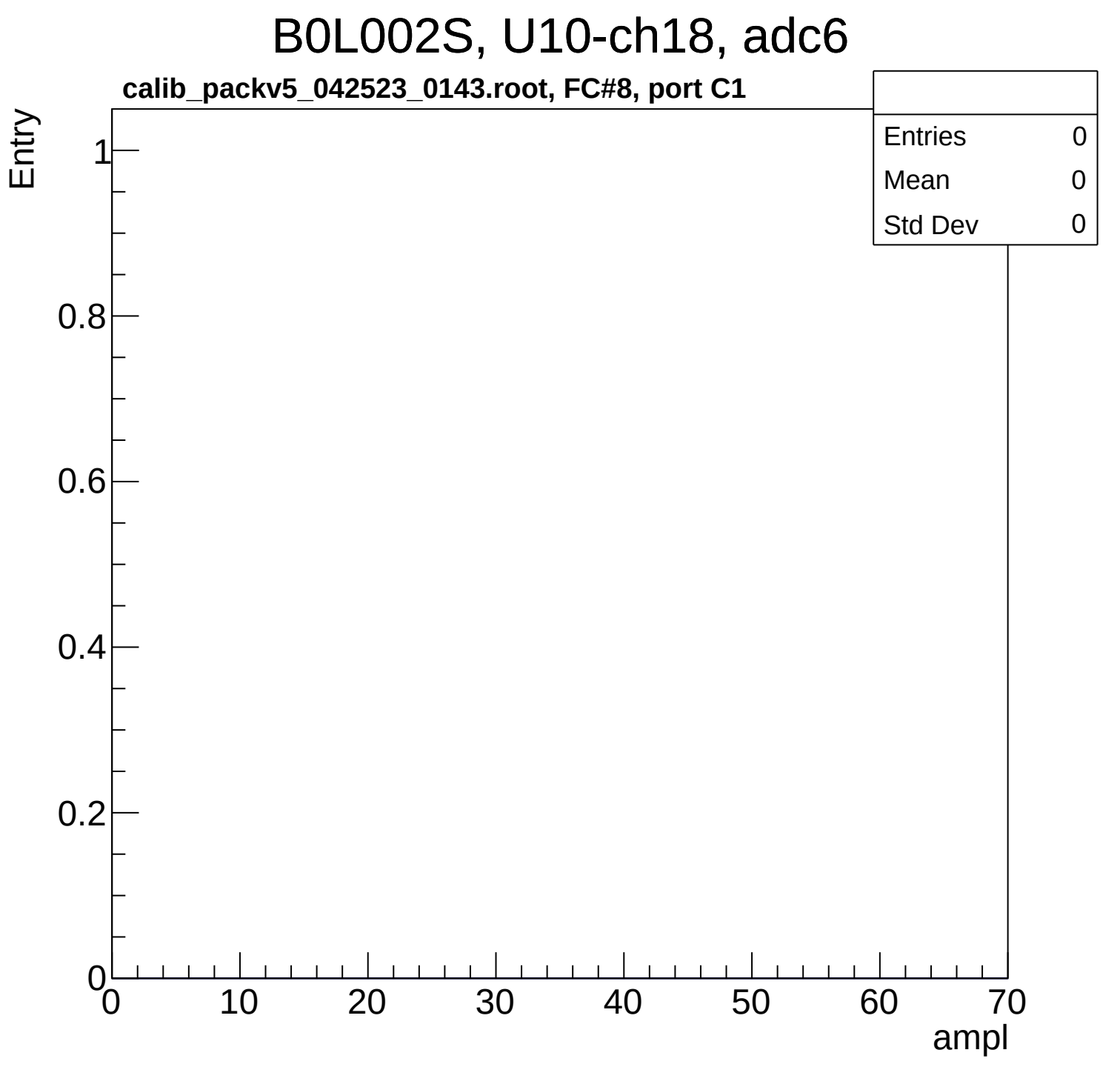

B0L002S, U10-ch18, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 37.83
Std Dev 3.317
Gaus mean : 38.2353
Gaus Width: 3.4933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

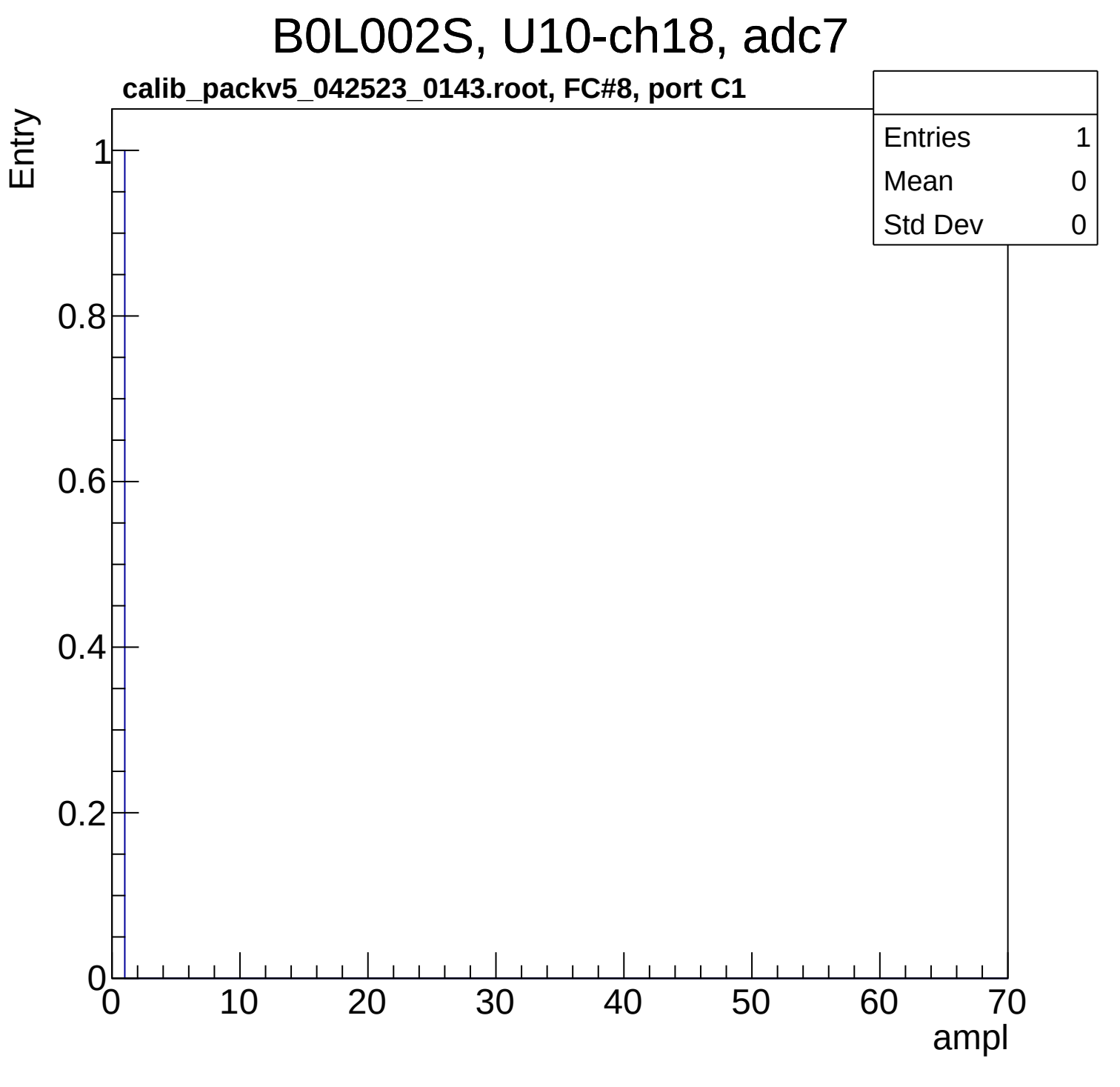

B0L002S, U10-ch18, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.39
Std Dev 2.836
Gaus mean : 45.1634
Gaus Width: 3.1269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

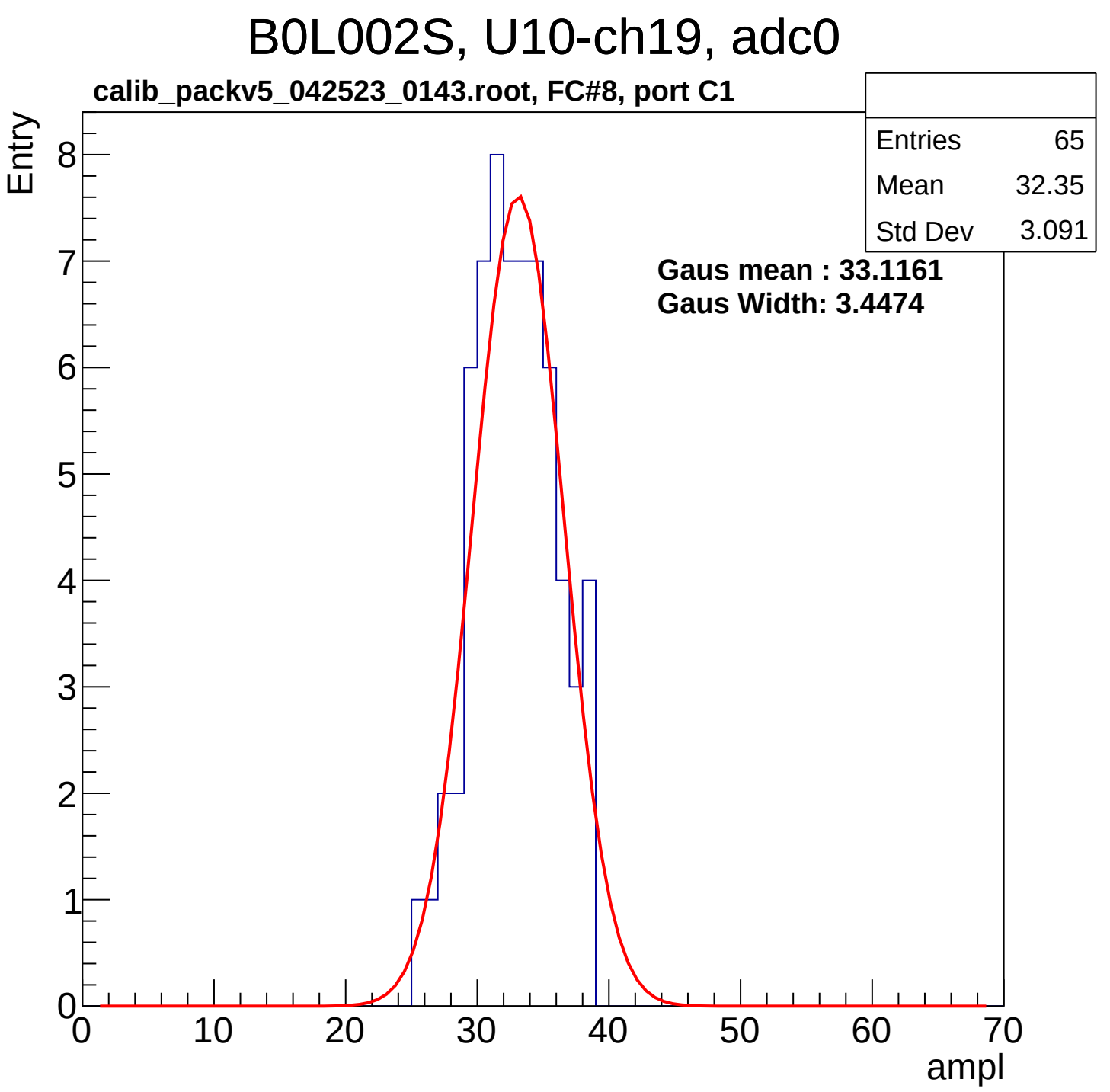

B0L002S, U10-ch18, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.4
Std Dev 3.459
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

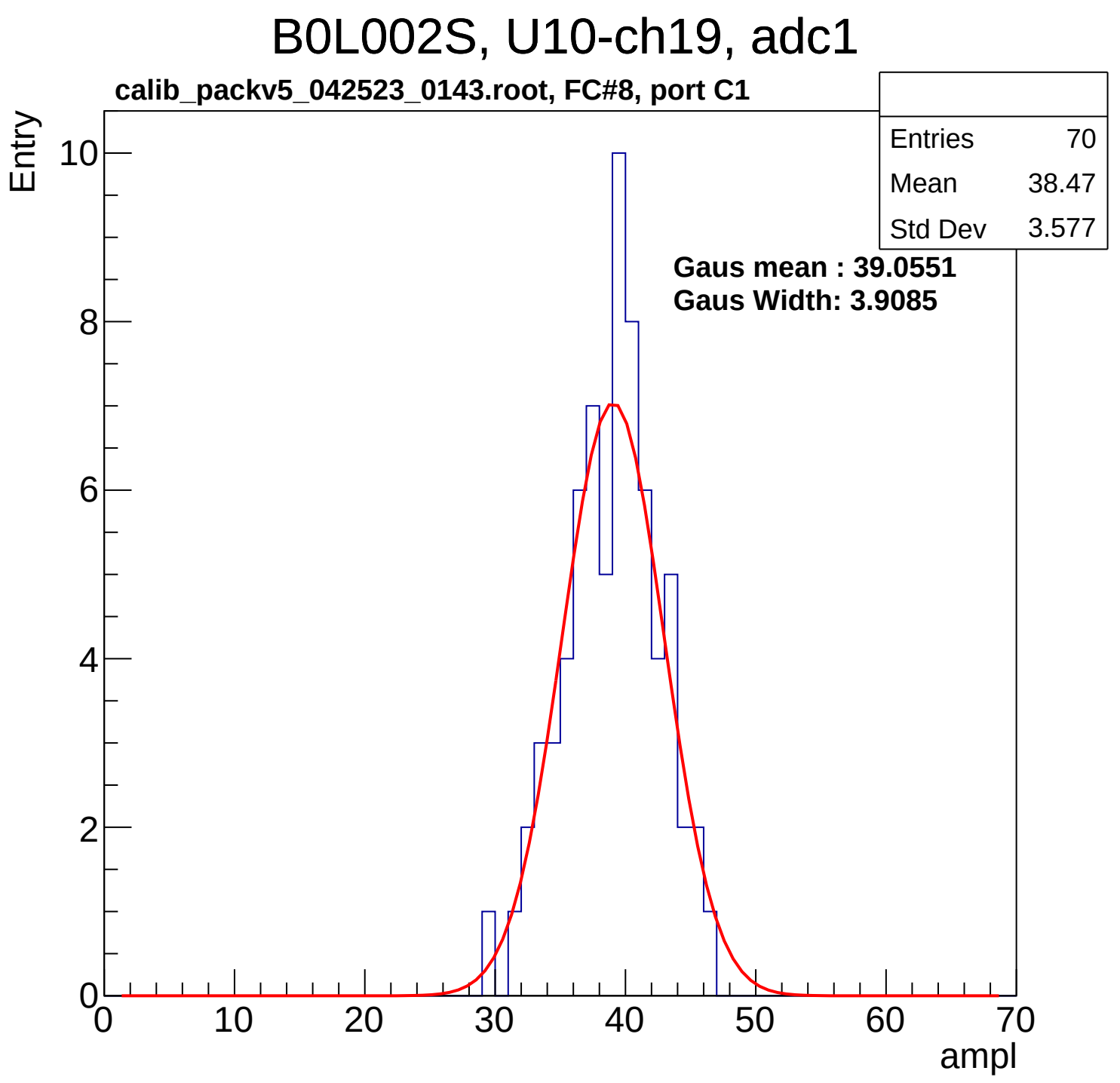

B0L002S, U10-ch18, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.52
Std Dev 2.818
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

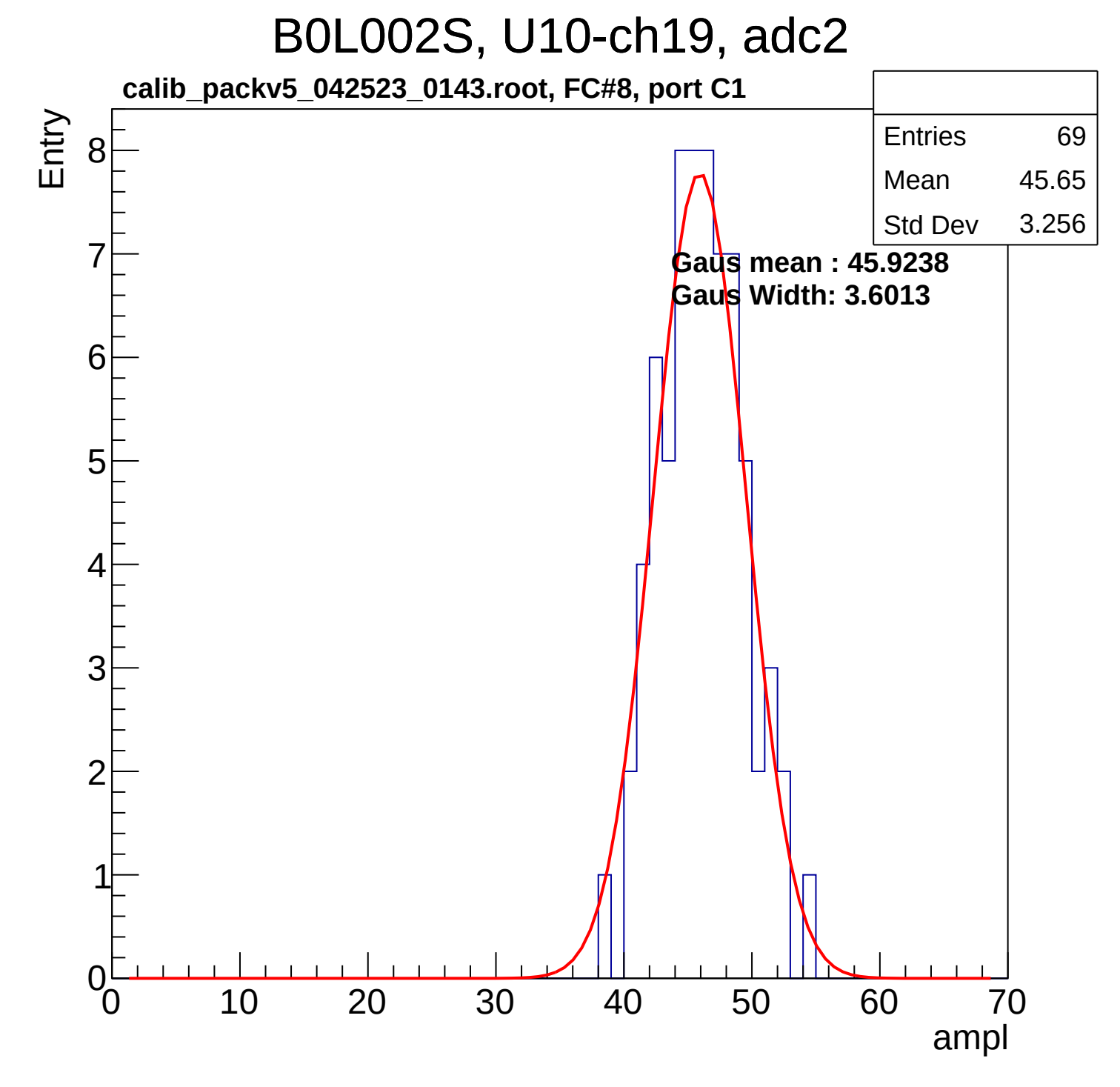

B0L002S, U10-ch18, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.24
Std Dev 11.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

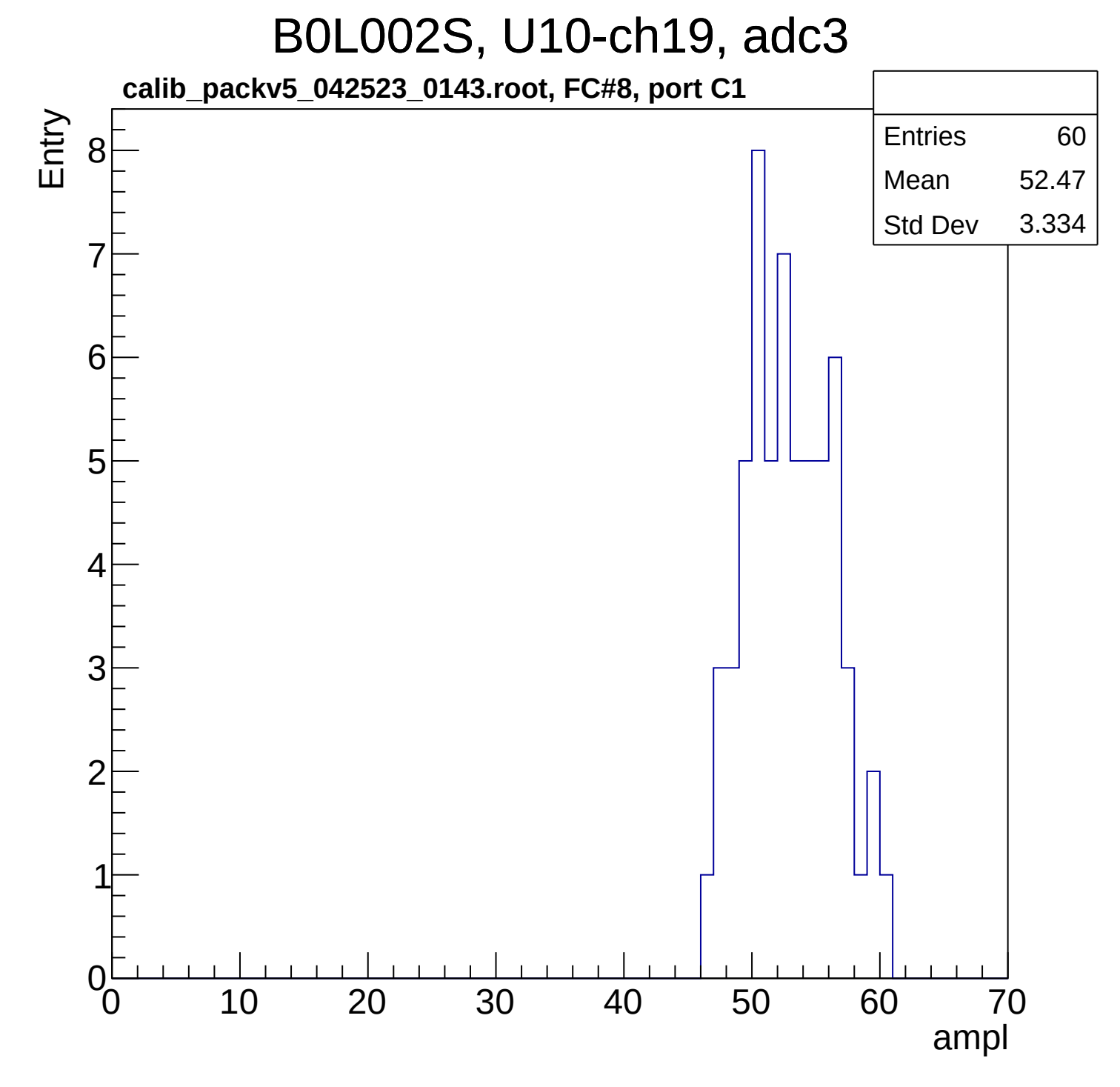

B0L002S, U10-ch18, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

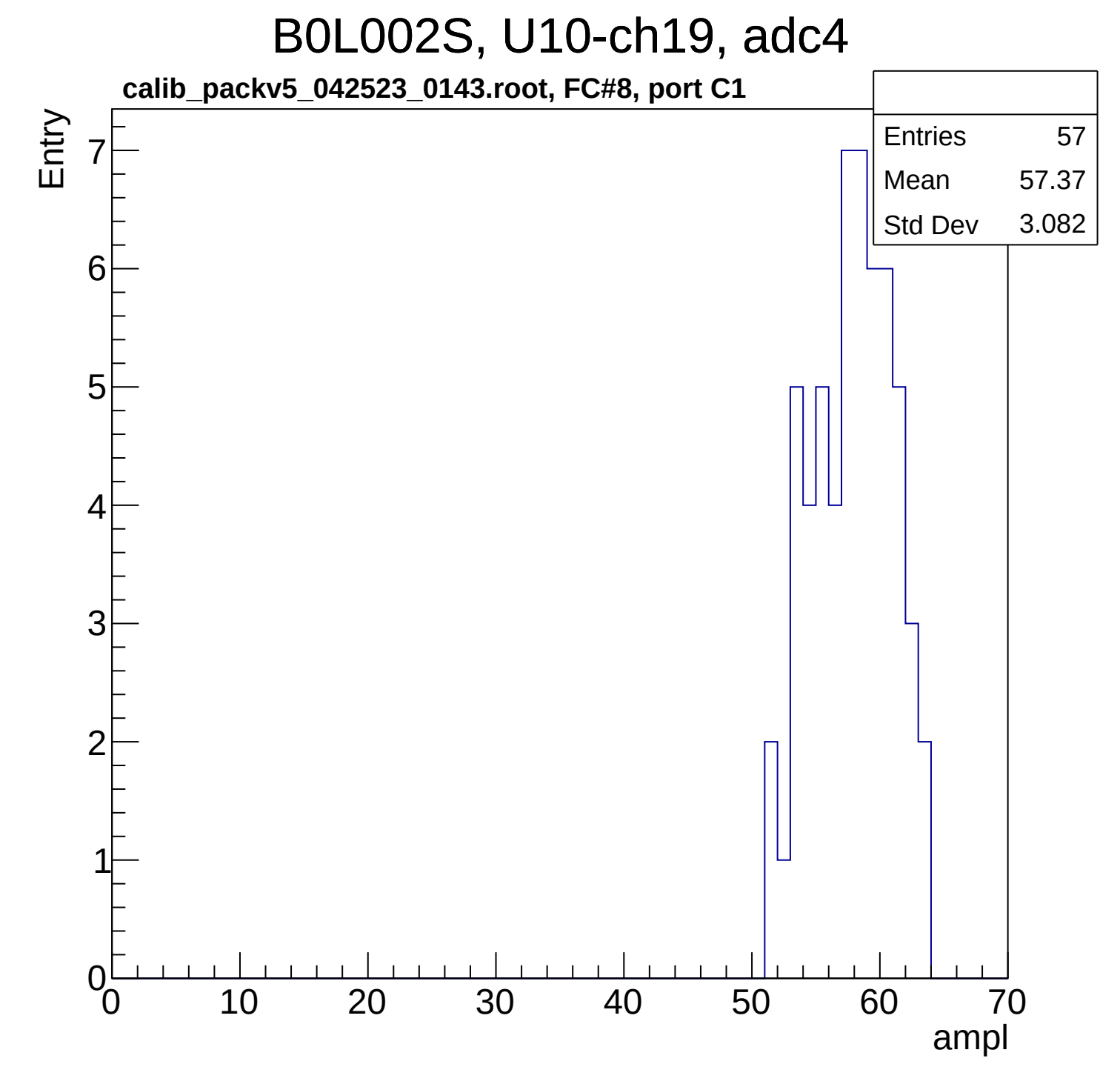

B0L002S, U10-ch18, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

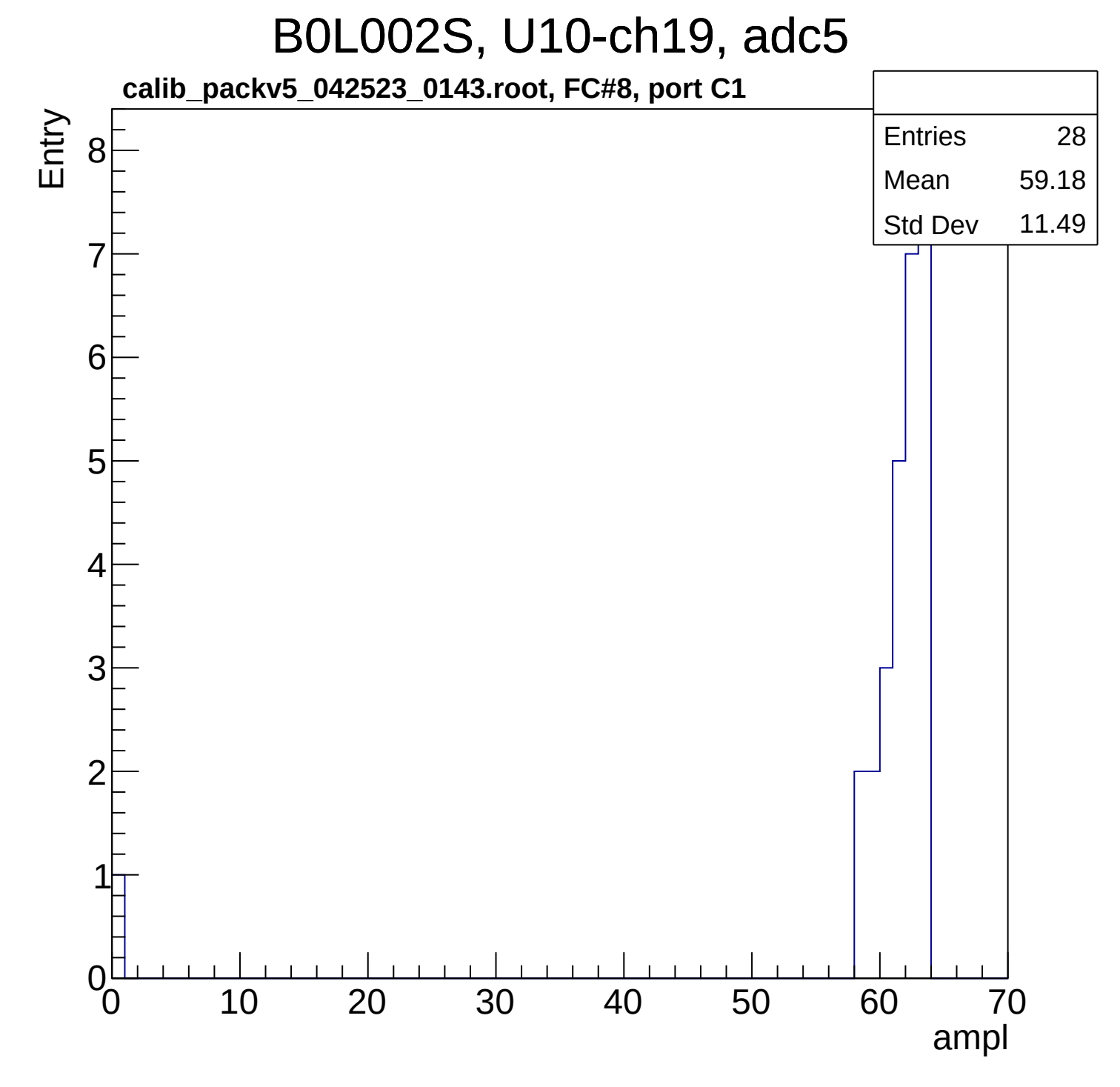

B0L002S, U10-ch19, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 32.35
Std Dev 3.091
Gaus mean : 33.1161
Gaus Width: 3.4474
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

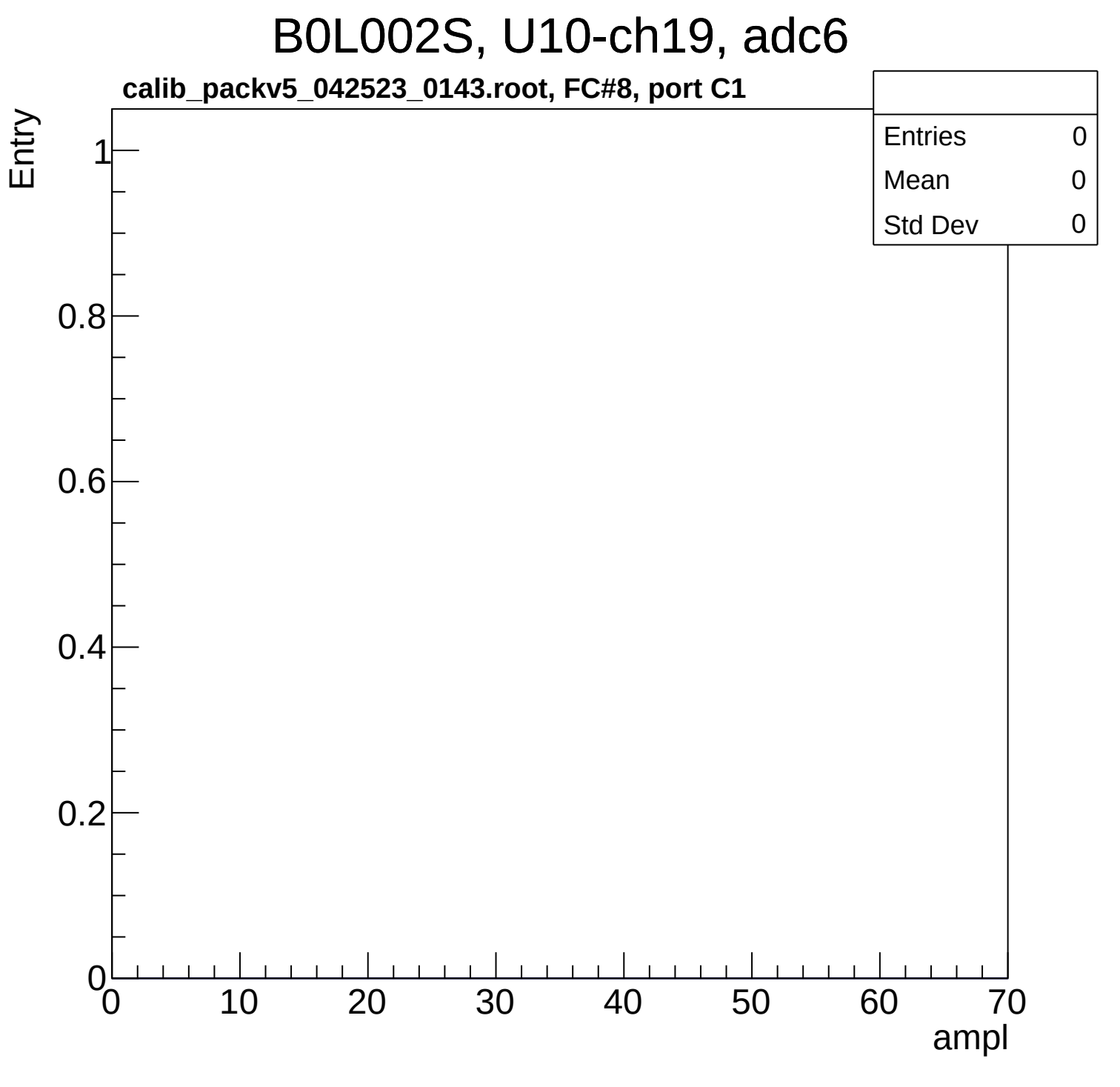

B0L002S, U10-ch19, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 38.47
Std Dev 3.577
Gaus mean : 39.0551
Gaus Width: 3.9085
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

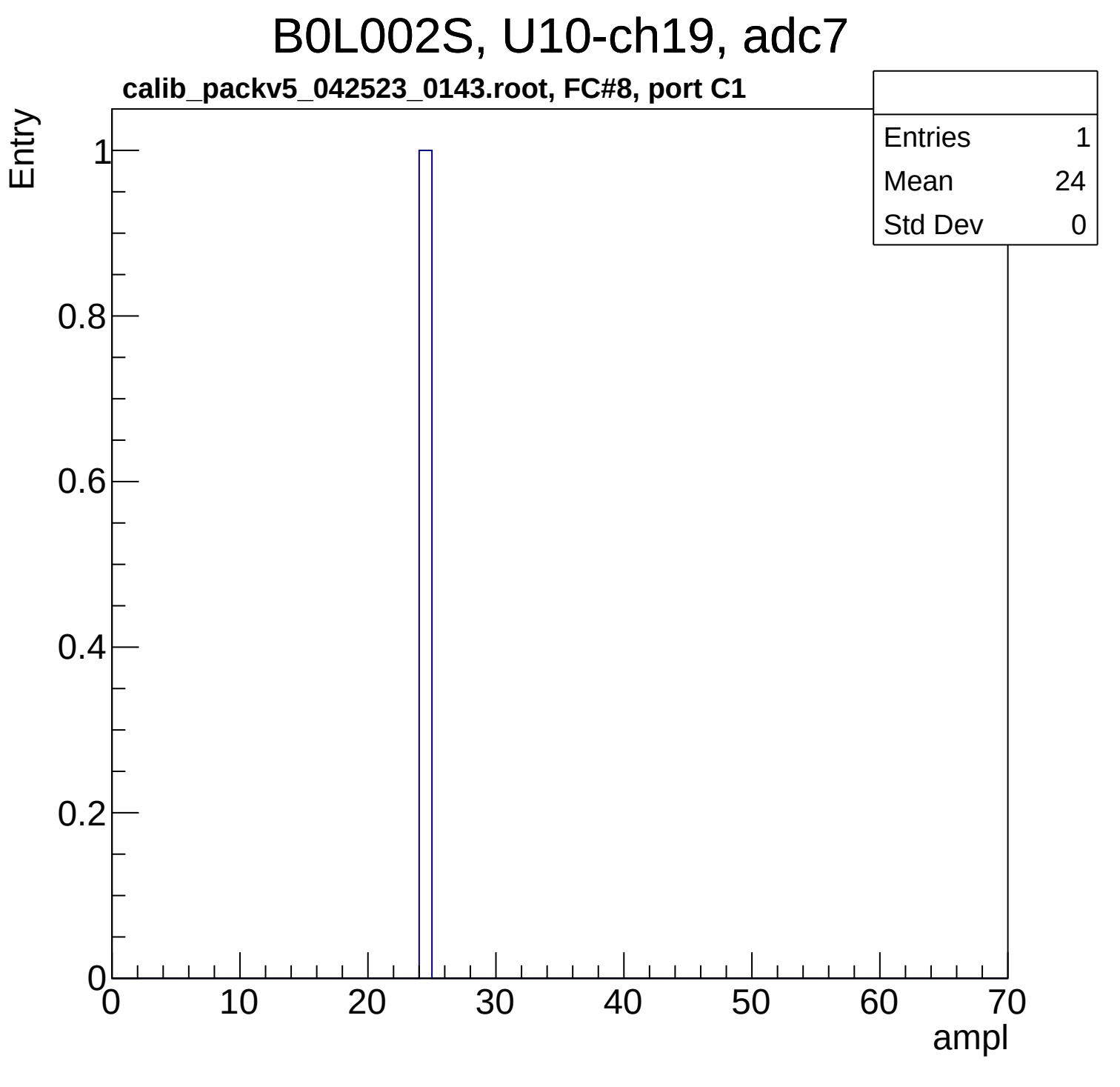

B0L002S, U10-ch19, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 45.65
Std Dev 3.256
Gaus mean : 45.9238
Gaus Width: 3.6013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

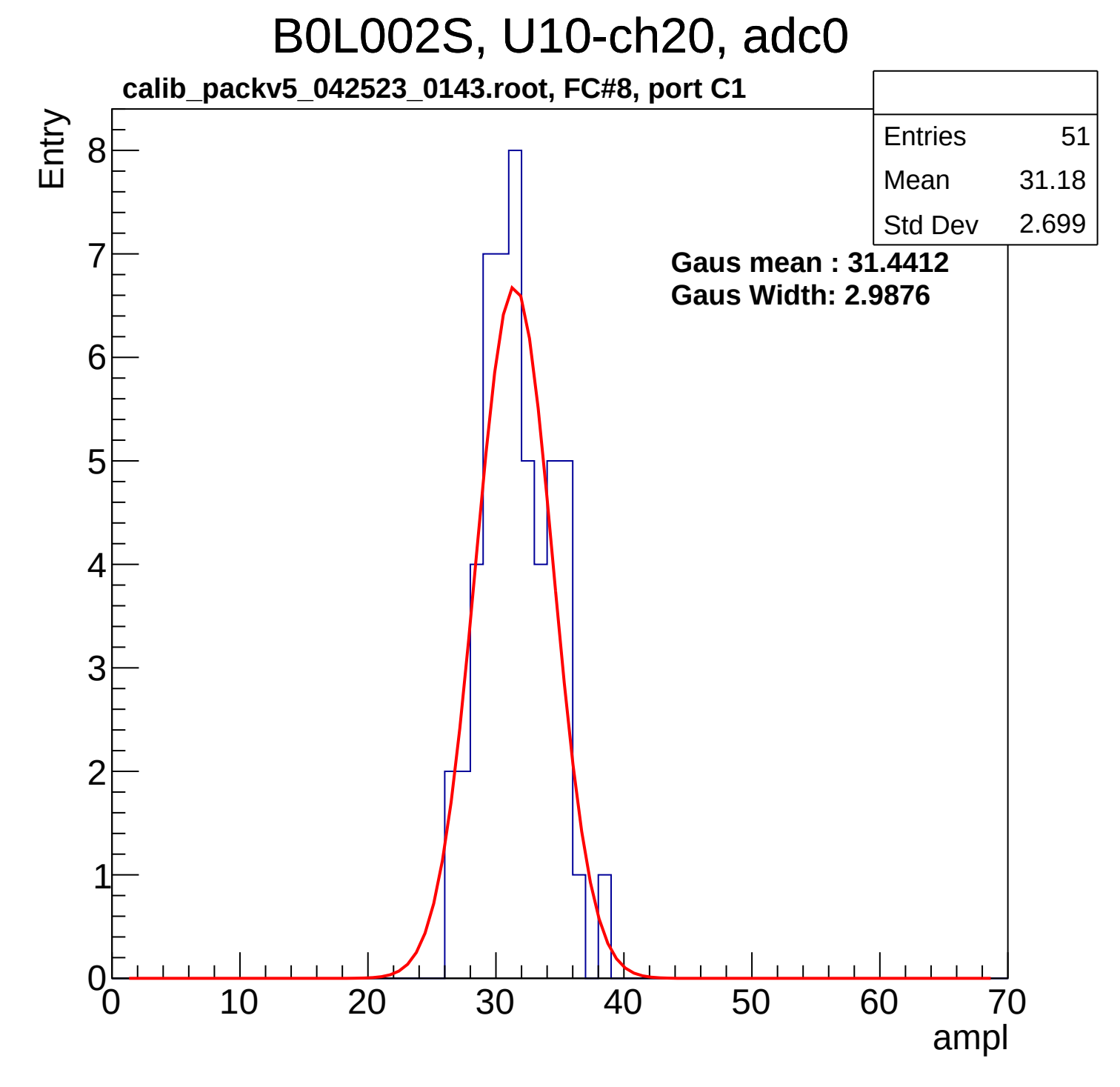

B0L002S, U10-ch19, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 52.47
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

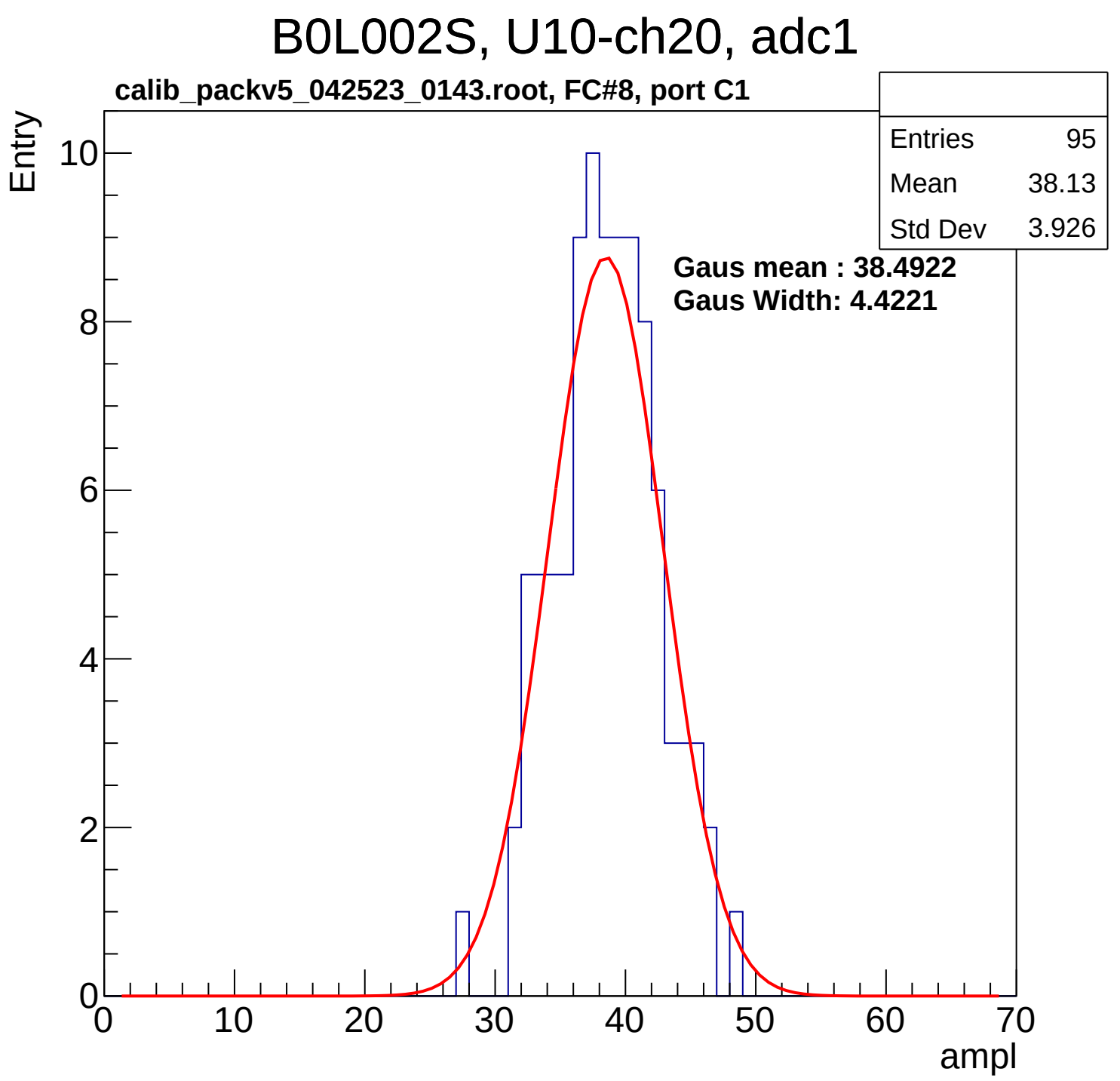

B0L002S, U10-ch19, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.37
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

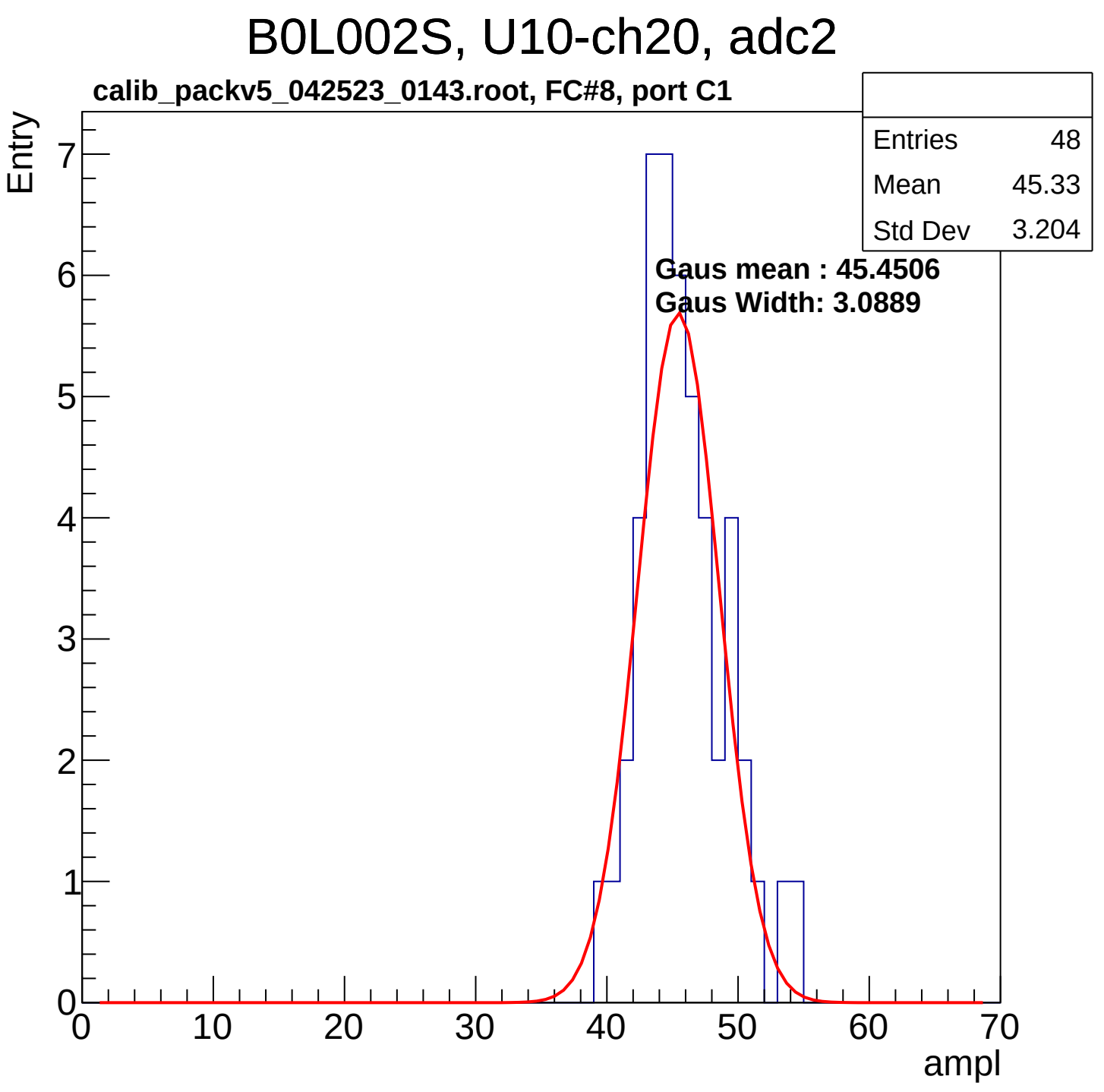

B0L002S, U10-ch19, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 59.18
Std Dev 11.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

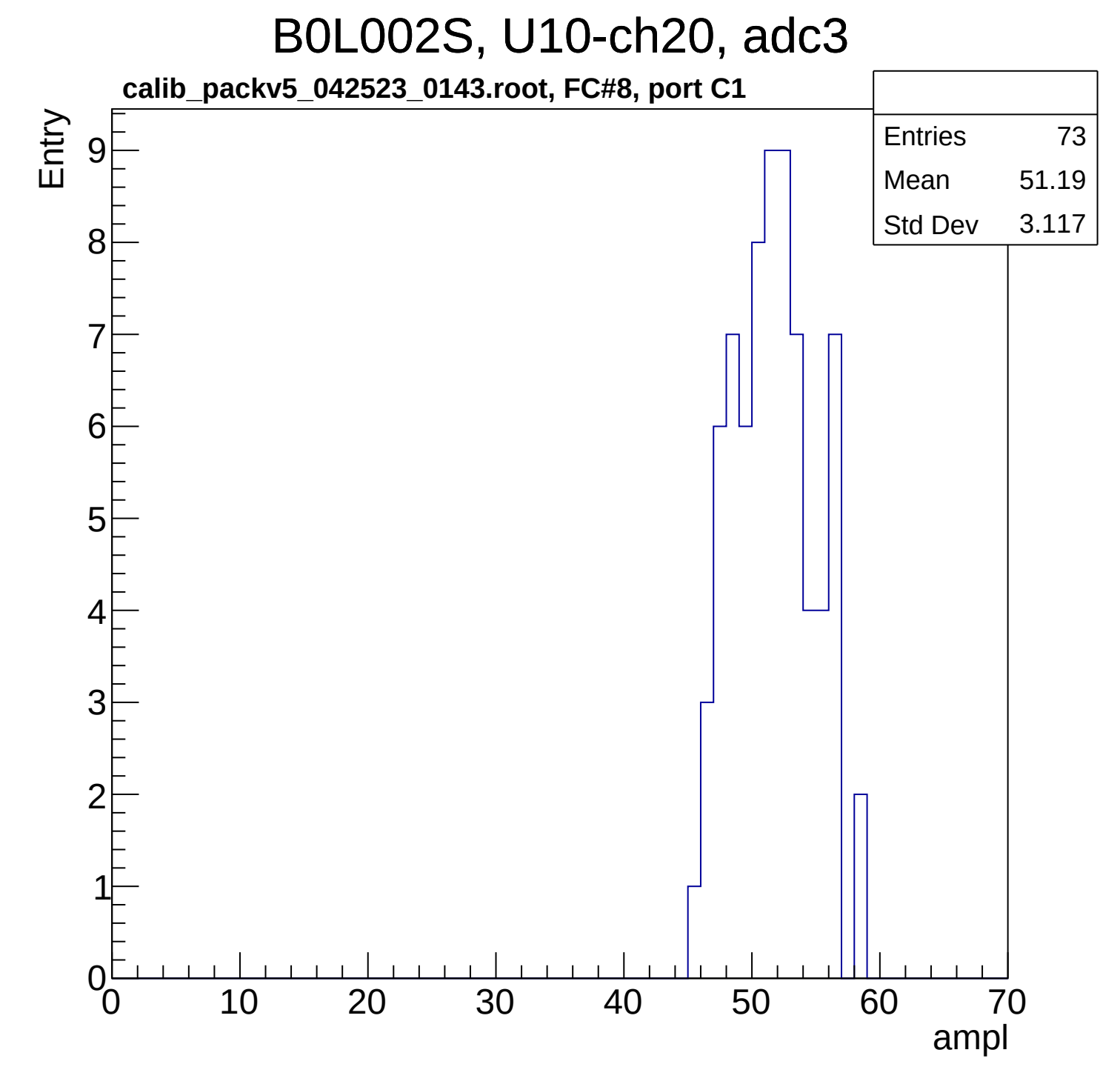

B0L002S, U10-ch19, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

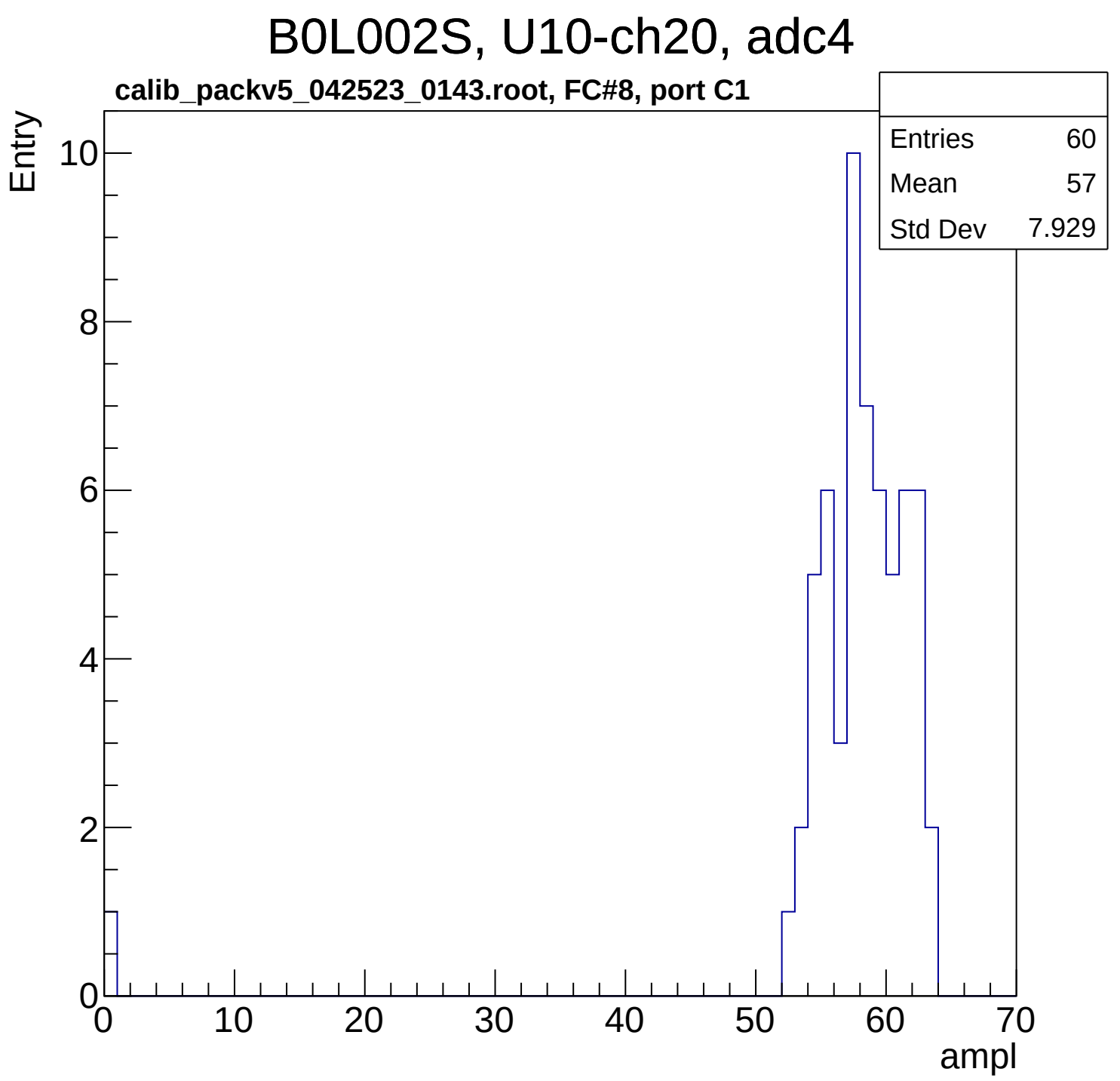

B0L002S, U10-ch19, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

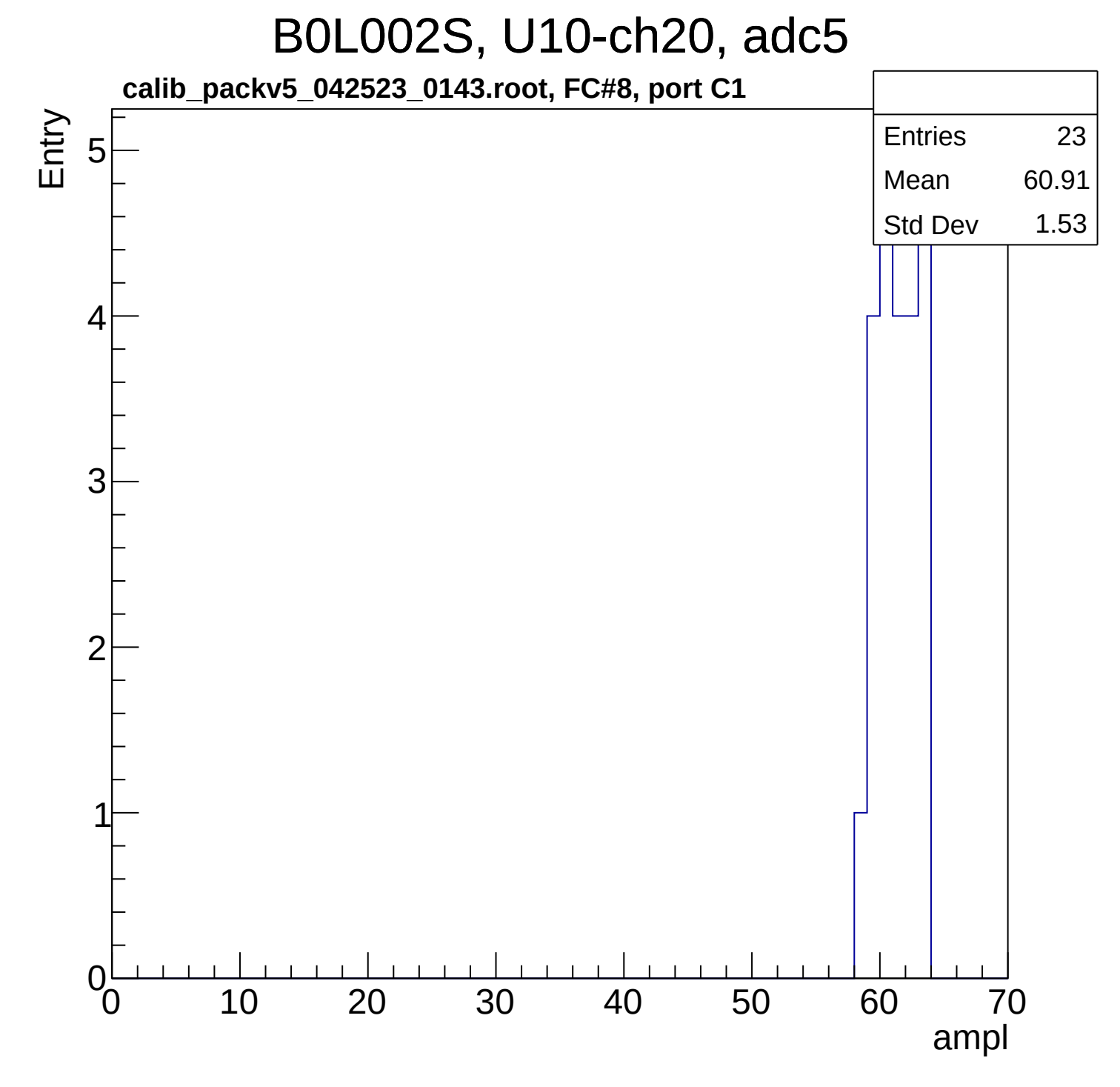

B0L002S, U10-ch20, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 31.18
Std Dev 2.699
Gaus mean : 31.4412
Gaus Width: 2.9876
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

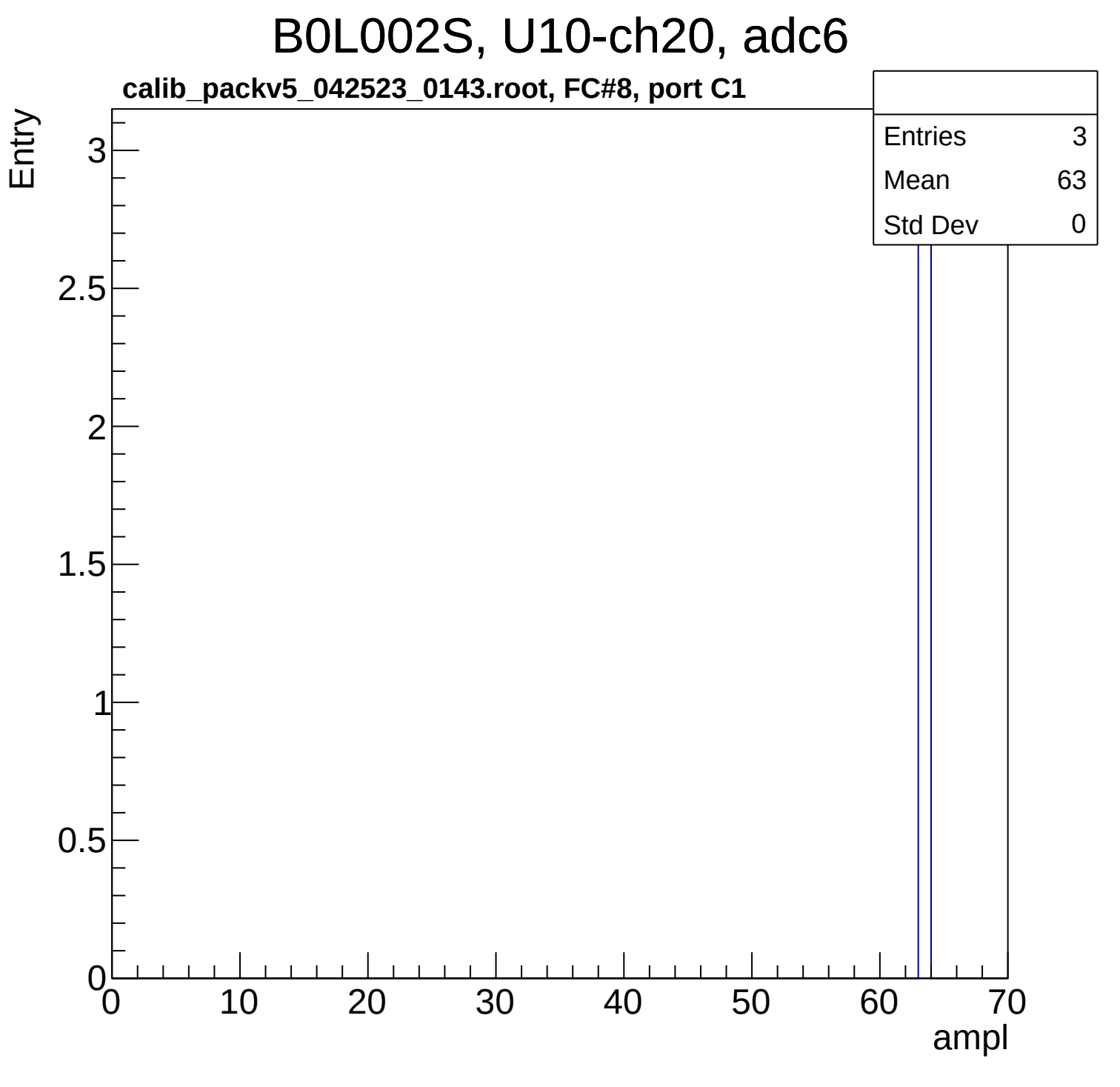

B0L002S, U10-ch20, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 95
Mean 38.13
Std Dev 3.926
Gaus mean : 38.4922
Gaus Width: 4.4221
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

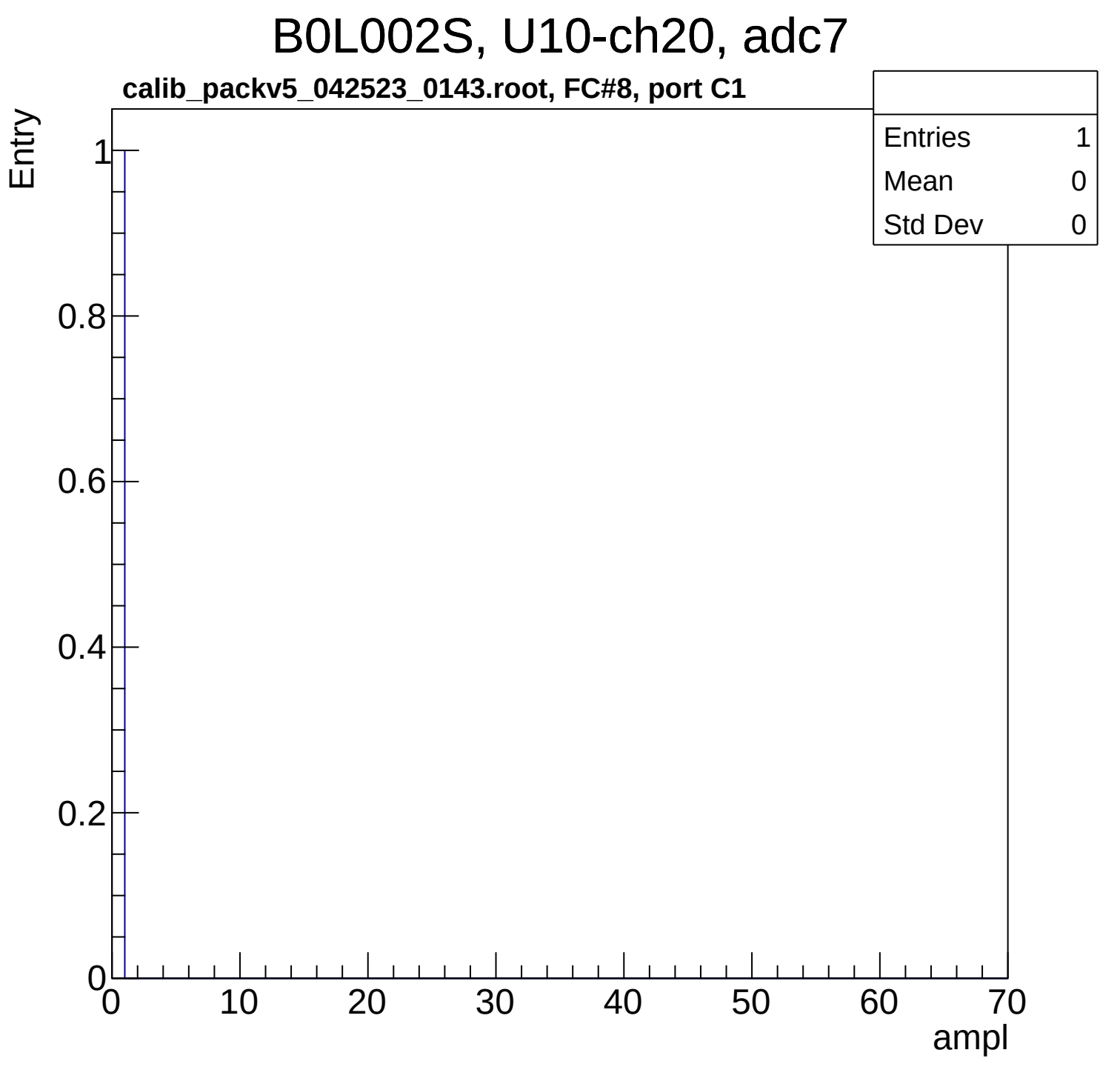

B0L002S, U10-ch20, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 45.33
Std Dev 3.204
Gaus mean : 45.4506
Gaus Width: 3.0889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

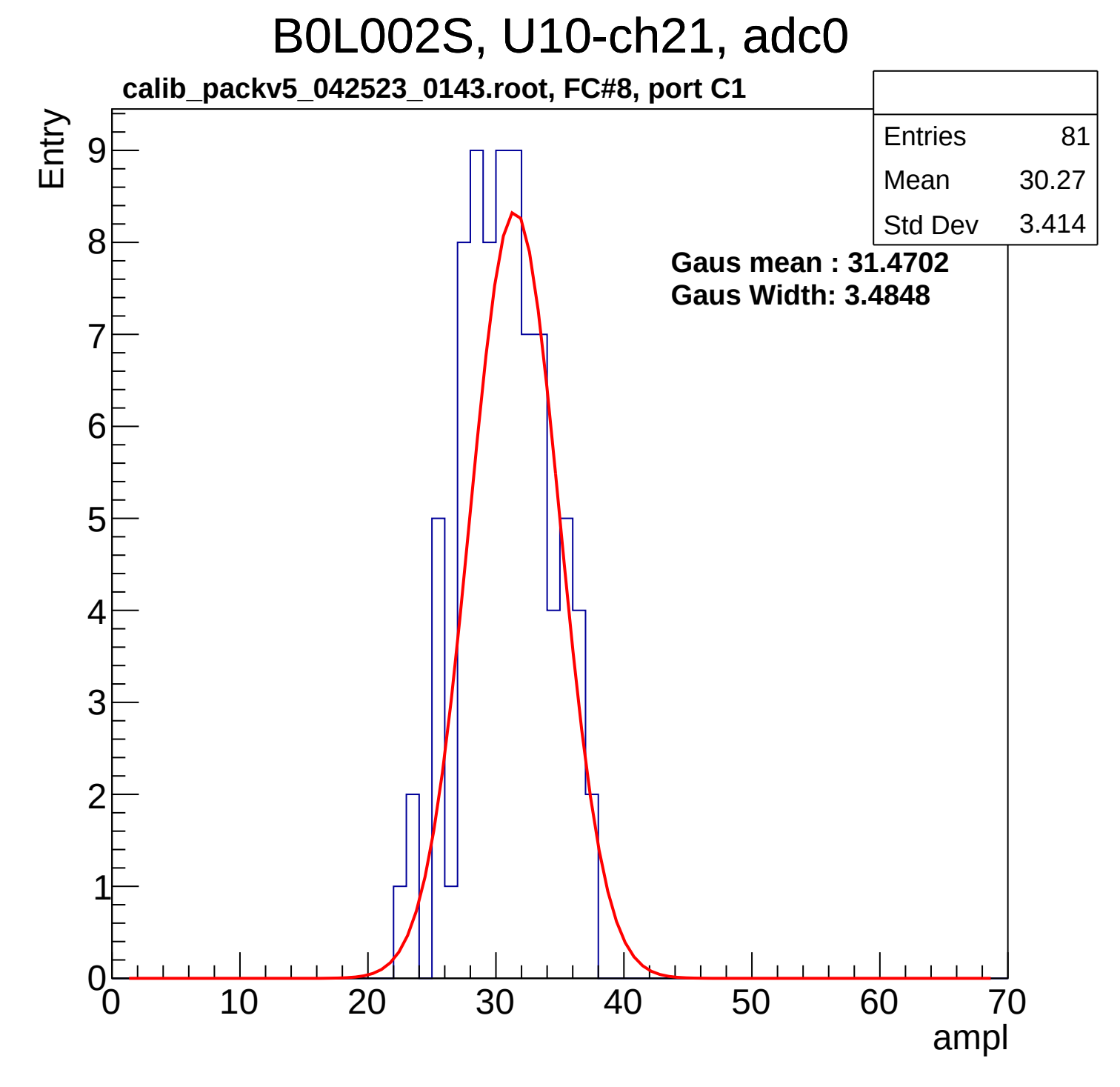

B0L002S, U10-ch20, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 51.19
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

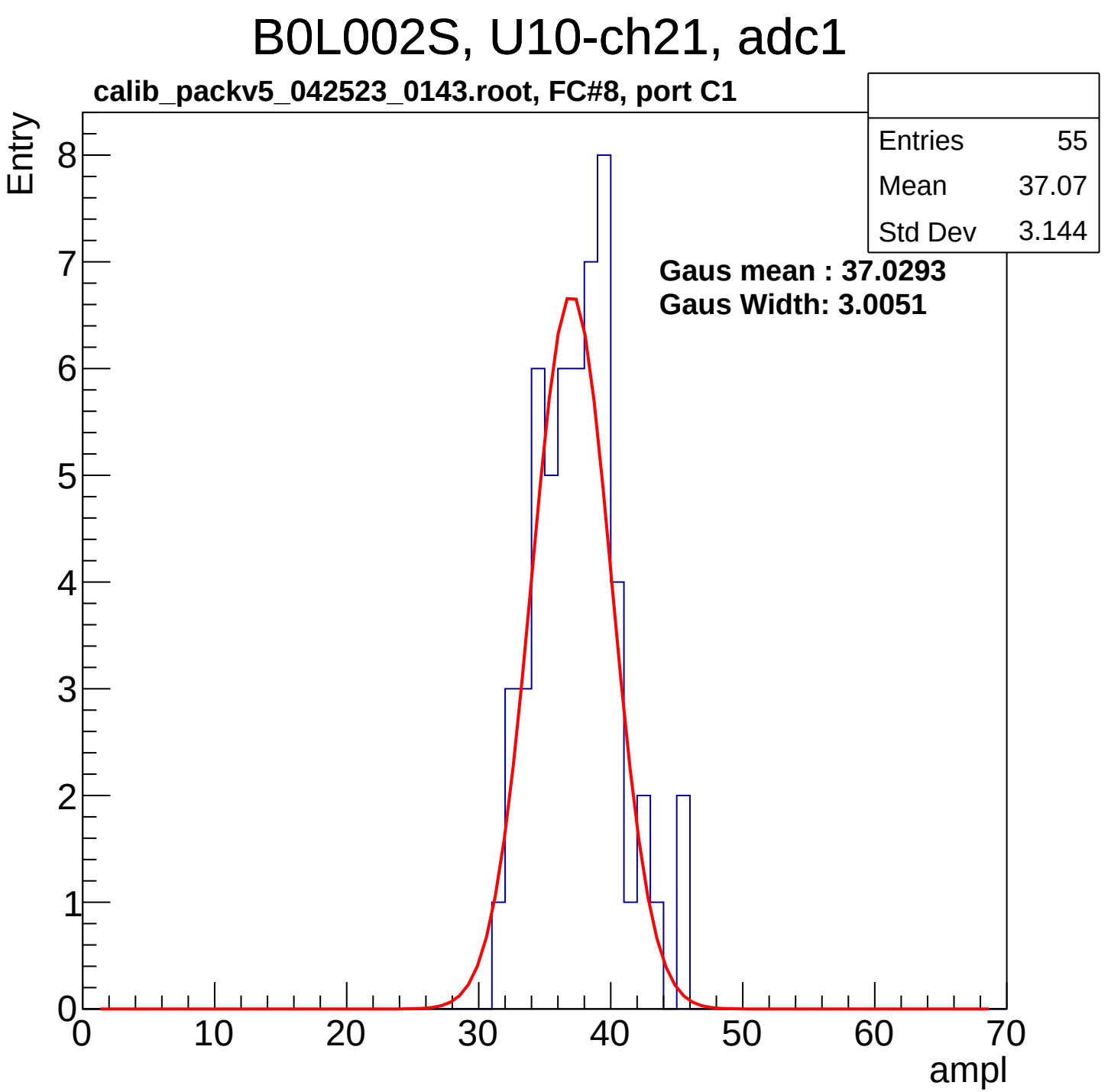

B0L002S, U10-ch20, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 57
Std Dev 7.929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

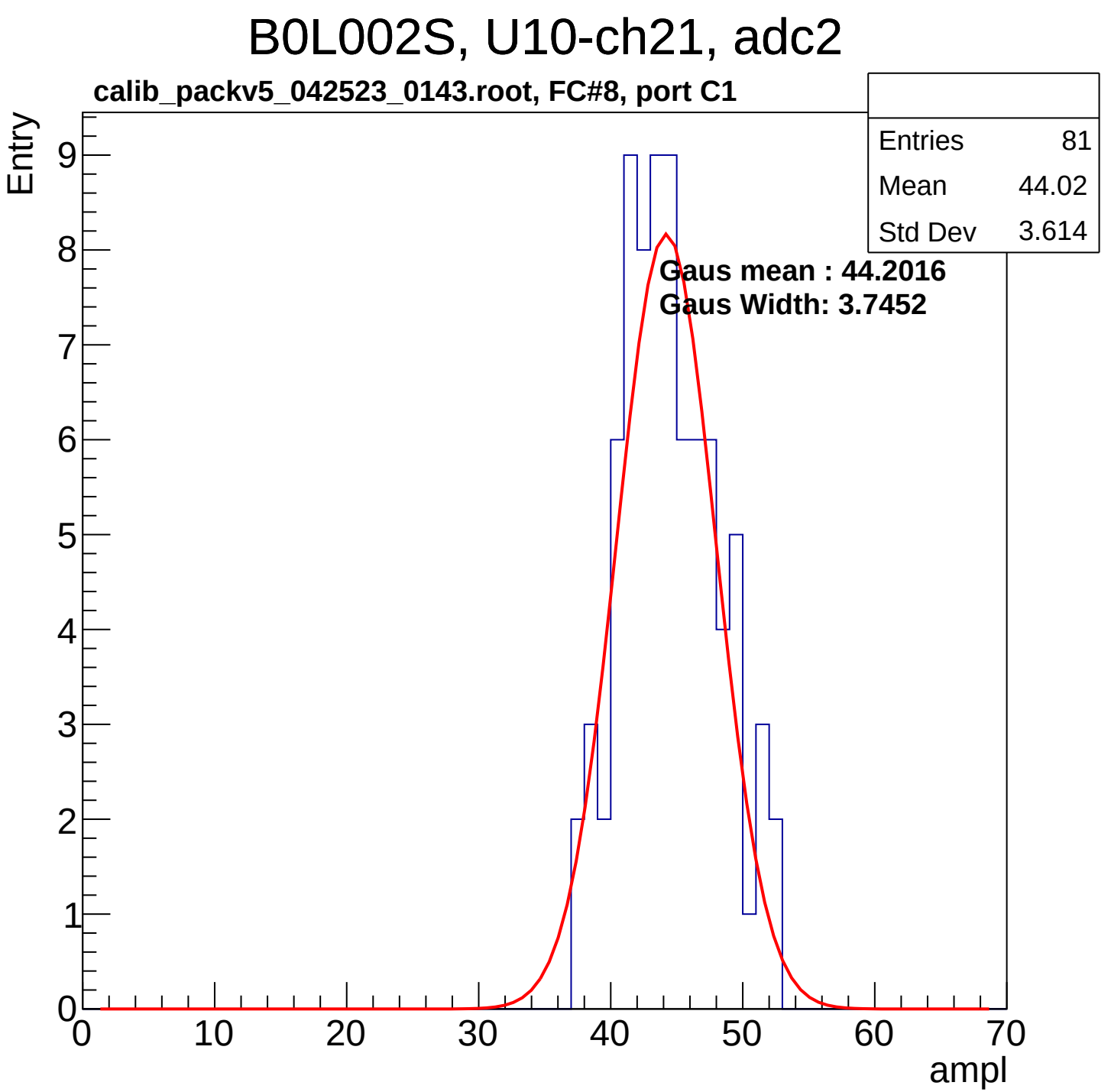

B0L002S, U10-ch20, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 60.91
Std Dev 1.53
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

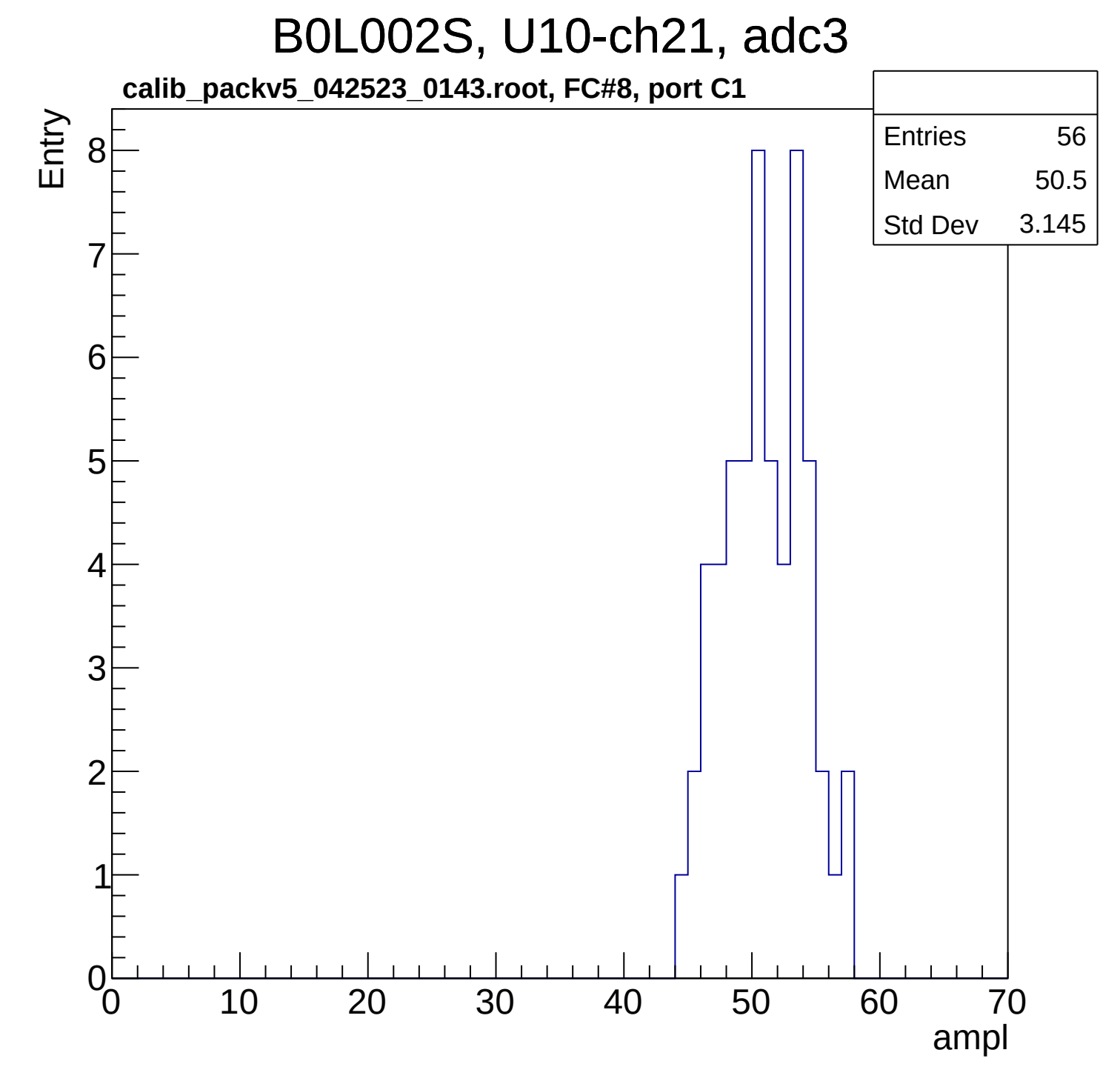

B0L002S, U10-ch20, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

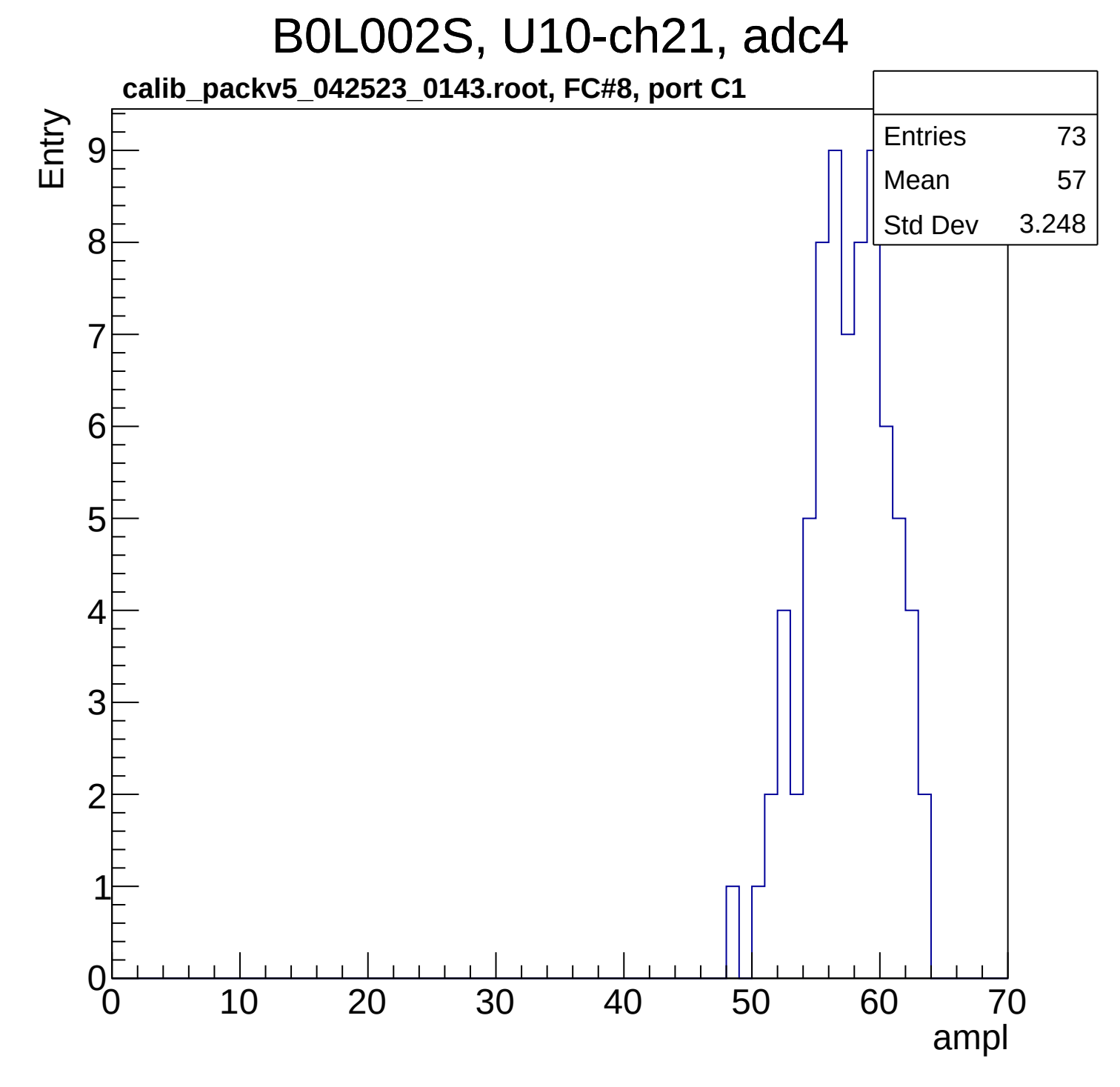

B0L002S, U10-ch20, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

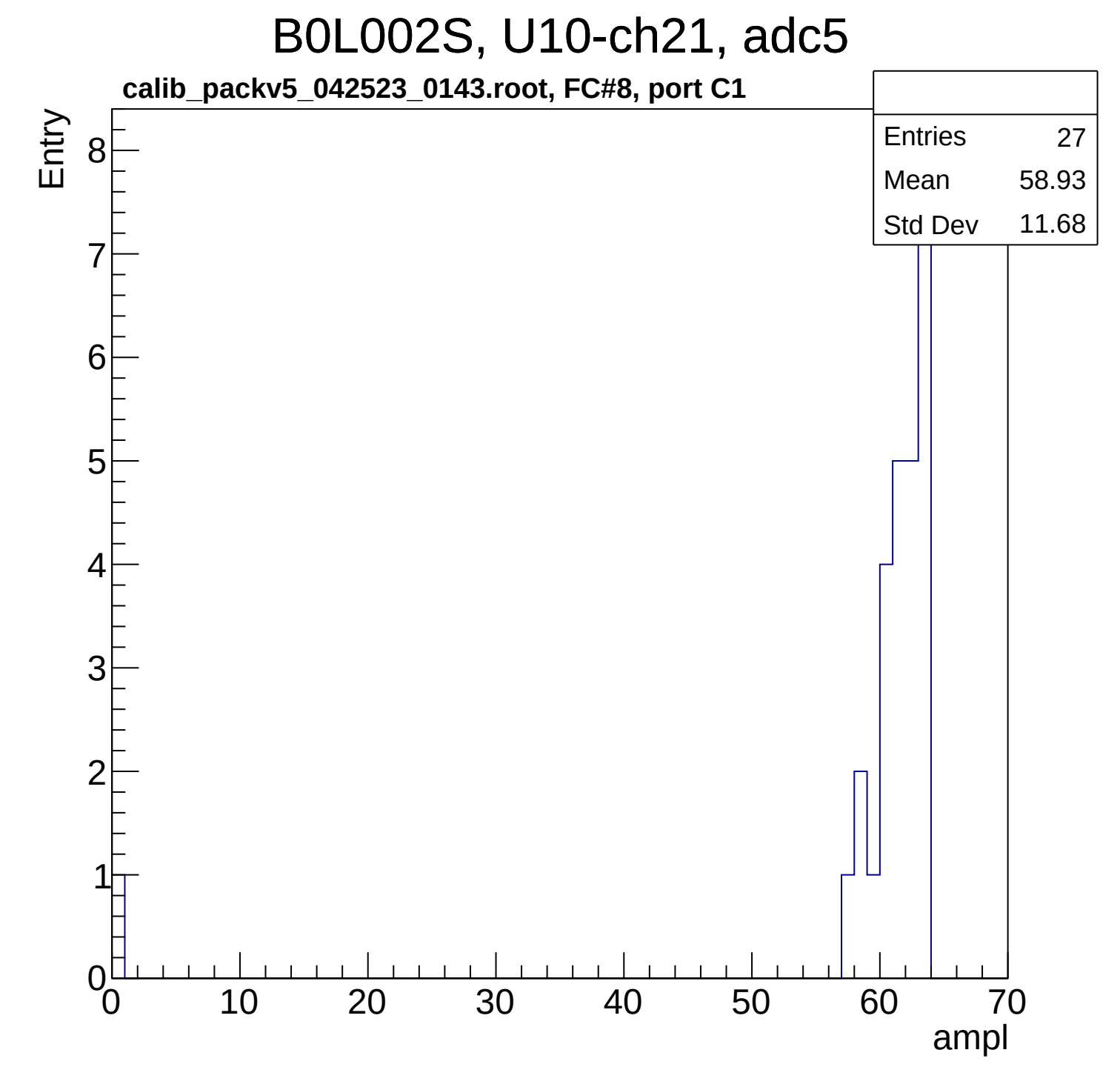

B0L002S, U10-ch21, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 30.27
Std Dev 3.414
Gaus mean : 31.4702
Gaus Width: 3.4848
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

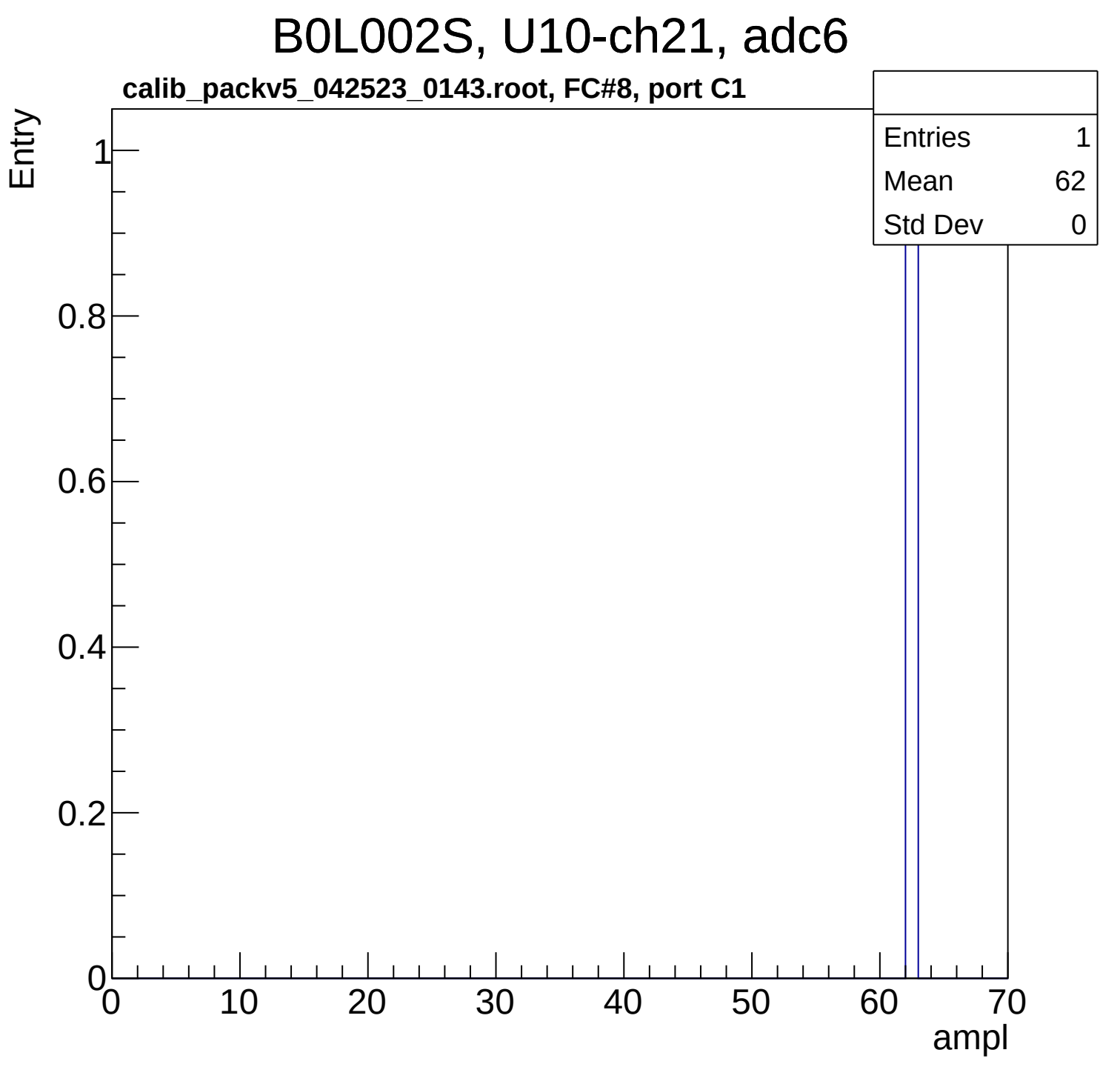

B0L002S, U10-ch21, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 37.07
Std Dev 3.144
Gaus mean : 37.0293
Gaus Width: 3.0051
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

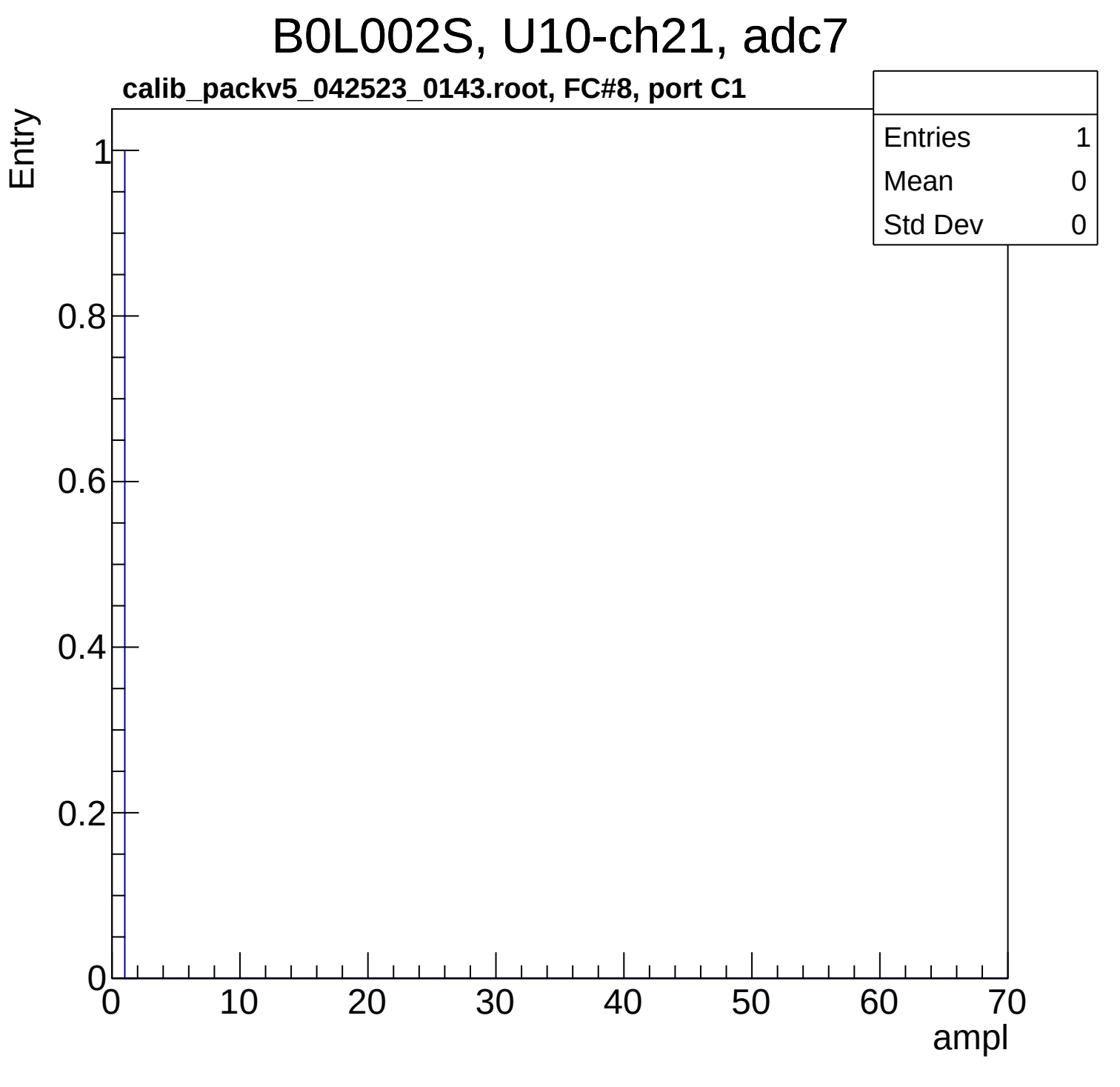

B0L002S, U10-ch21, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 44.02
Std Dev 3.614
Gaus mean : 44.2016
Gaus Width: 3.7452
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

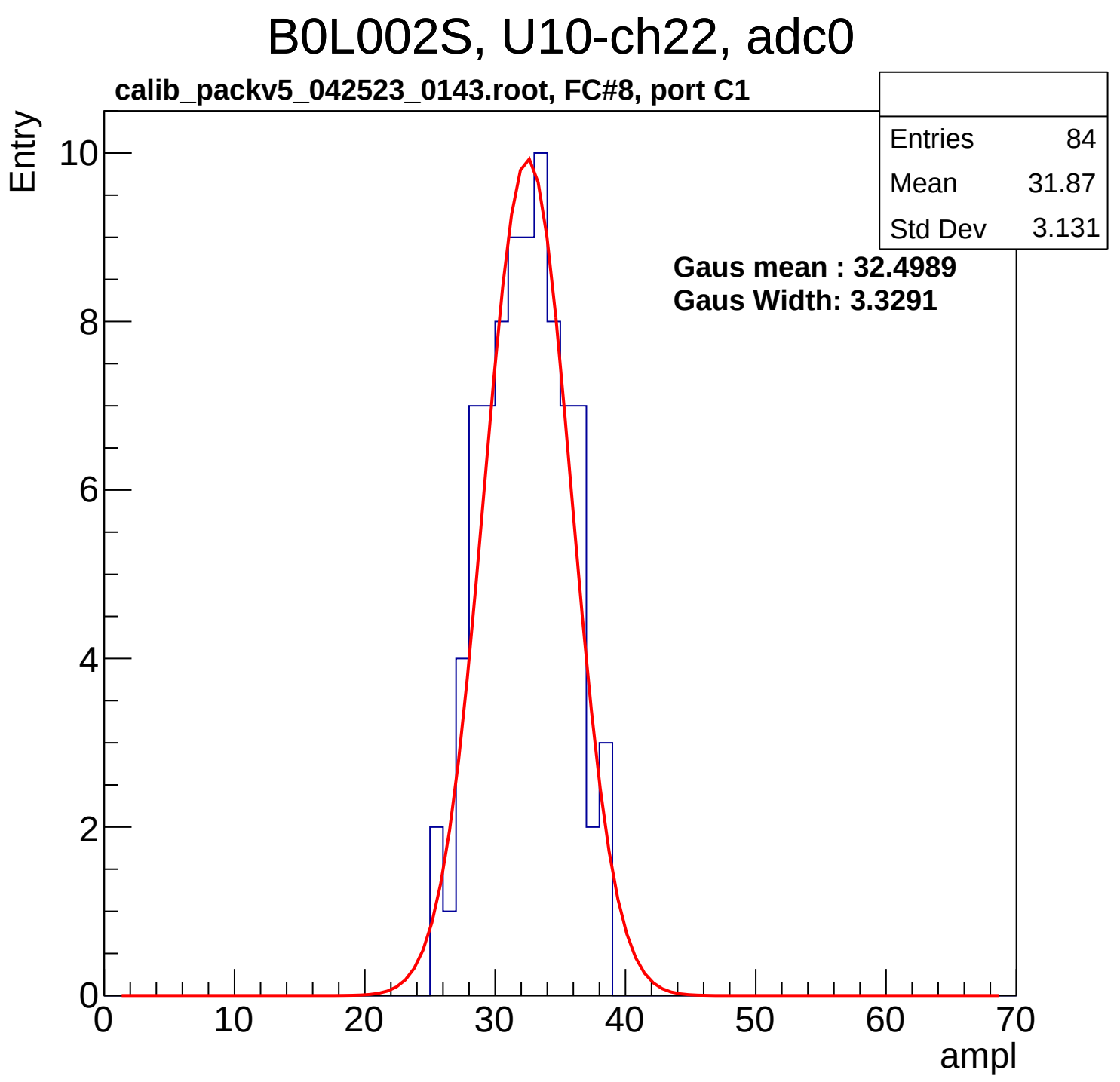

B0L002S, U10-ch21, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 50.5
Std Dev 3.145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

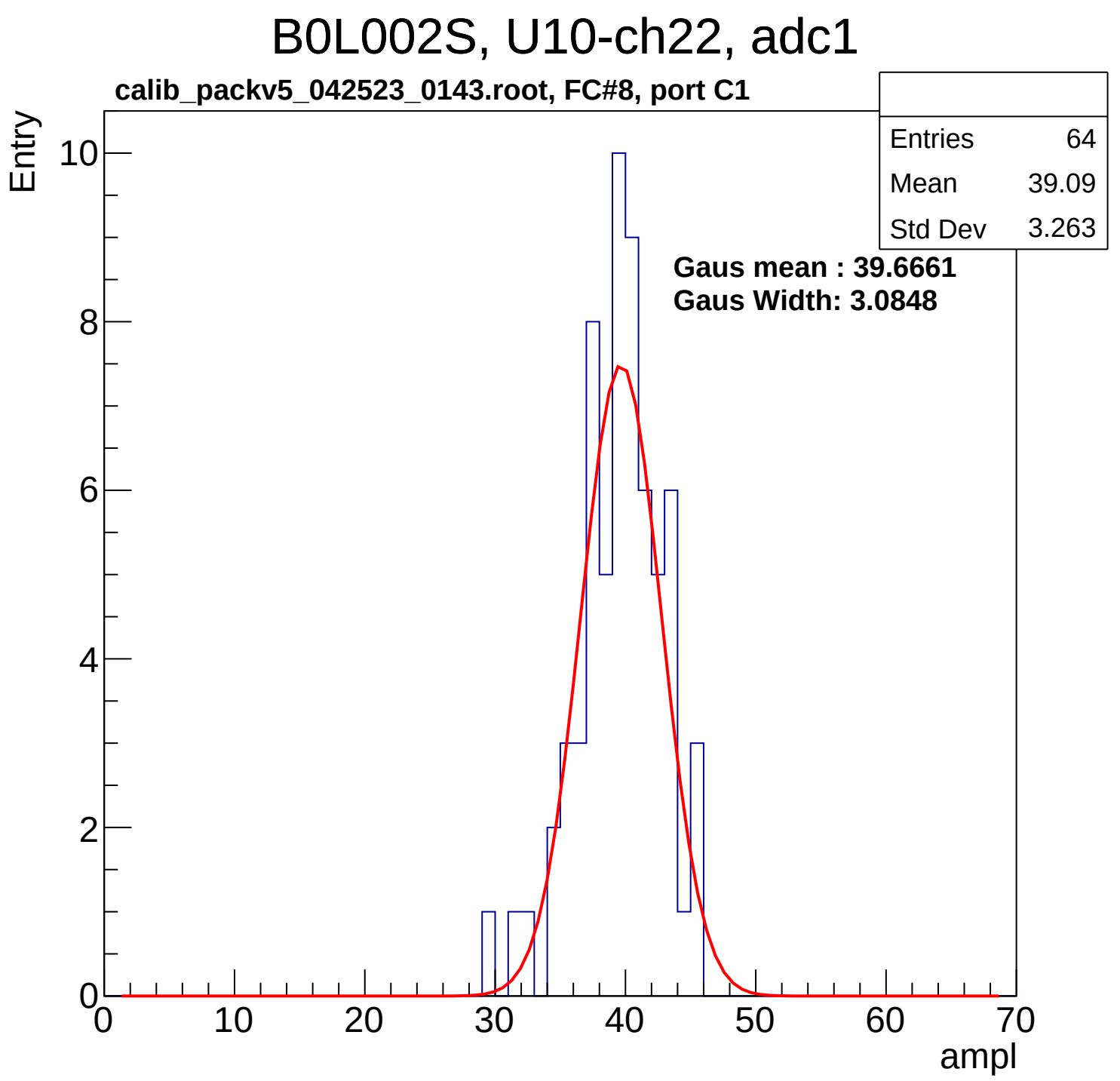

B0L002S, U10-ch21, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 57
Std Dev 3.248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

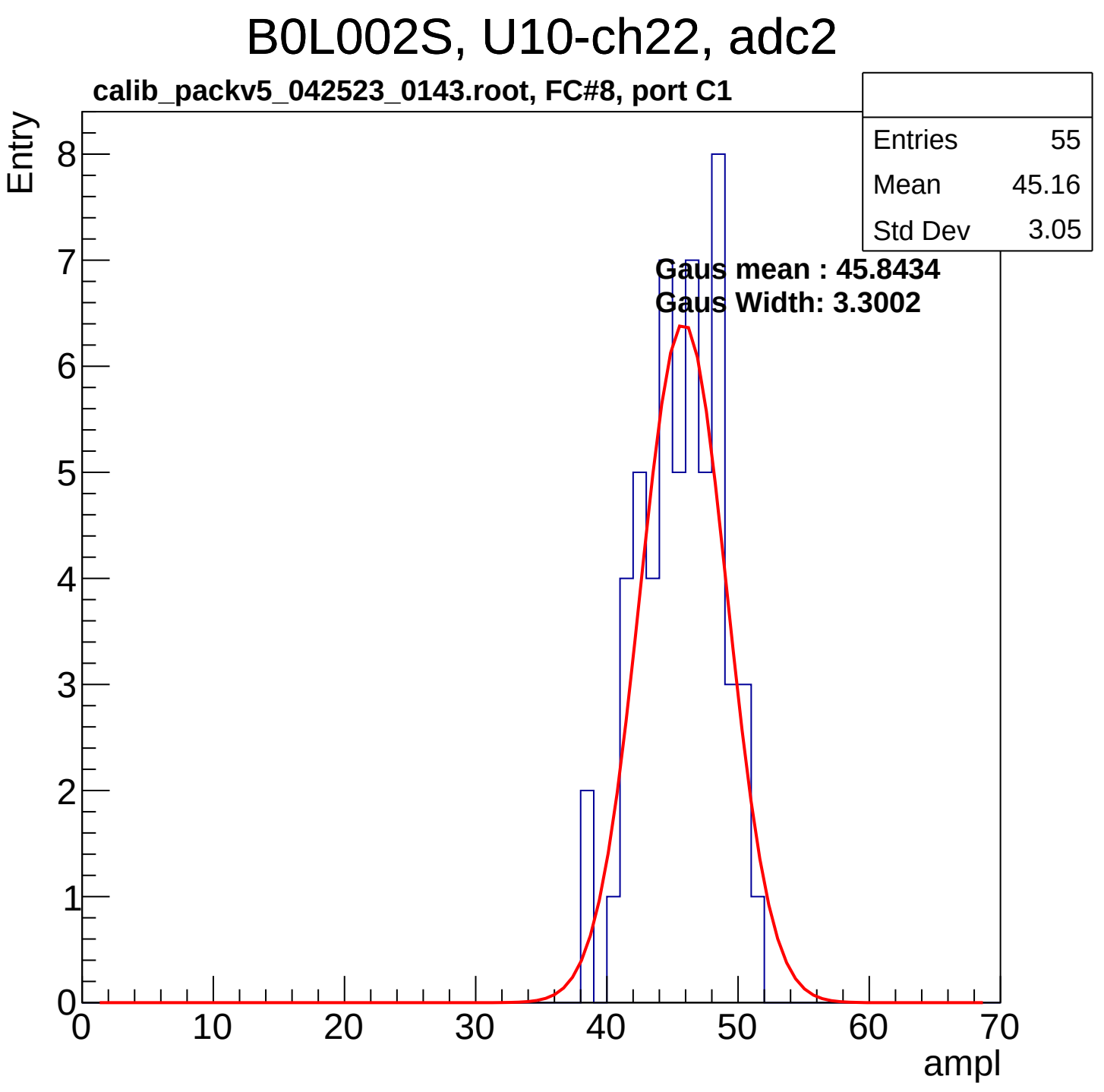

B0L002S, U10-ch21, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 58.93
Std Dev 11.68
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

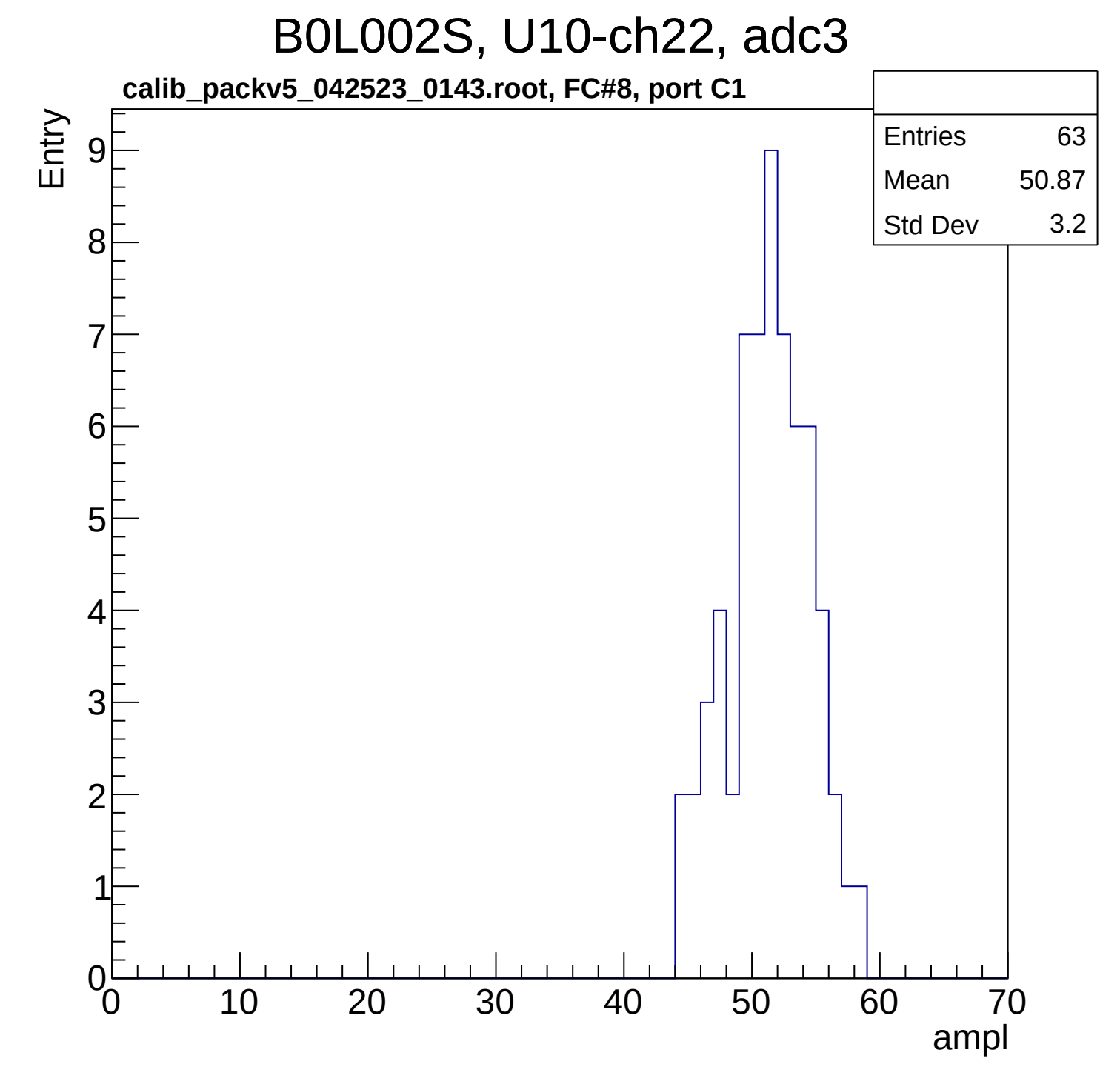

B0L002S, U10-ch21, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

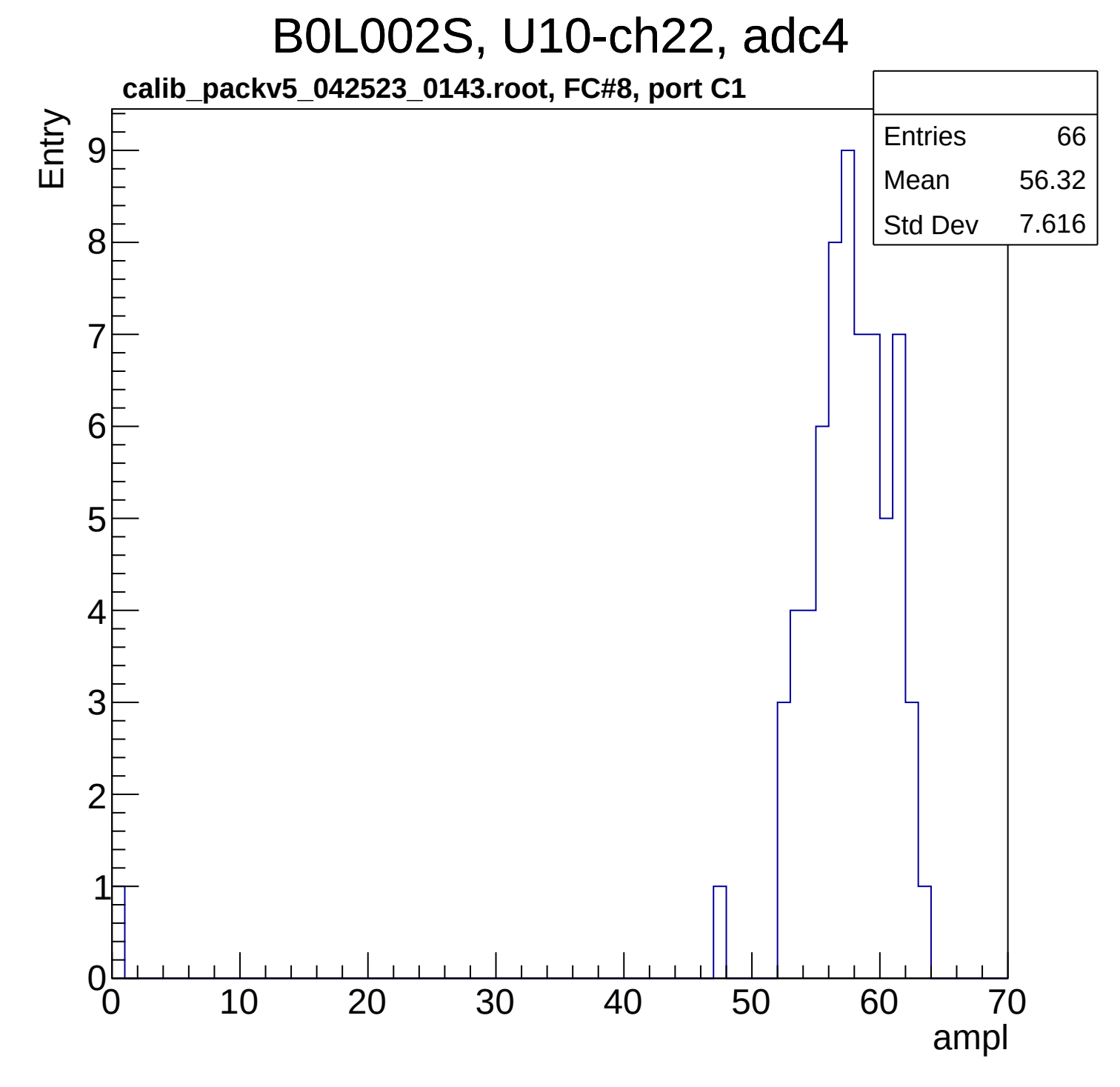

B0L002S, U10-ch21, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

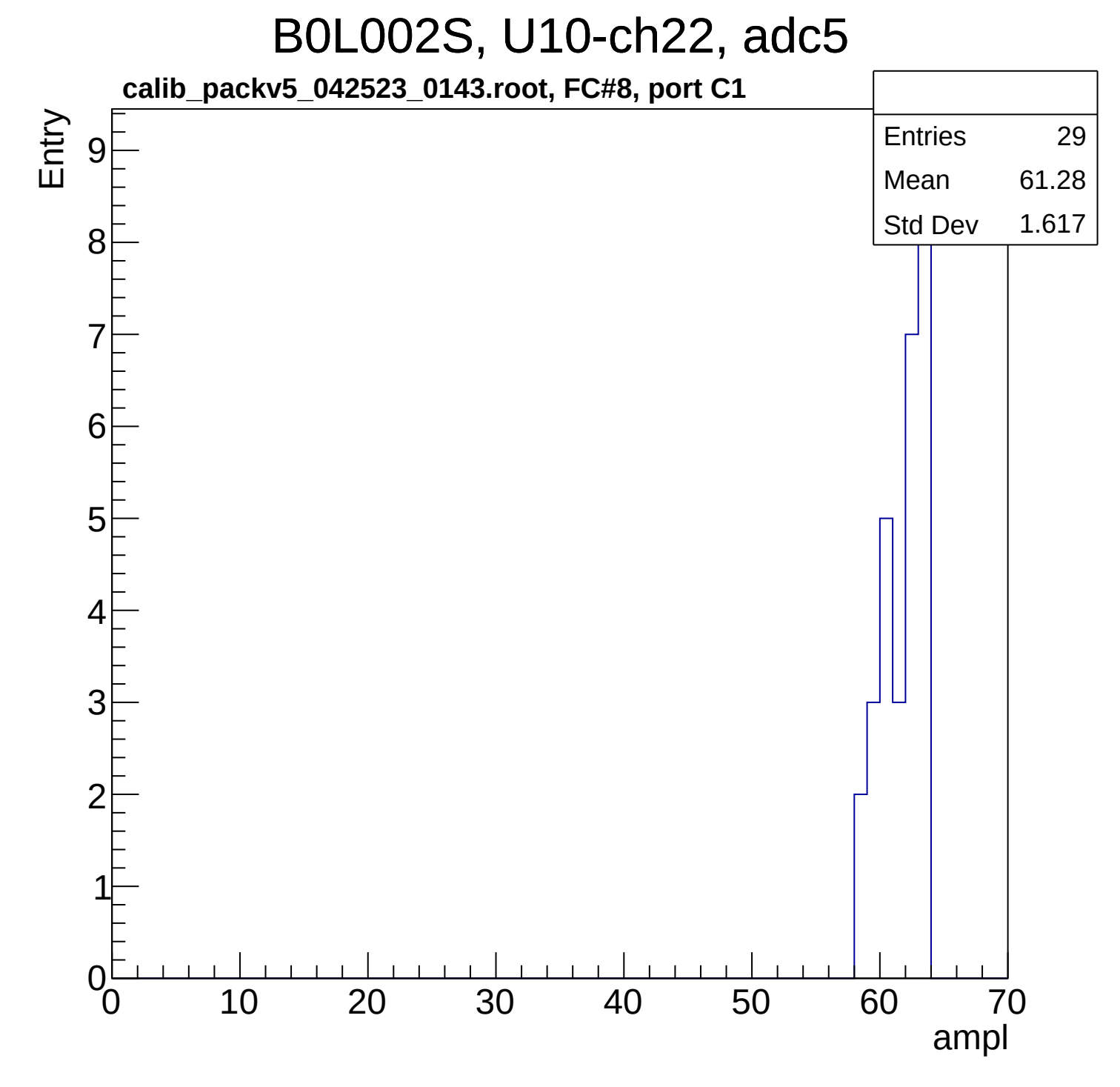

B0L002S, U10-ch22, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 31.87
Std Dev 3.131
Gaus mean : 32.4989
Gaus Width: 3.3291
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

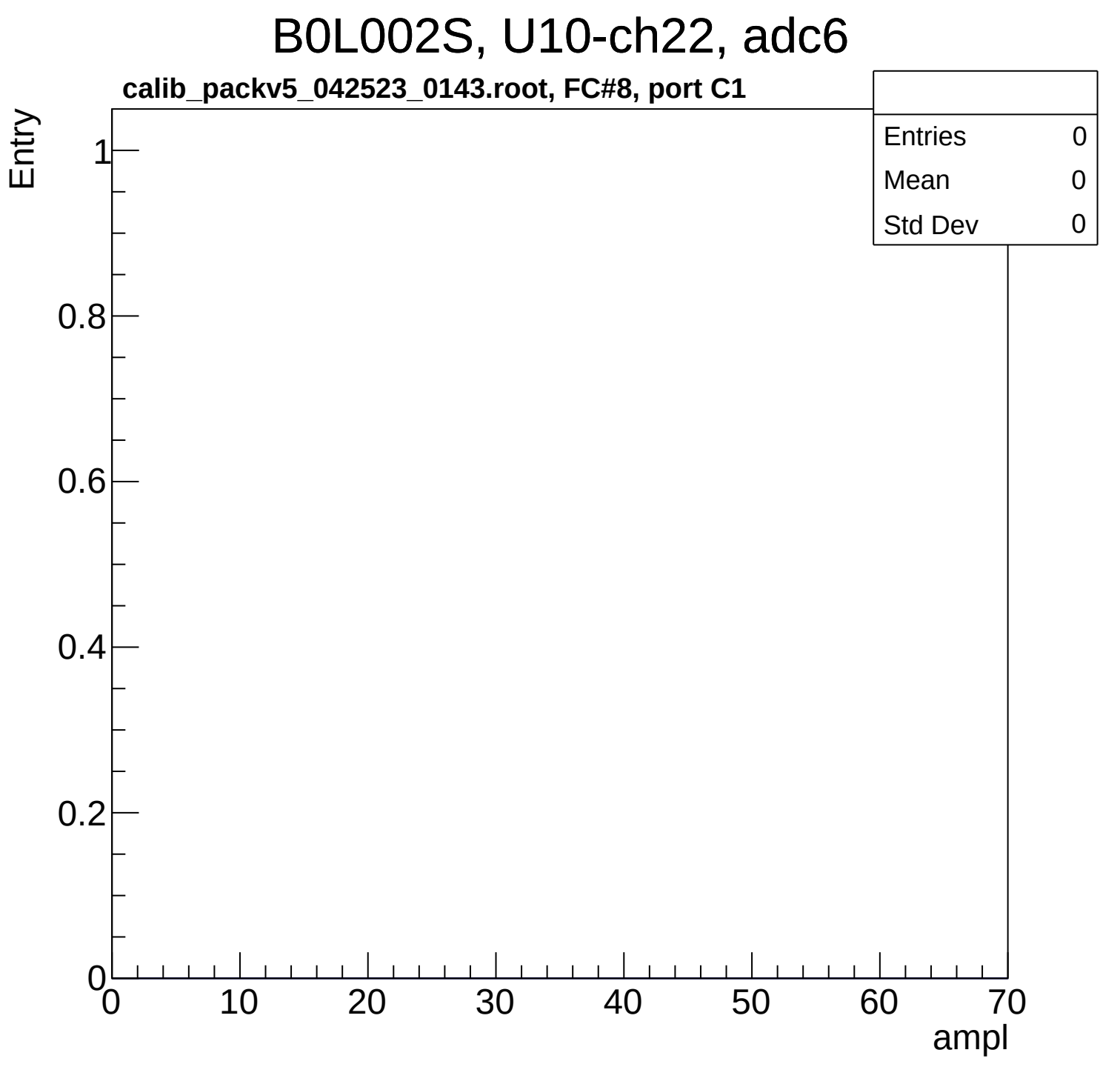

B0L002S, U10-ch22, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 39.09
Std Dev 3.263
Gaus mean : 39.6661
Gaus Width: 3.0848
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

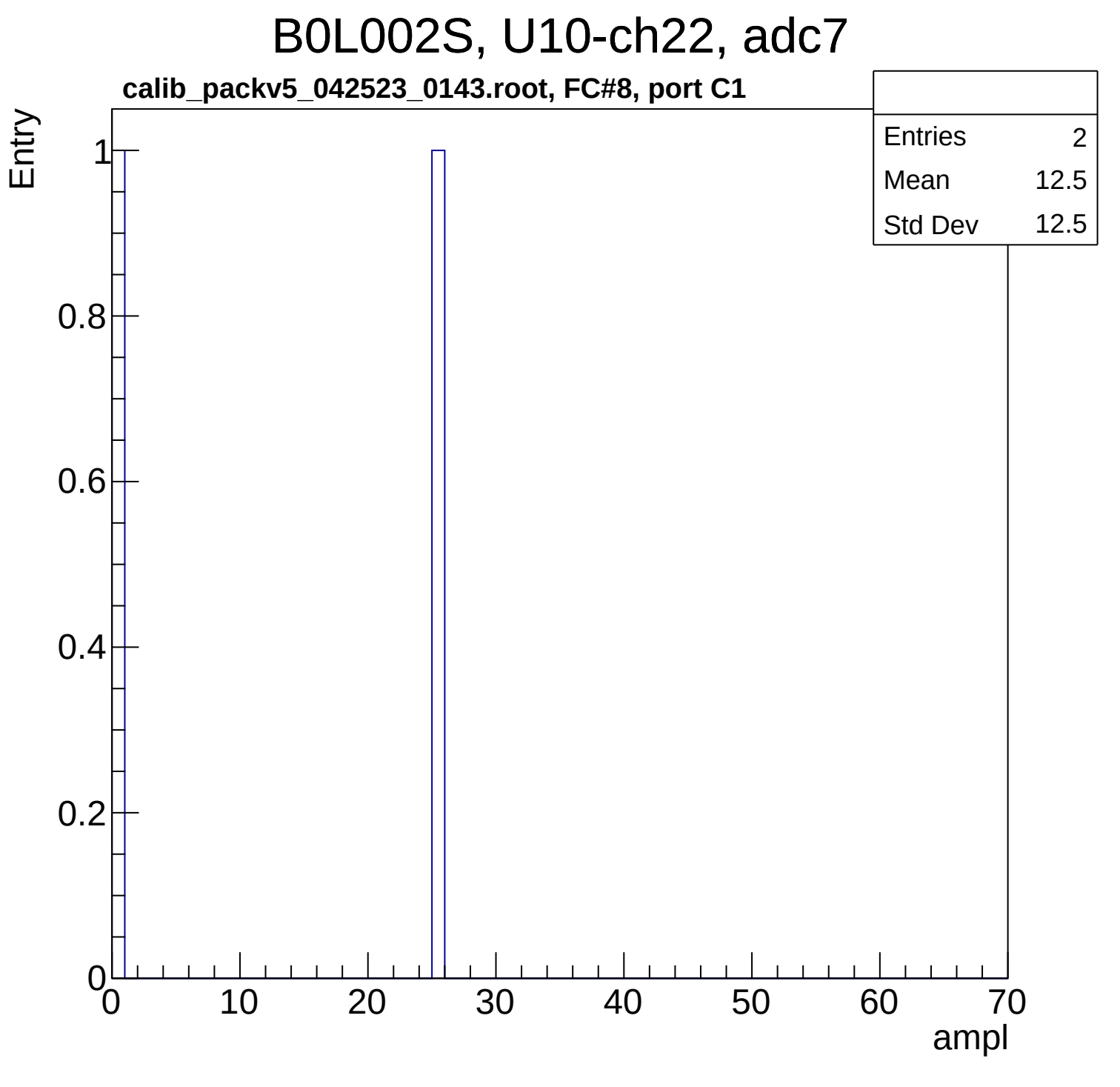

B0L002S, U10-ch22, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.16
Std Dev 3.05
Gaus mean : 45.8434
Gaus Width: 3.3002
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

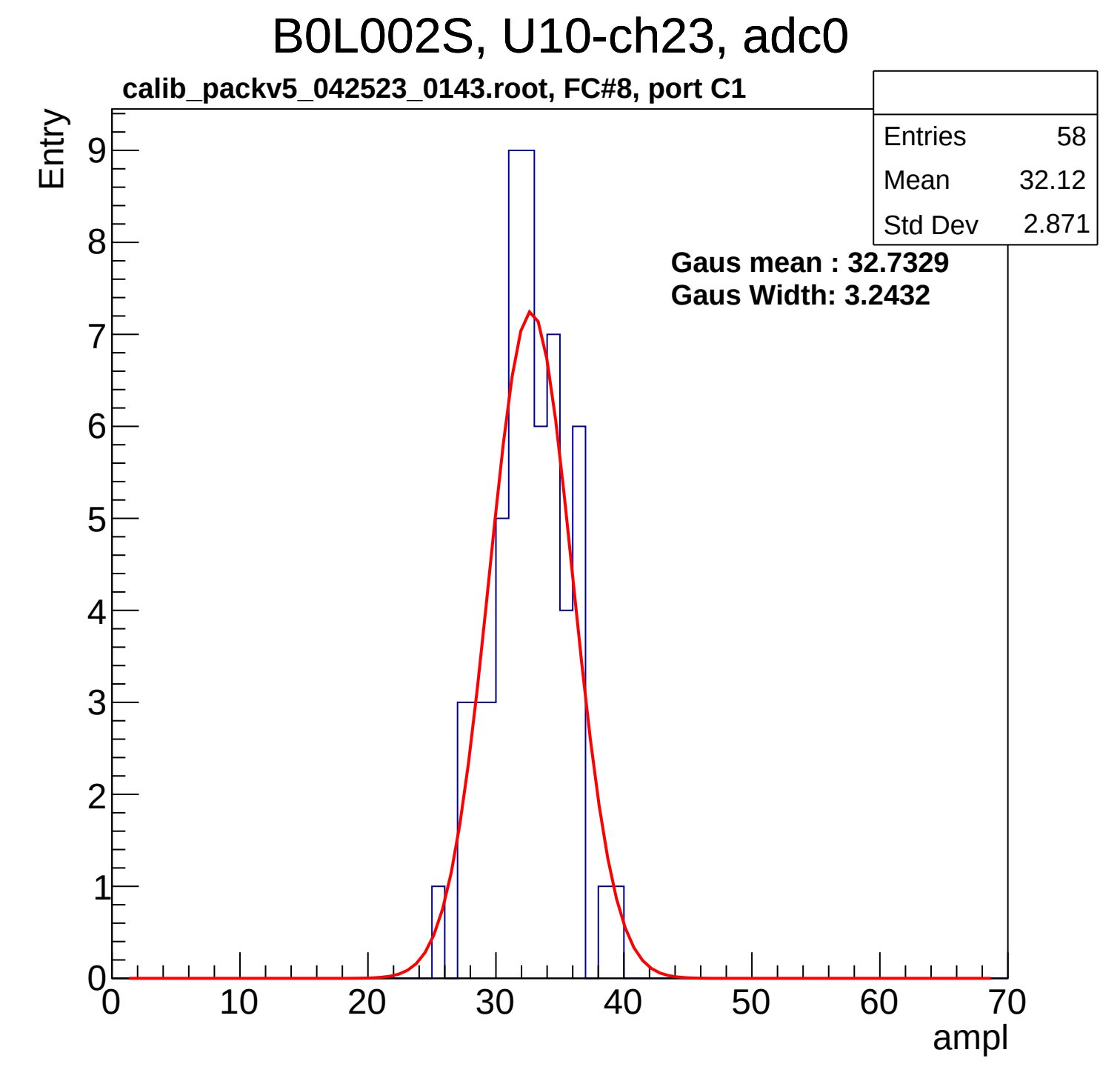

B0L002S, U10-ch22, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.87
Std Dev 3.2
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

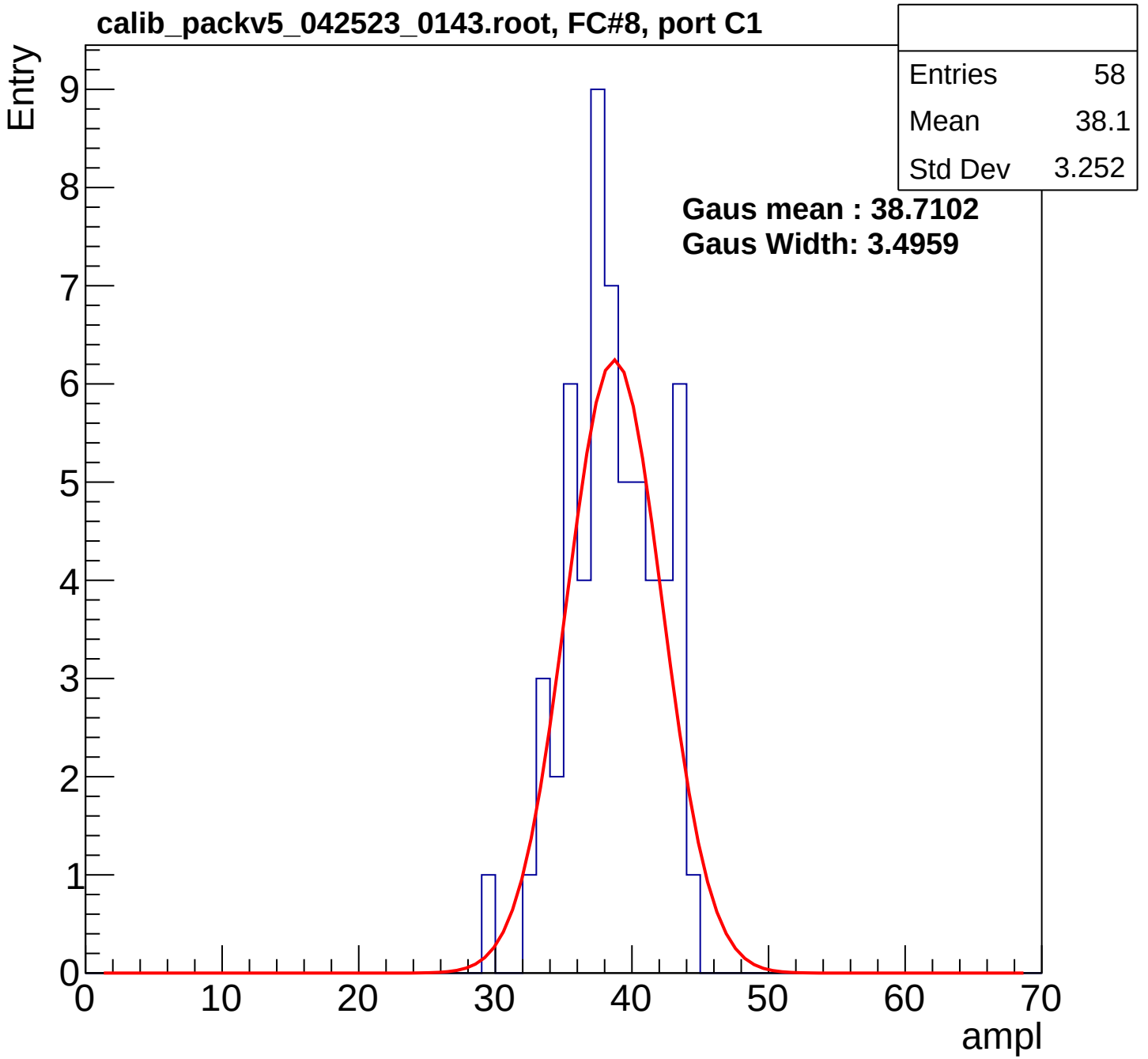

B0L002S, U10-ch22, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 56.32
Std Dev 7.616
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

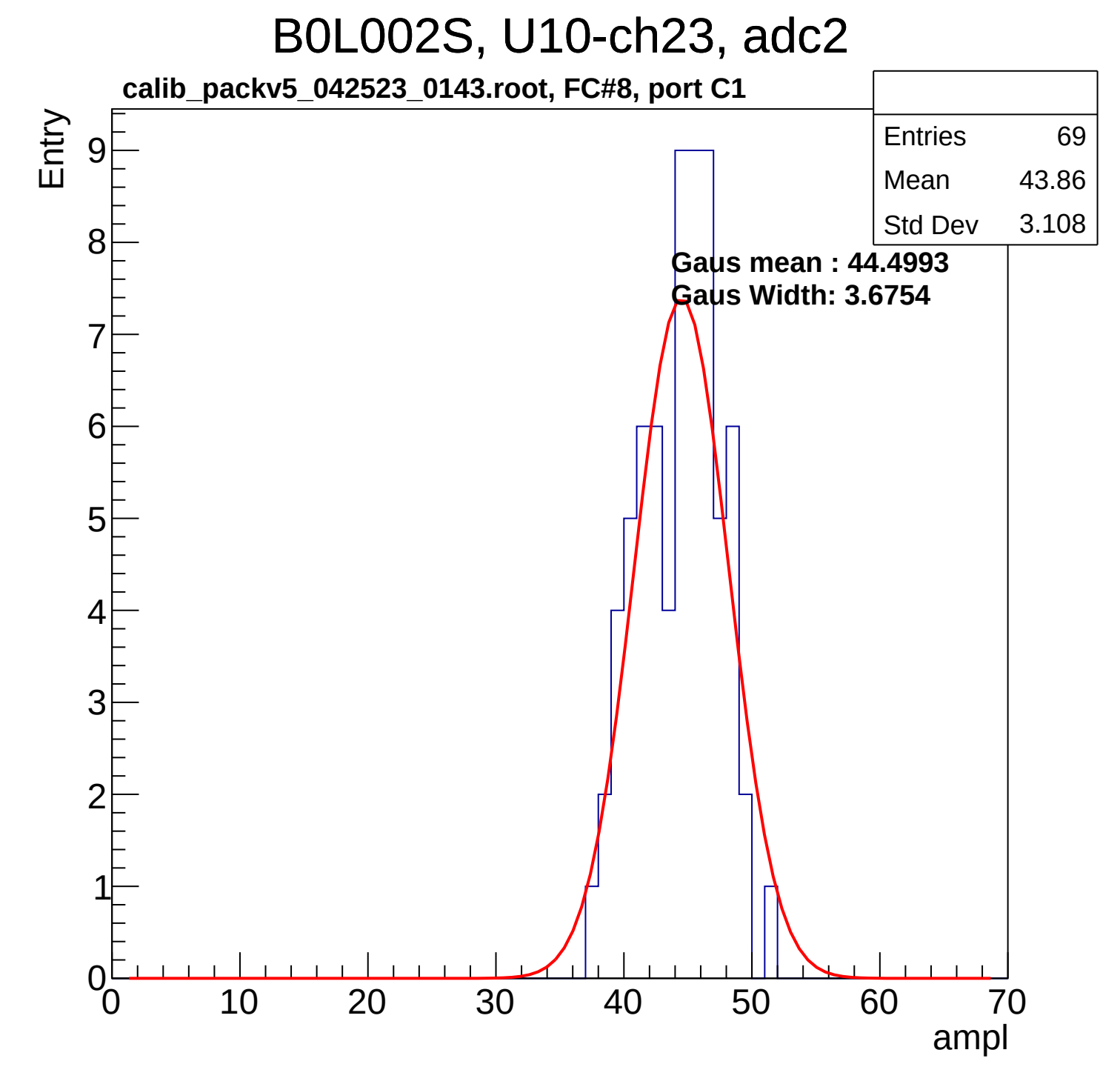

B0L002S, U10-ch22, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 61.28
Std Dev 1.617
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

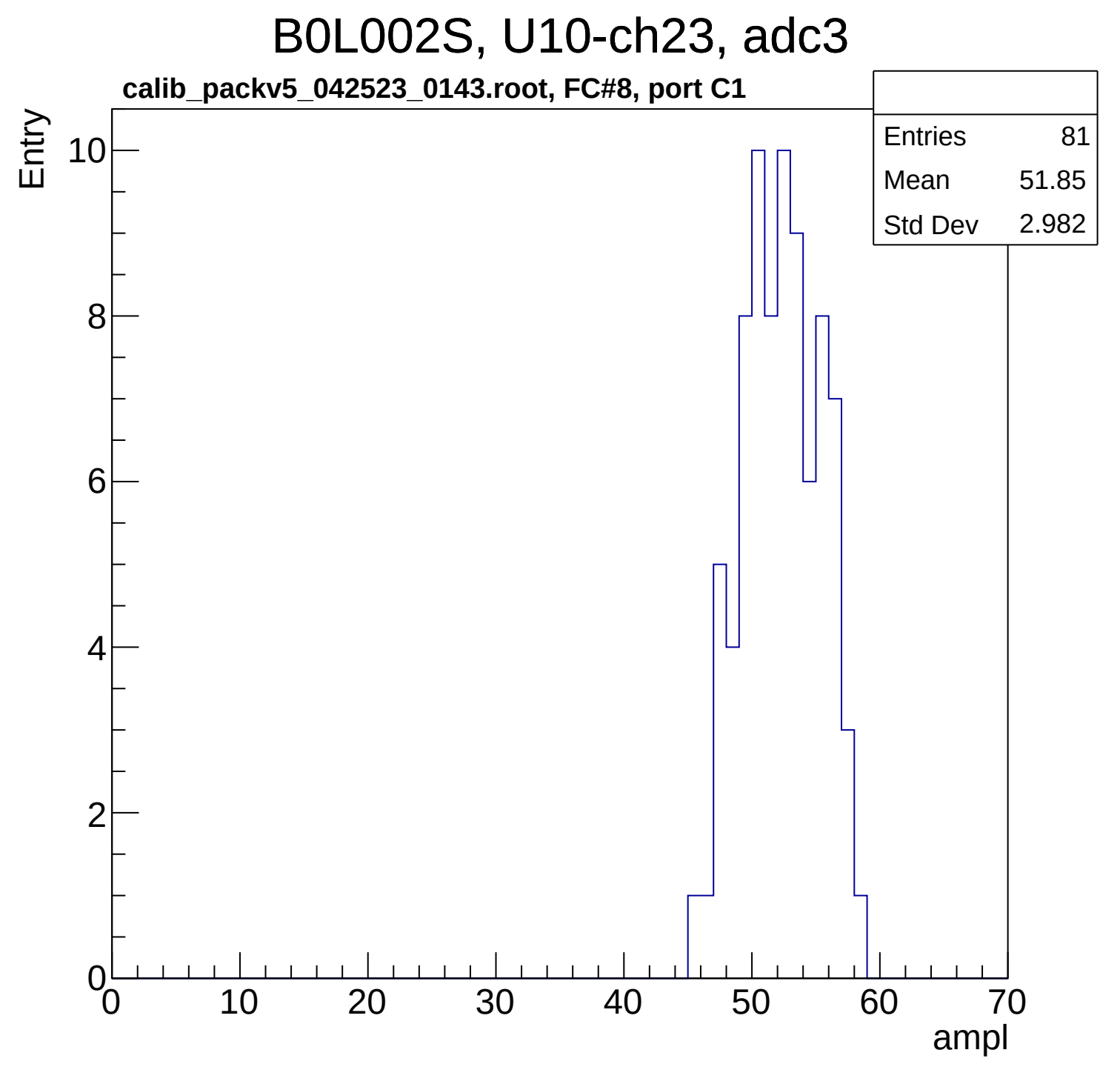

B0L002S, U10-ch22, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

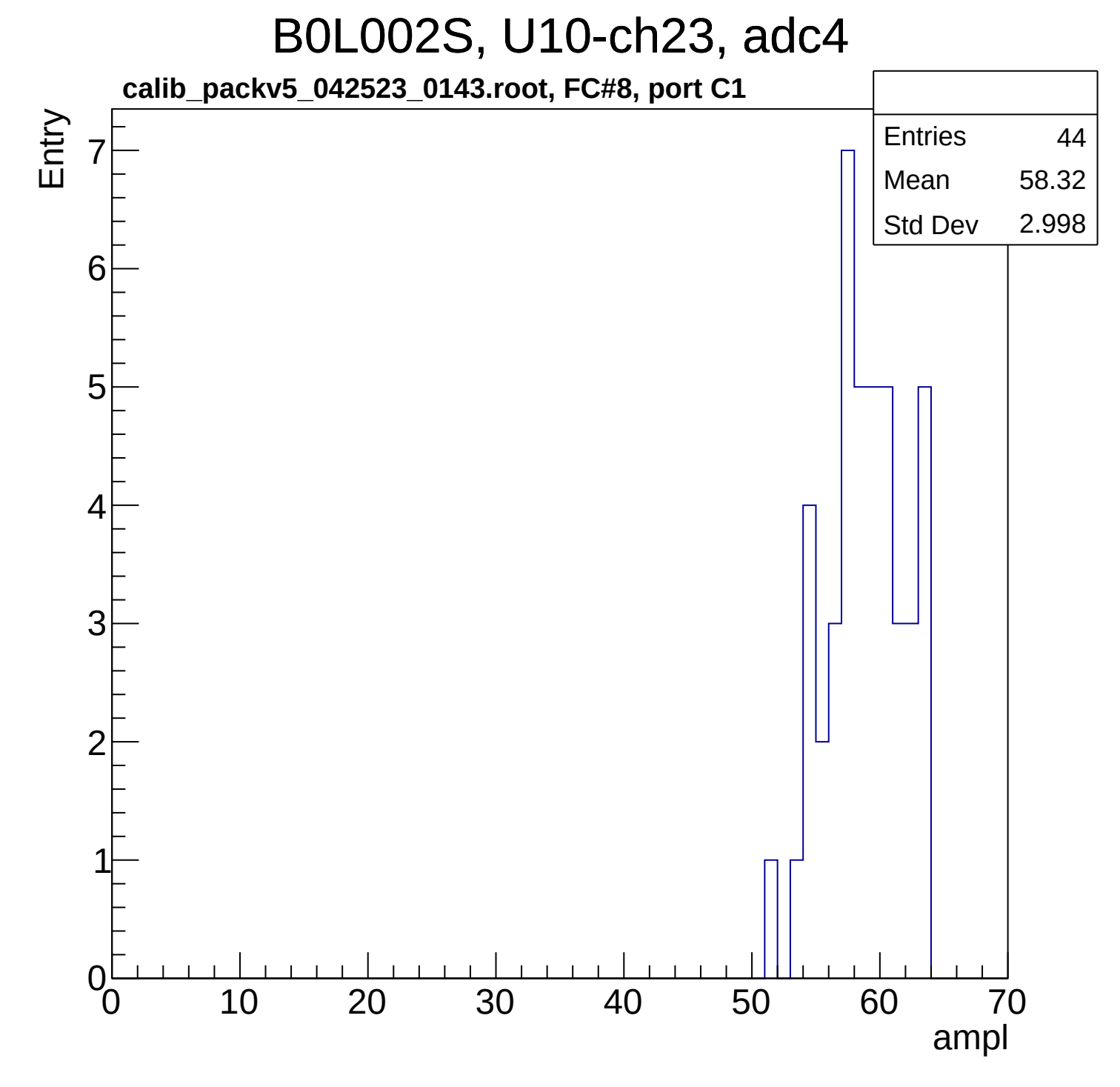

B0L002S, U10-ch22, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

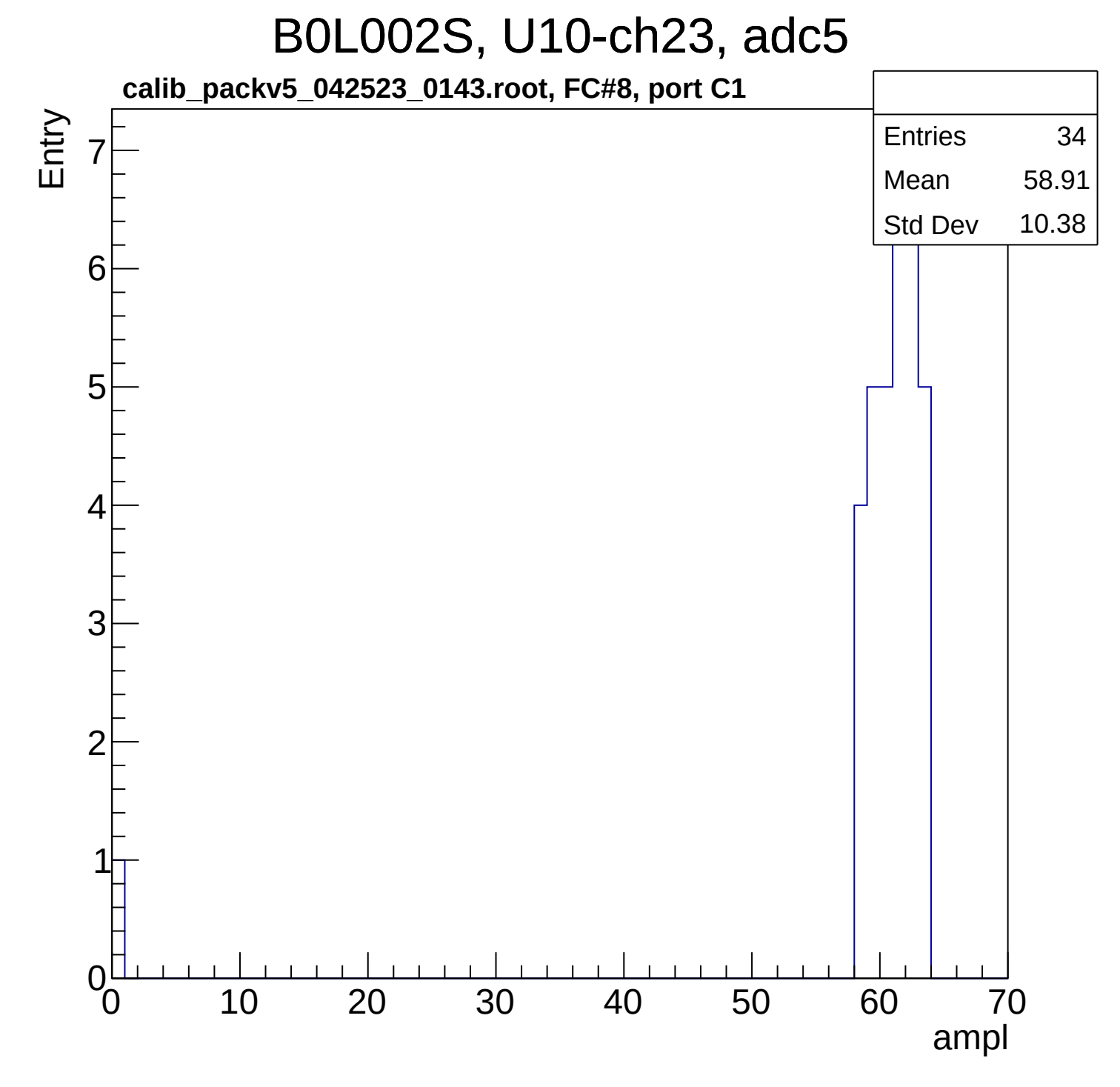

B0L002S, U10-ch23, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 32.12
Std Dev 2.871
Gaus mean : 32.7329
Gaus Width: 3.2432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

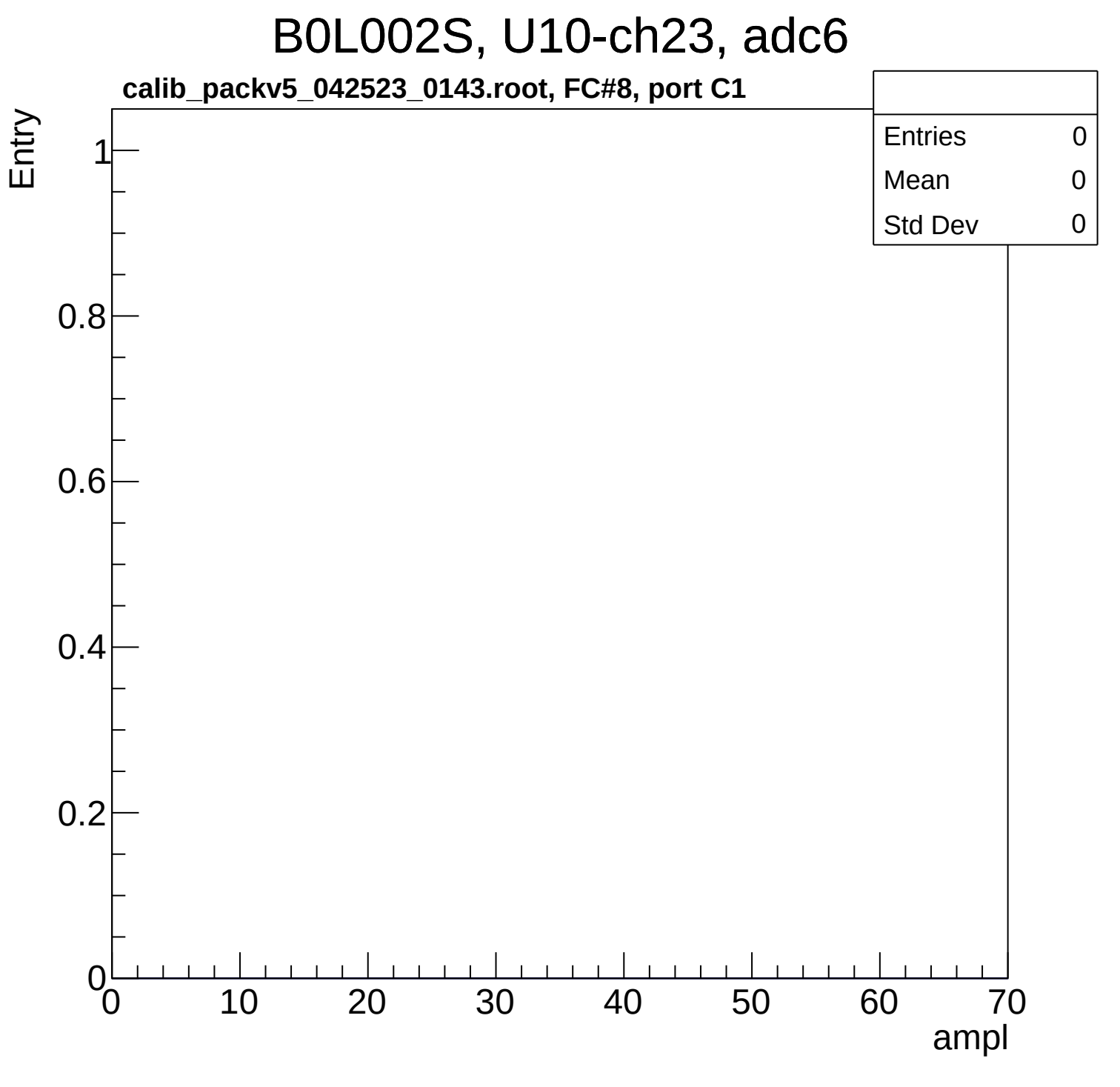

B0L002S, U10-ch23, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 38.1
Std Dev 3.252
Gaus mean : 38.7102
Gaus Width: 3.4959
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

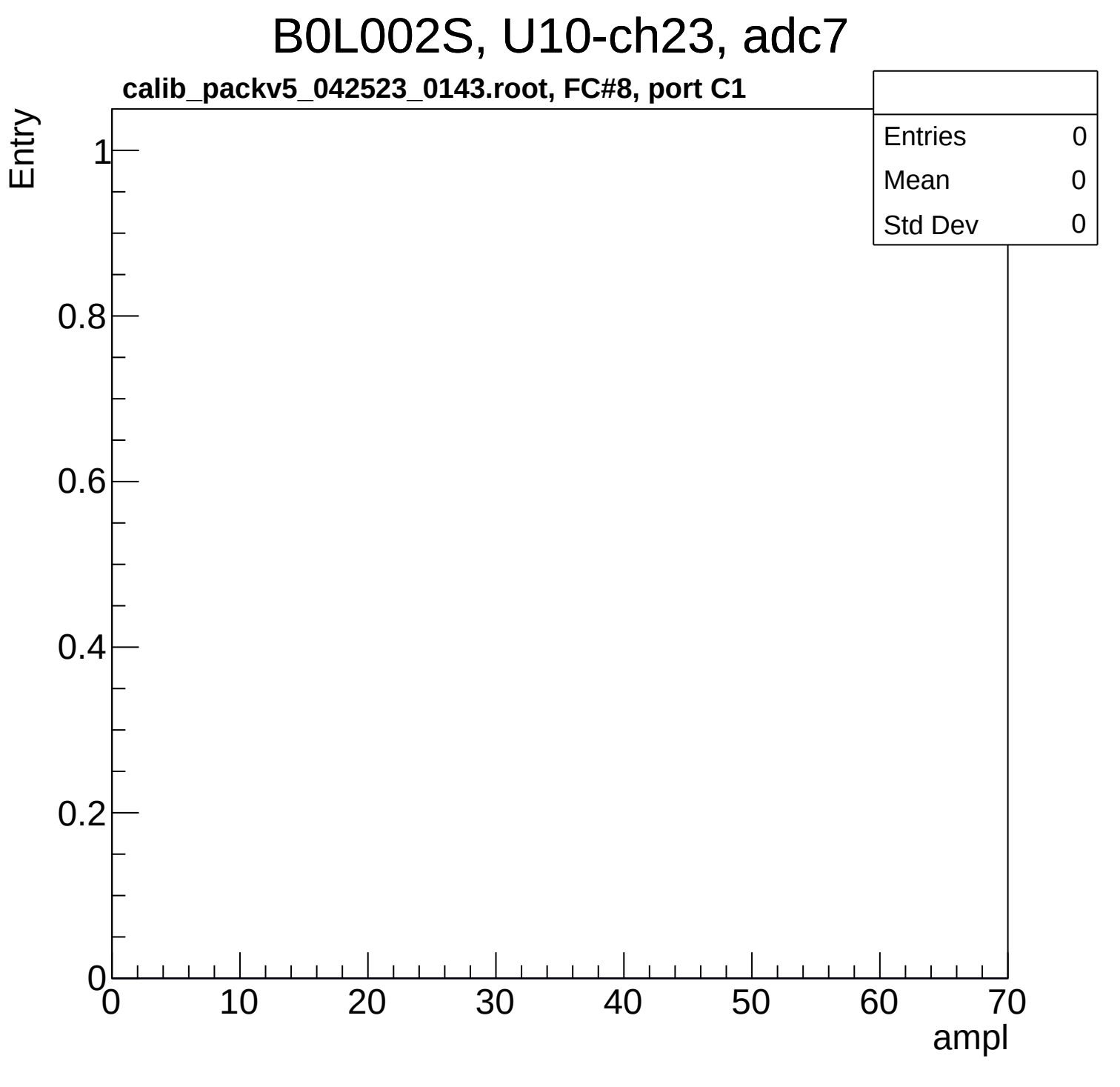

B0L002S, U10-ch23, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.86
Std Dev 3.108
Gaus mean : 44.4993
Gaus Width: 3.6754
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

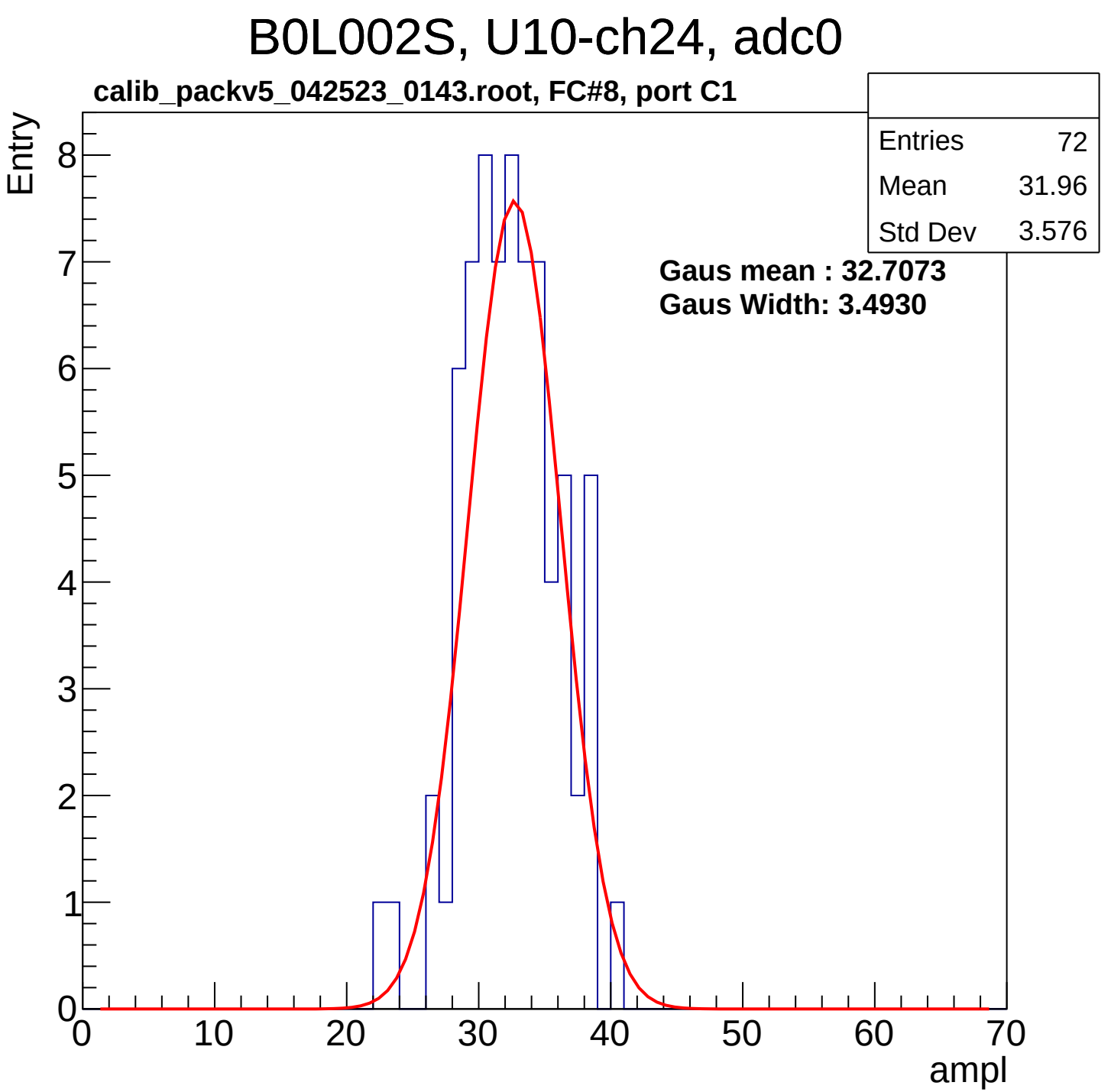

B0L002S, U10-ch23, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 51.85
Std Dev 2.982
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

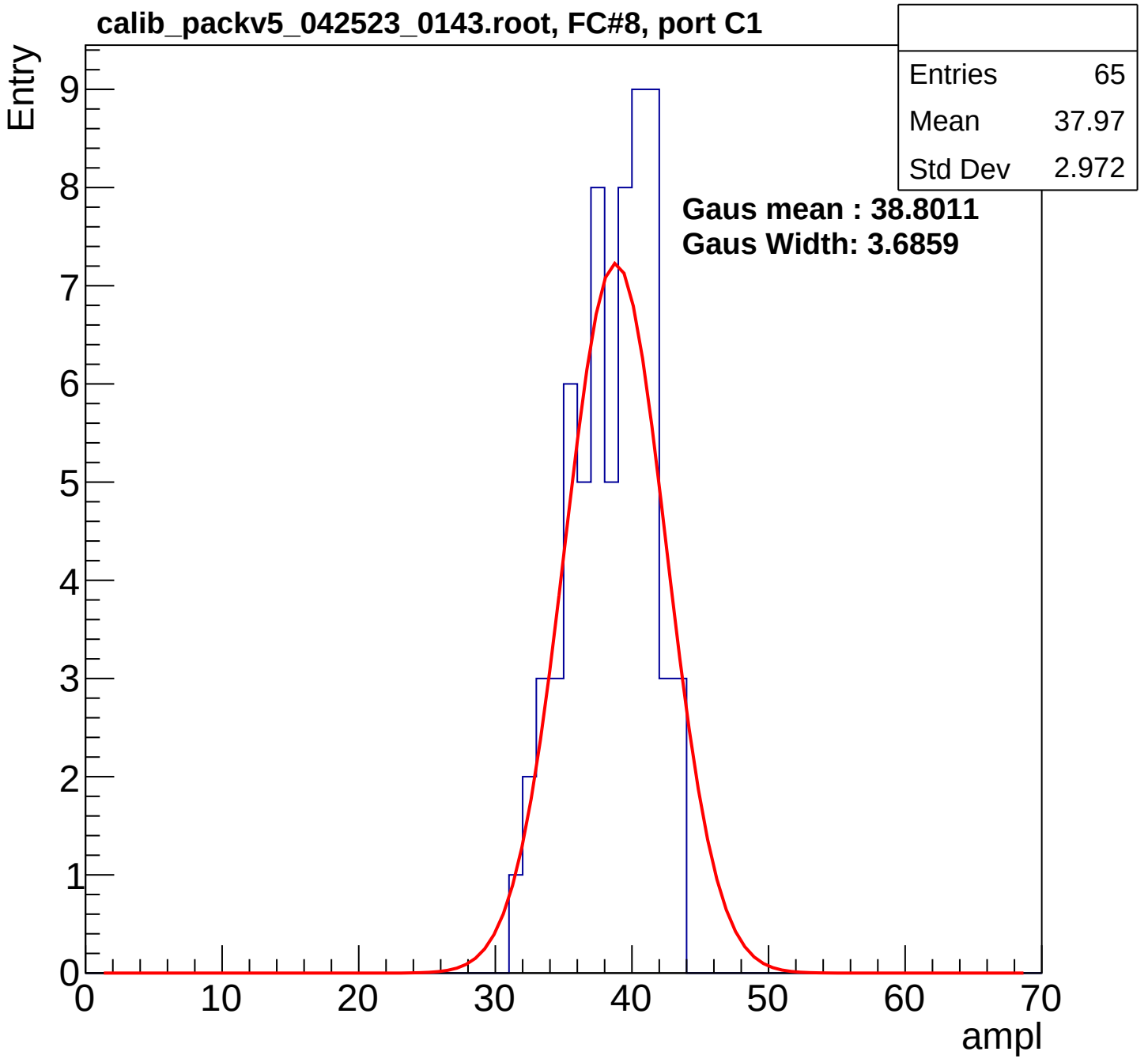

B0L002S, U10-ch23, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 58.32
Std Dev 2.998
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

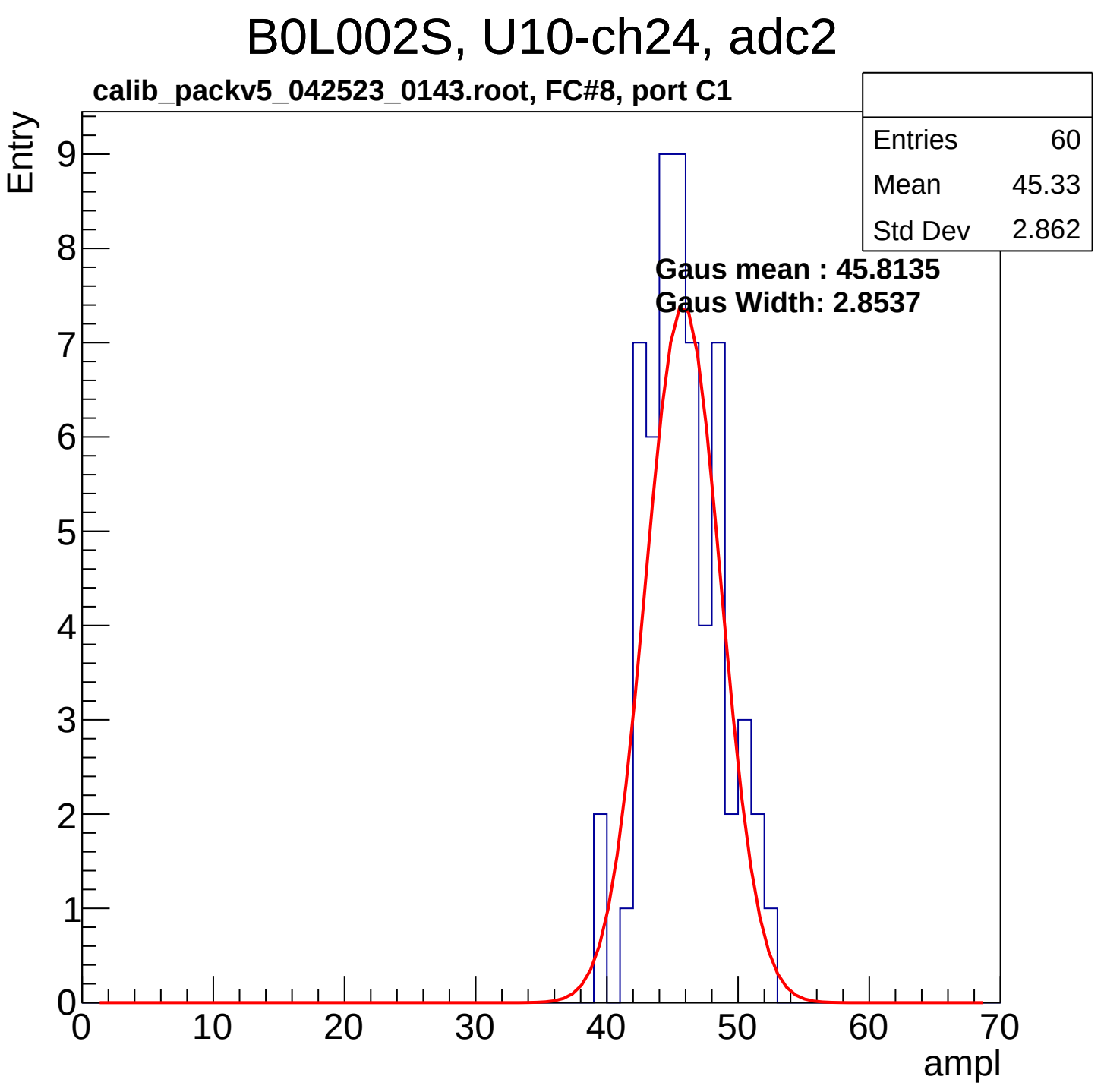

B0L002S, U10-ch23, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.91
Std Dev 10.38
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

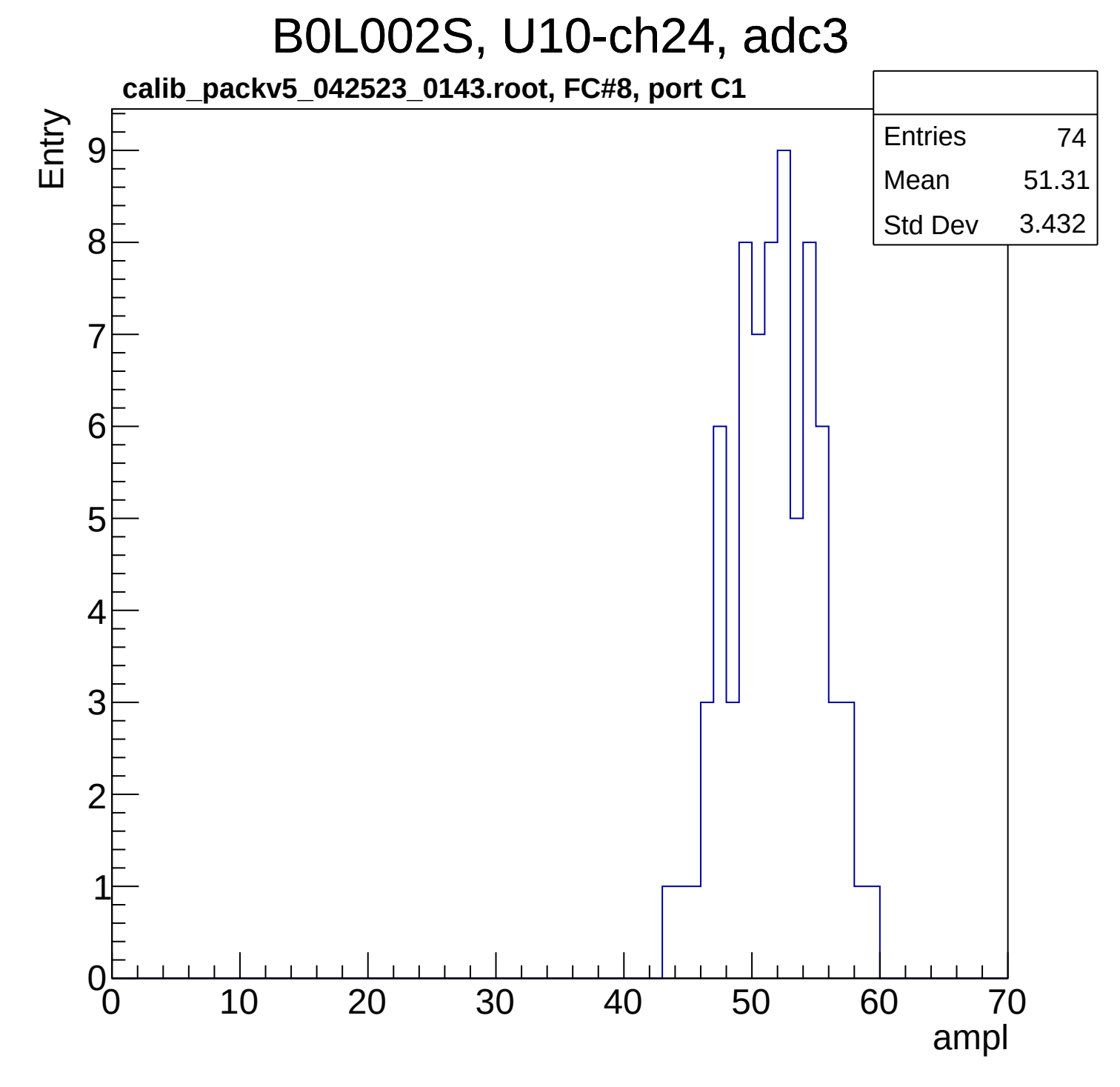

B0L002S, U10-ch23, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

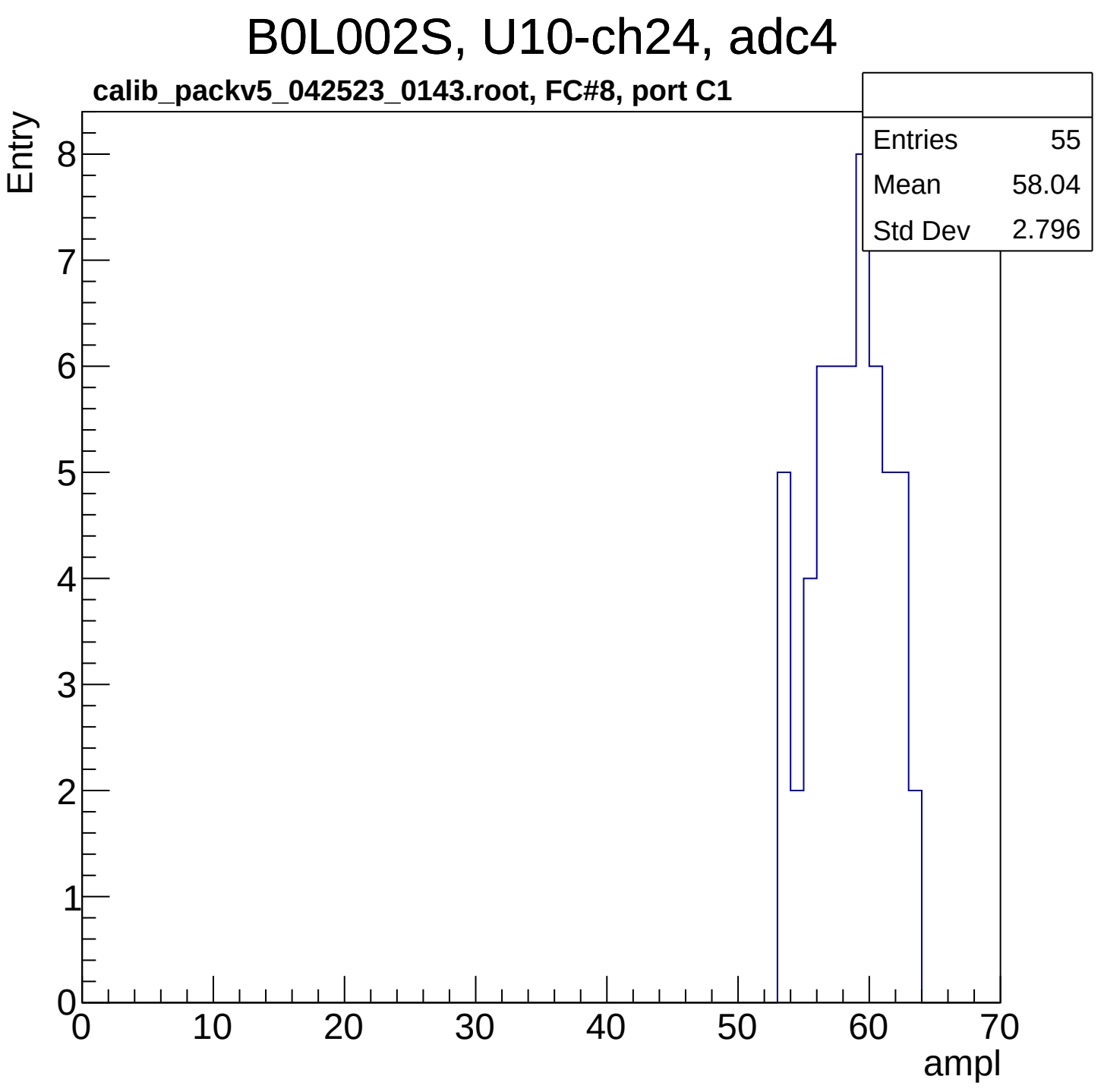

B0L002S, U10-ch23, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

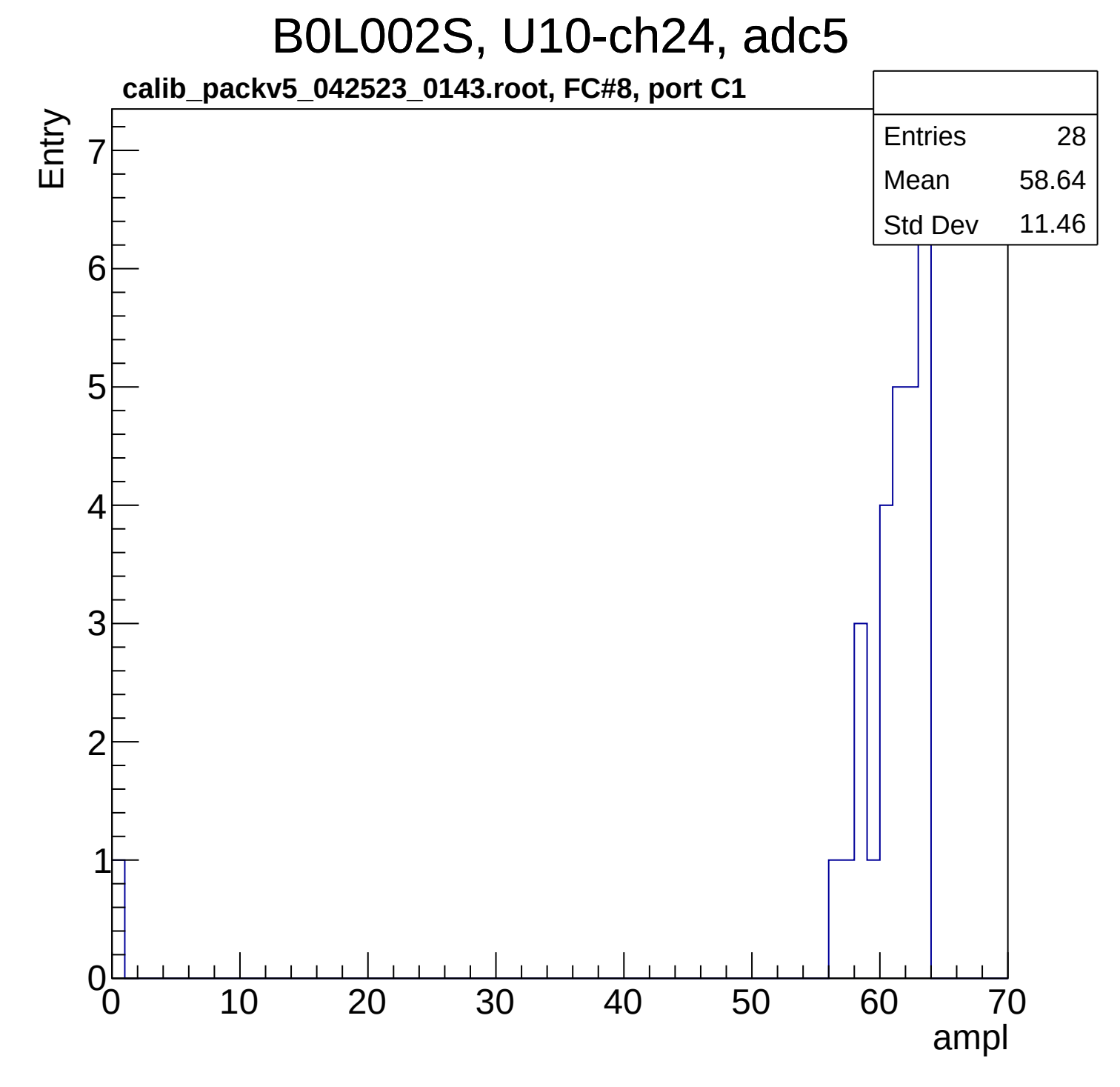

B0L002S, U10-ch24, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.96
Std Dev 3.576
Gaus mean : 32.7073
Gaus Width: 3.4930
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

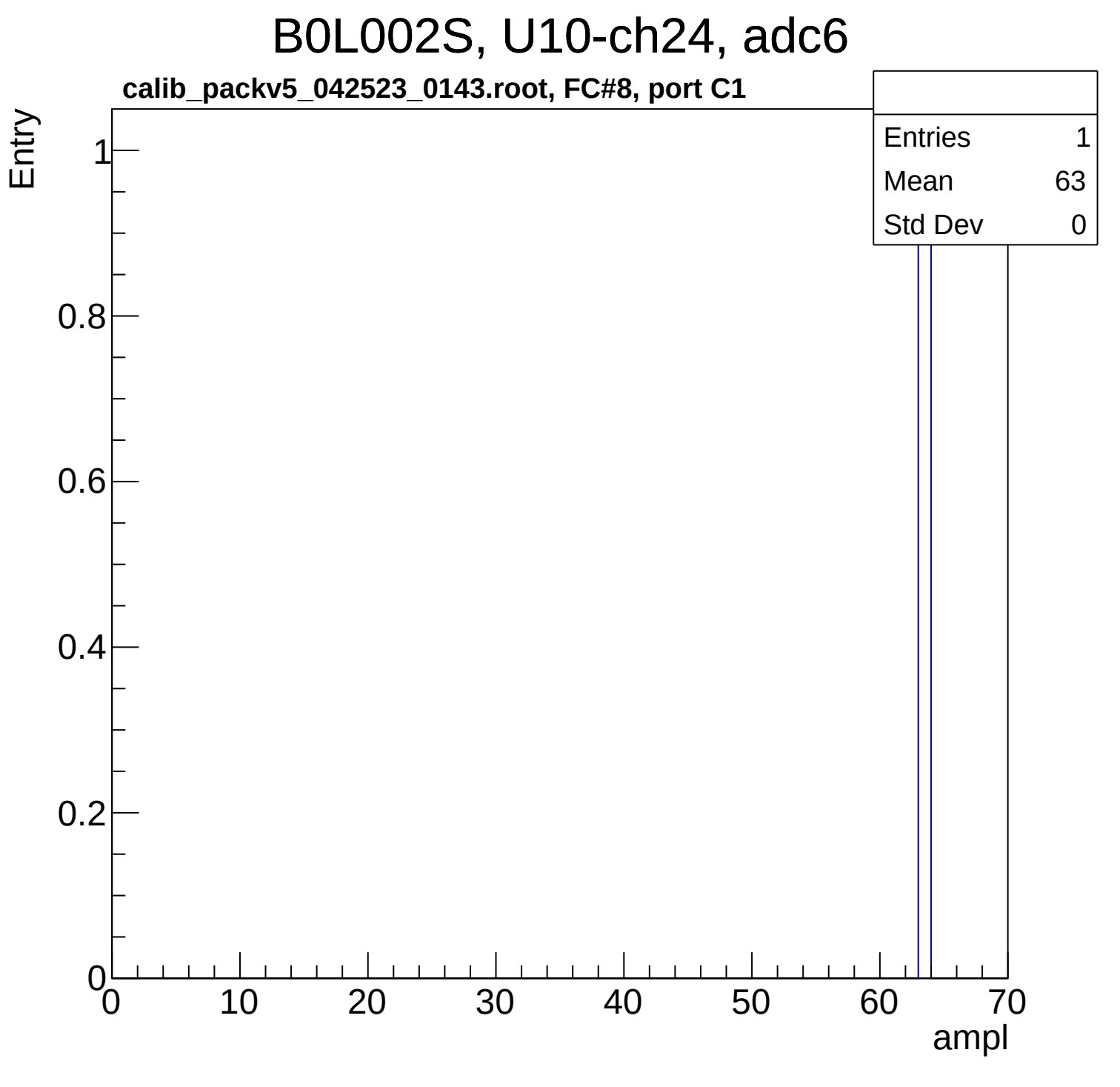

B0L002S, U10-ch24, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.97
Std Dev 2.972
Gaus mean : 38.8011
Gaus Width: 3.6859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

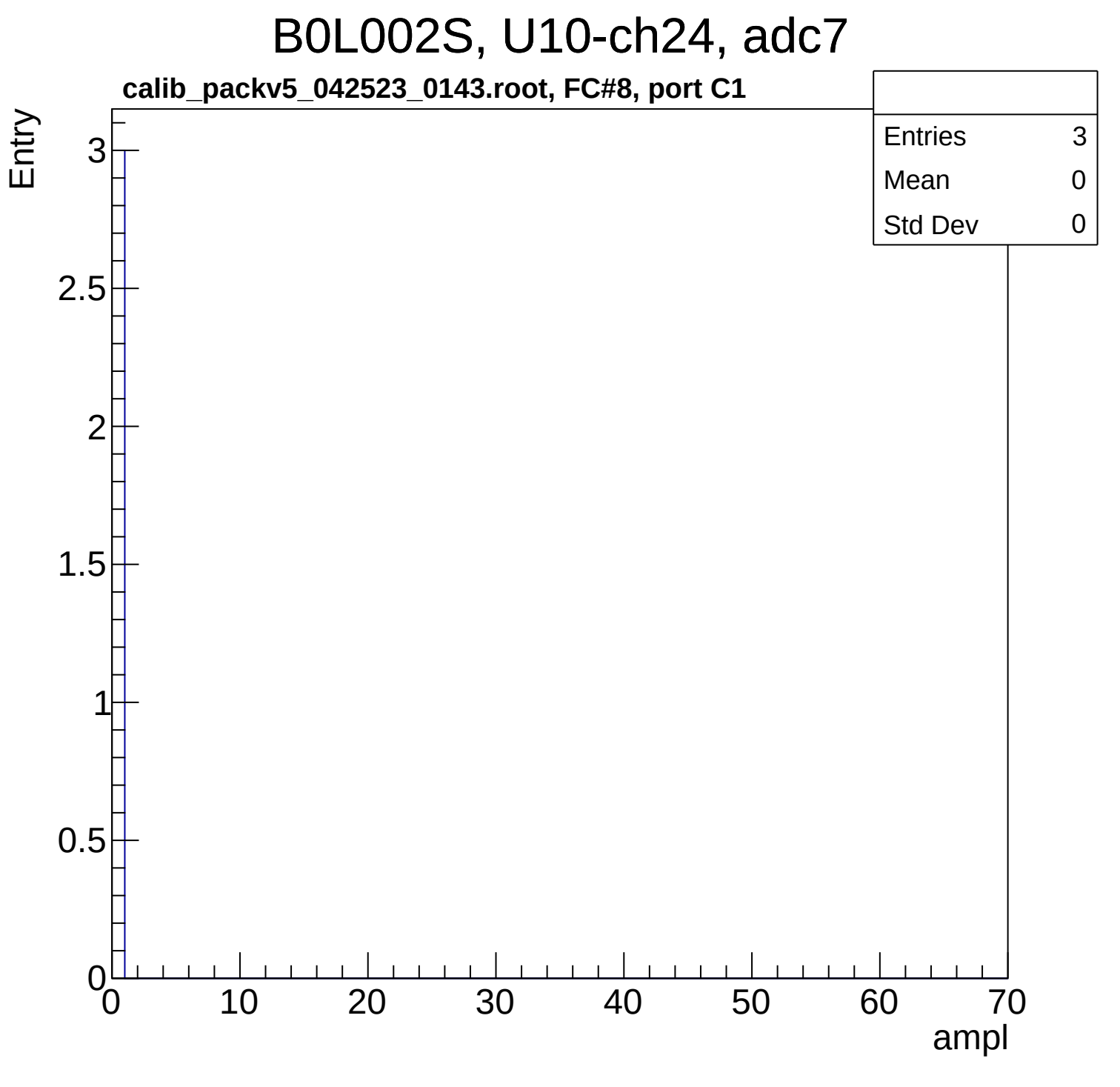

B0L002S, U10-ch24, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.33
Std Dev 2.862
Gaus mean : 45.8135
Gaus Width: 2.8537
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

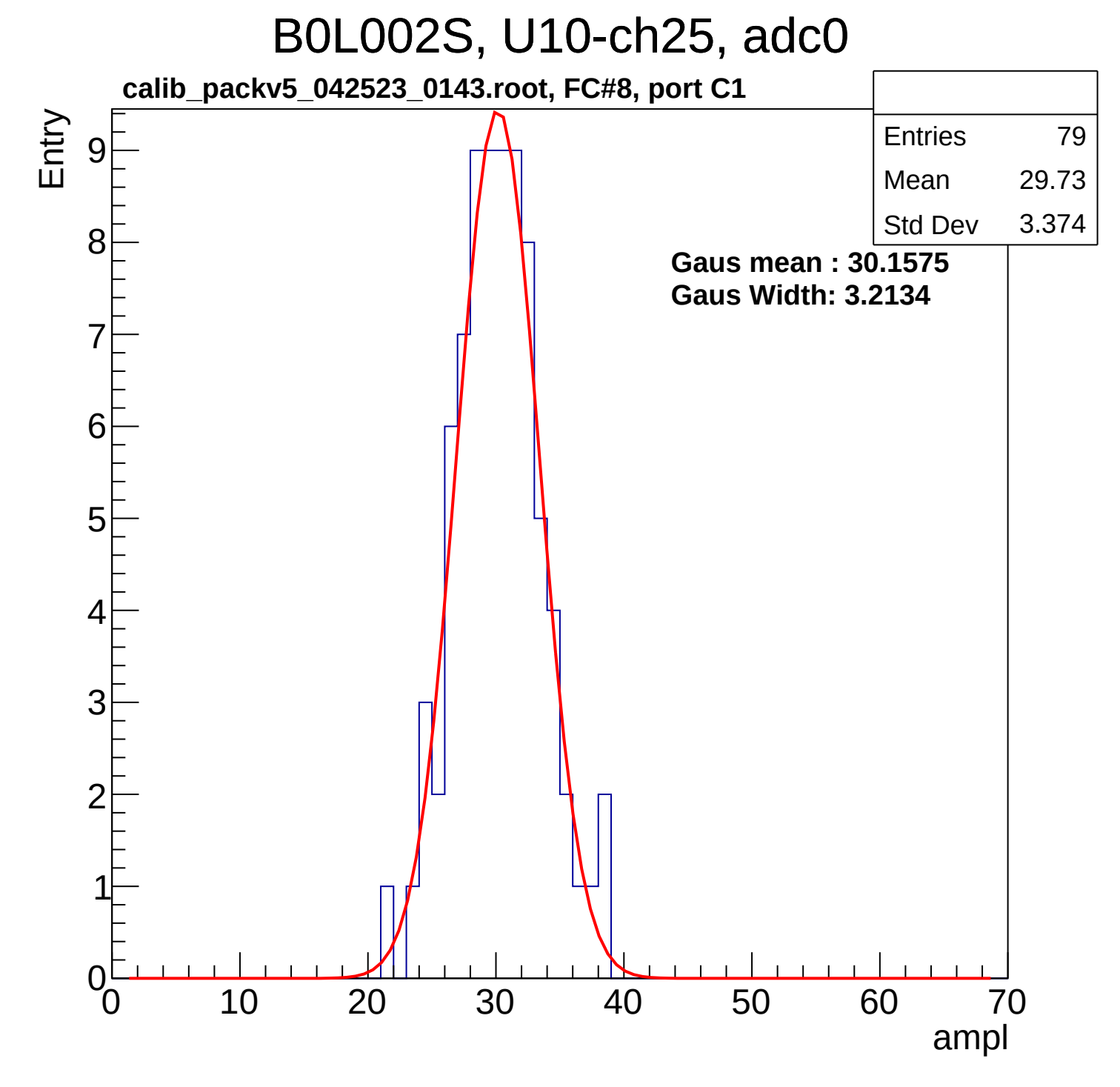

B0L002S, U10-ch24, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 51.31
Std Dev 3.432
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

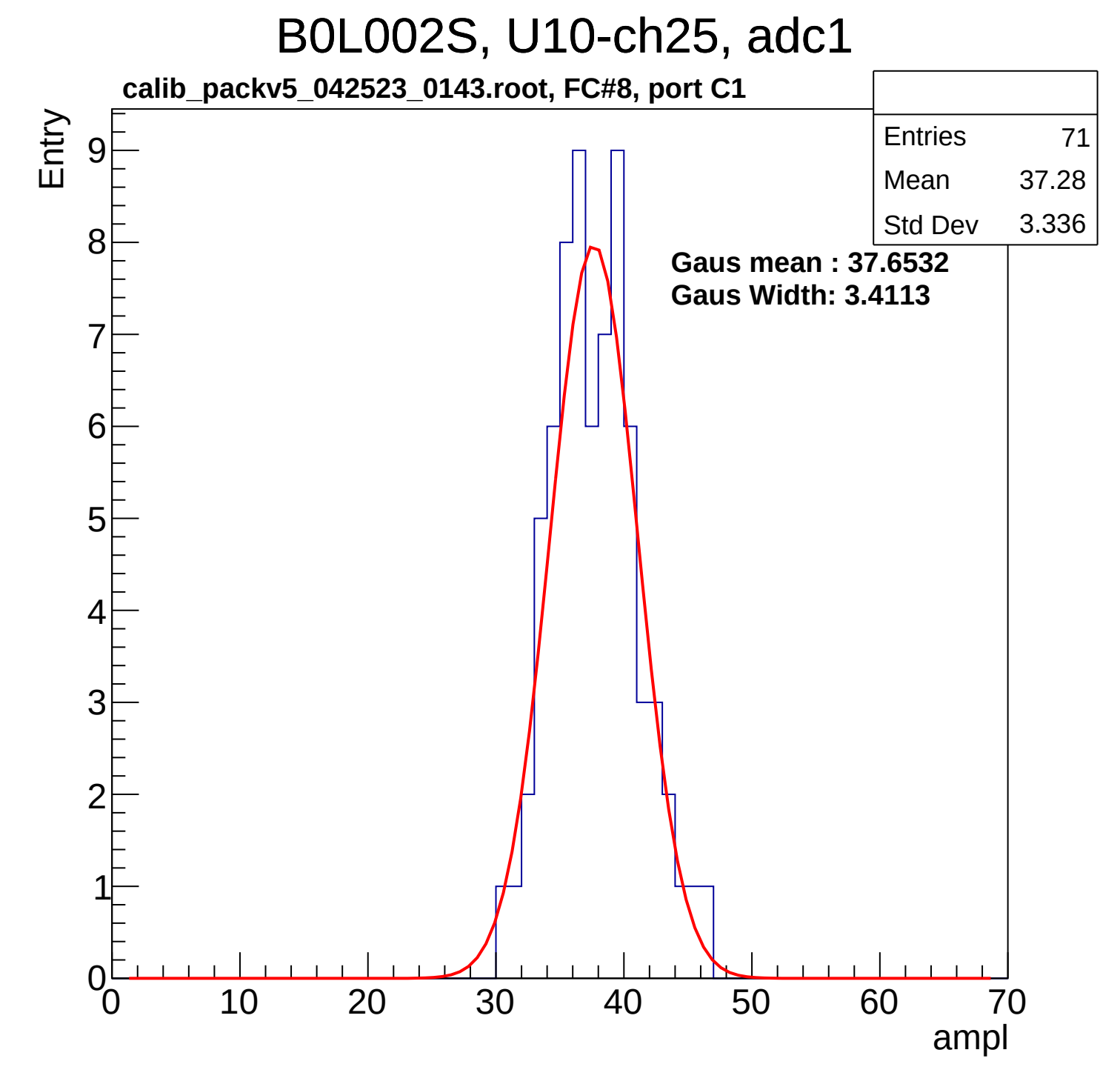

B0L002S, U10-ch24, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 58.04
Std Dev 2.796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

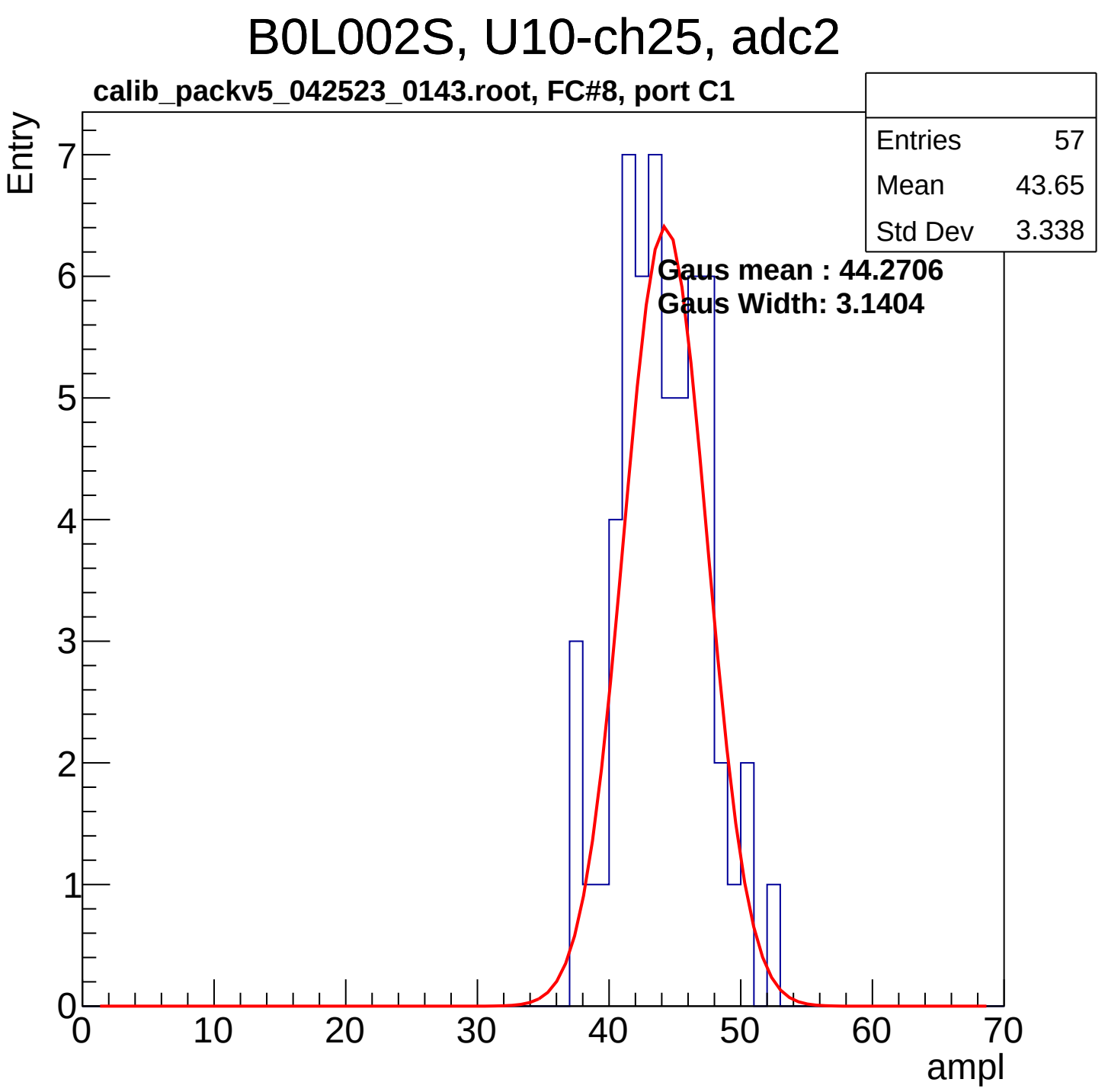

B0L002S, U10-ch24, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.64
Std Dev 11.46
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

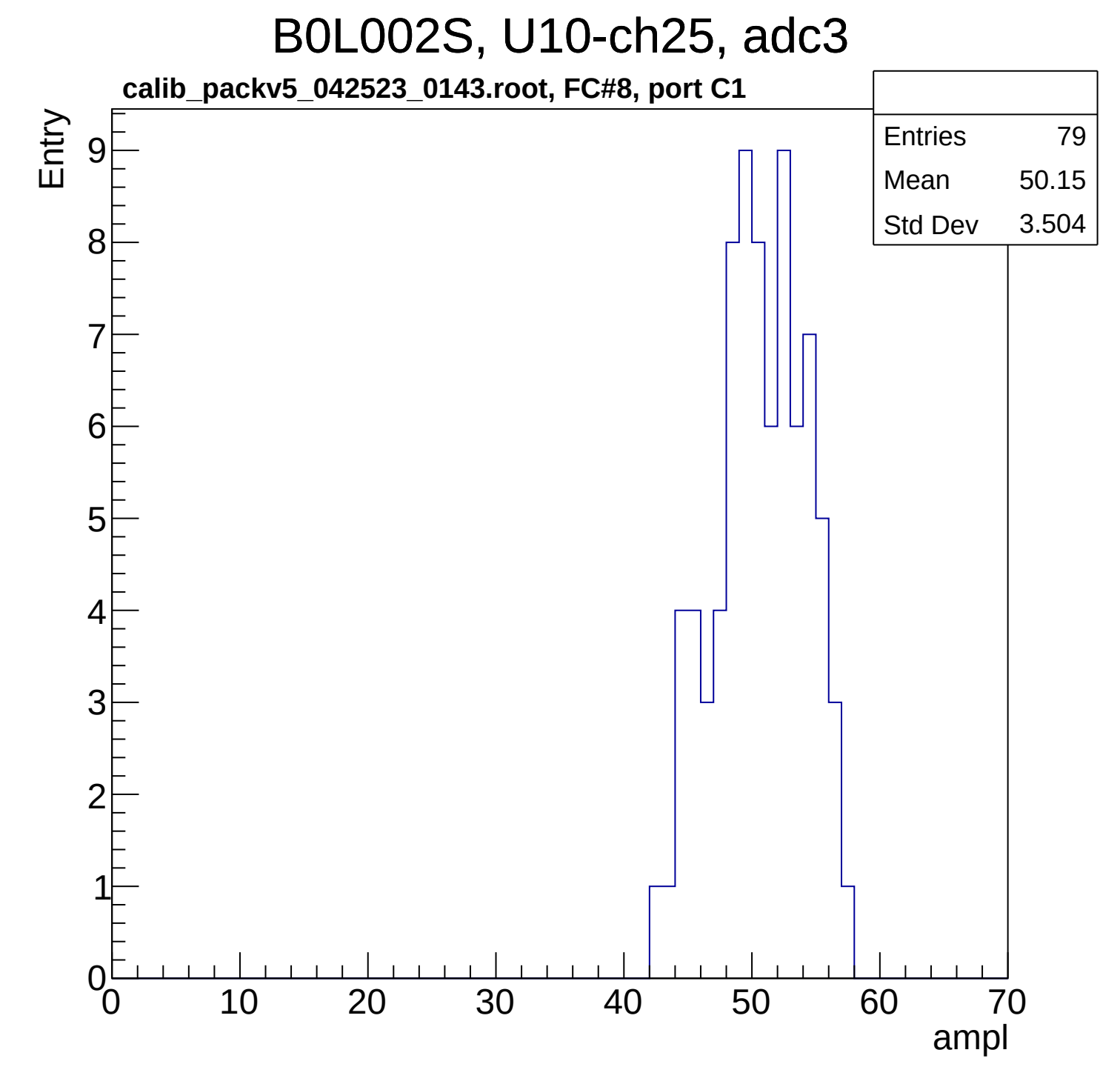

B0L002S, U10-ch24, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

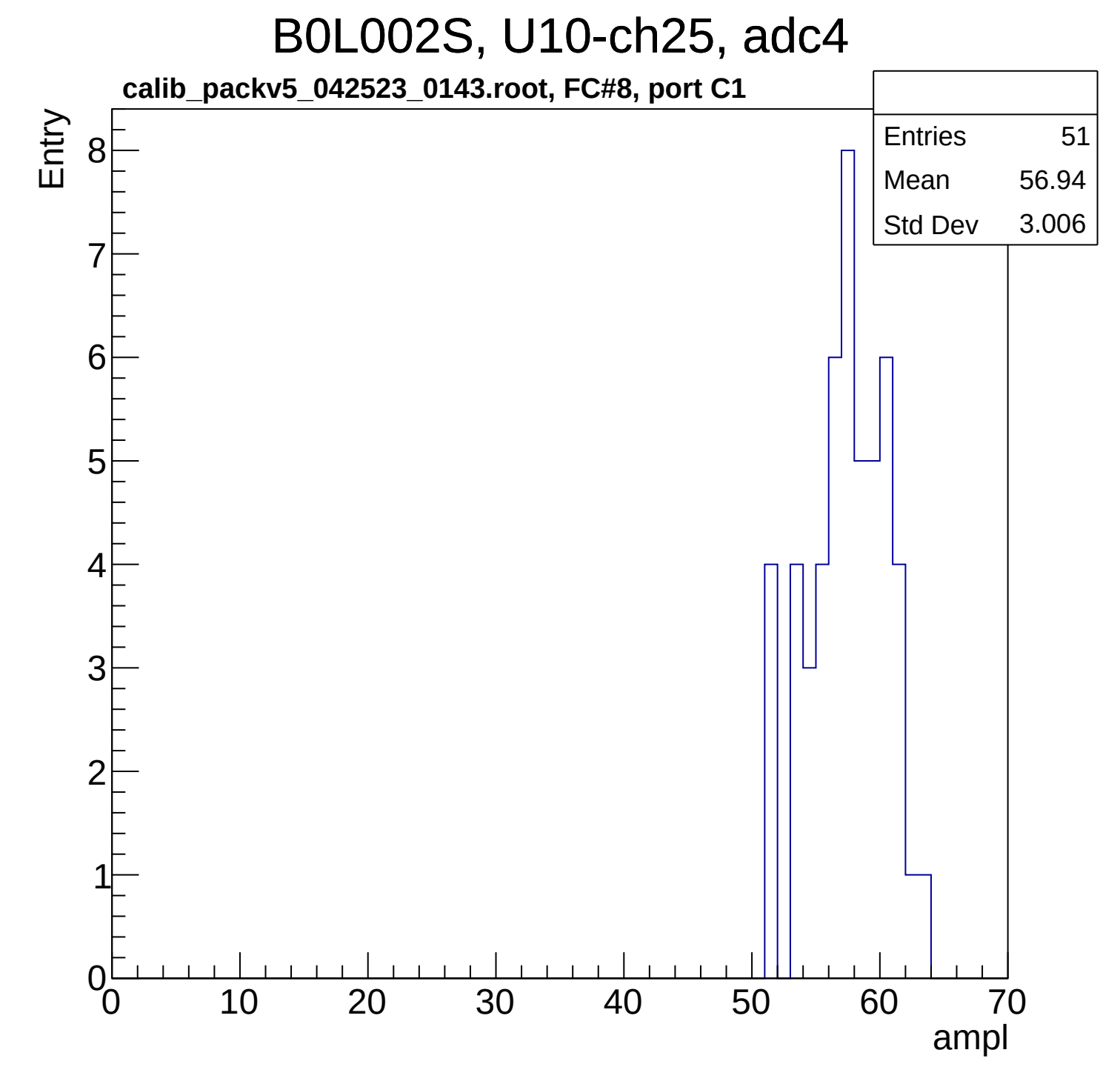

B0L002S, U10-ch24, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

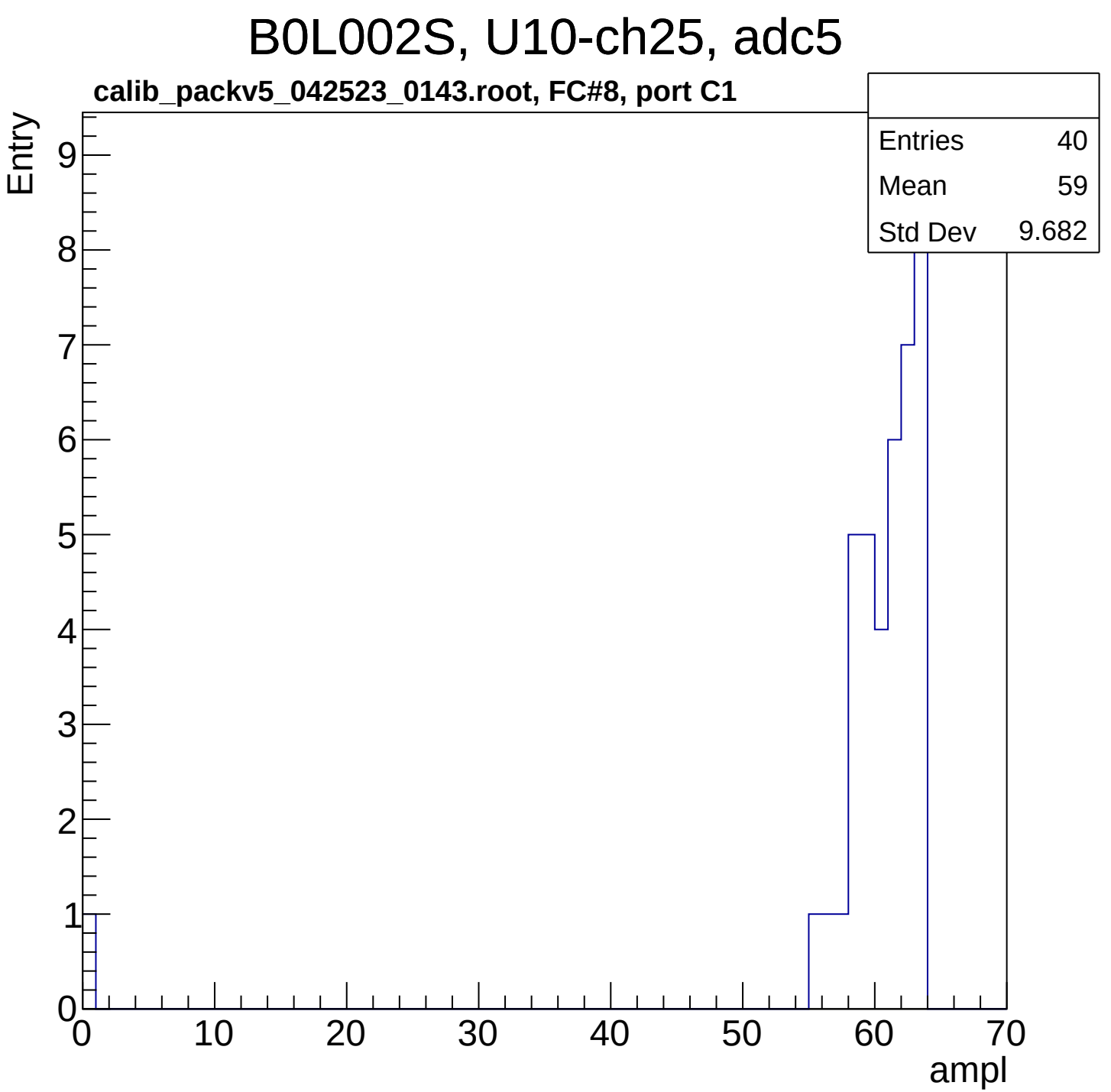

B0L002S, U10-ch25, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 29.73
Std Dev 3.374
Gaus mean : 30.1575
Gaus Width: 3.2134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

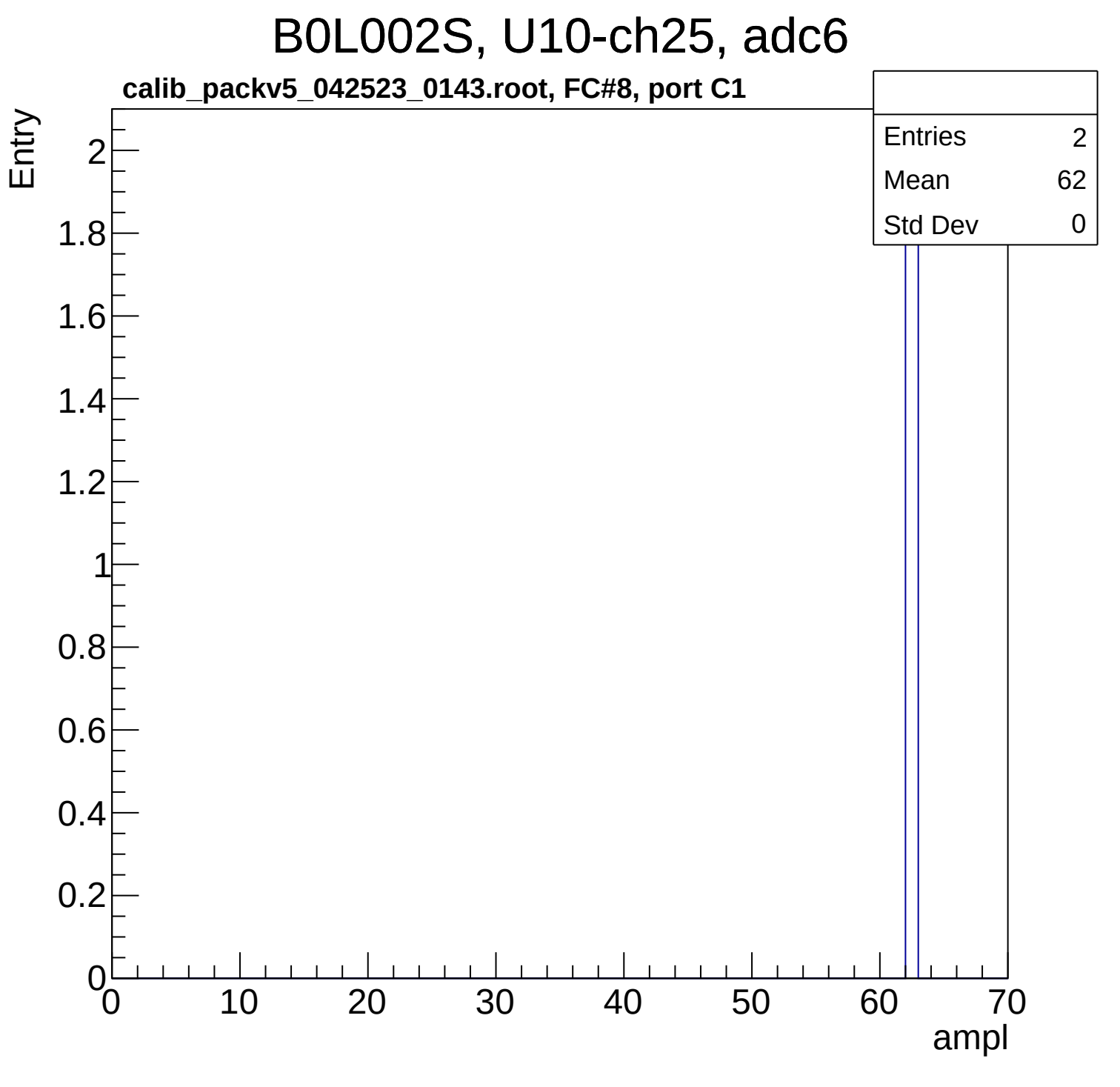

B0L002S, U10-ch25, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.28
Std Dev 3.336
Gaus mean : 37.6532
Gaus Width: 3.4113
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

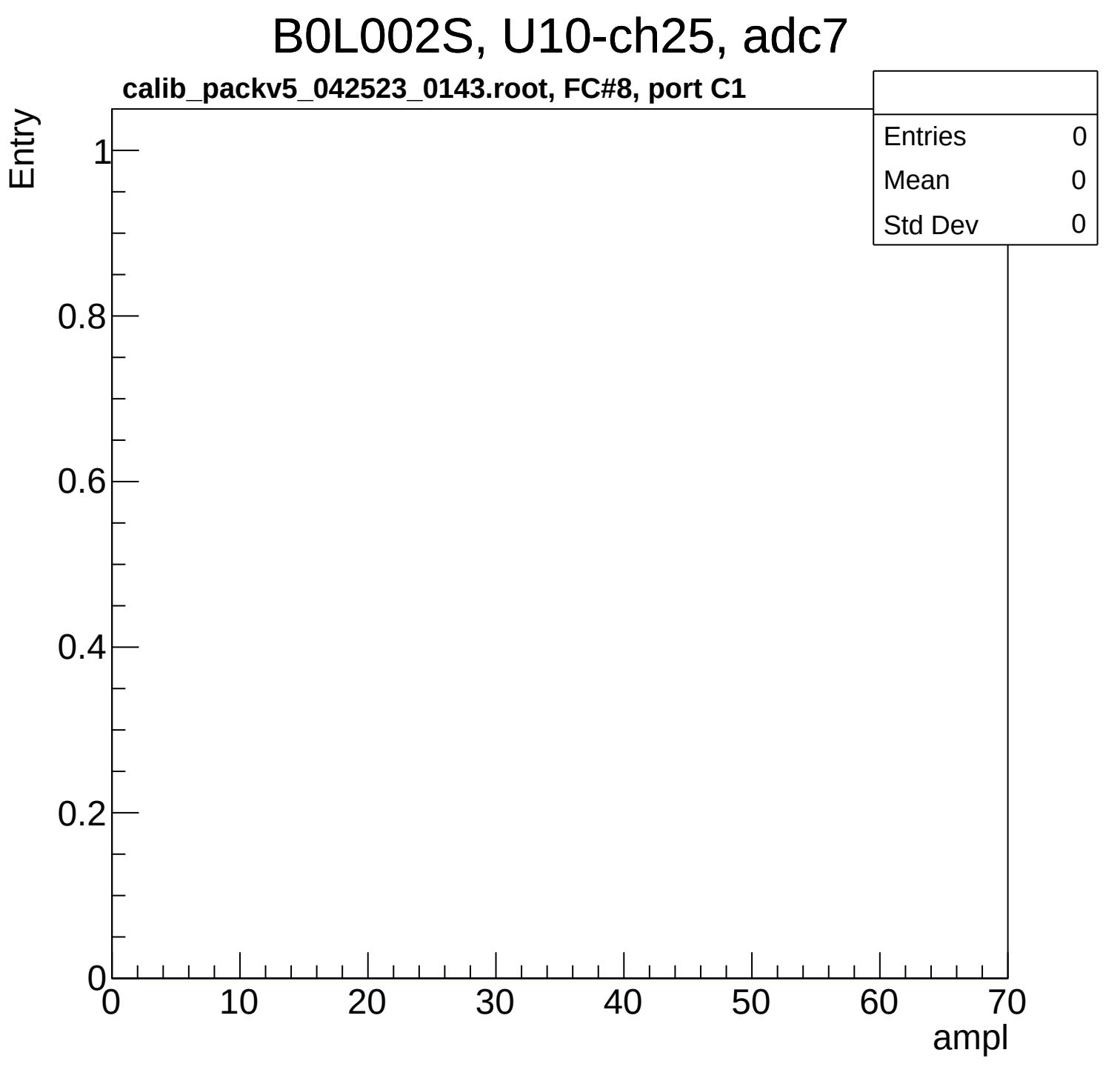

B0L002S, U10-ch25, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.65
Std Dev 3.338
Gaus mean : 44.2706
Gaus Width: 3.1404
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

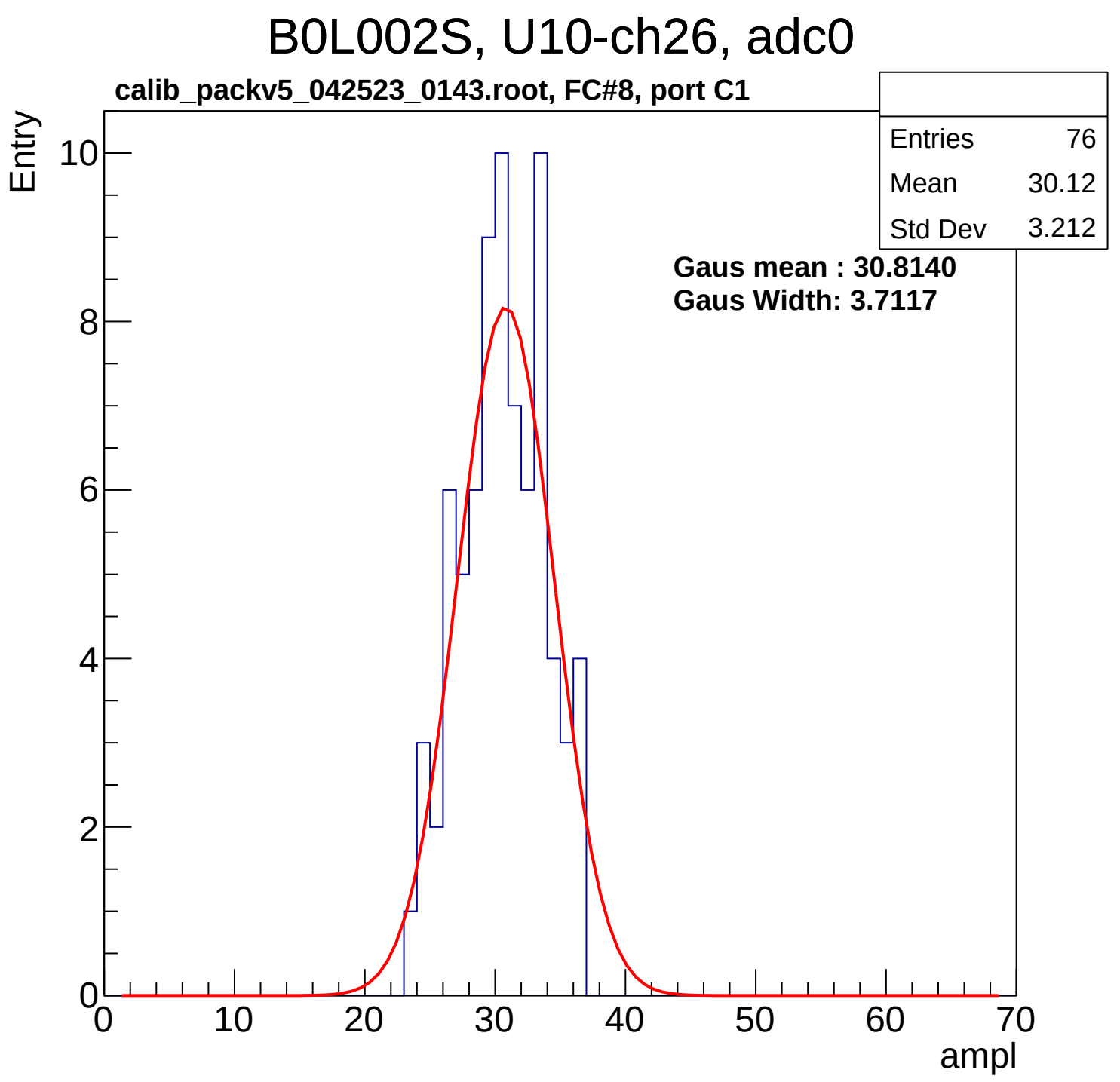

B0L002S, U10-ch25, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 50.15
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

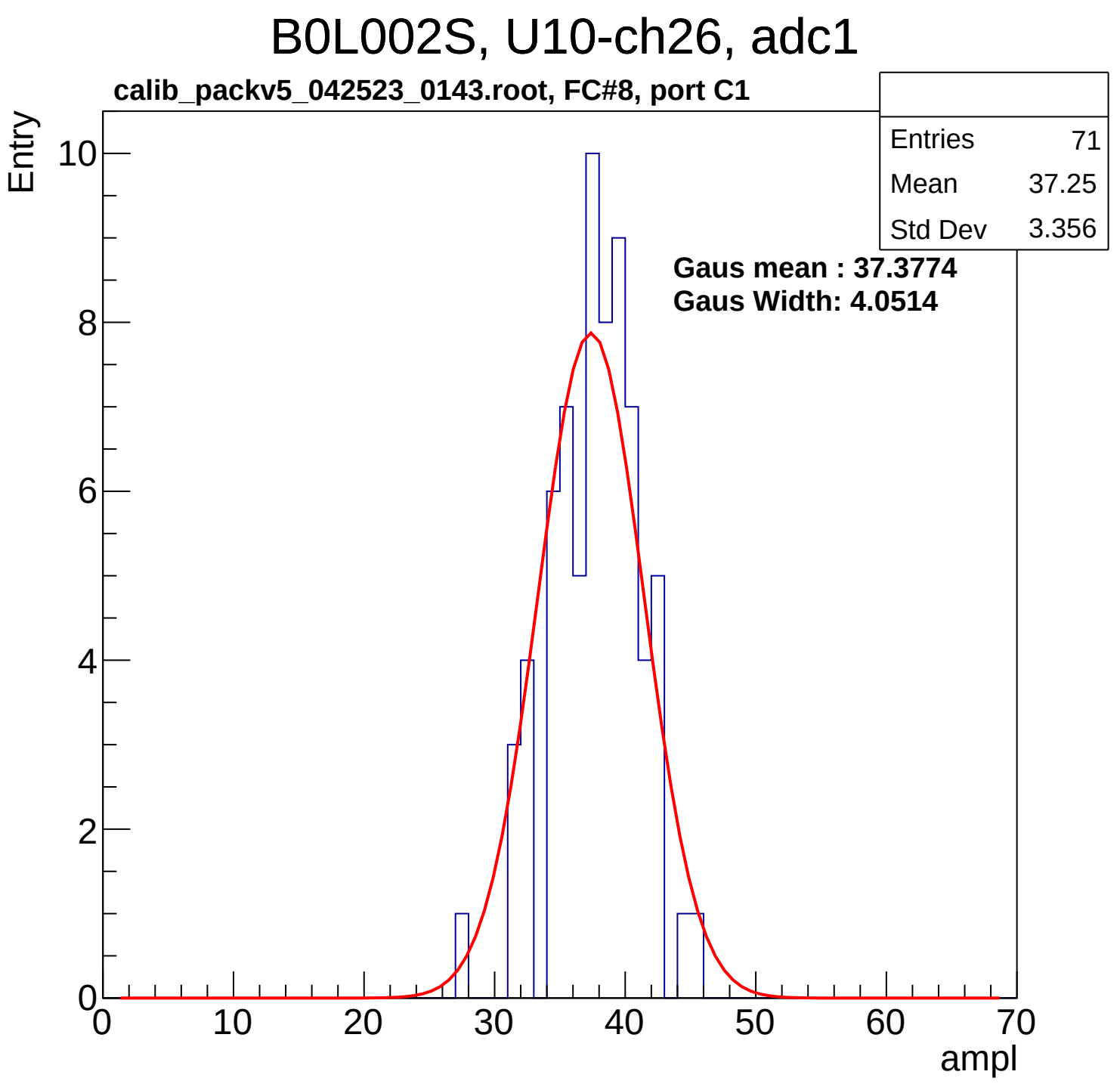

B0L002S, U10-ch25, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 56.94
Std Dev 3.006
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

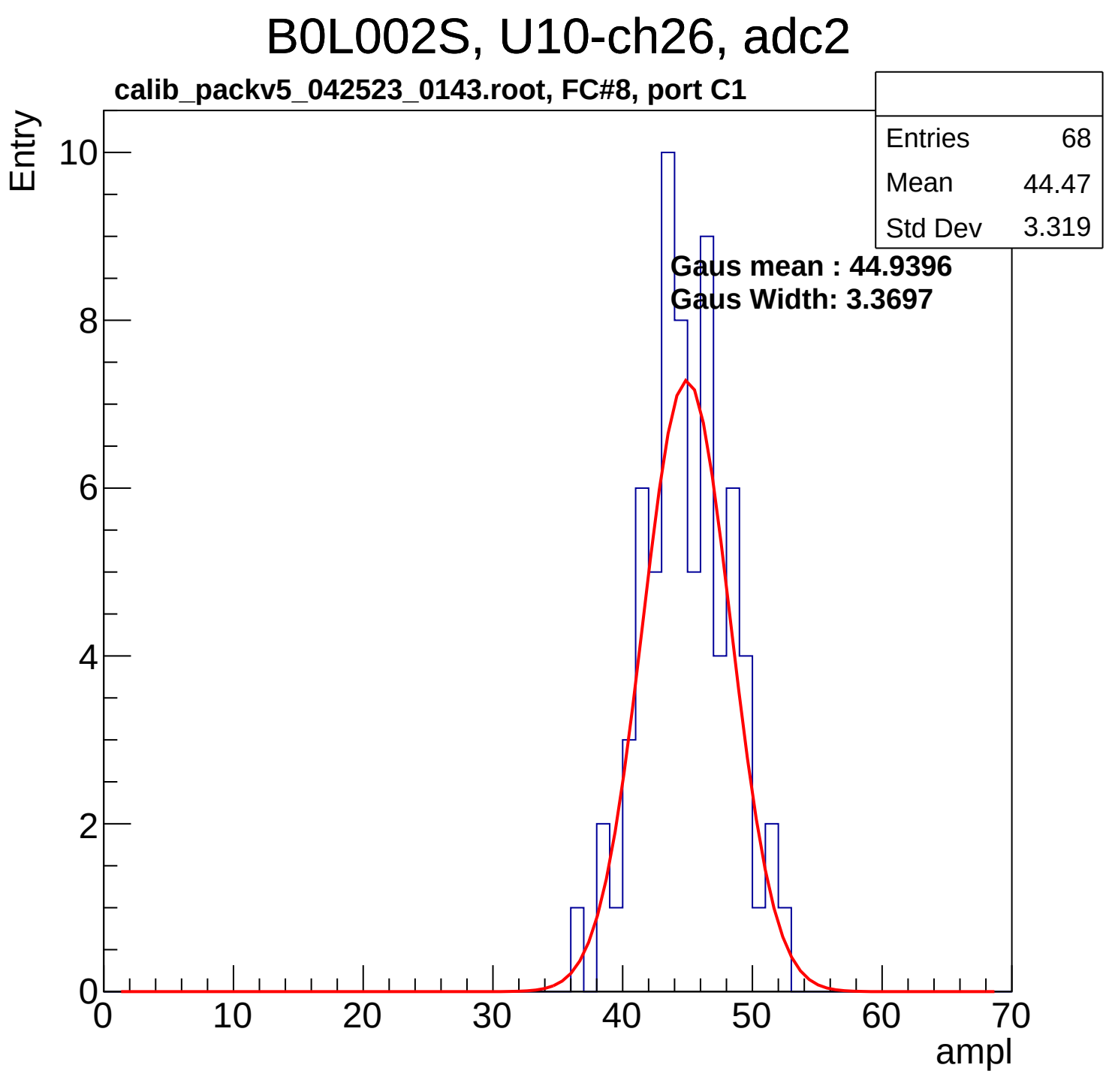

B0L002S, U10-ch25, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 59
Std Dev 9.682
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

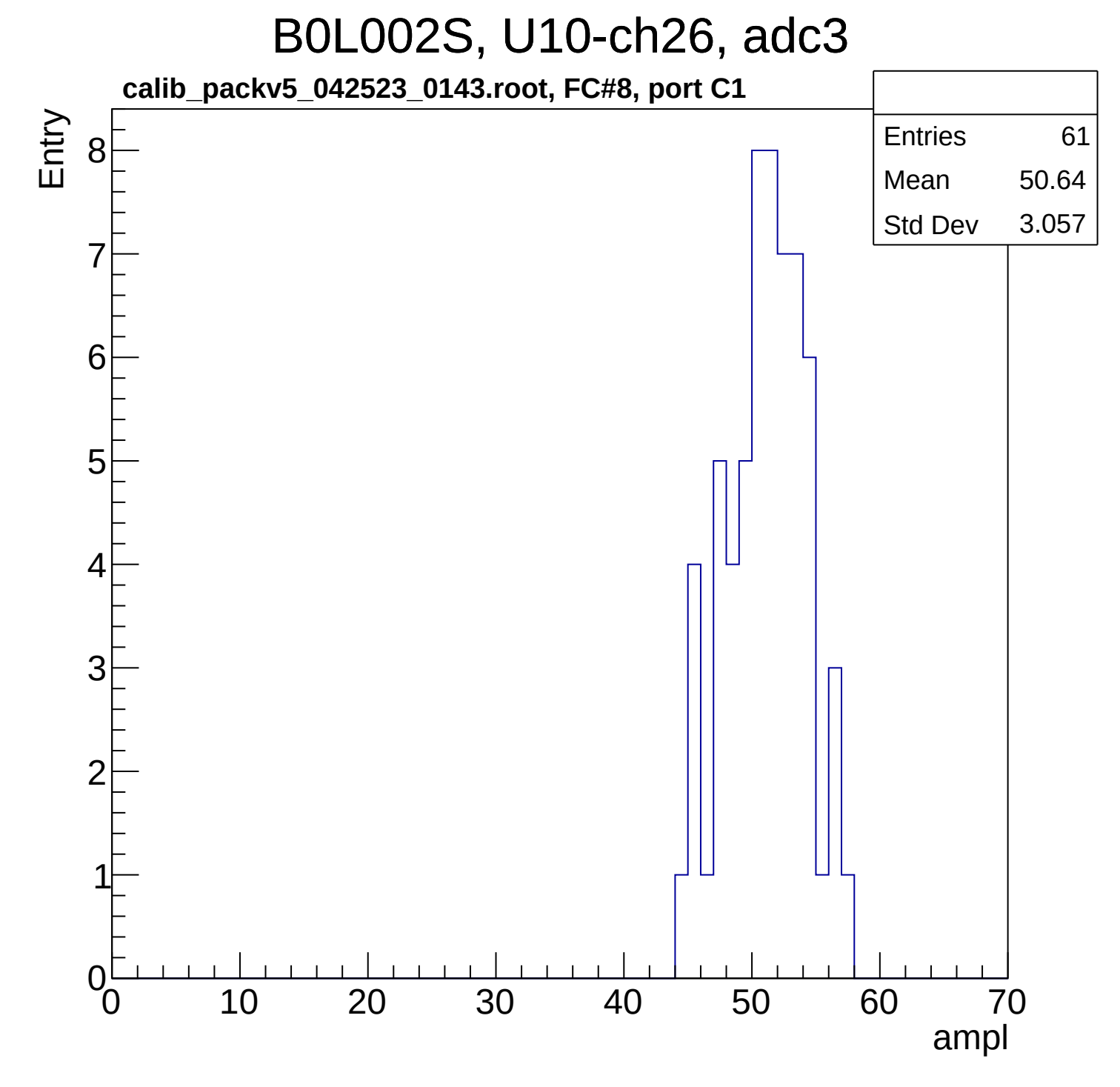

B0L002S, U10-ch25, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

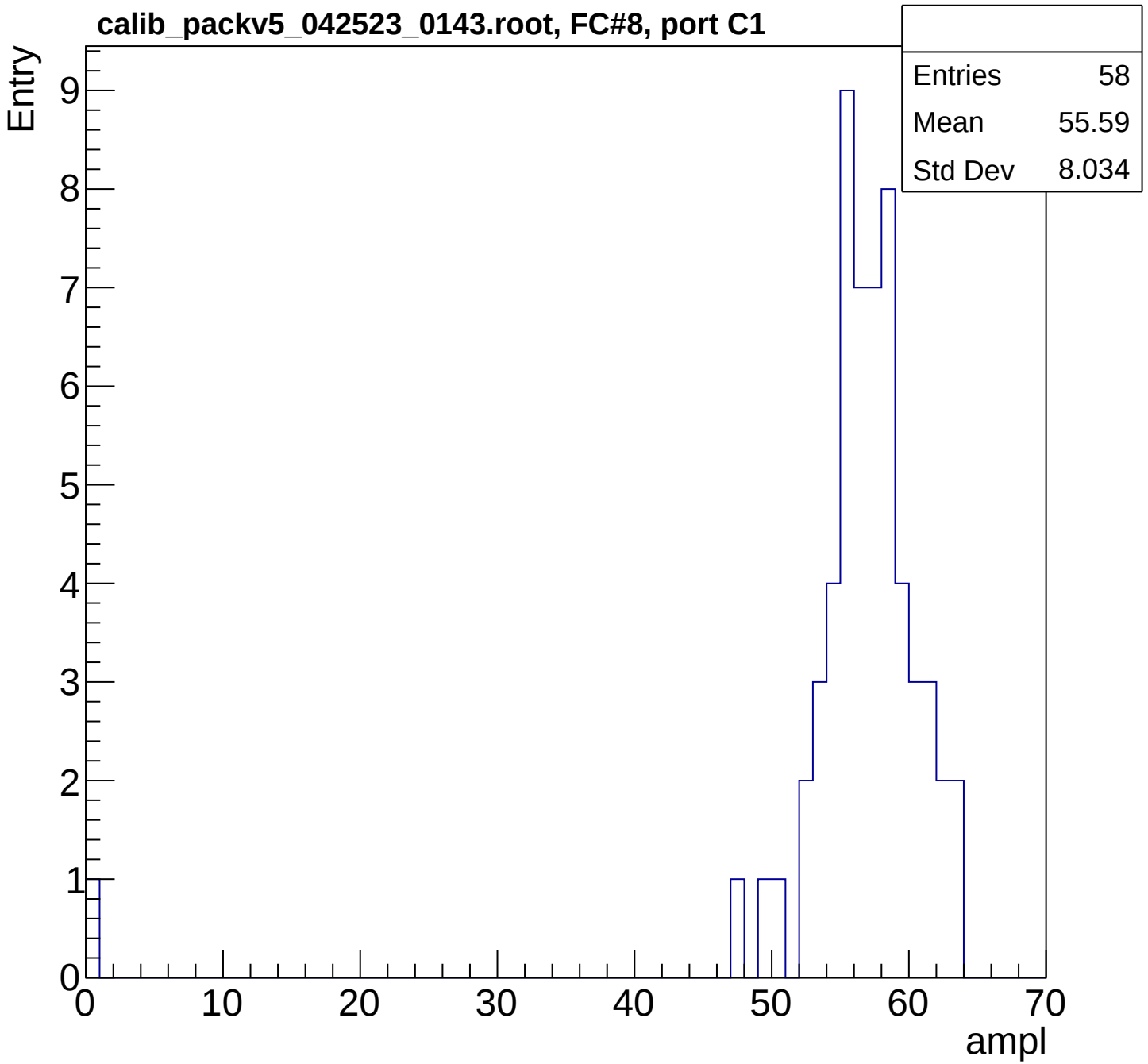

B0L002S, U10-ch25, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

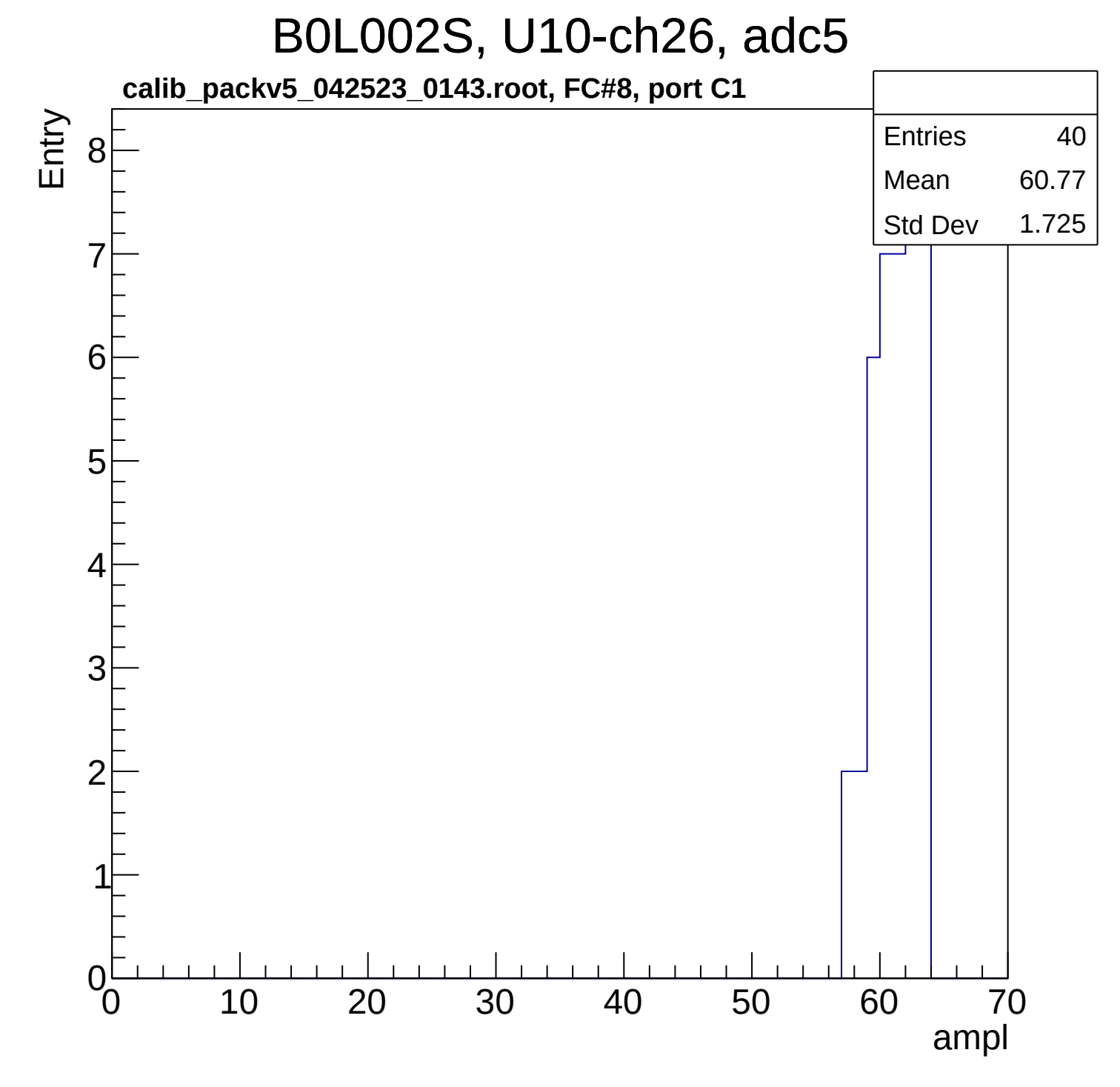

B0L002S, U10-ch26, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.12
Std Dev 3.212
Gaus mean : 30.8140
Gaus Width: 3.7117
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

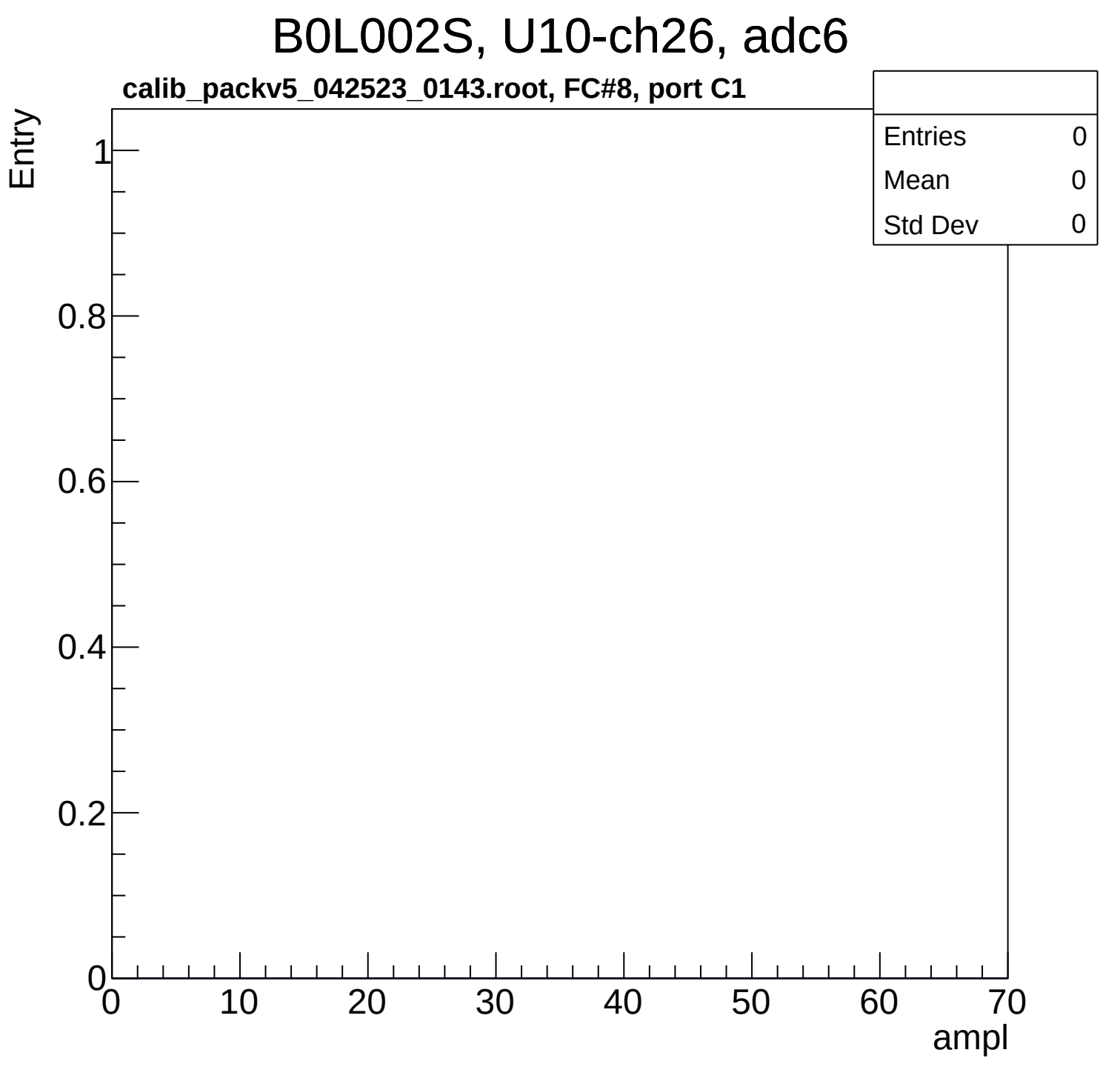

B0L002S, U10-ch26, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.25
Std Dev 3.356
Gaus mean : 37.3774
Gaus Width: 4.0514
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

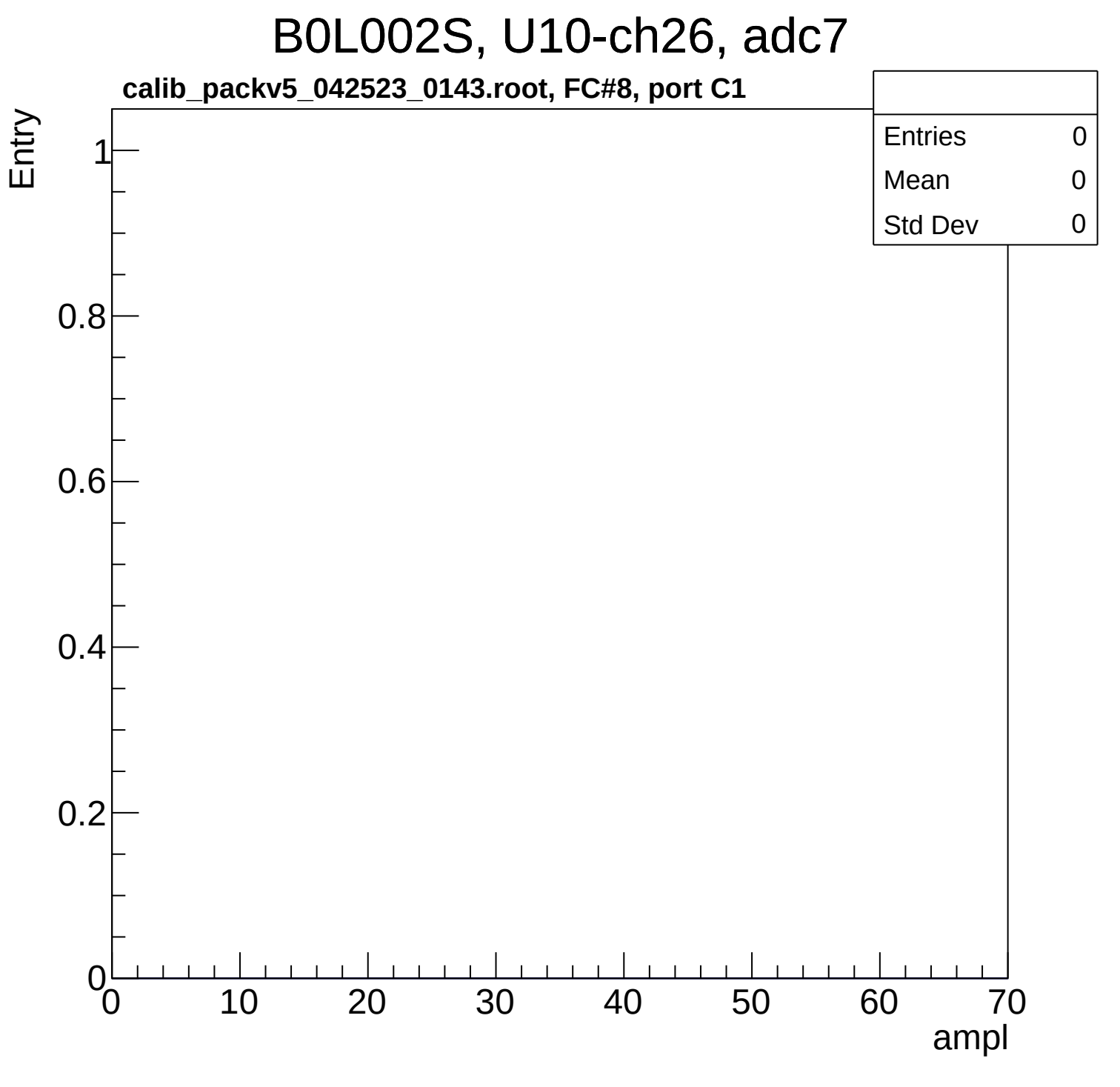

B0L002S, U10-ch26, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.47
Std Dev 3.319
Gaus mean : 44.9396
Gaus Width: 3.3697
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

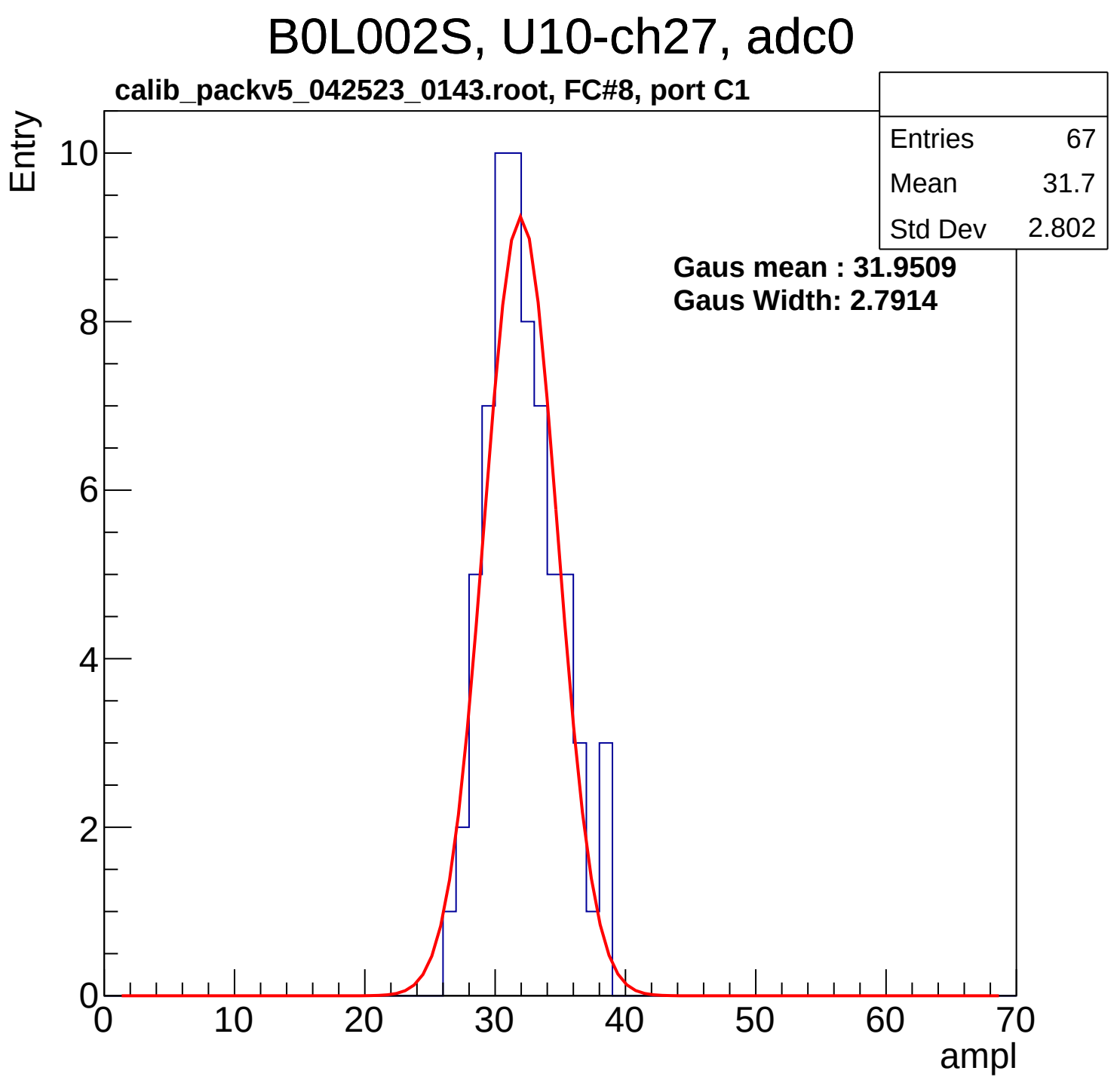

B0L002S, U10-ch26, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 50.64
Std Dev 3.057
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

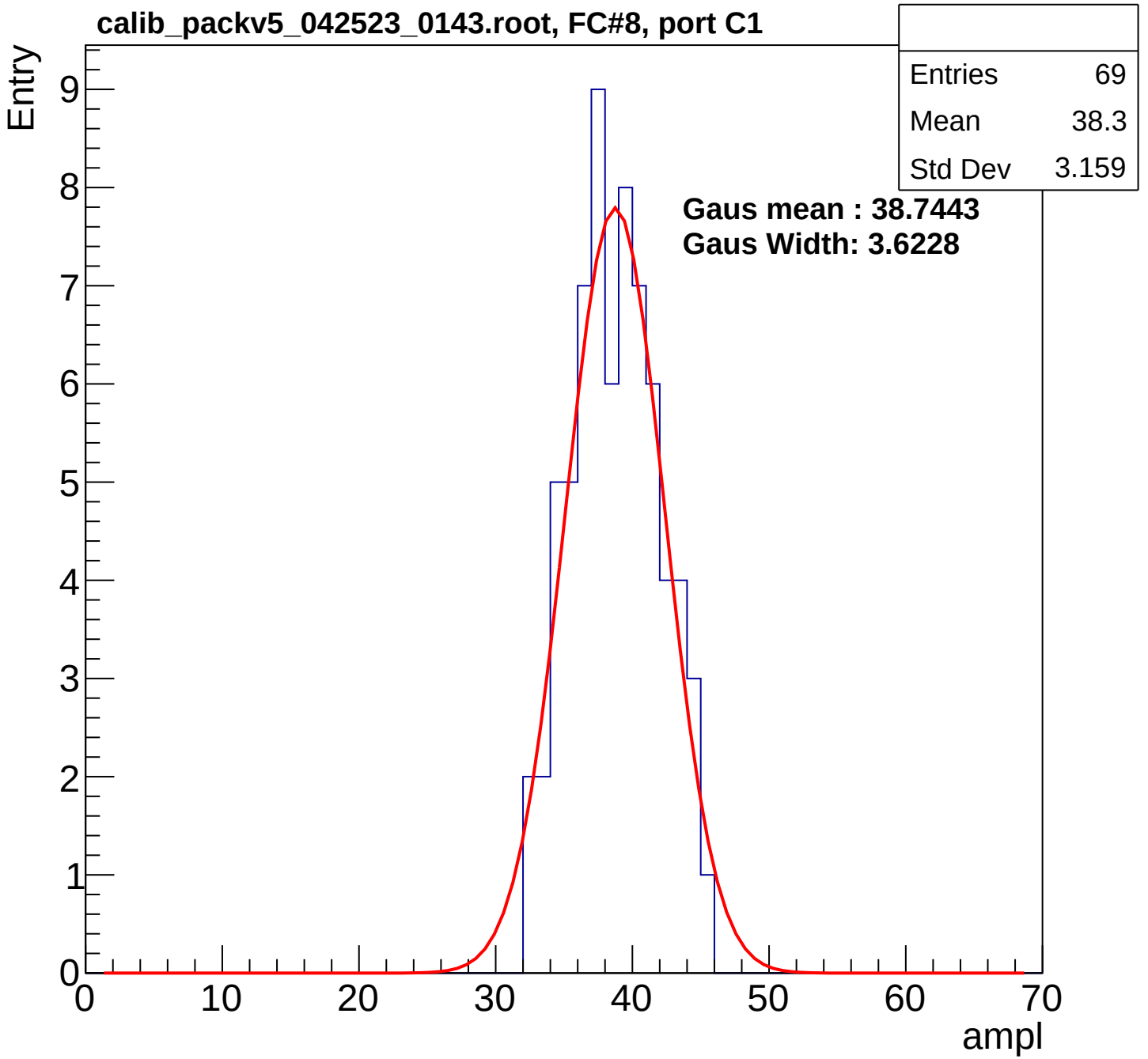

B0L002S, U10-ch26, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 55.59
Std Dev 8.034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

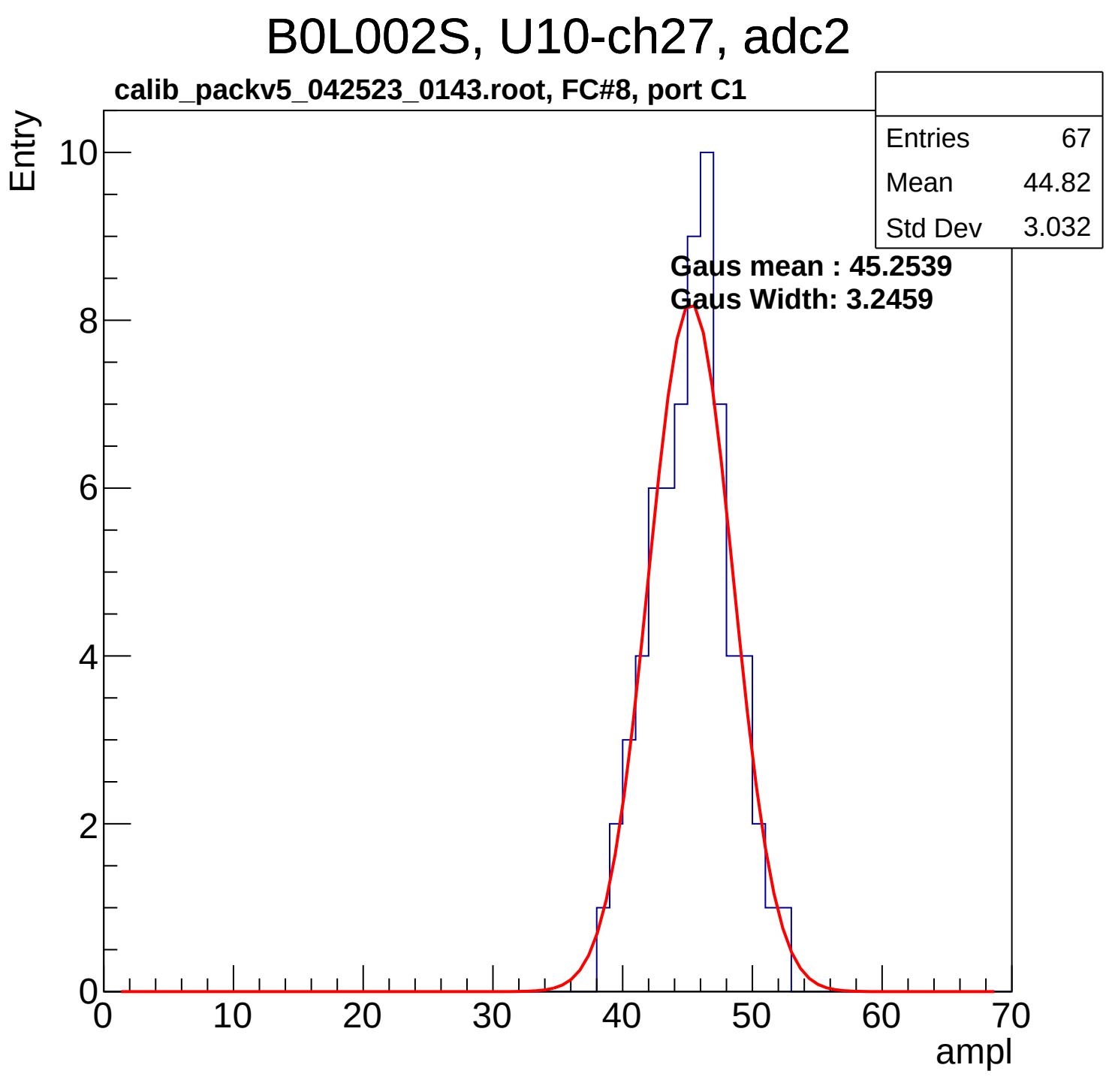

B0L002S, U10-ch26, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 40
Mean 60.77
Std Dev 1.725
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

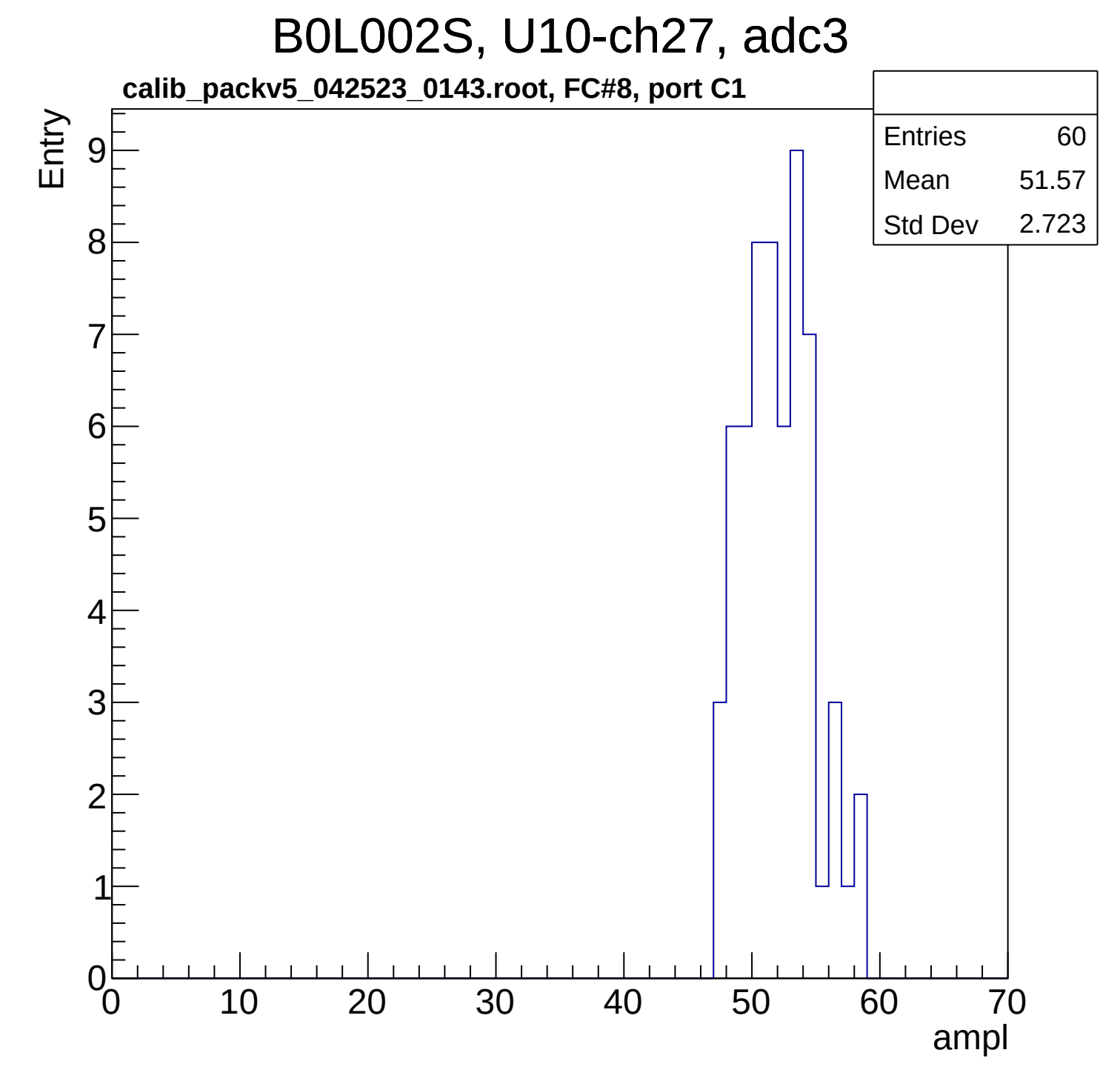

B0L002S, U10-ch26, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

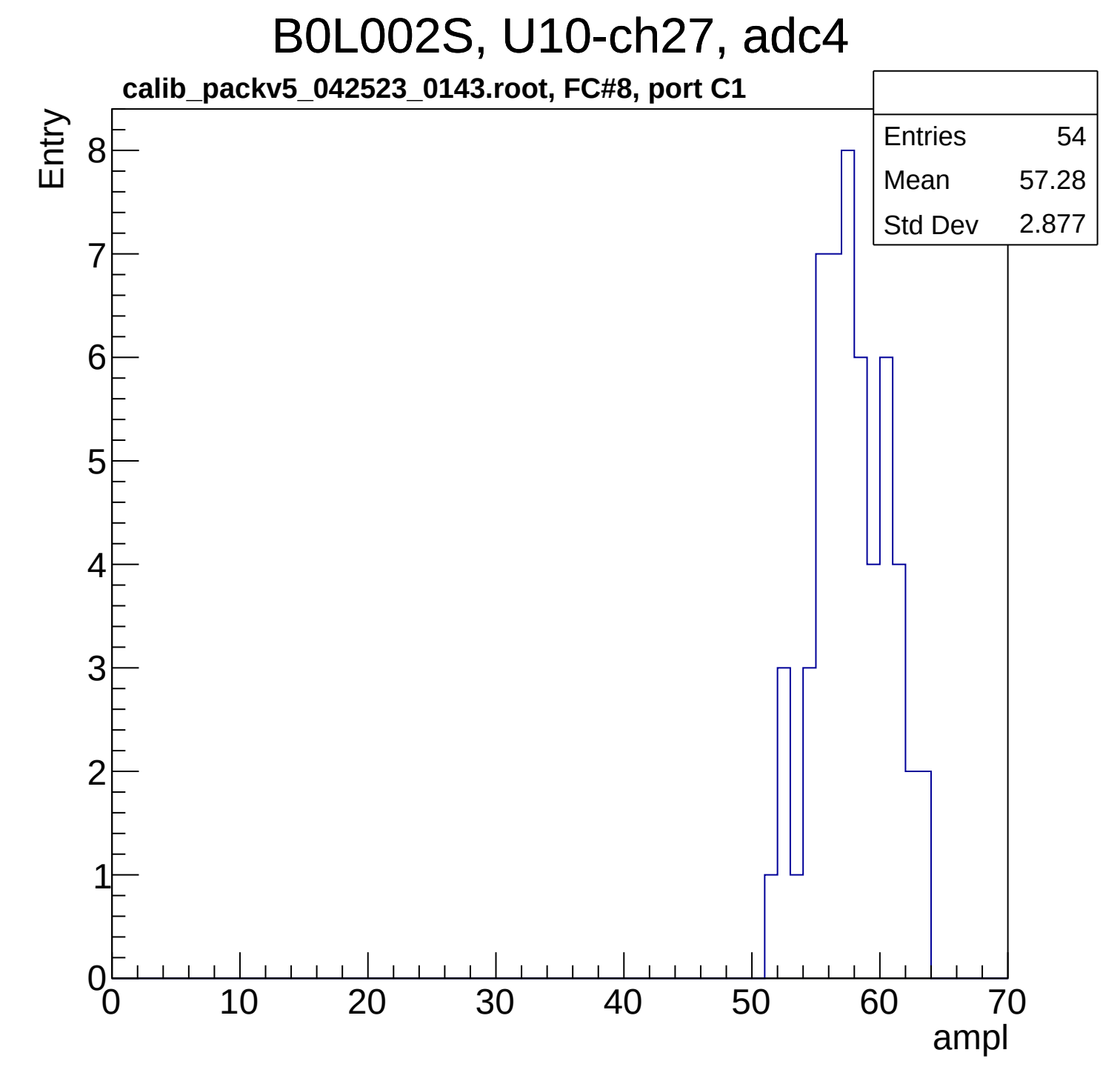

B0L002S, U10-ch26, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

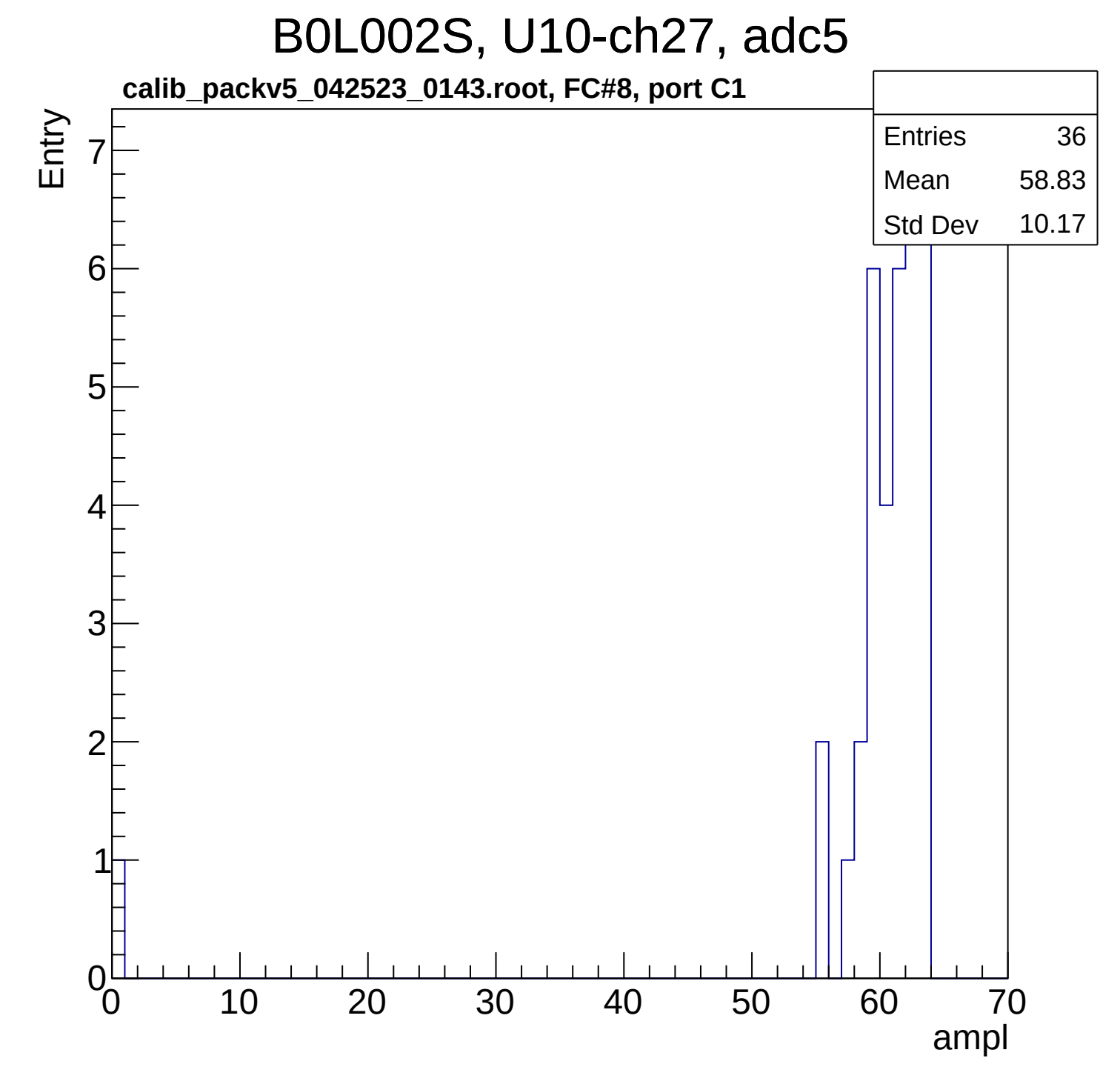

B0L002S, U10-ch27, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.7
Std Dev 2.802
Gaus mean : 31.9509
Gaus Width: 2.7914
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

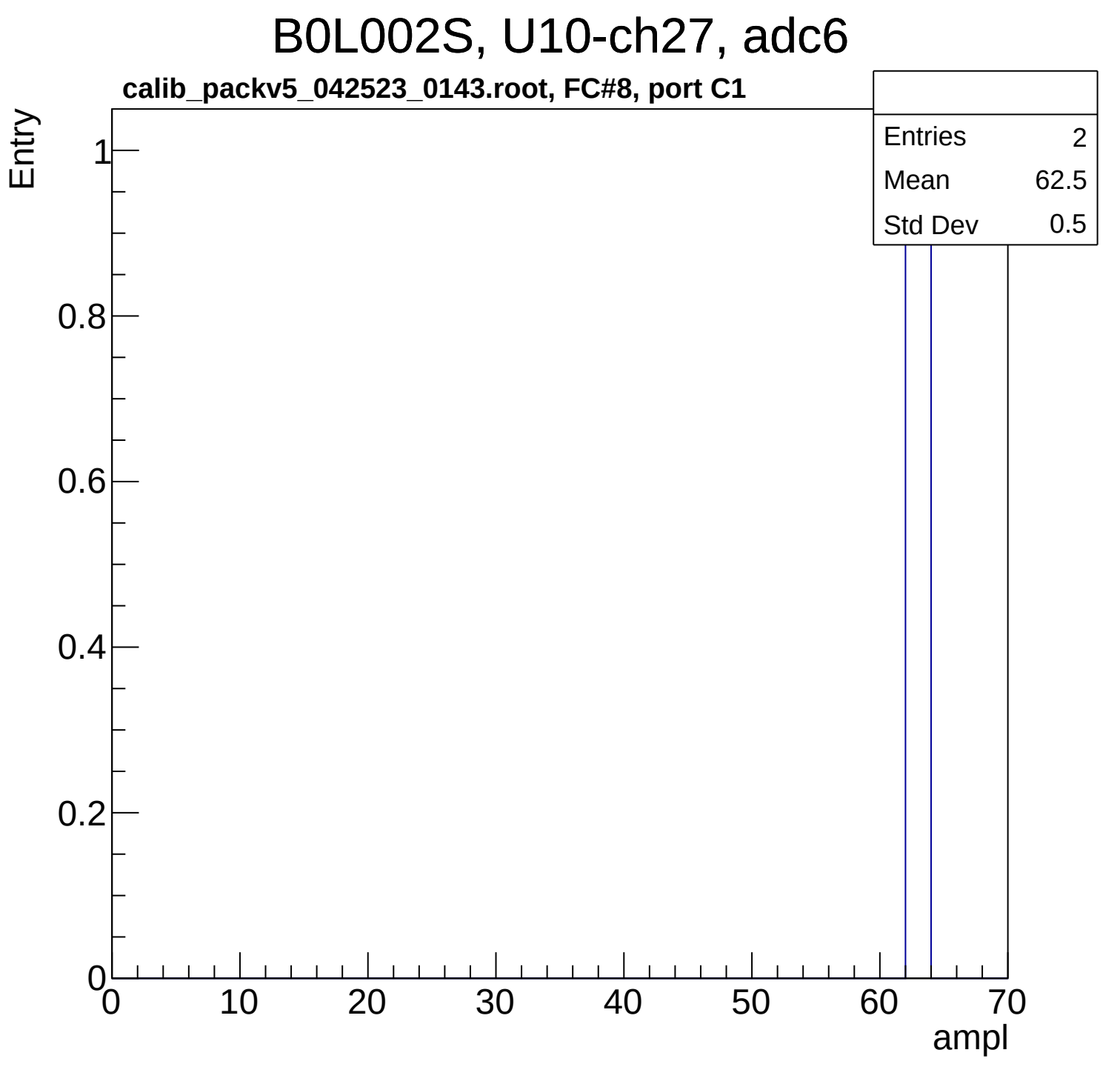

B0L002S, U10-ch27, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 38.3
Std Dev 3.159
Gaus mean : 38.7443
Gaus Width: 3.6228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

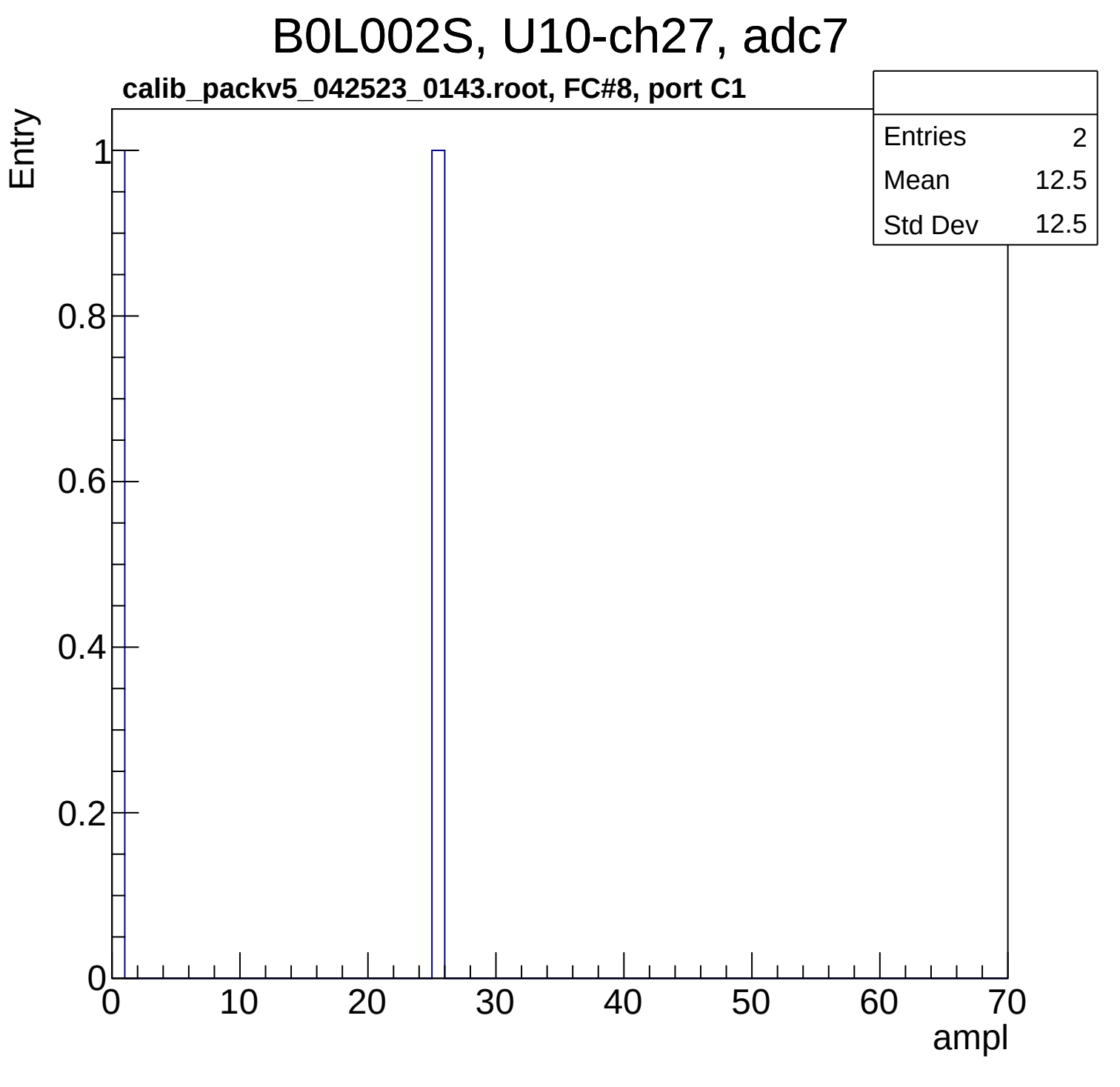

B0L002S, U10-ch27, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 44.82
Std Dev 3.032
Gaus mean : 45.2539
Gaus Width: 3.2459
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

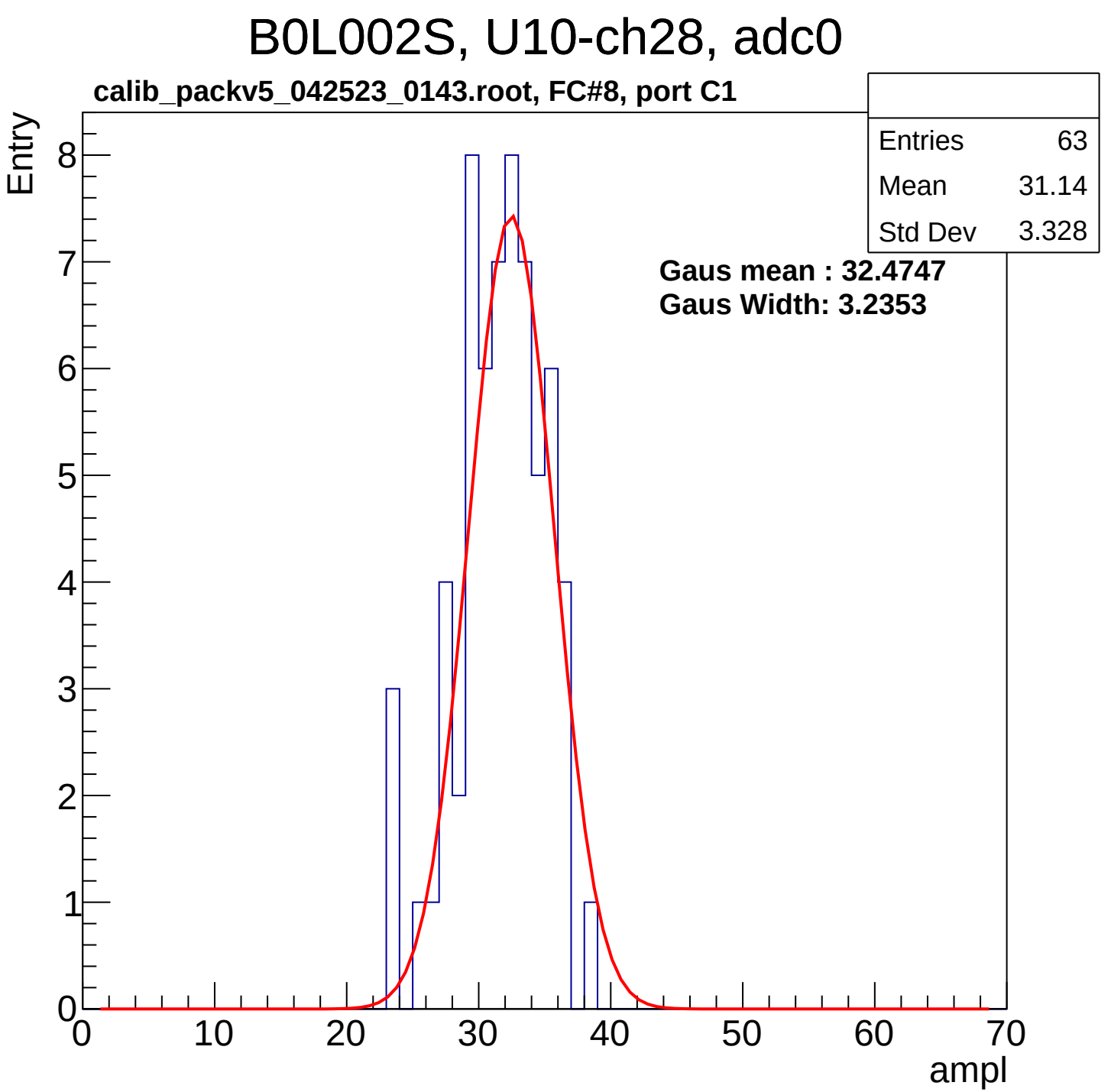

B0L002S, U10-ch27, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 51.57
Std Dev 2.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

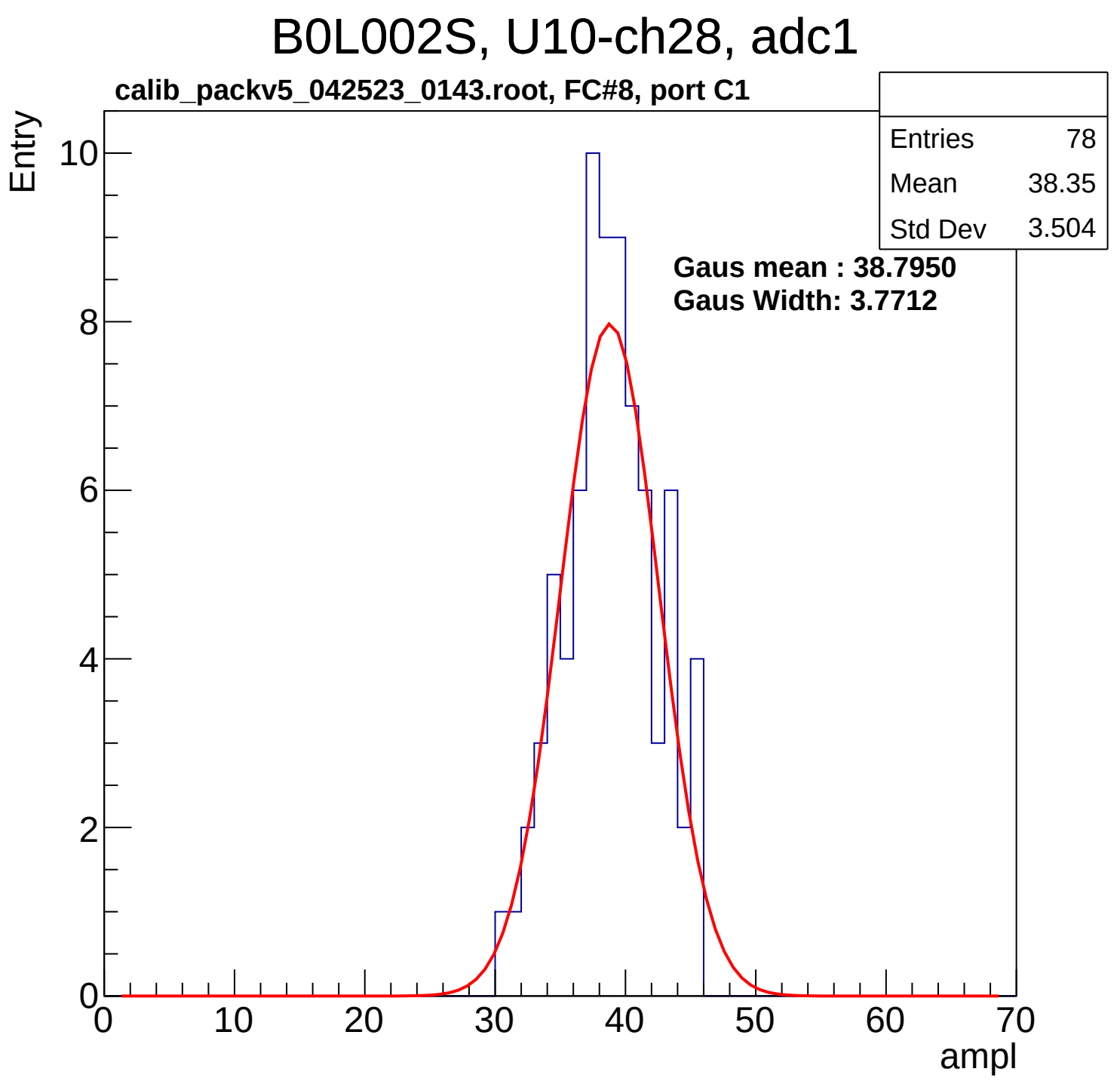

B0L002S, U10-ch27, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.28
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

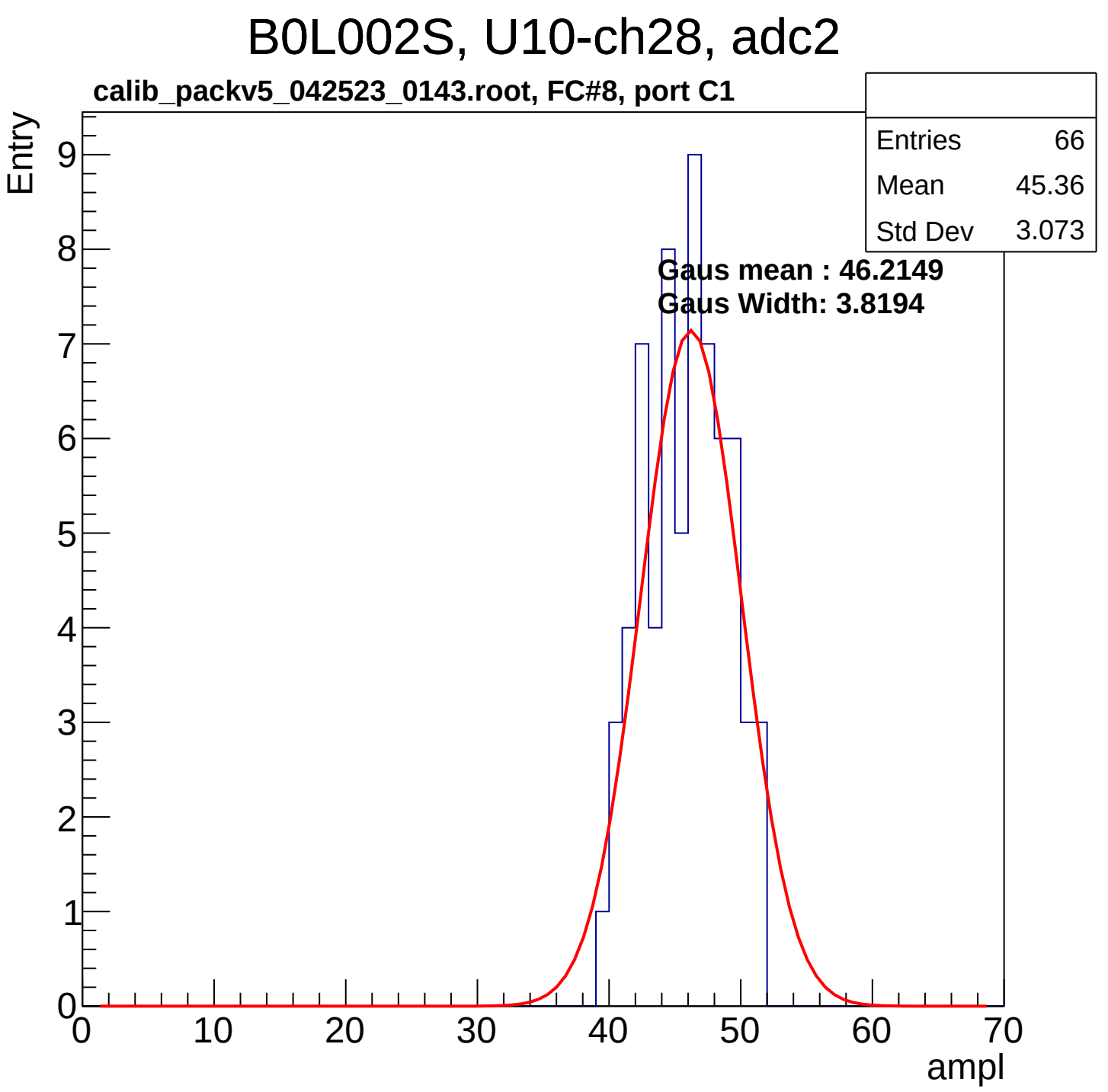

B0L002S, U10-ch27, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.83
Std Dev 10.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

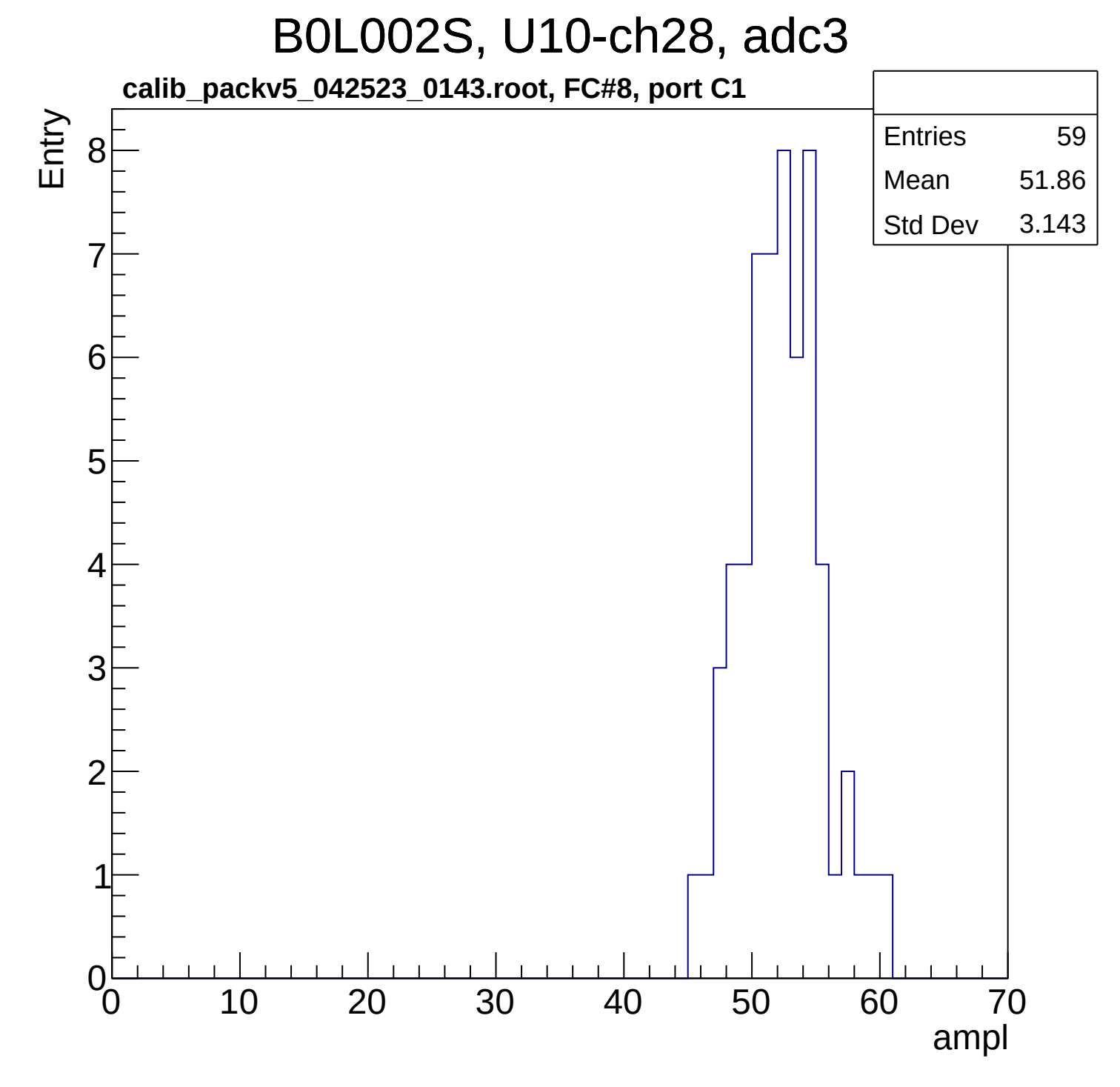

B0L002S, U10-ch27, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

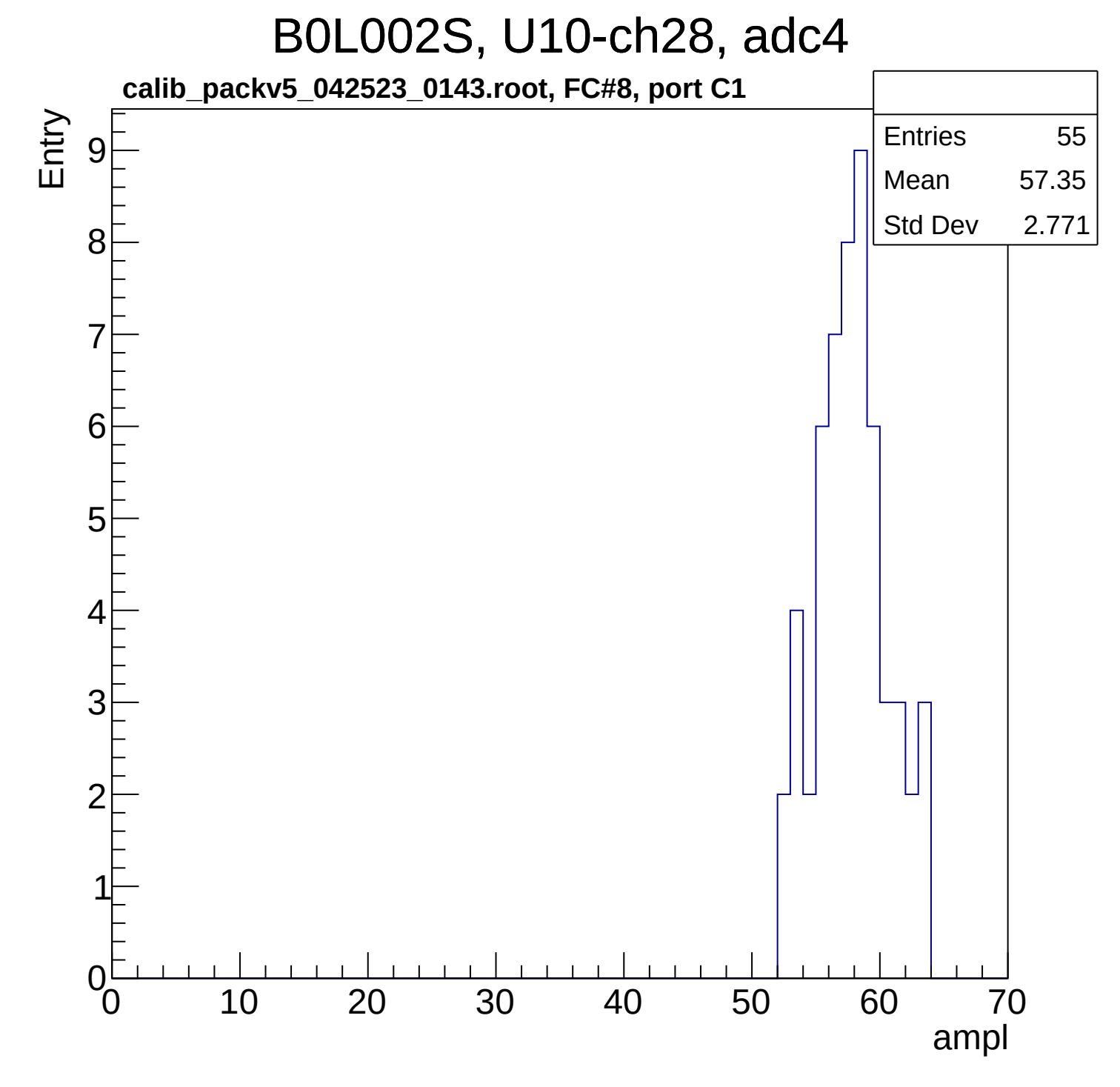

B0L002S, U10-ch27, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

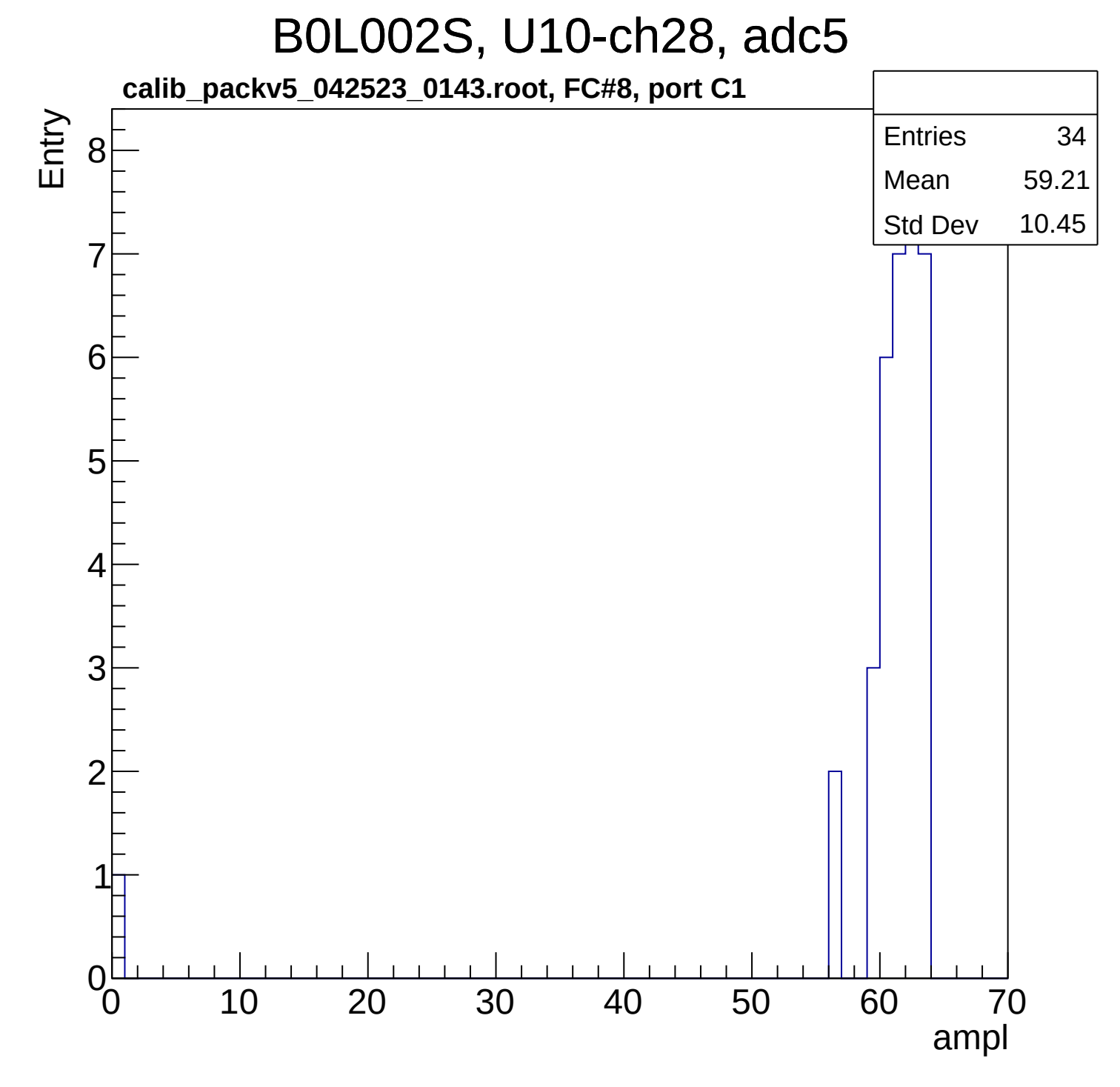

B0L002S, U10-ch28, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.14
Std Dev 3.328
Gaus mean : 32.4747
Gaus Width: 3.2353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

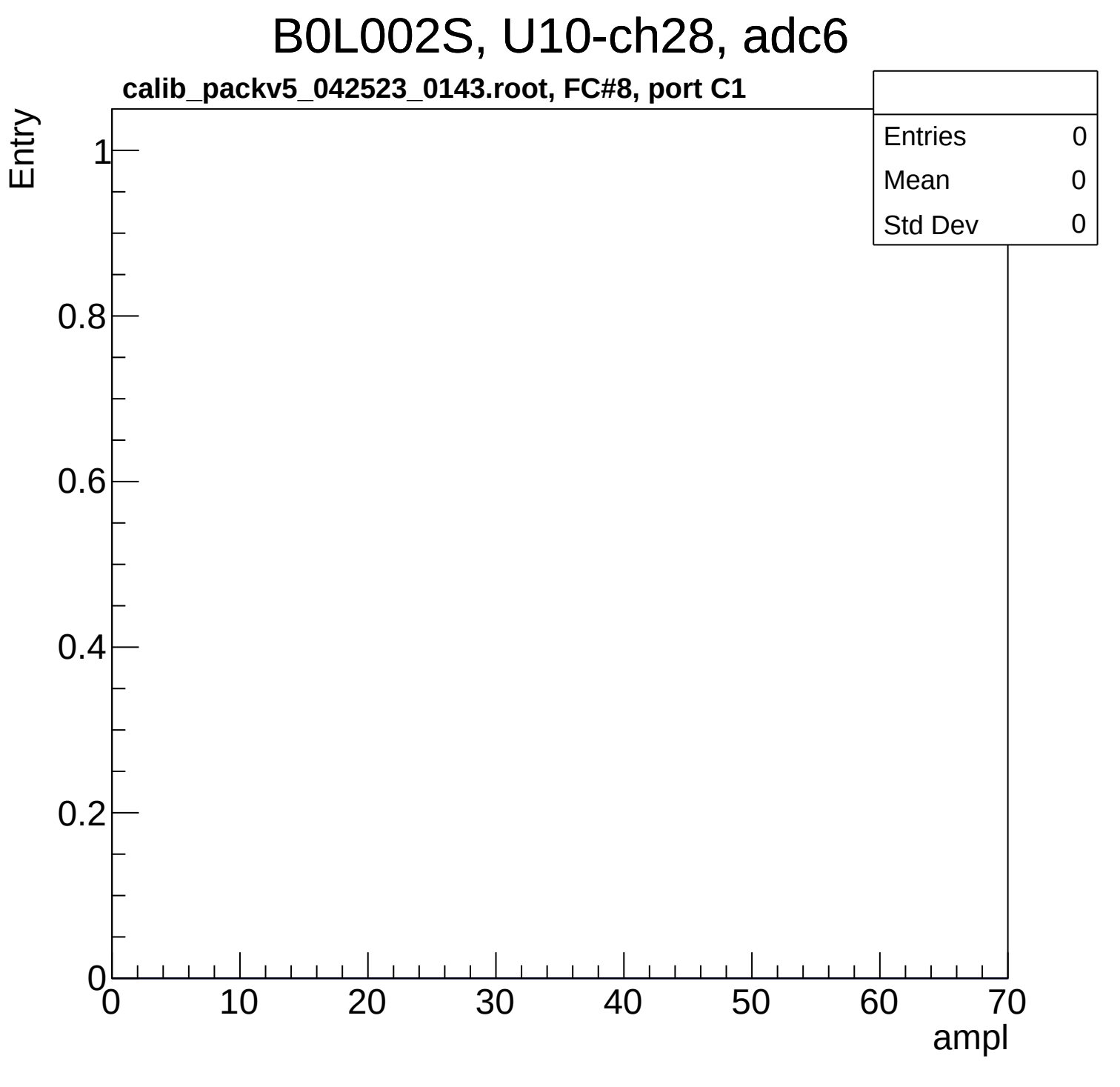

B0L002S, U10-ch28, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.35
Std Dev 3.504
Gaus mean : 38.7950
Gaus Width: 3.7712
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

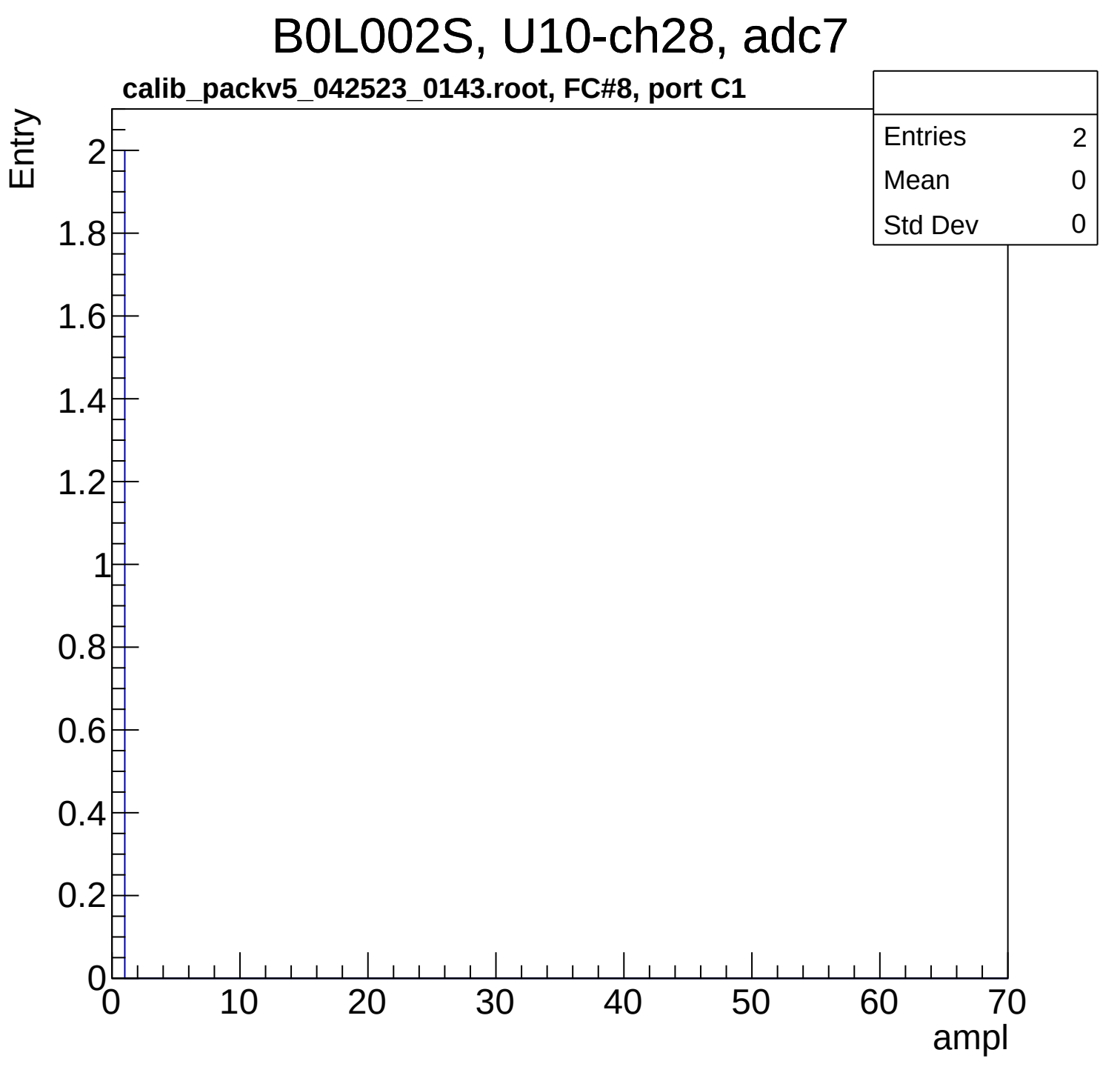

B0L002S, U10-ch28, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 45.36
Std Dev 3.073
Gaus mean : 46.2149
Gaus Width: 3.8194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

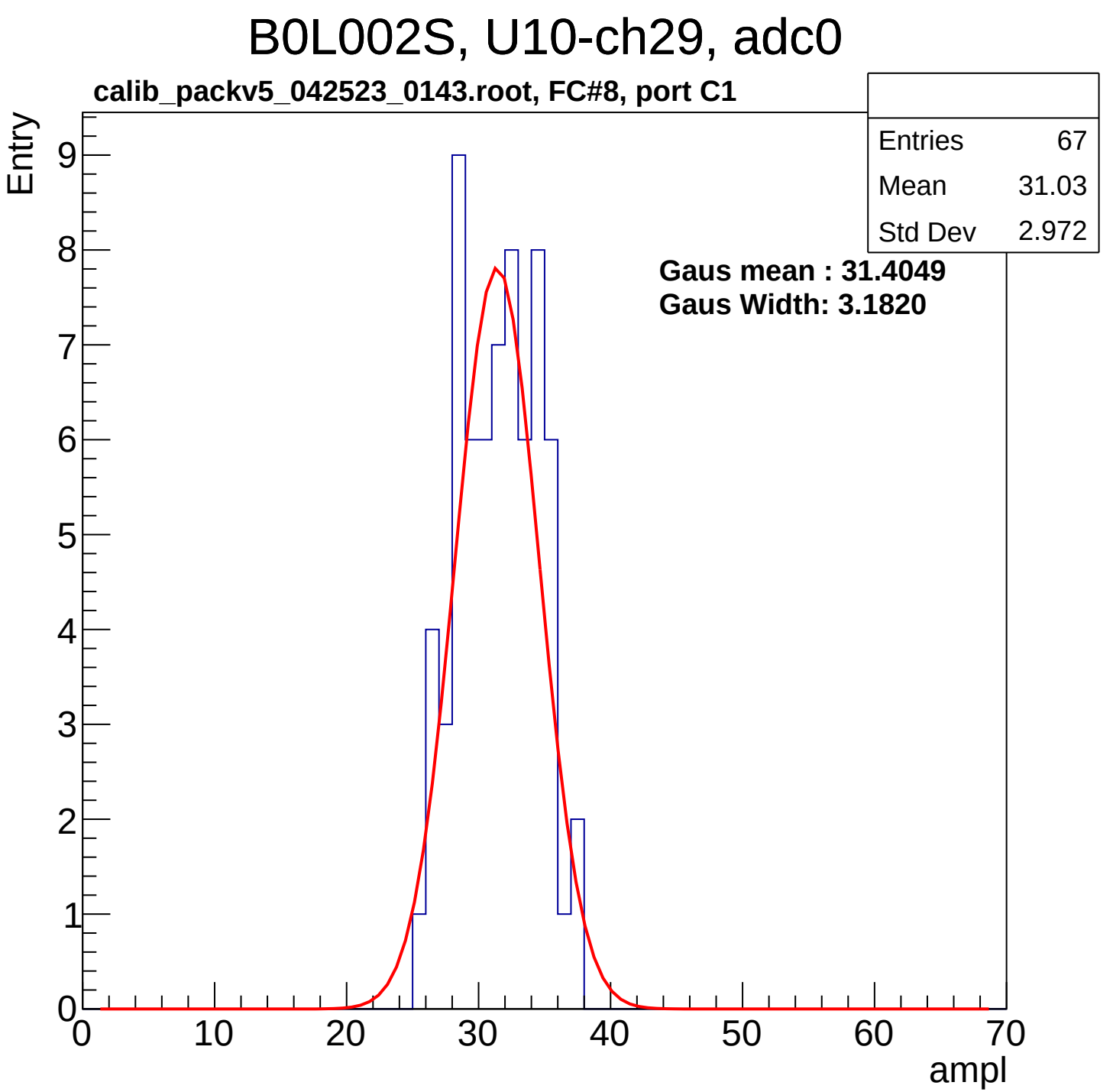

B0L002S, U10-ch28, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 51.86
Std Dev 3.143
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

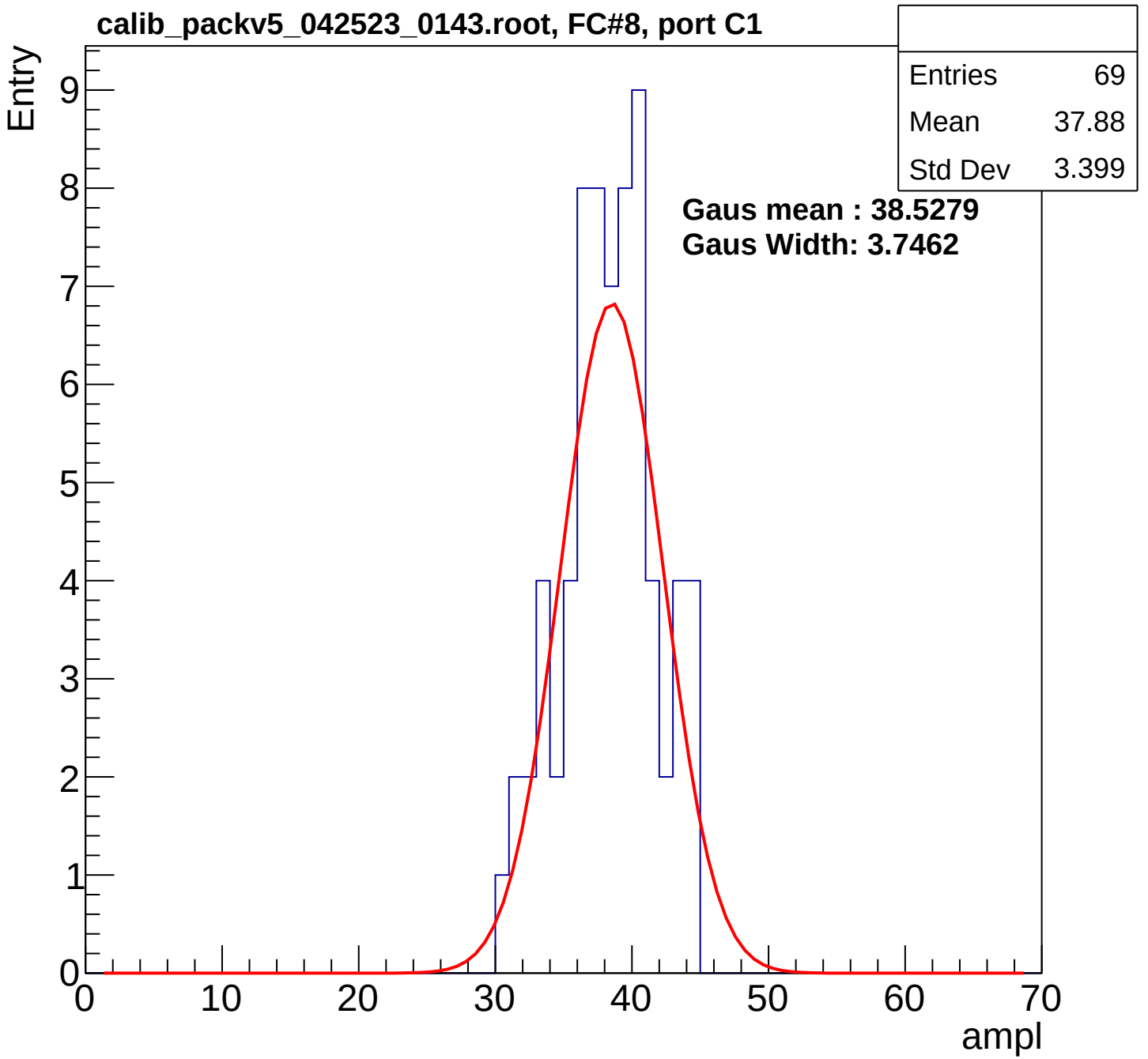

B0L002S, U10-ch28, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.35
Std Dev 2.771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

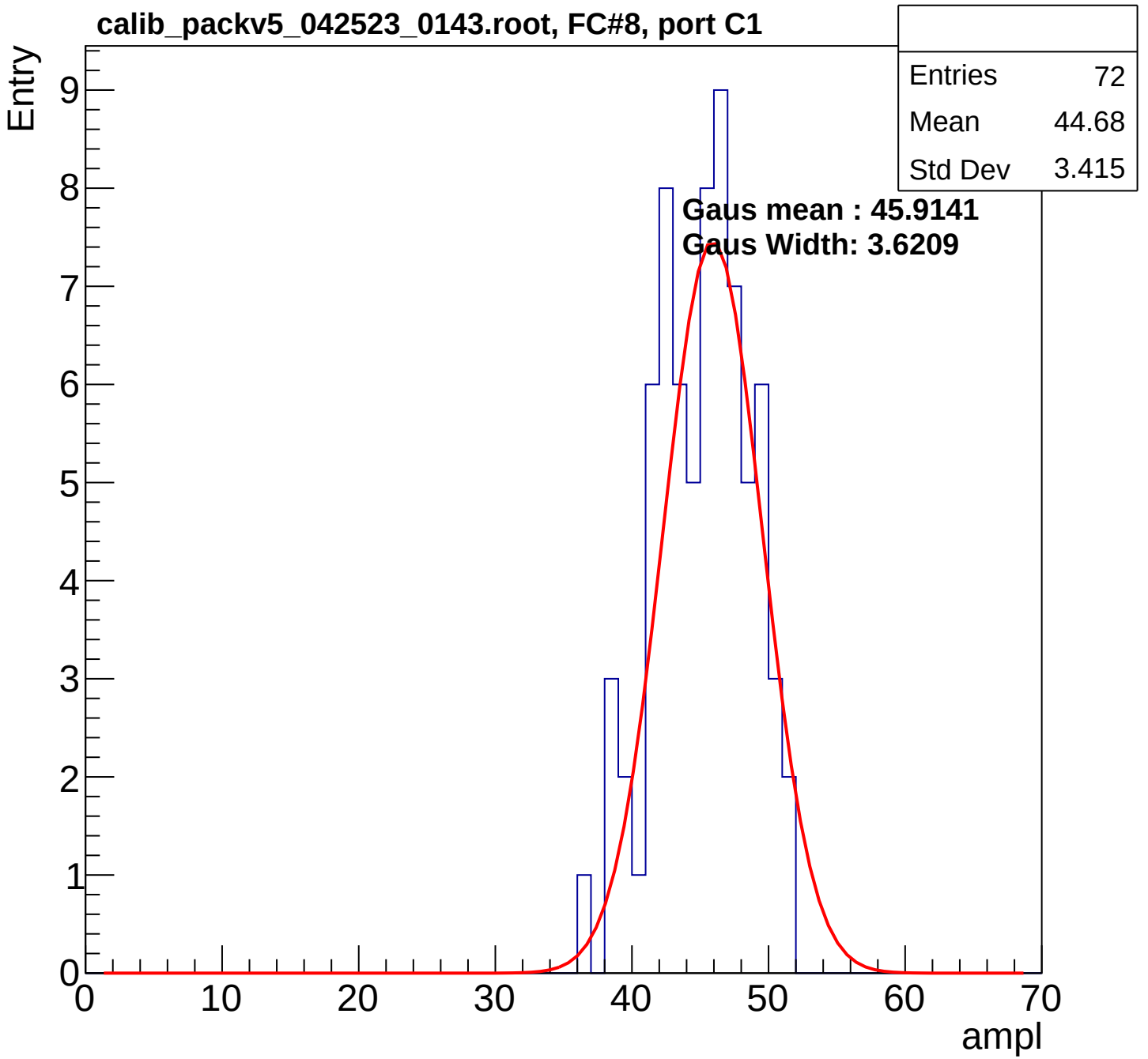

B0L002S, U10-ch28, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.21
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

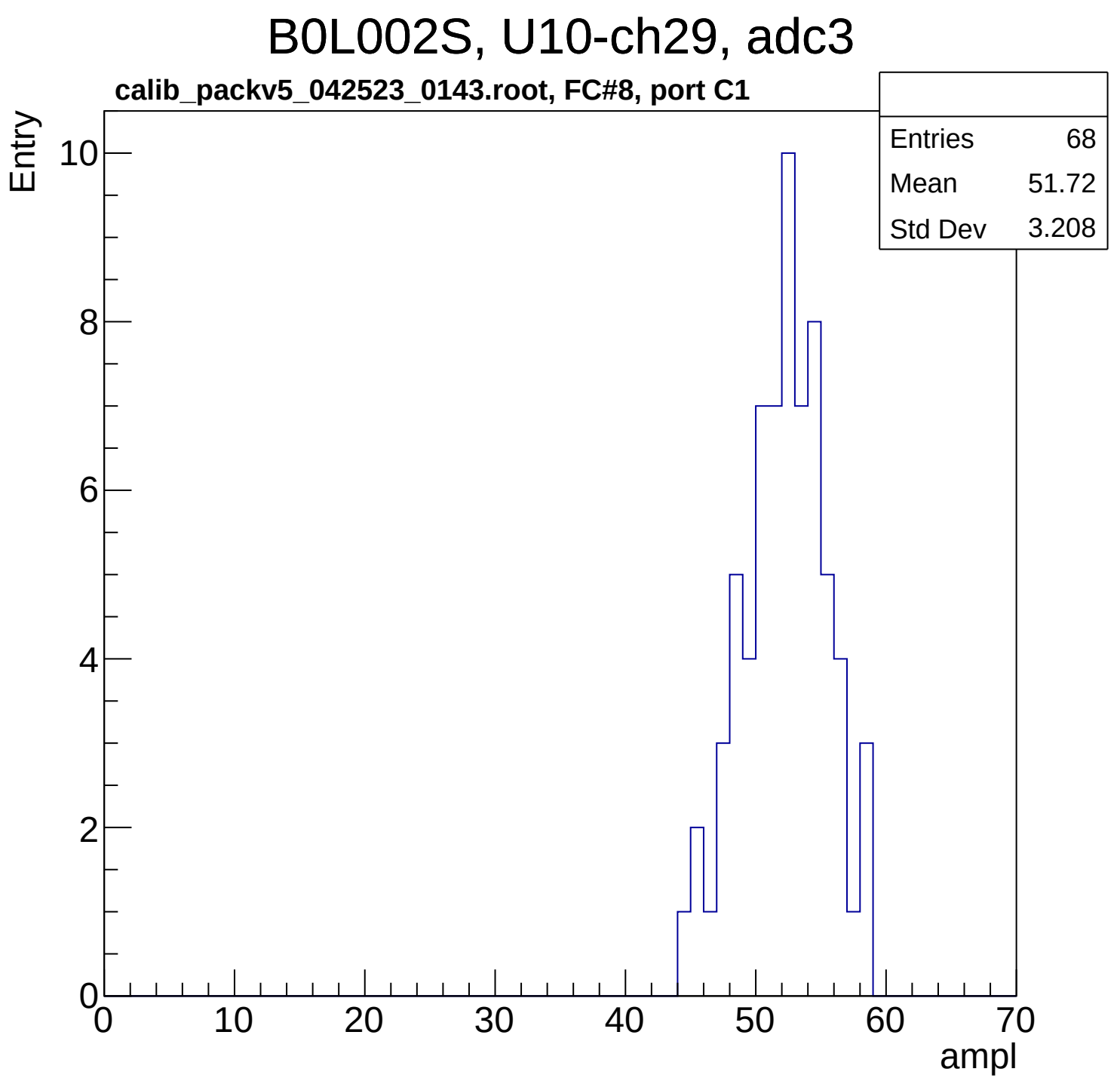

B0L002S, U10-ch28, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

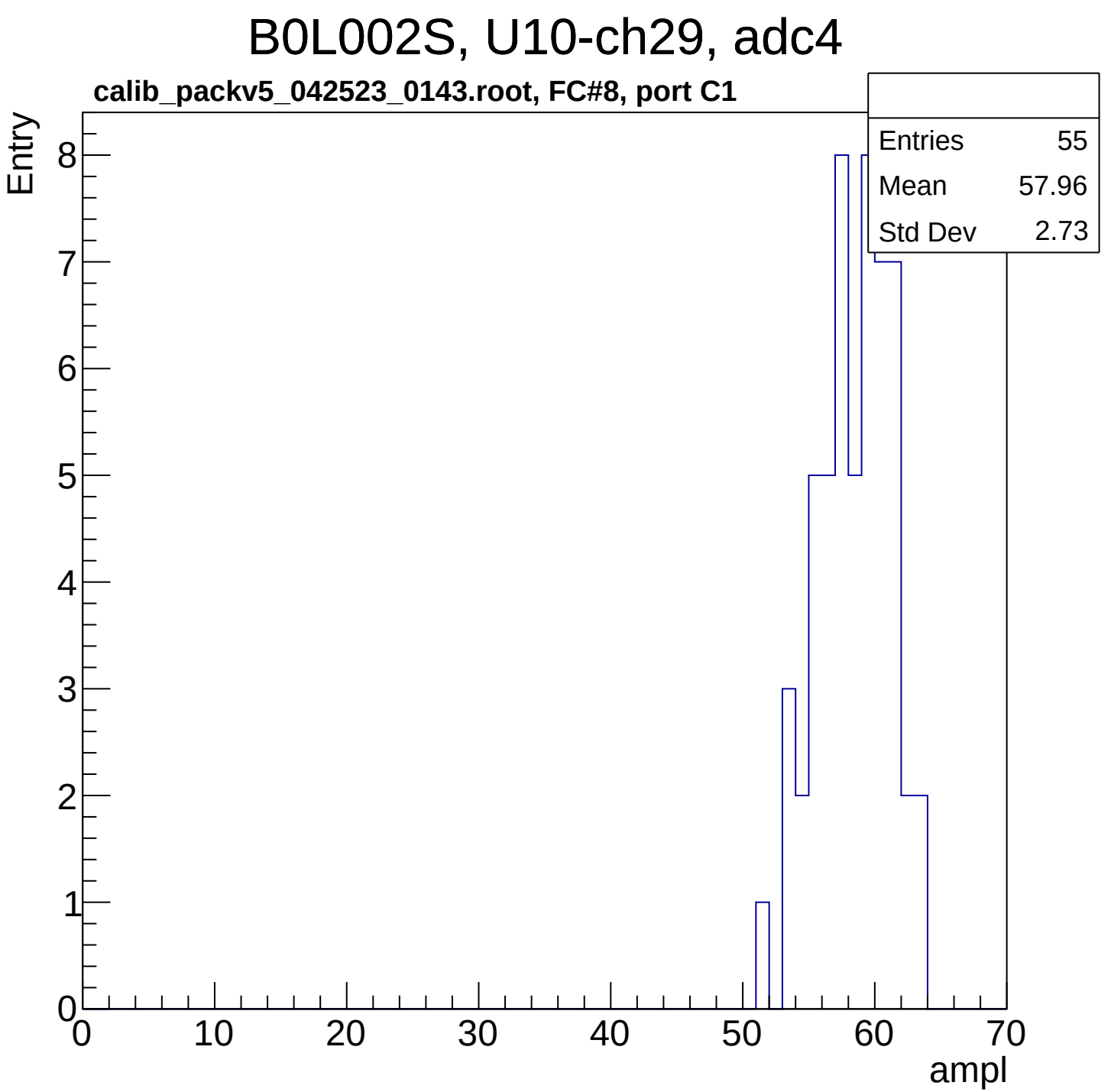

B0L002S, U10-ch28, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

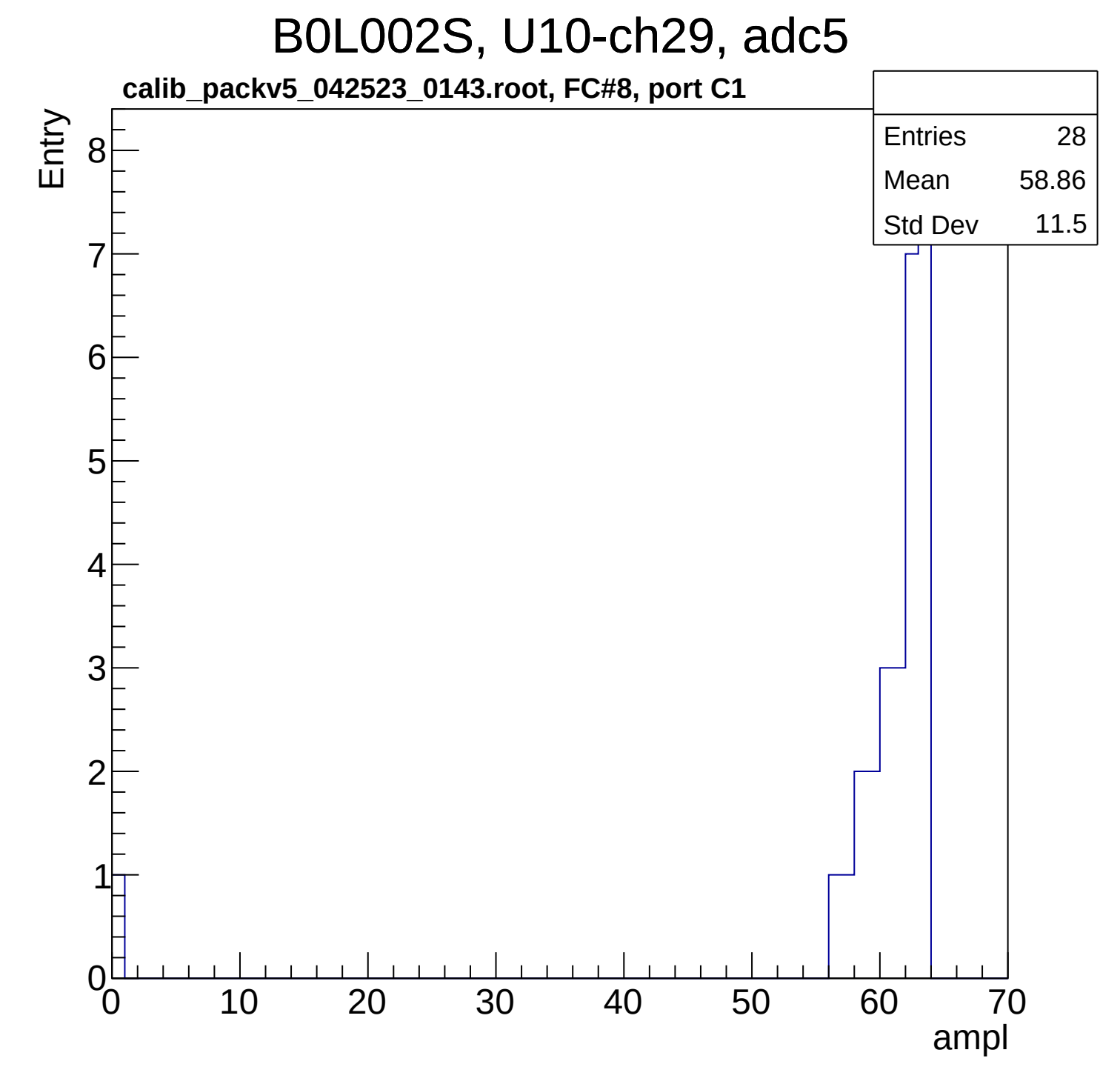

B0L002S, U10-ch29, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.03
Std Dev 2.972
Gaus mean : 31.4049
Gaus Width: 3.1820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

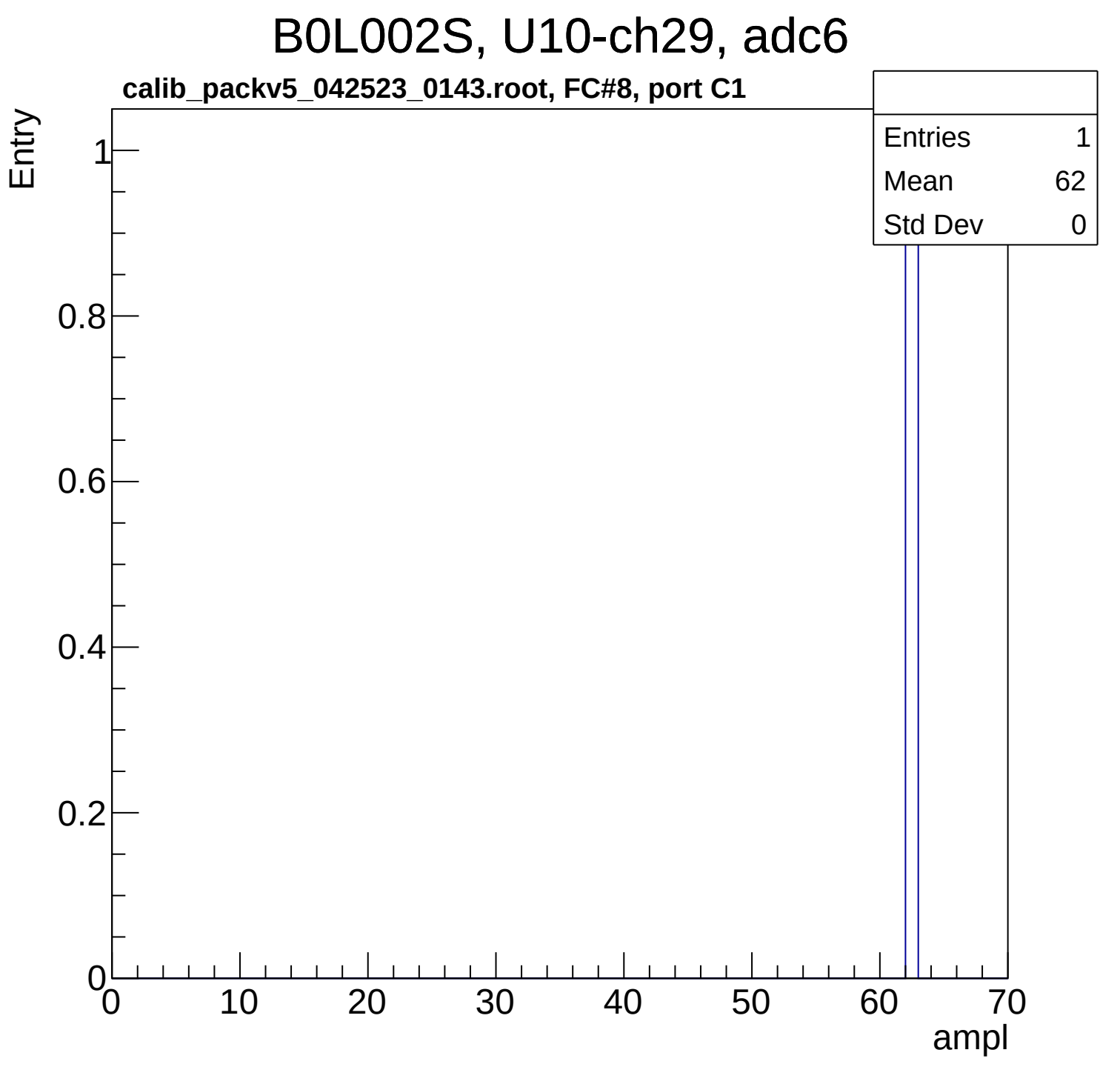

B0L002S, U10-ch29, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 37.88
Std Dev 3.399
Gaus mean : 38.5279
Gaus Width: 3.7462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

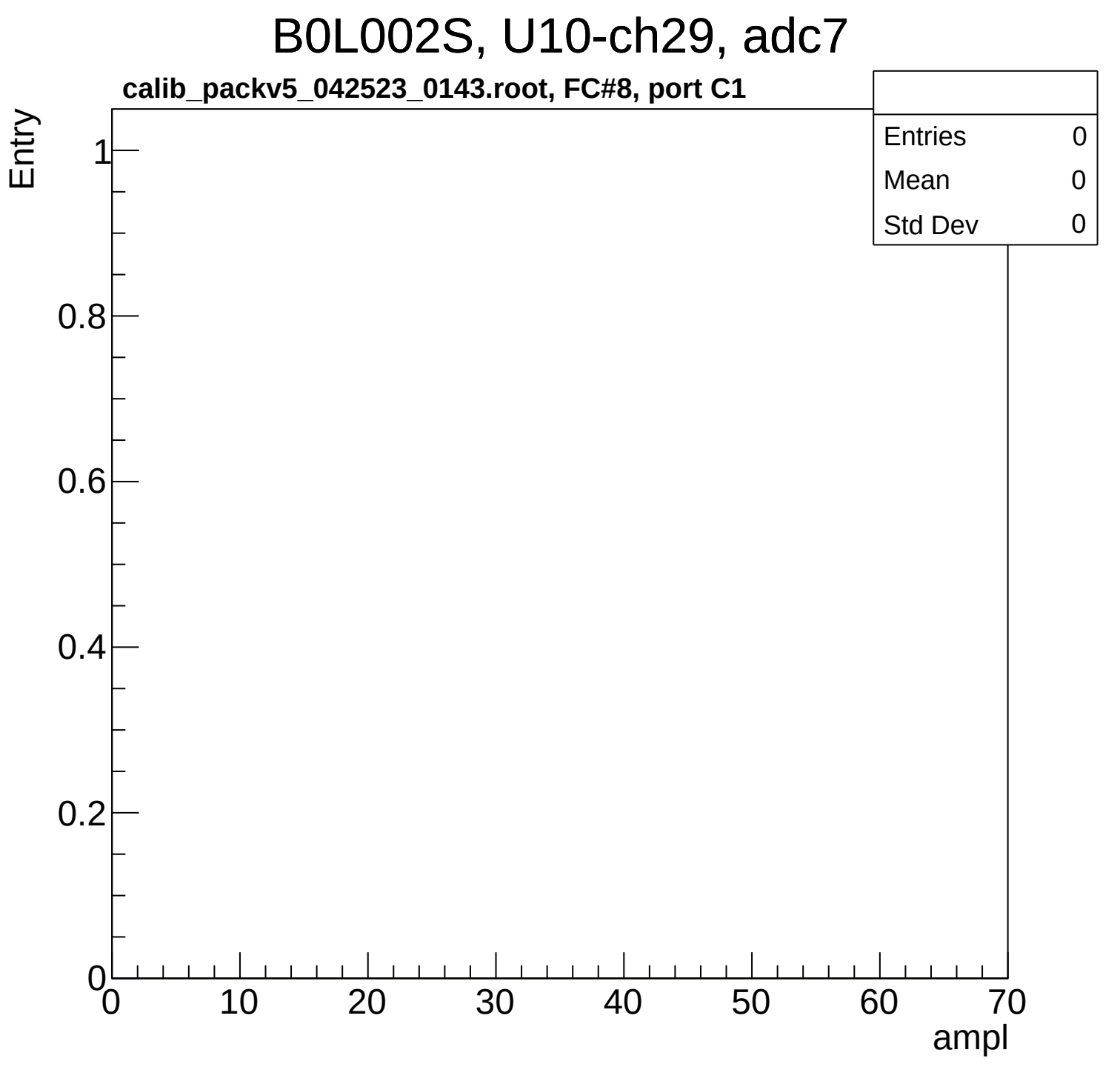

B0L002S, U10-ch29, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.68
Std Dev 3.415
Gaus mean : 45.9141
Gaus Width: 3.6209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

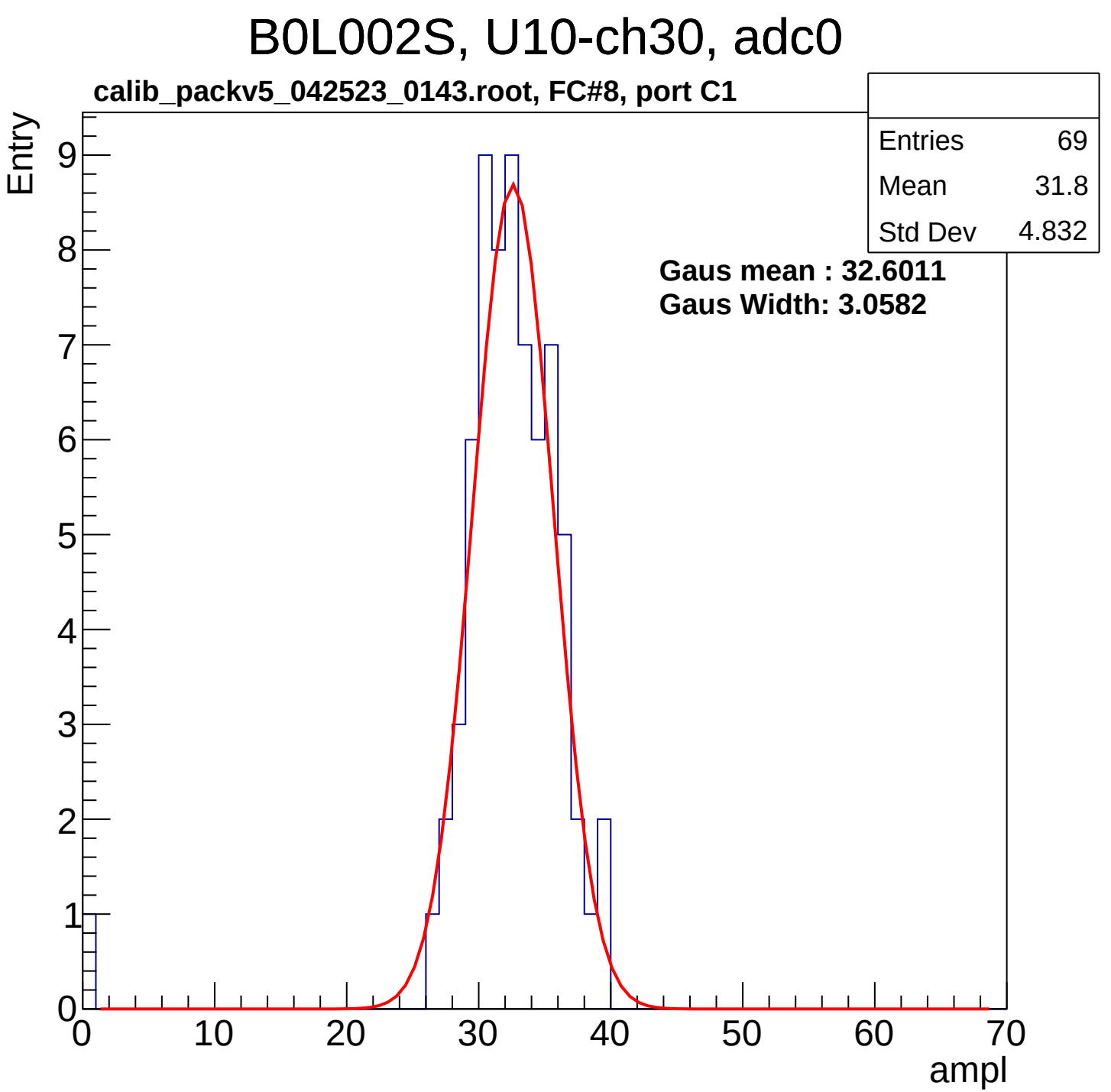

B0L002S, U10-ch29, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.72
Std Dev 3.208
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

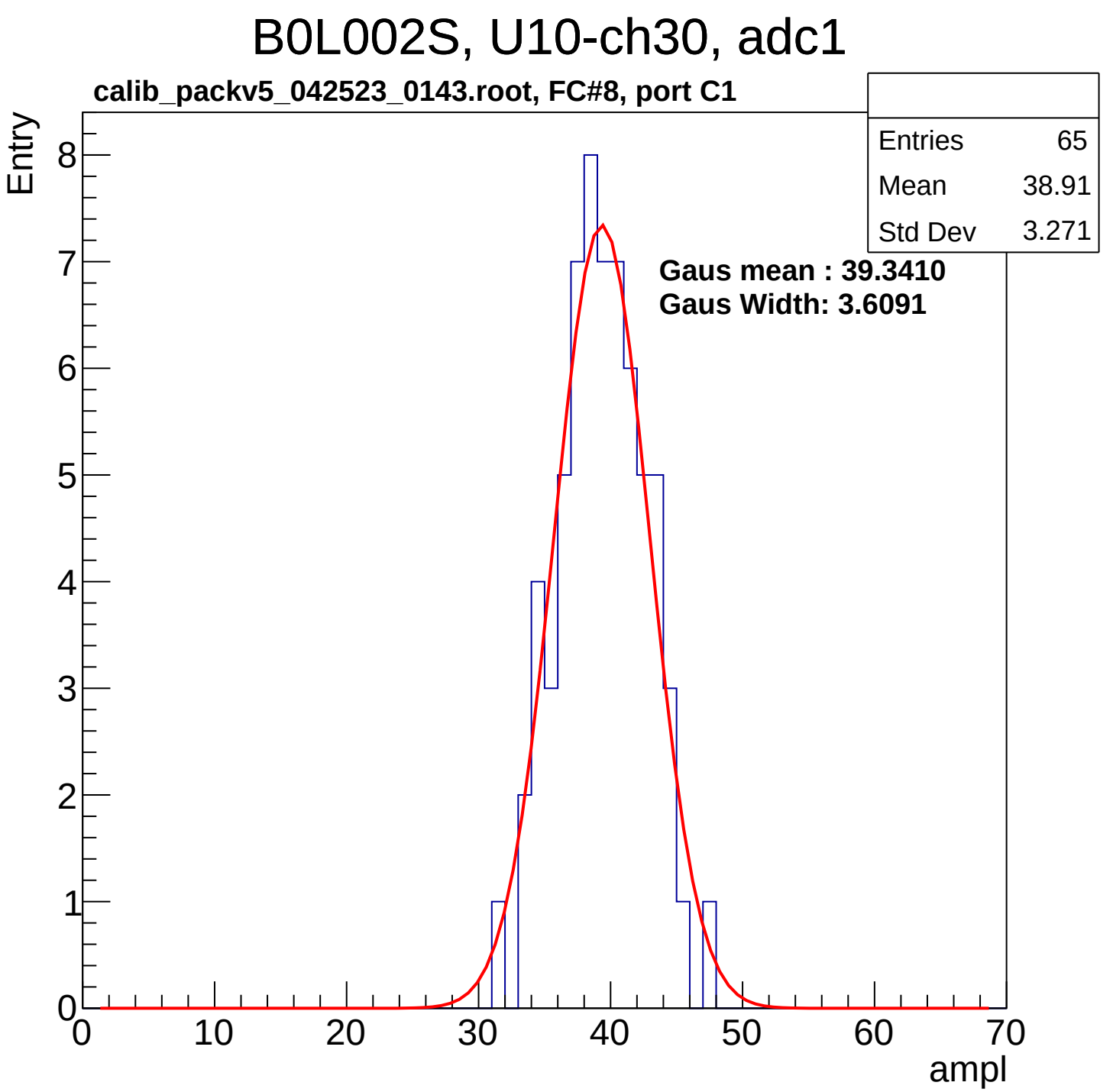

B0L002S, U10-ch29, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.96
Std Dev 2.73
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

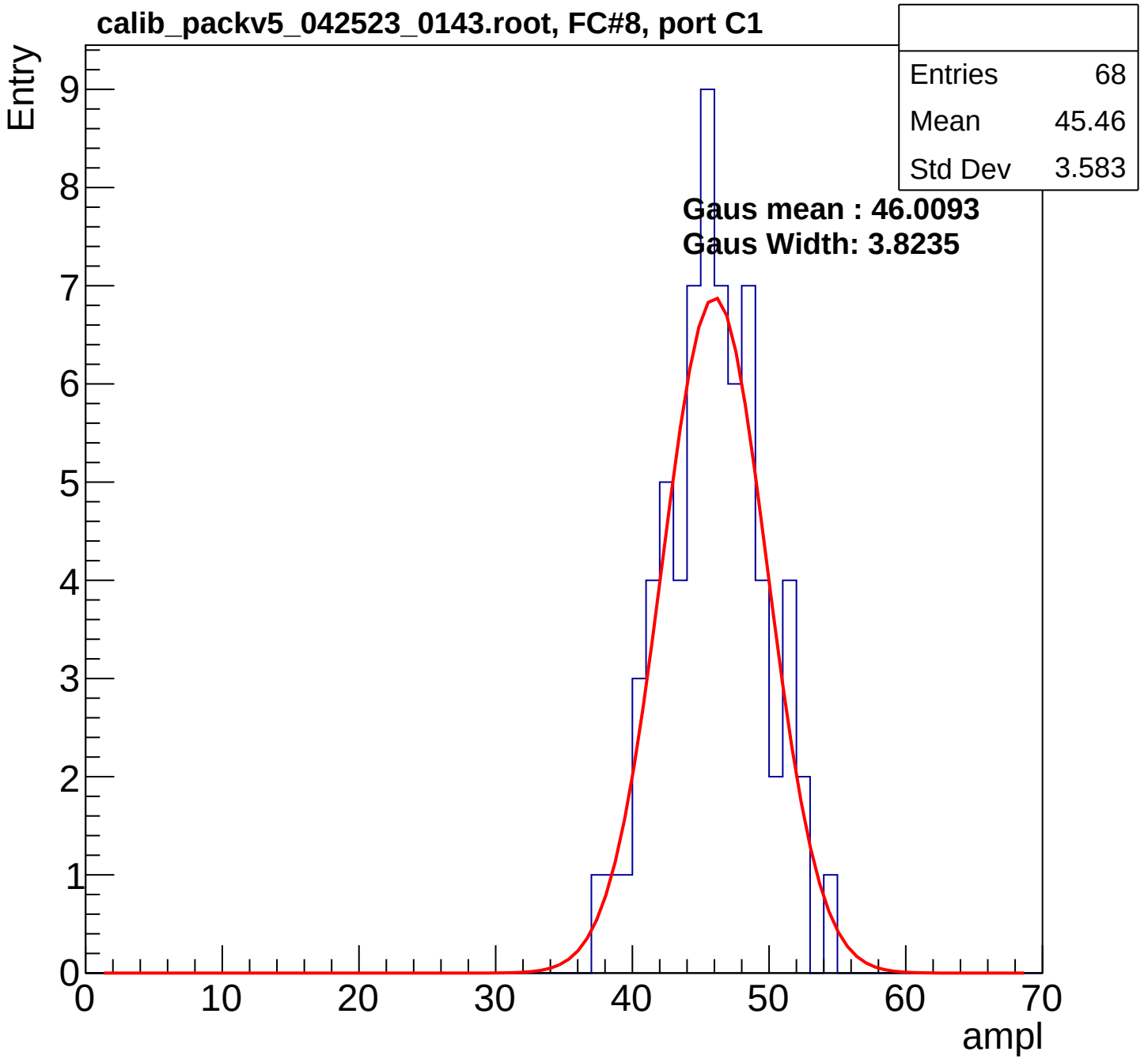

B0L002S, U10-ch29, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.86
Std Dev 11.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

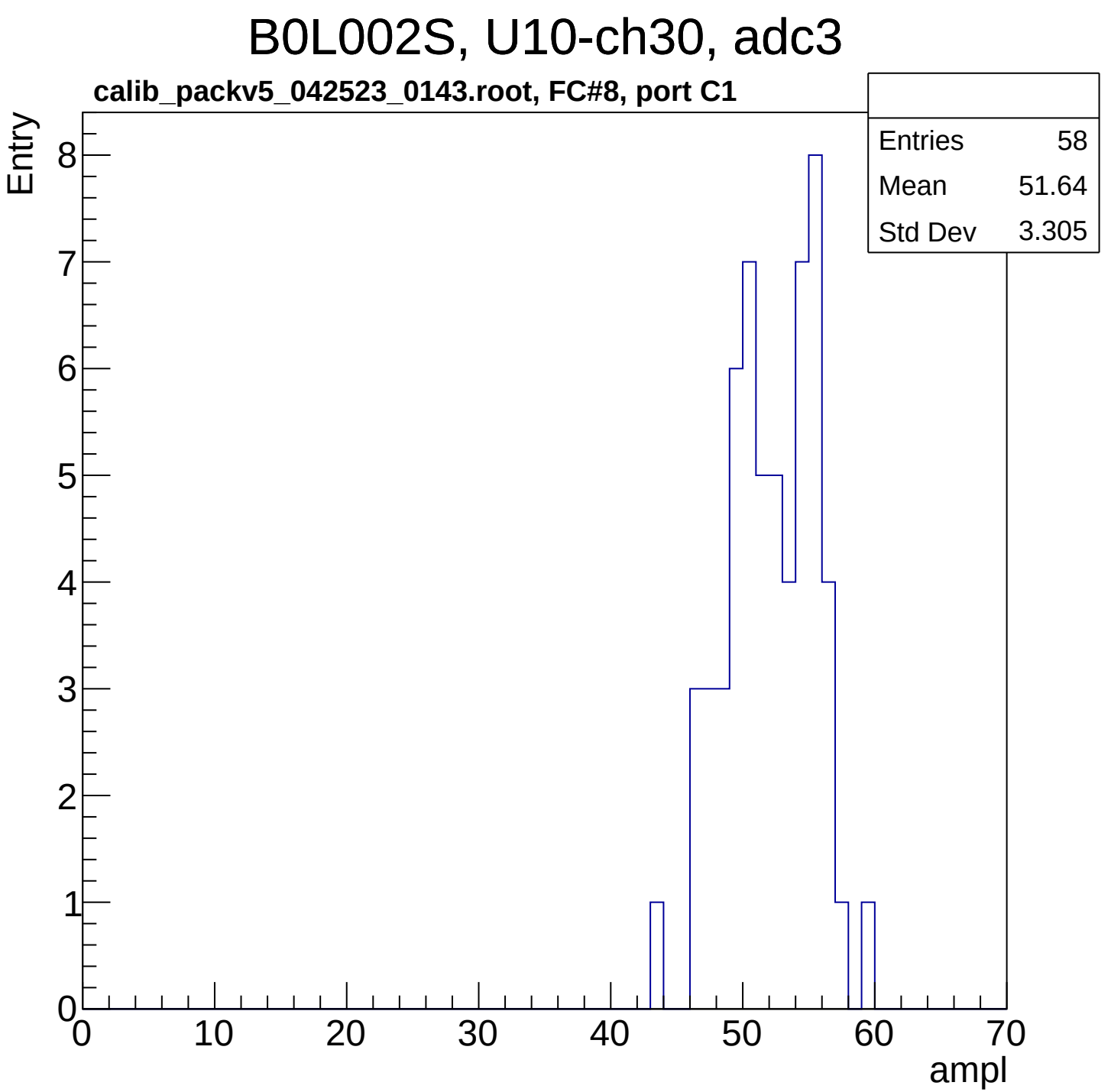

B0L002S, U10-ch29, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

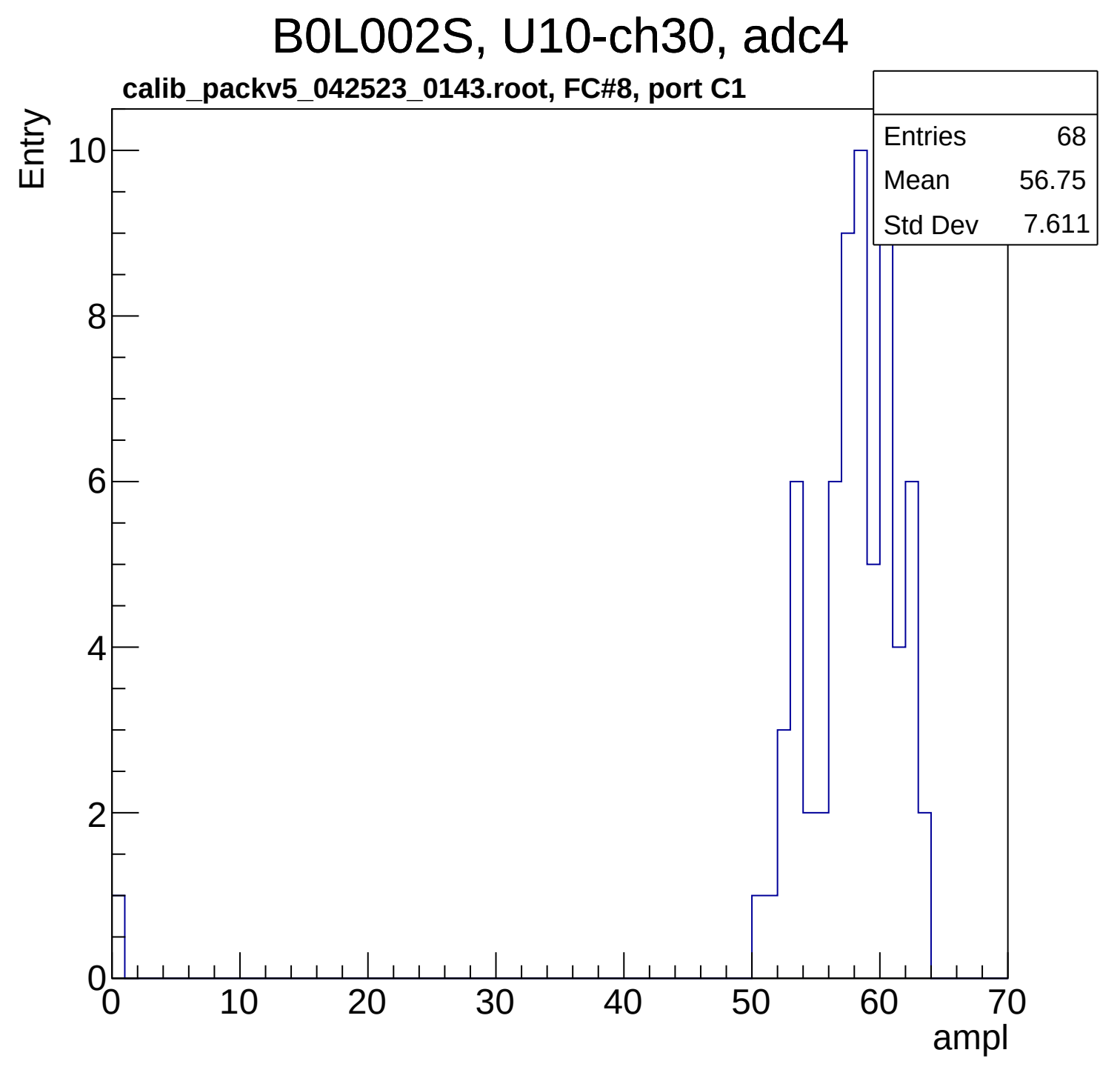

B0L002S, U10-ch29, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

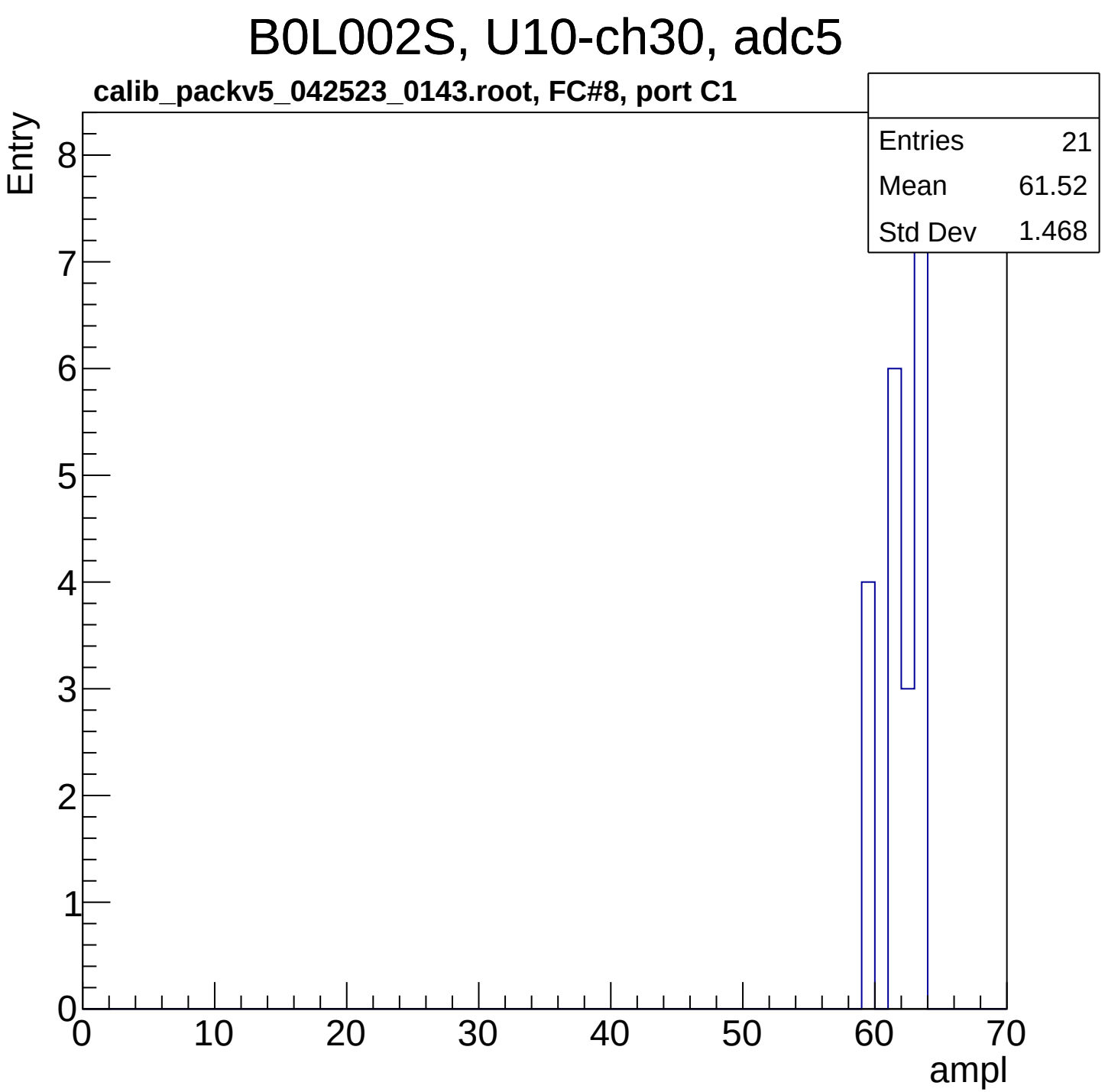

B0L002S, U10-ch30, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 31.8
Std Dev 4.832
Gaus mean : 32.6011
Gaus Width: 3.0582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

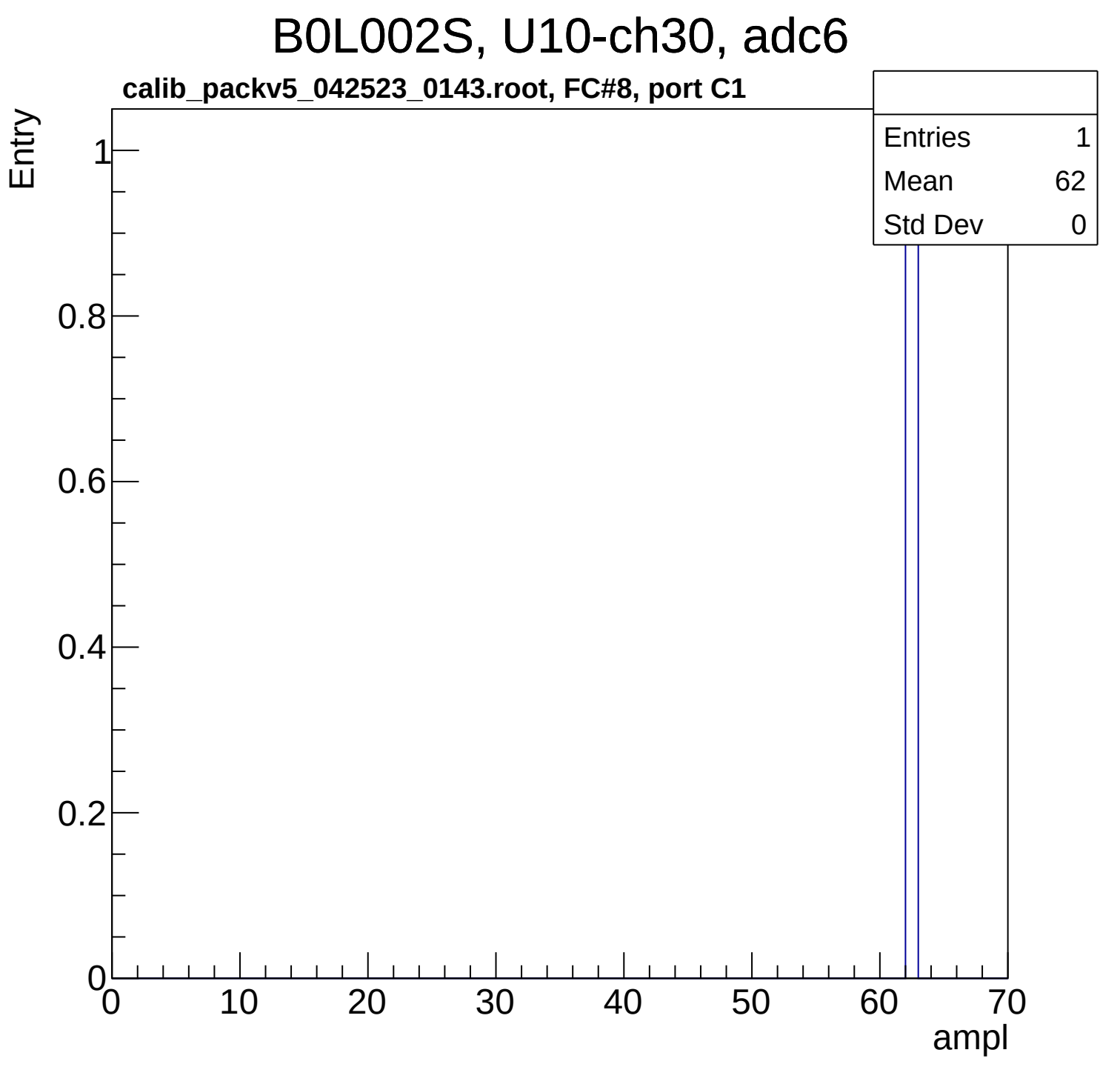

B0L002S, U10-ch30, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.91
Std Dev 3.271
Gaus mean : 39.3410
Gaus Width: 3.6091
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

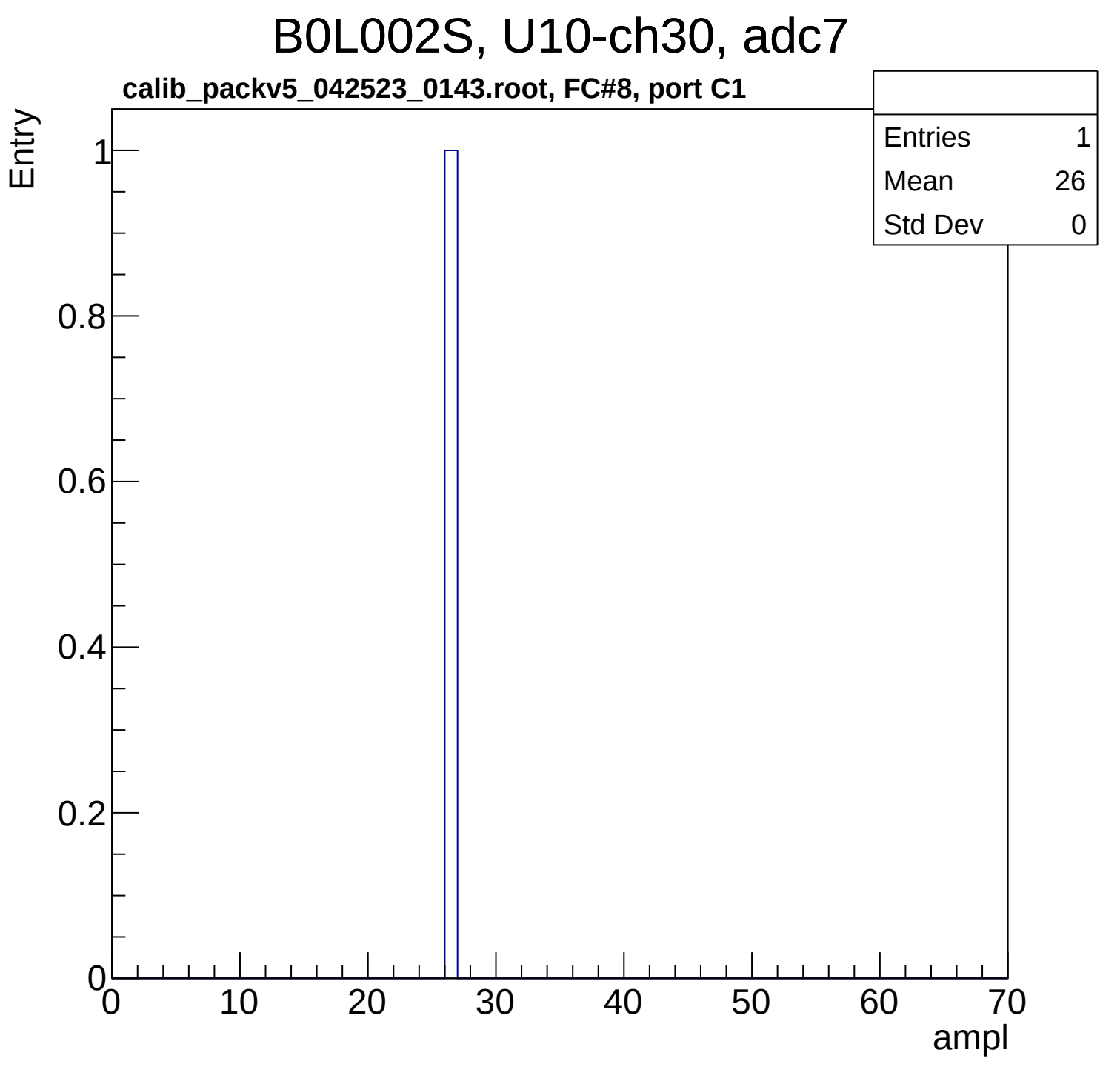

B0L002S, U10-ch30, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 45.46
Std Dev 3.583
Gaus mean : 46.0093
Gaus Width: 3.8235
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

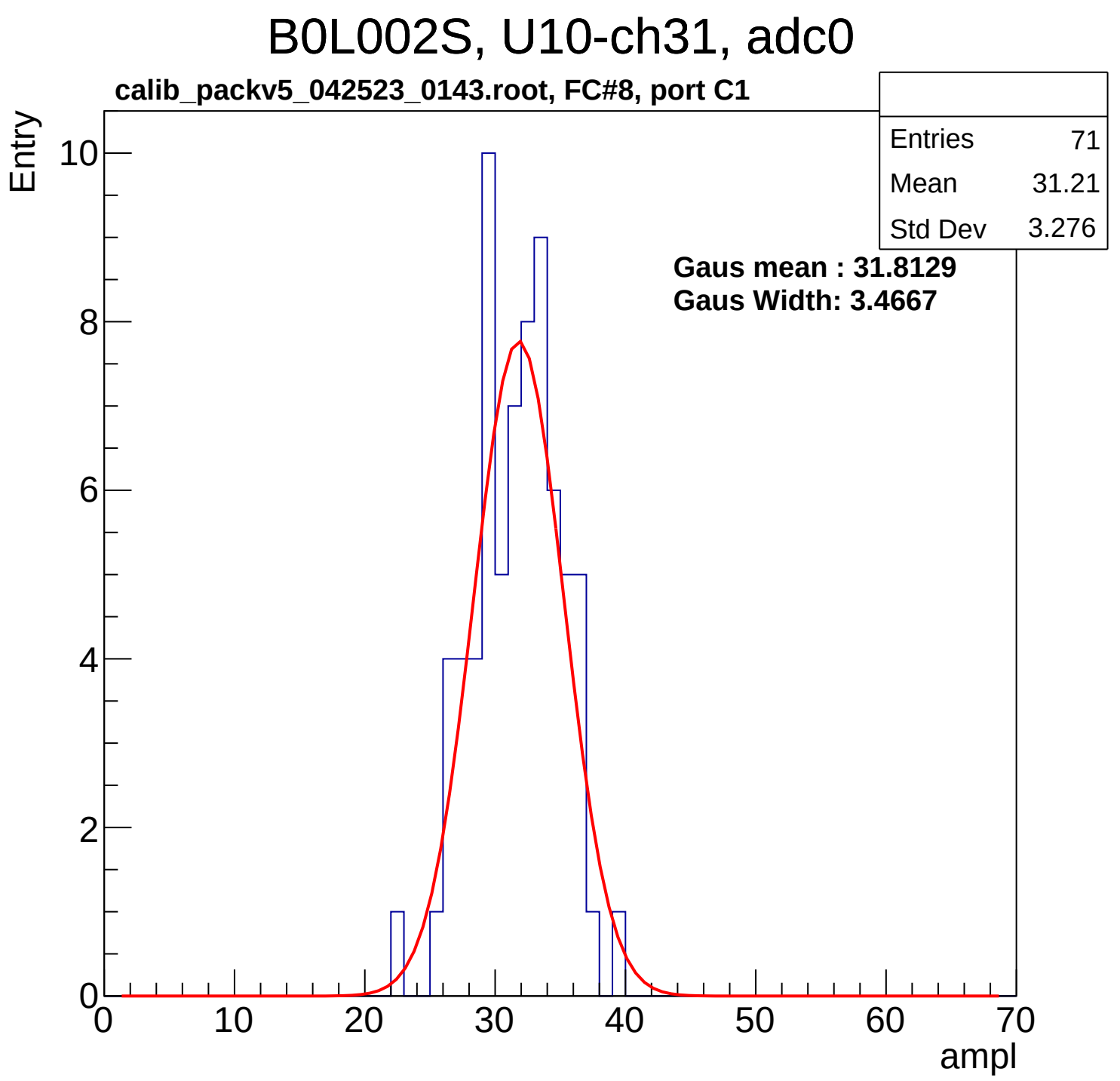

B0L002S, U10-ch30, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.64
Std Dev 3.305
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

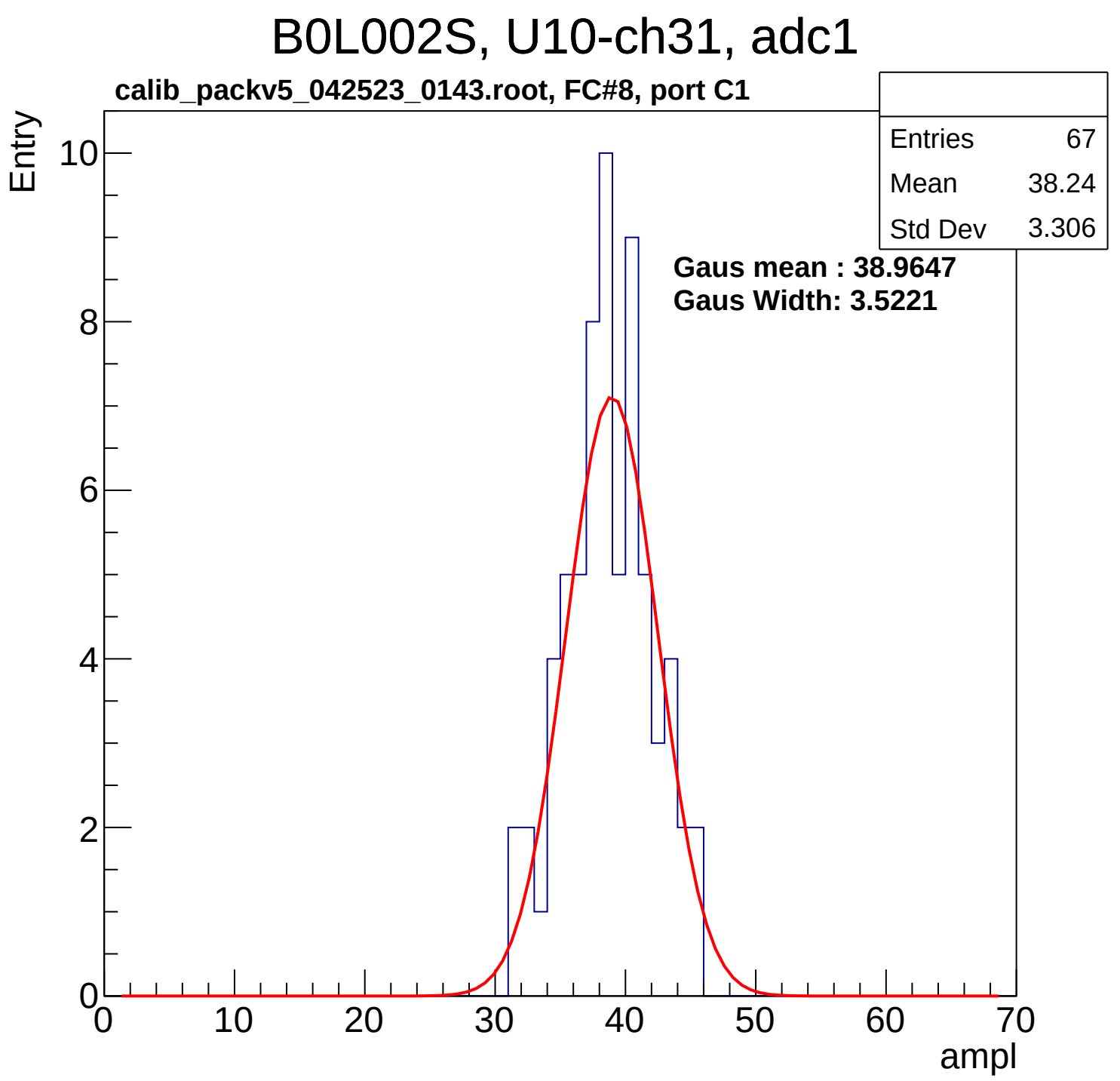

B0L002S, U10-ch30, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 56.75
Std Dev 7.611
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

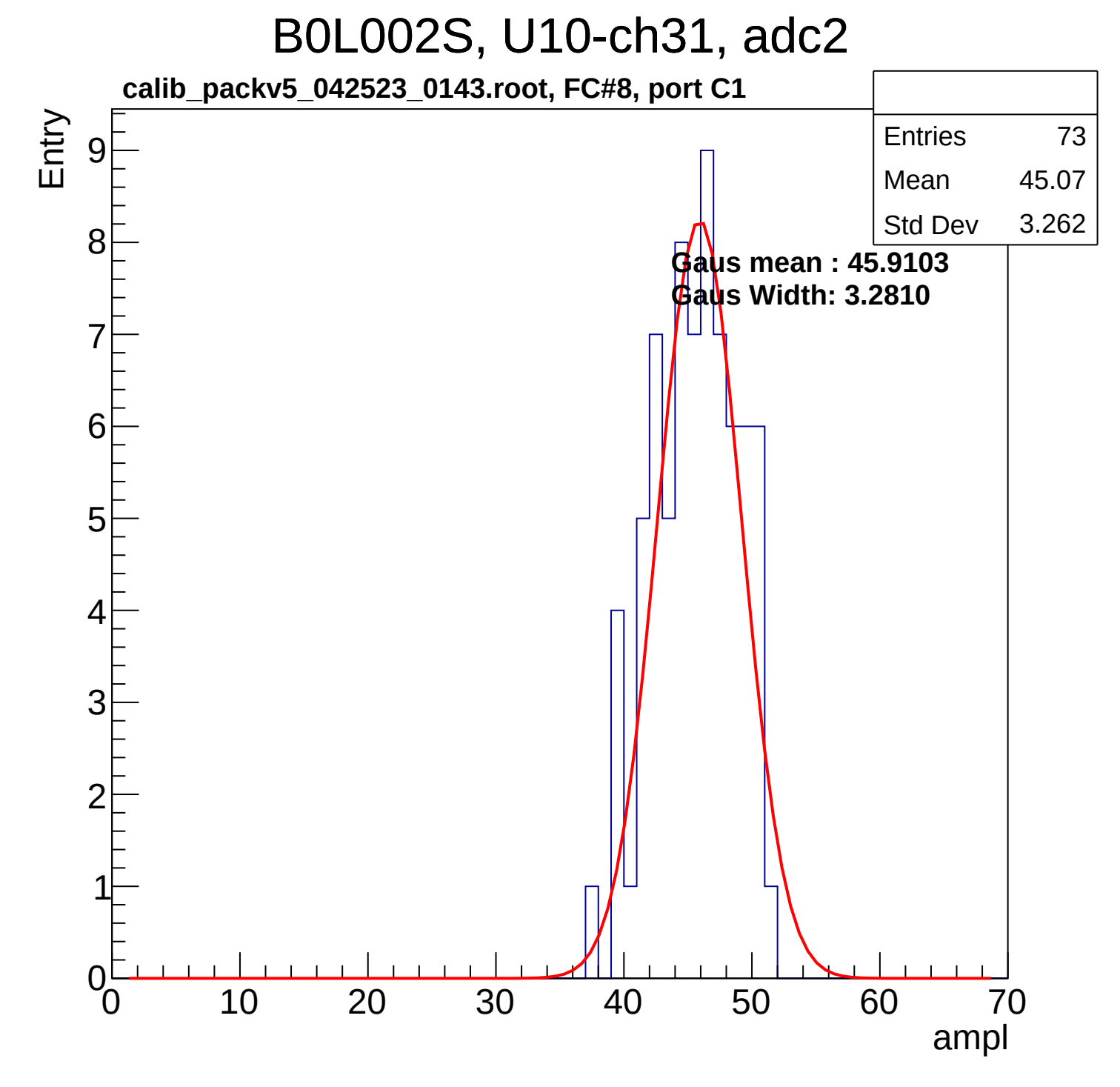

B0L002S, U10-ch30, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.52
Std Dev 1.468
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

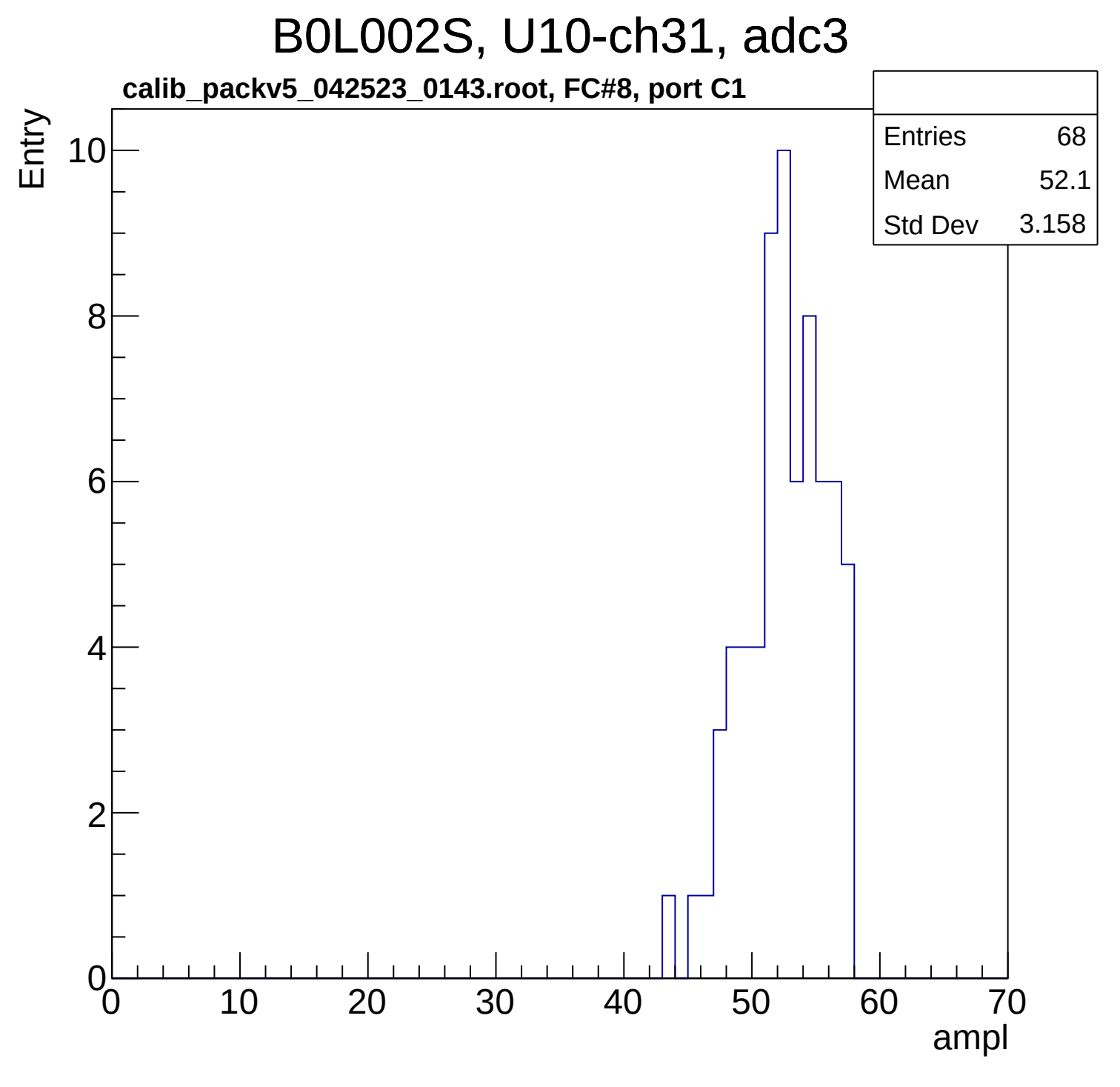

B0L002S, U10-ch30, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

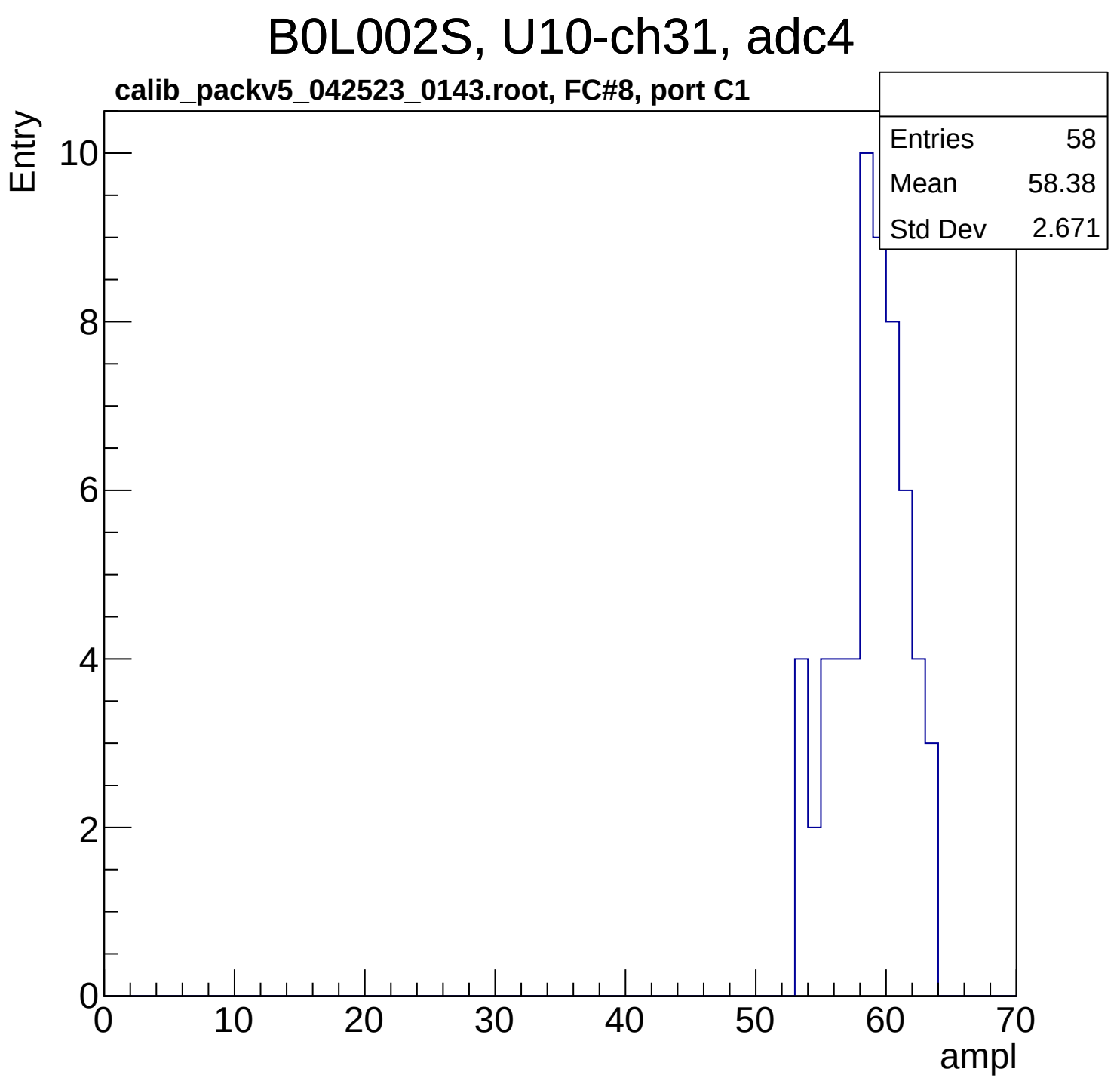

B0L002S, U10-ch30, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

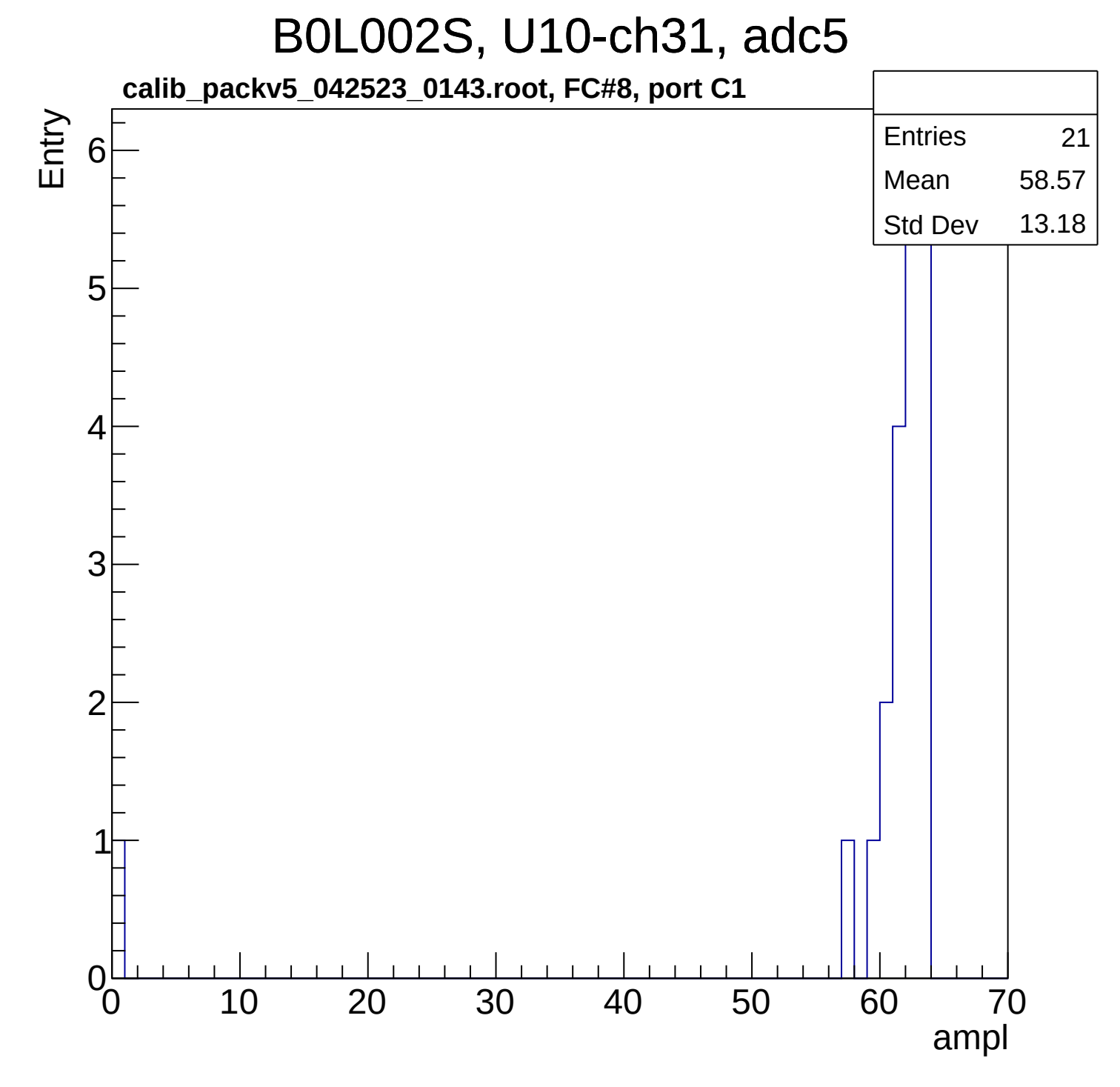

B0L002S, U10-ch31, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 31.21
Std Dev 3.276
Gaus mean : 31.8129
Gaus Width: 3.4667
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

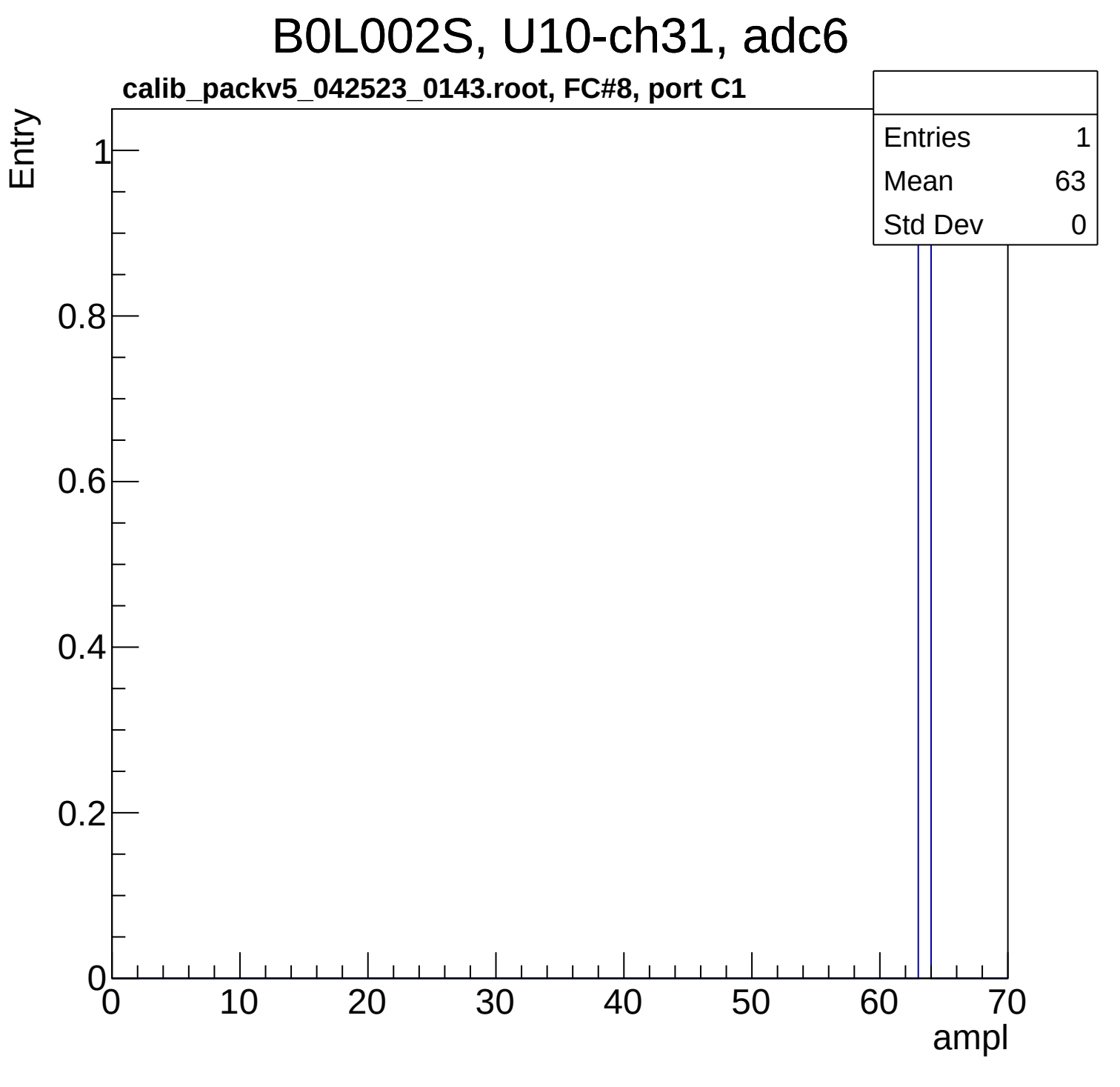

B0L002S, U10-ch31, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.24
Std Dev 3.306
Gaus mean : 38.9647
Gaus Width: 3.5221
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

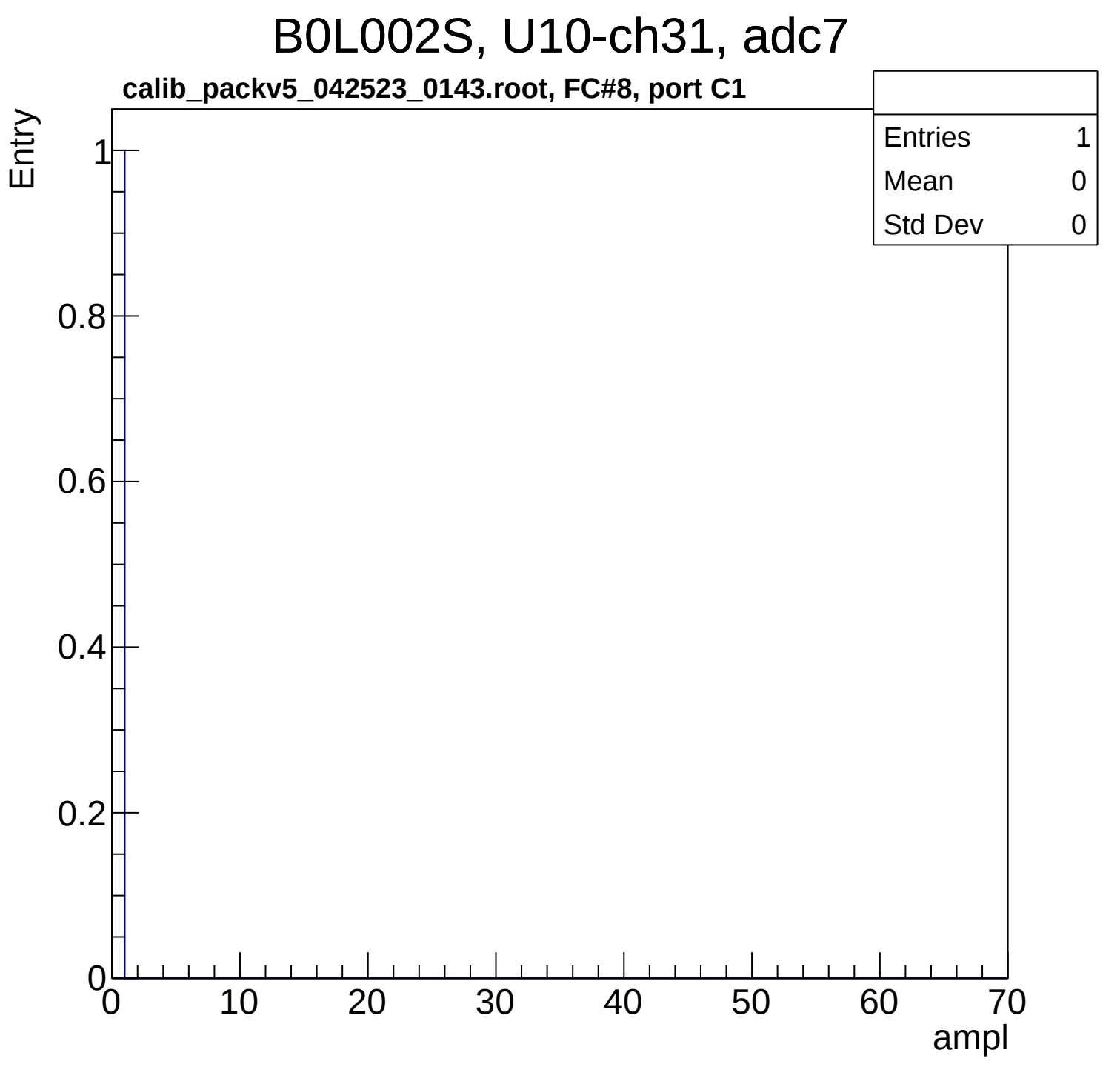

B0L002S, U10-ch31, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 45.07
Std Dev 3.262
Gaus mean : 45.9103
Gaus Width: 3.2810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

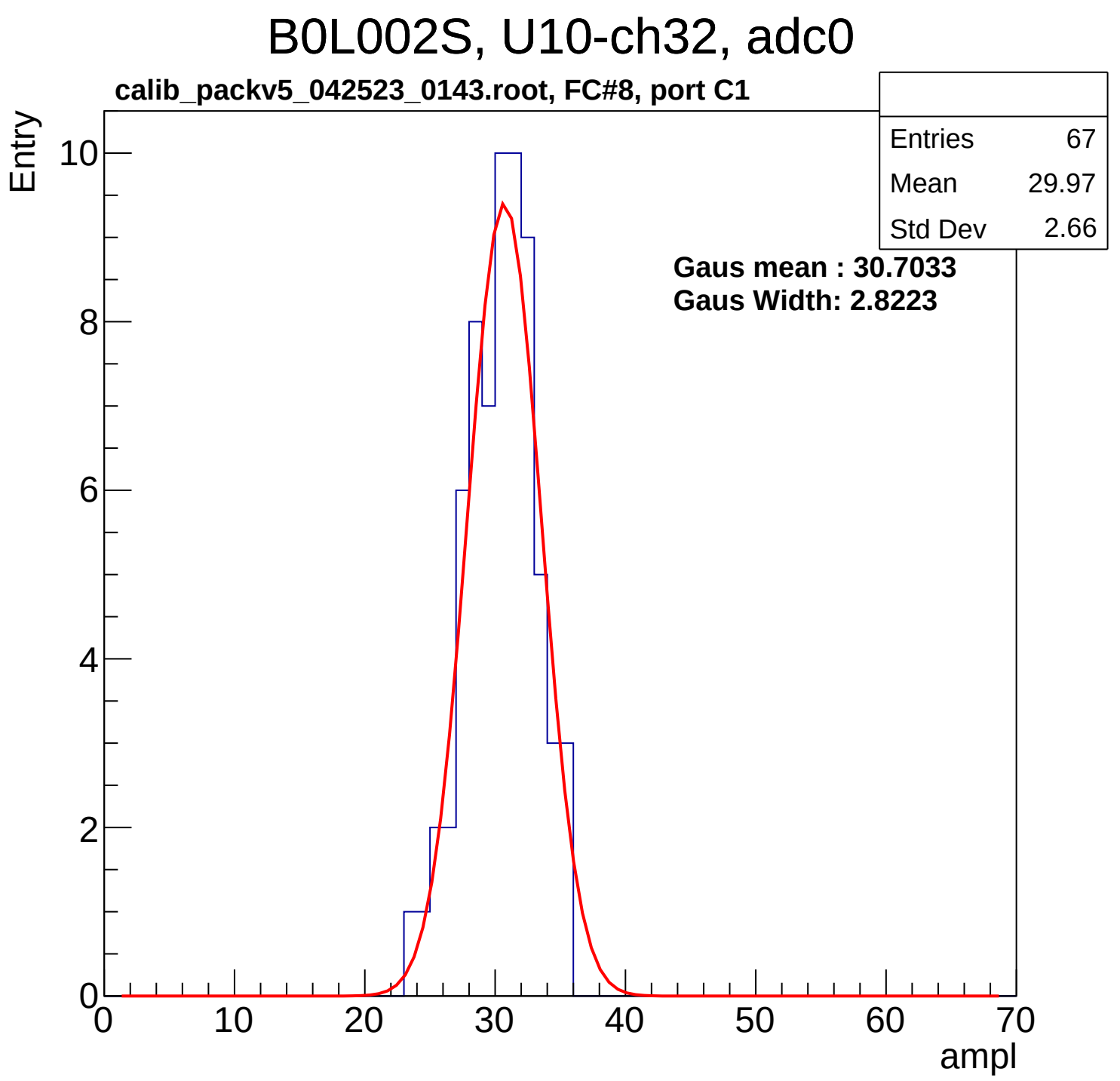

B0L002S, U10-ch31, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 52.1
Std Dev 3.158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

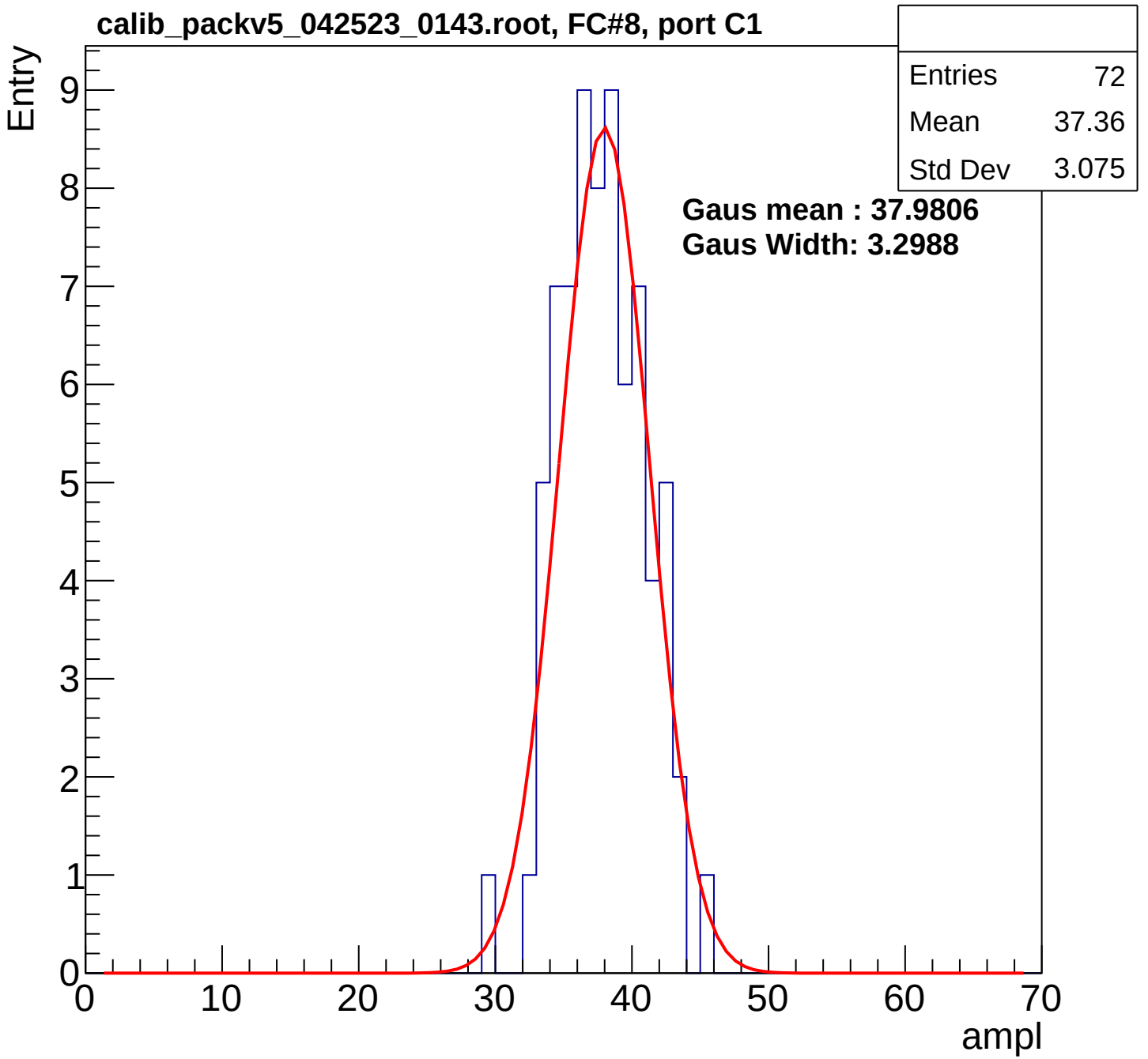

B0L002S, U10-ch31, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 58.38
Std Dev 2.671
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

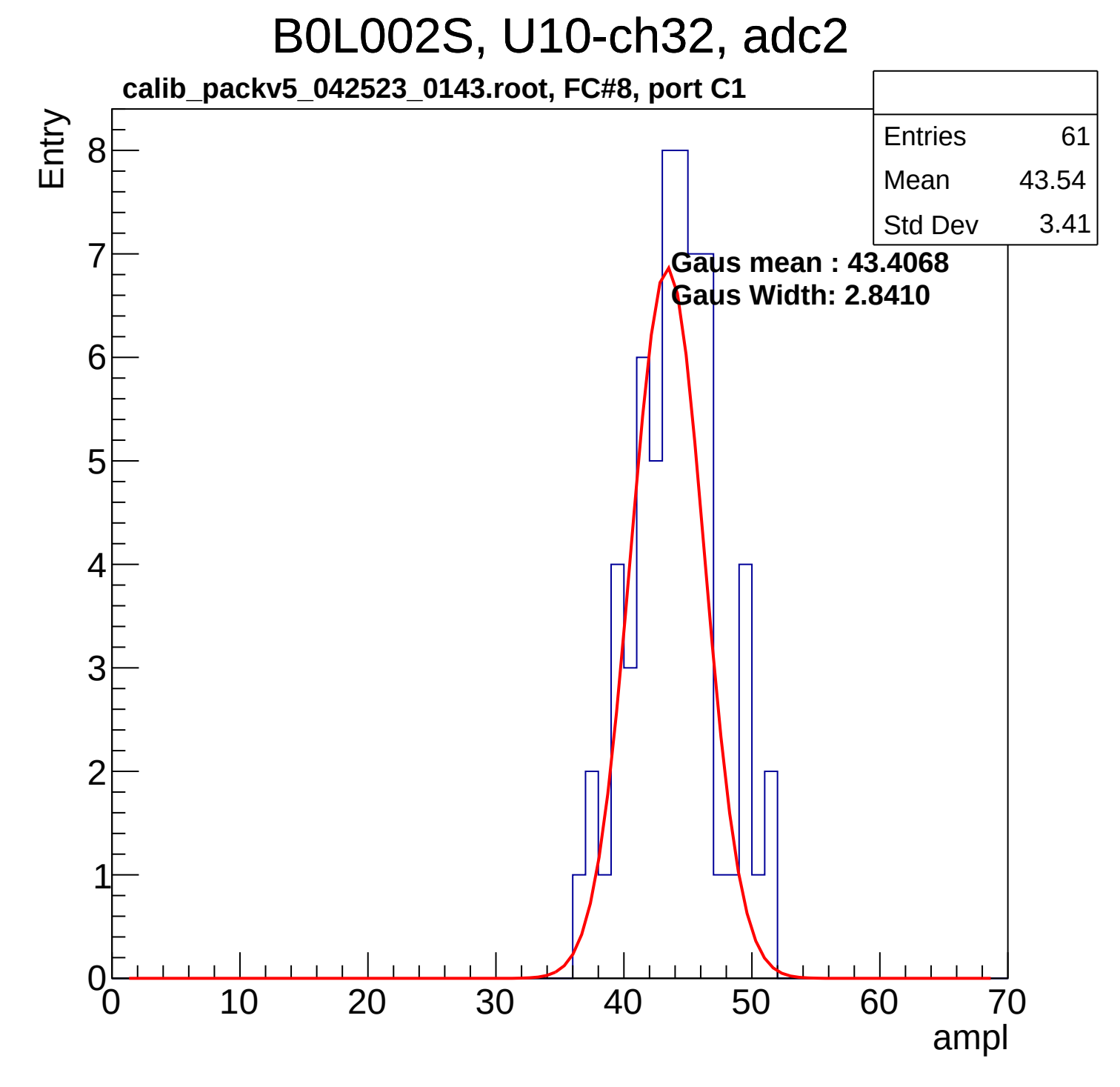

B0L002S, U10-ch31, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.57
Std Dev 13.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

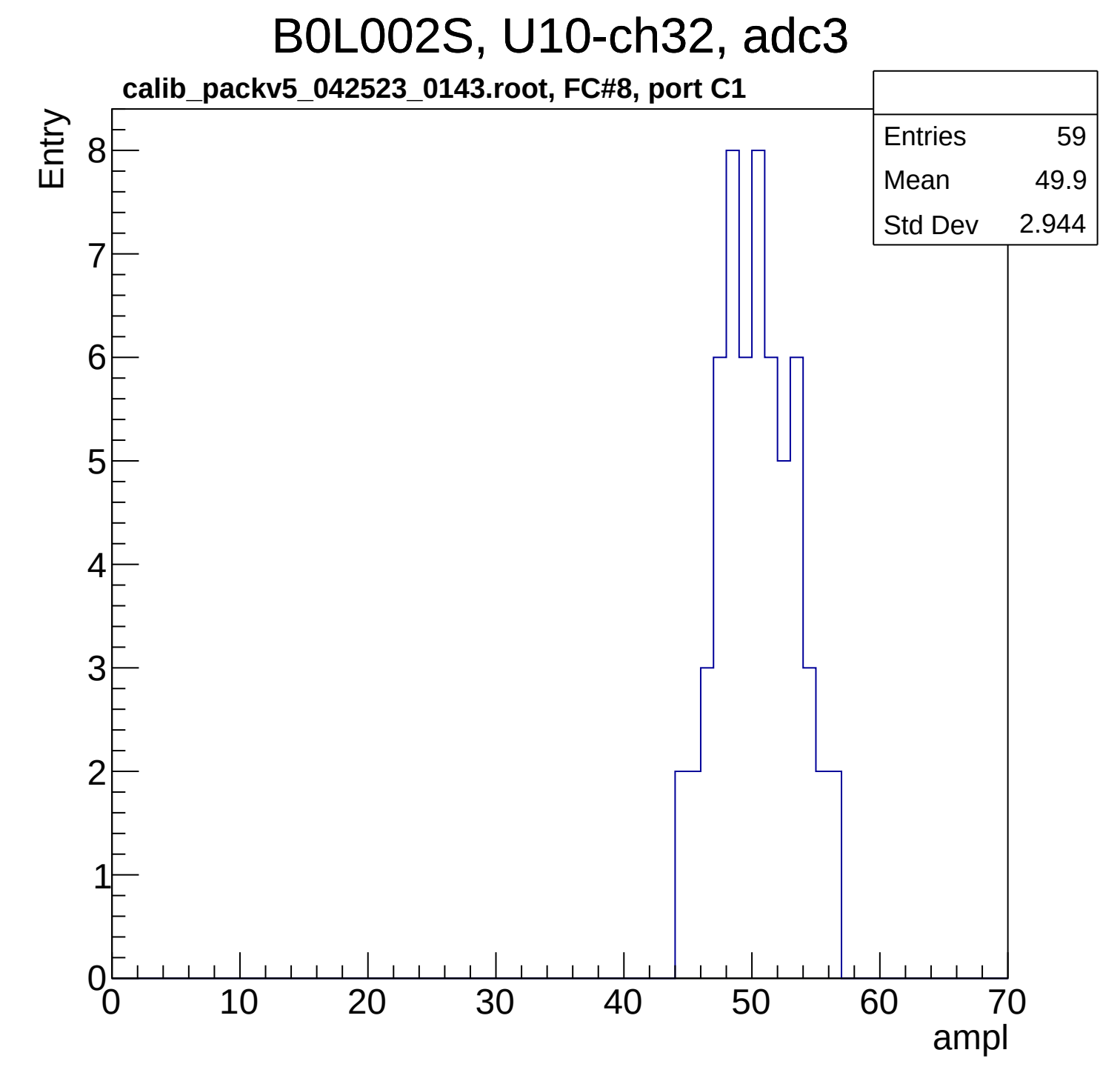

B0L002S, U10-ch31, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

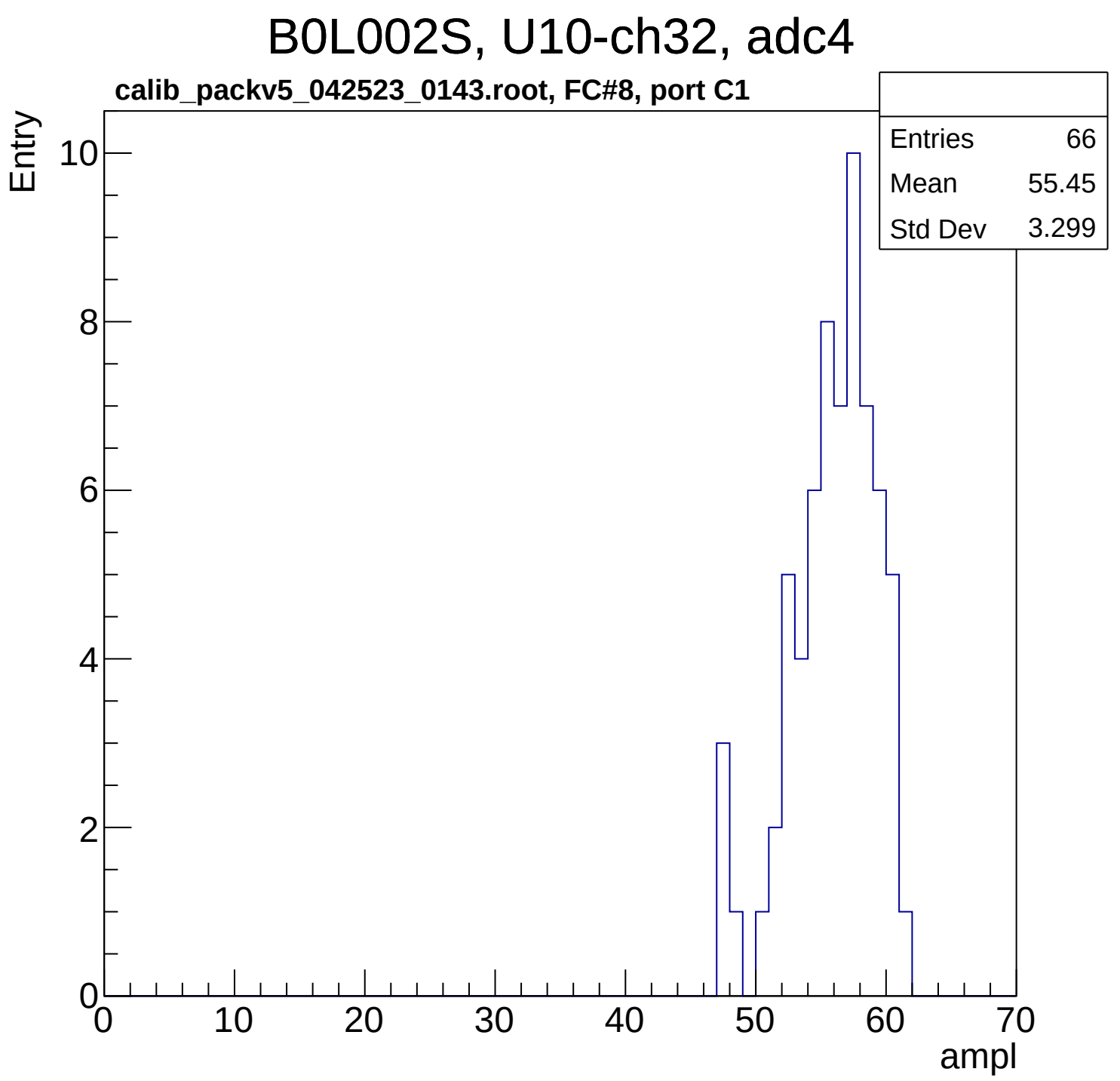

B0L002S, U10-ch31, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

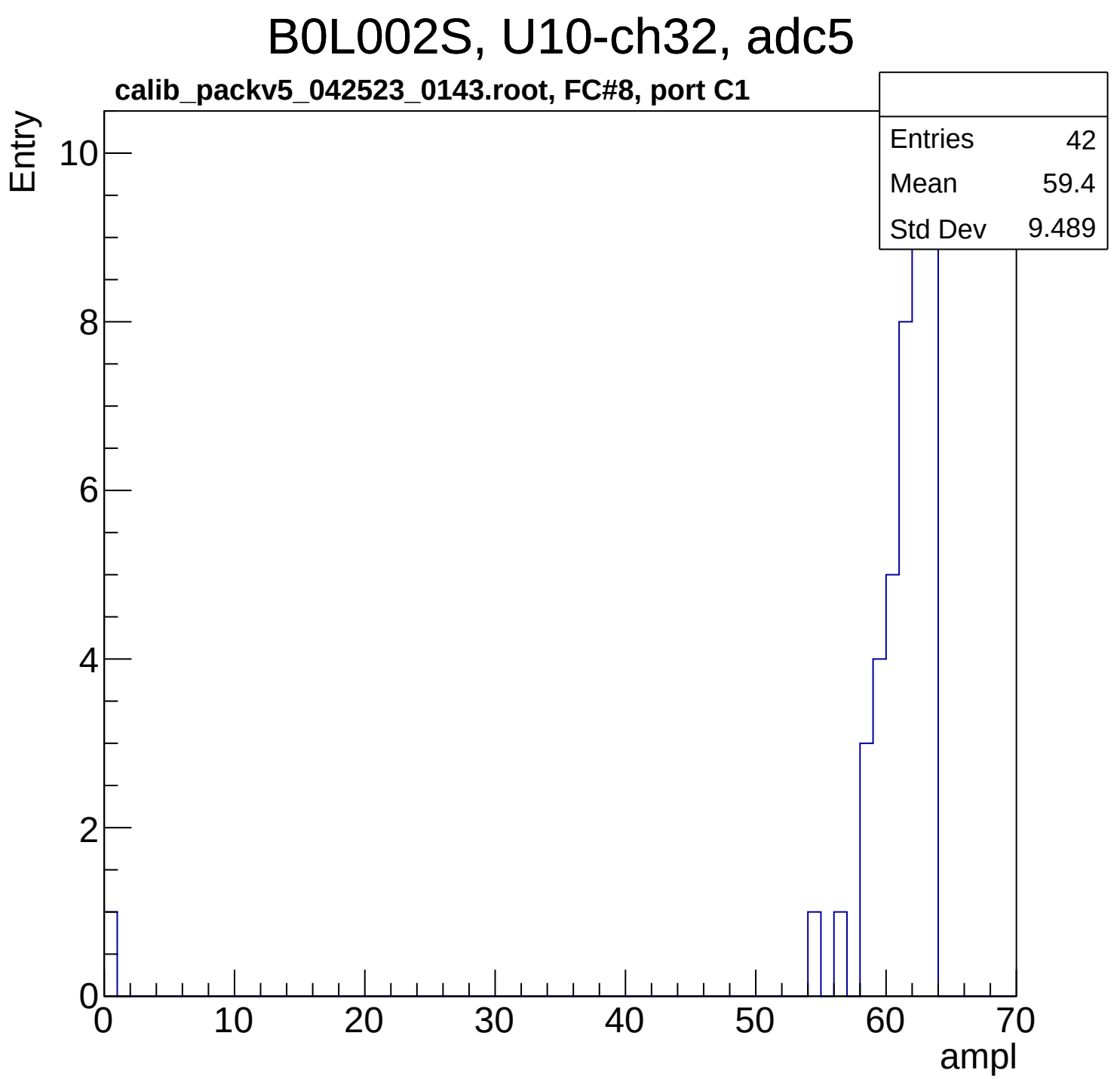

B0L002S, U10-ch32, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 29.97
Std Dev 2.66
Gaus mean : 30.7033
Gaus Width: 2.8223
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

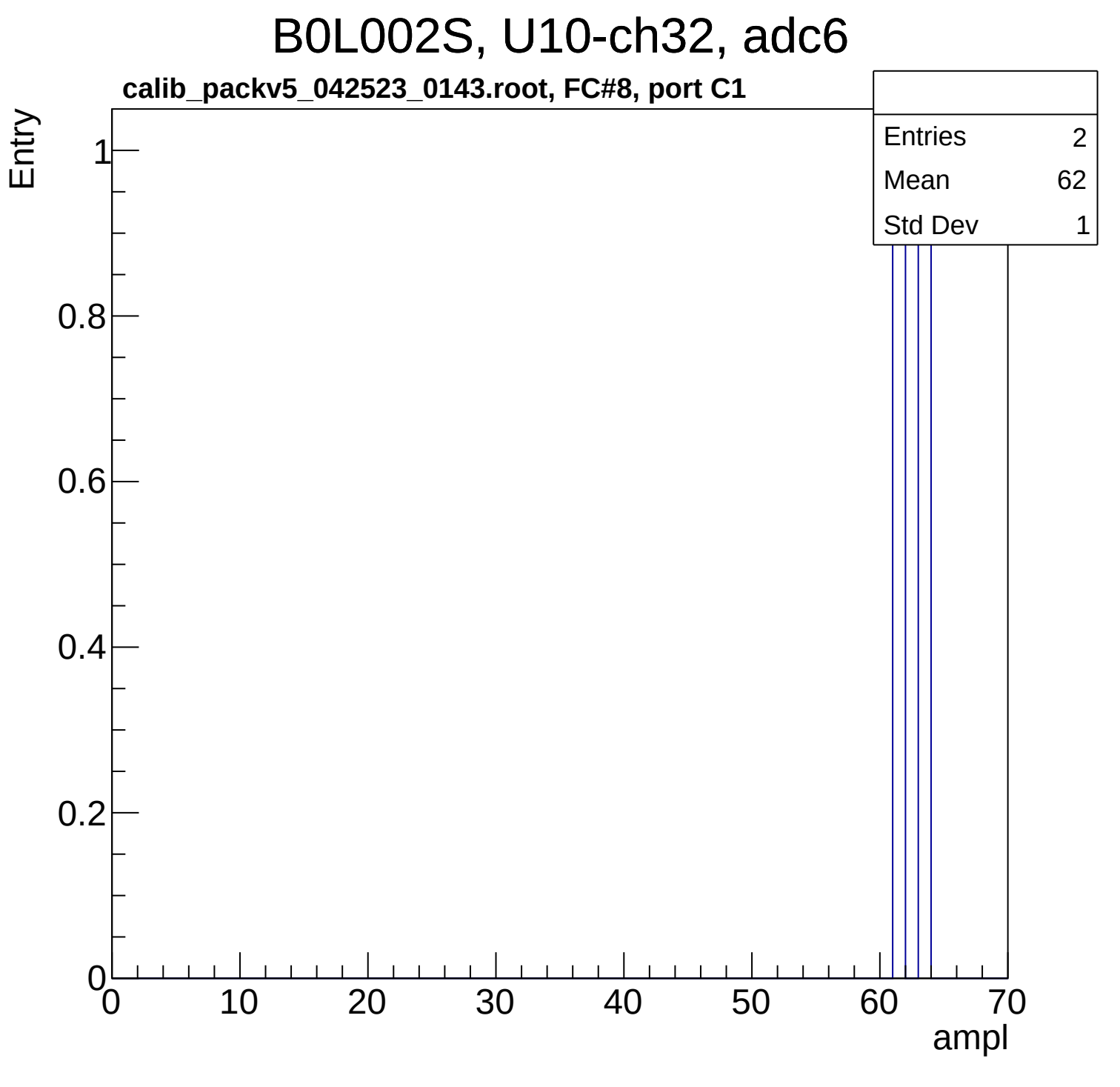

B0L002S, U10-ch32, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.36
Std Dev 3.075
Gaus mean : 37.9806
Gaus Width: 3.2988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

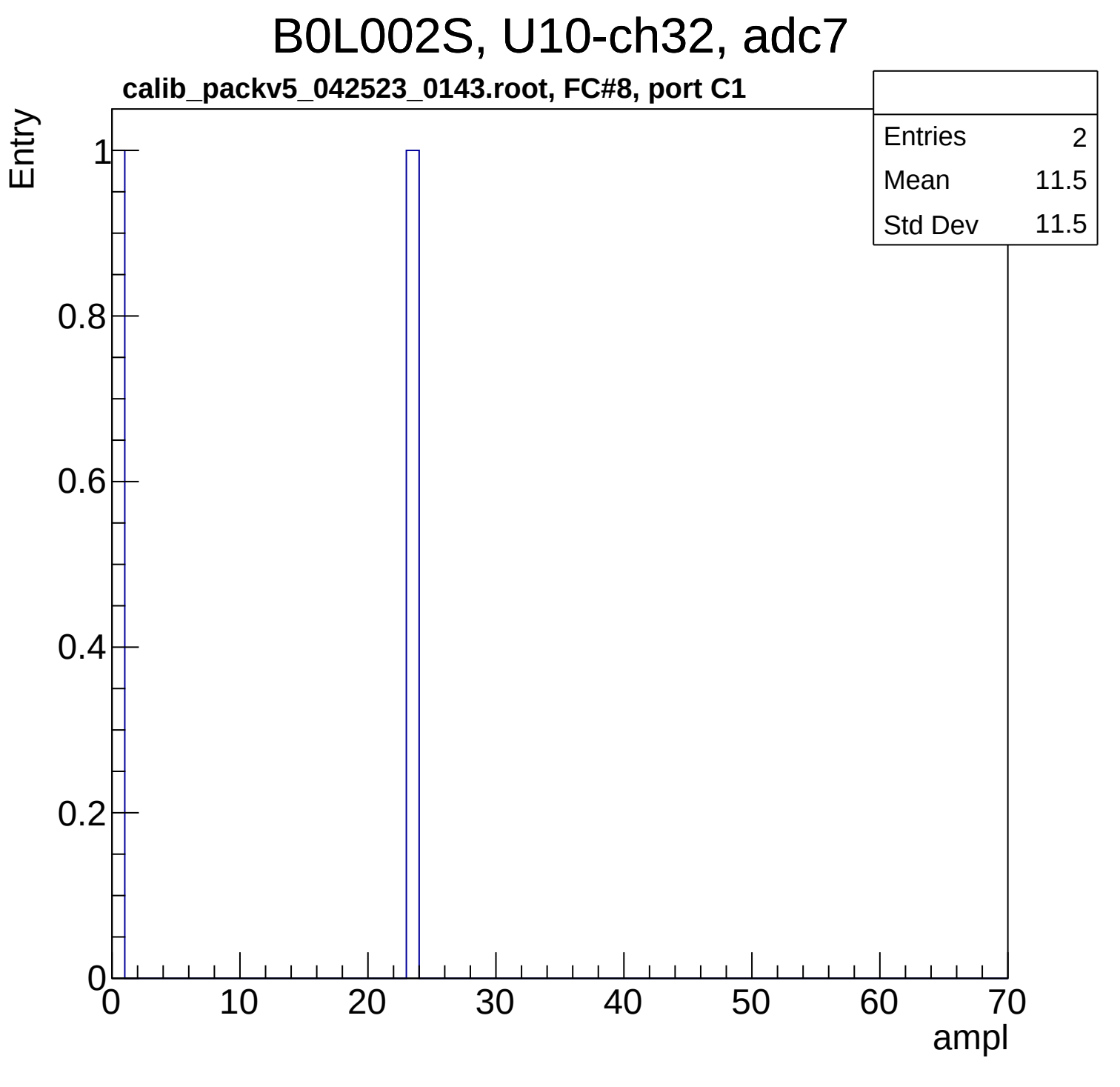

B0L002S, U10-ch32, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.54
Std Dev 3.41
Gaus mean : 43.4068
Gaus Width: 2.8410
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

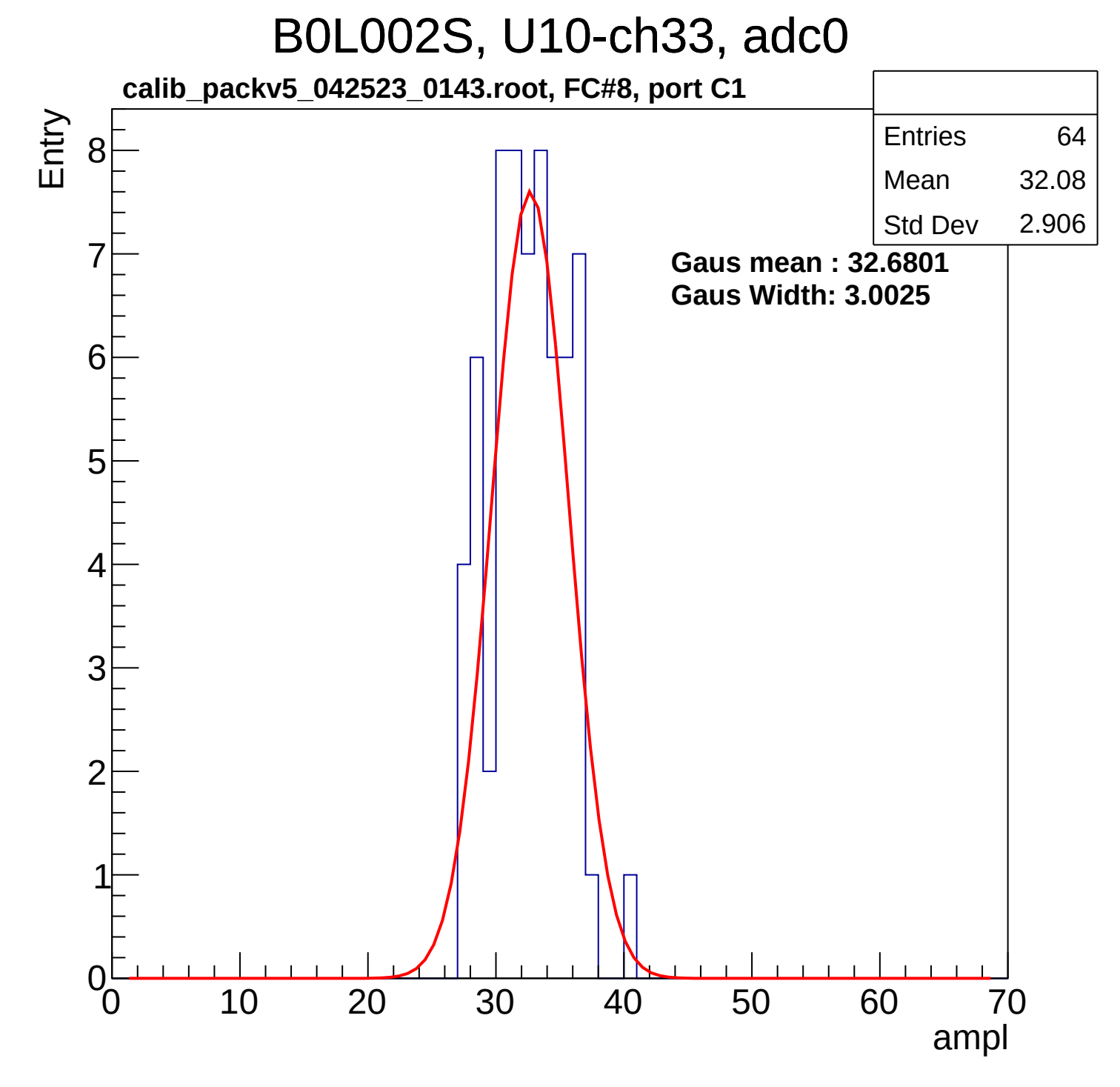

B0L002S, U10-ch32, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 49.9
Std Dev 2.944
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

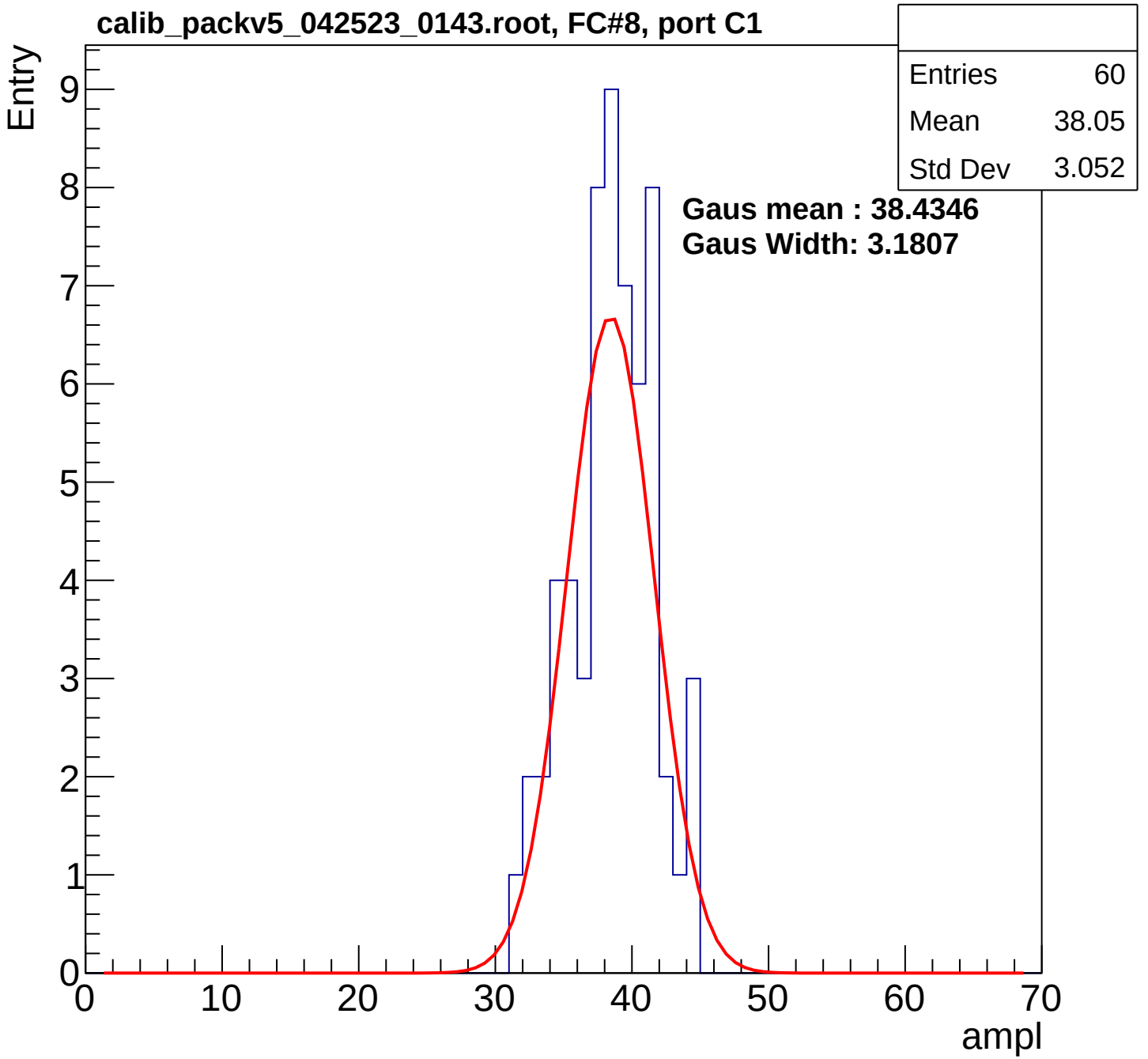

B0L002S, U10-ch32, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 55.45
Std Dev 3.299
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

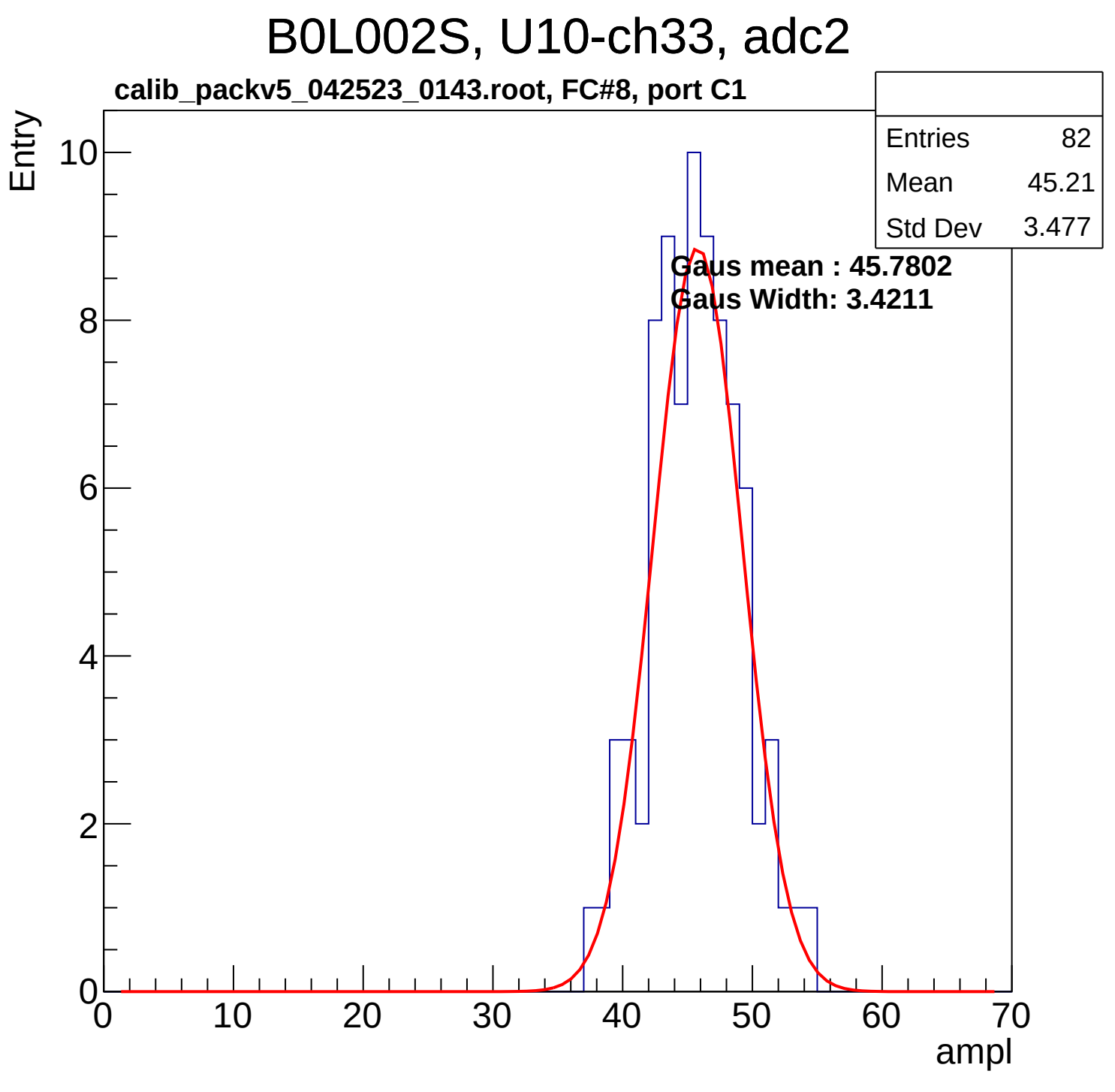

B0L002S, U10-ch32, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 59.4
Std Dev 9.489
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

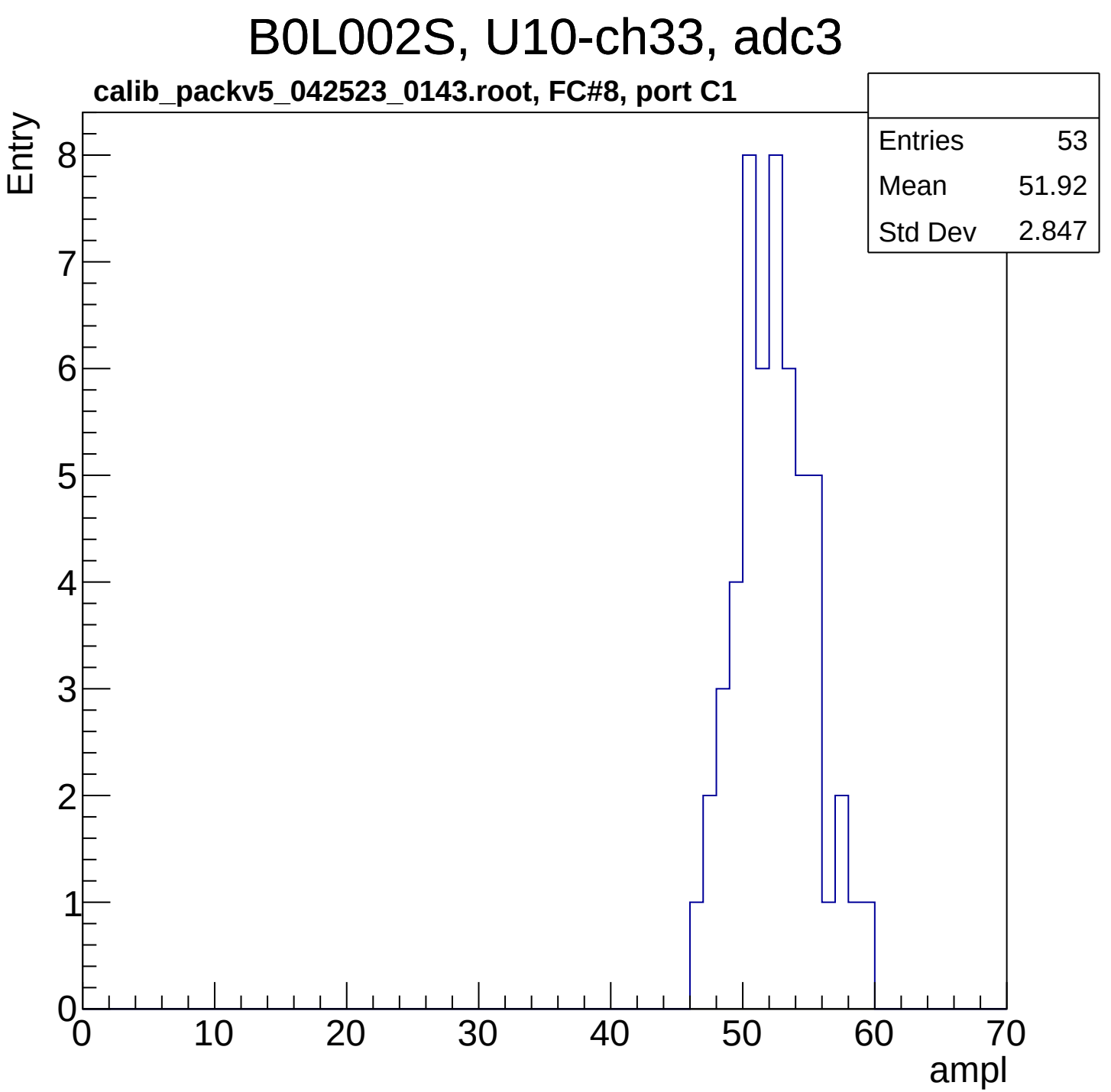

B0L002S, U10-ch32, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

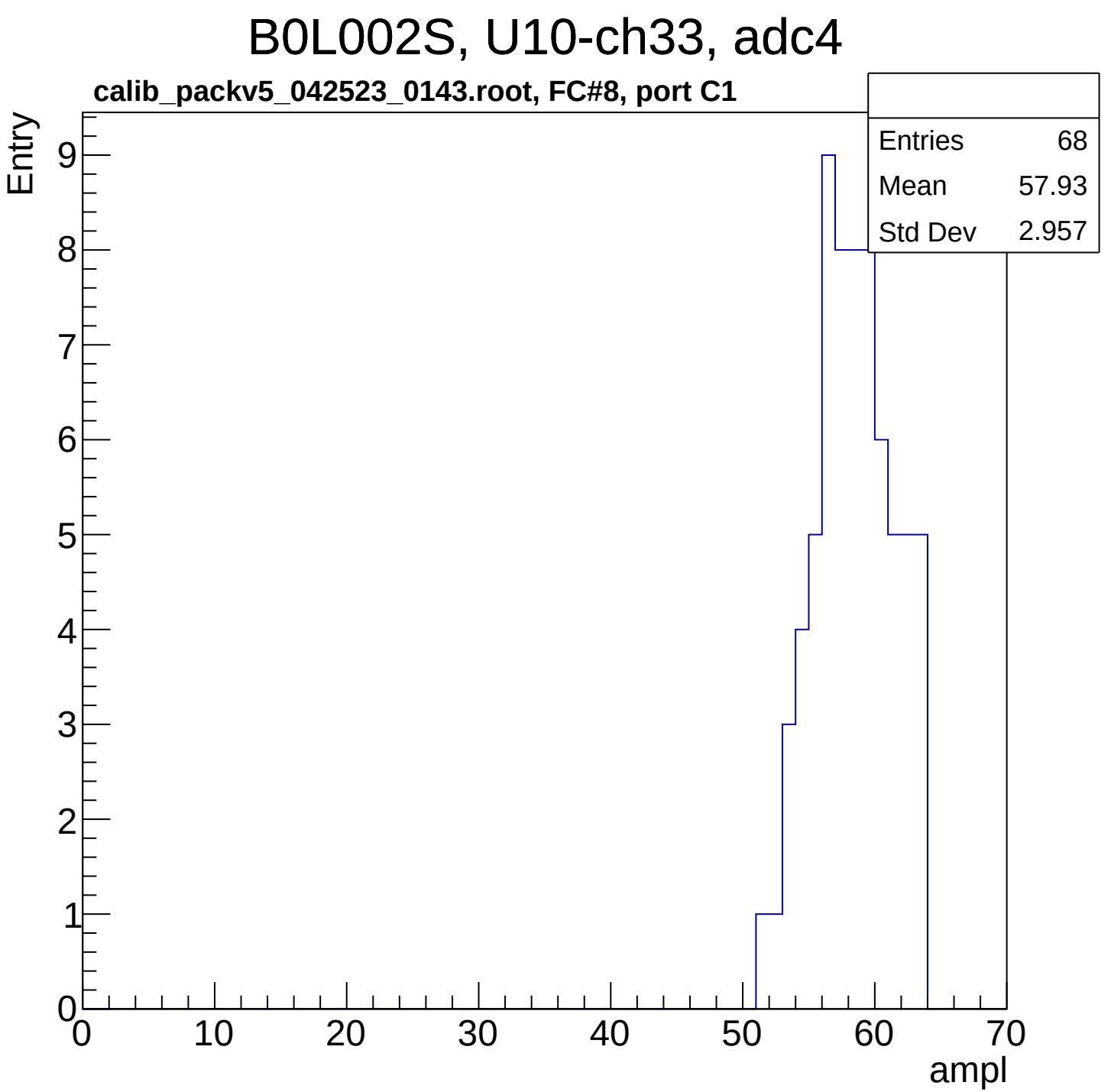

B0L002S, U10-ch32, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

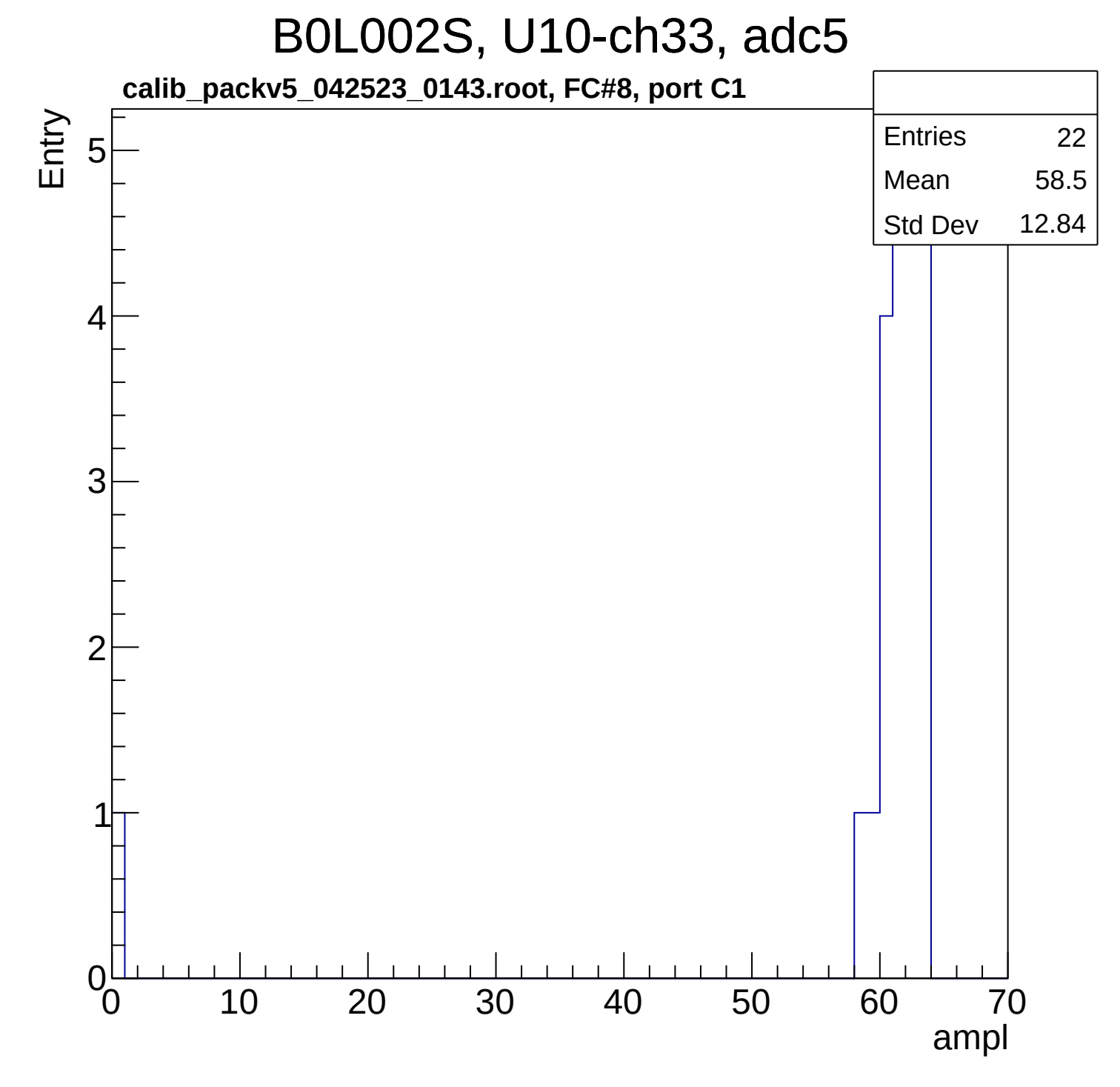

B0L002S, U10-ch33, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 32.08
Std Dev 2.906
Gaus mean : 32.6801
Gaus Width: 3.0025
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

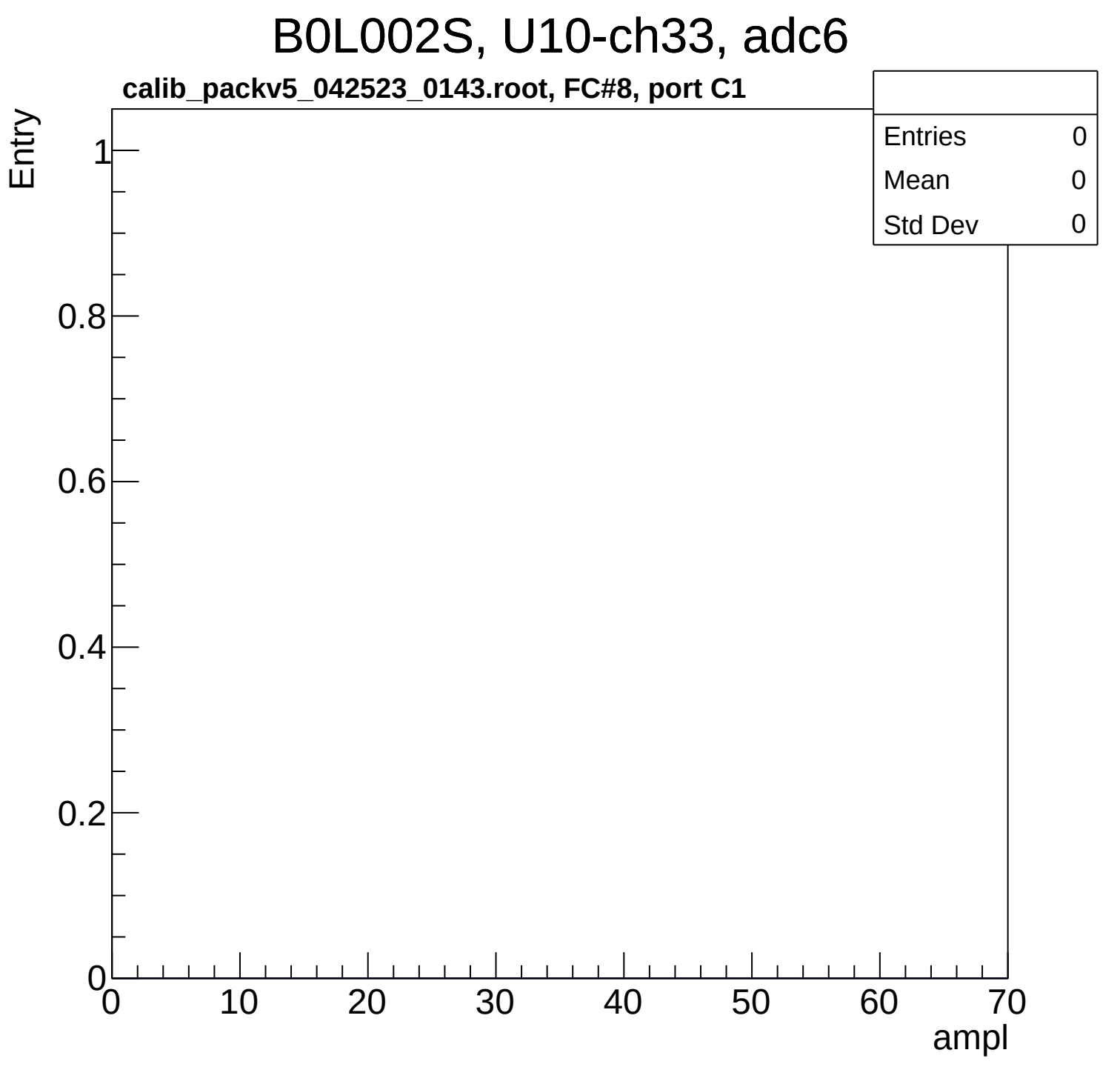

B0L002S, U10-ch33, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.05
Std Dev 3.052
Gaus mean : 38.4346
Gaus Width: 3.1807
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

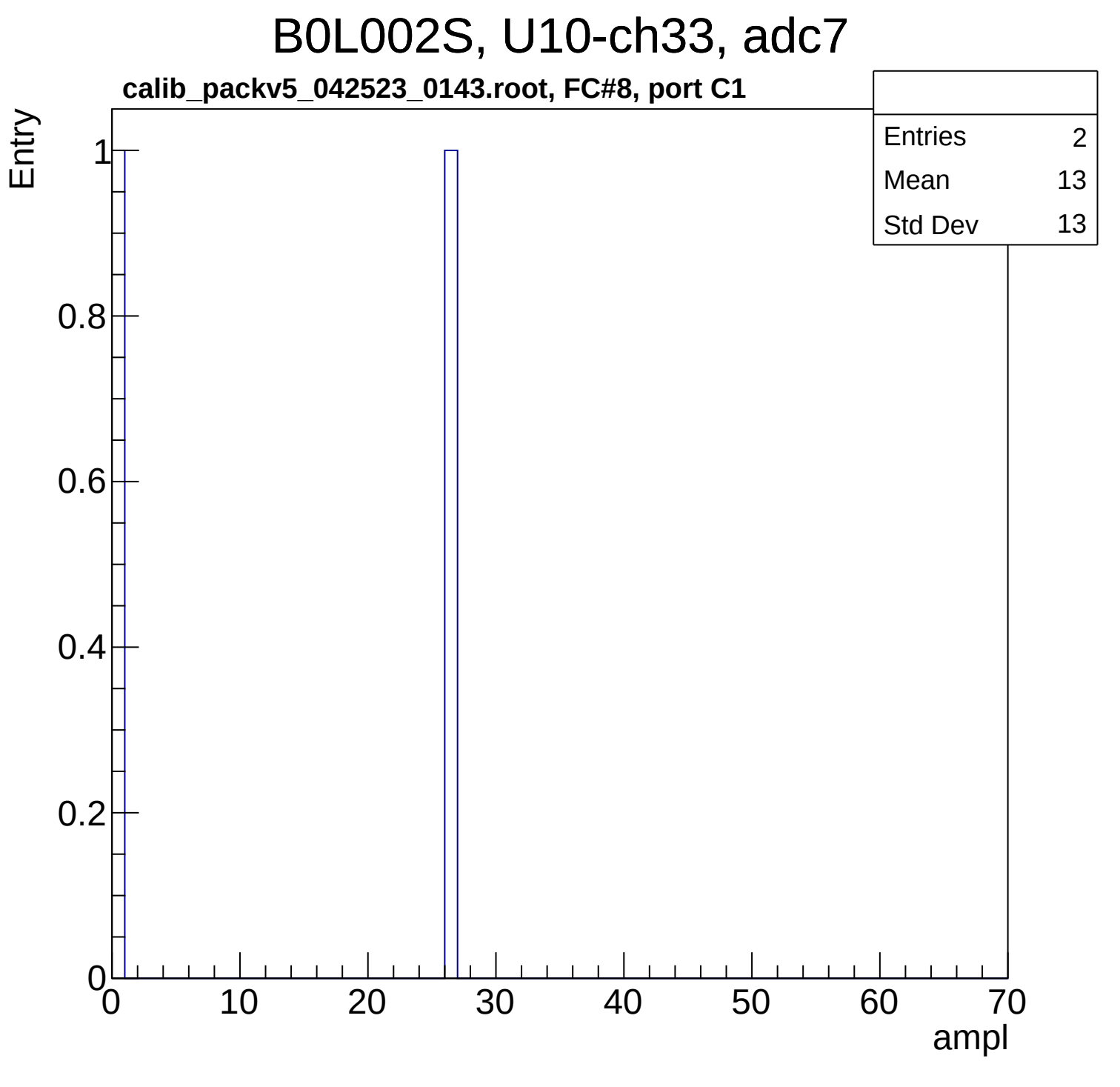

B0L002S, U10-ch33, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 45.21
Std Dev 3.477
Gaus mean : 45.7802
Gaus Width: 3.4211
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

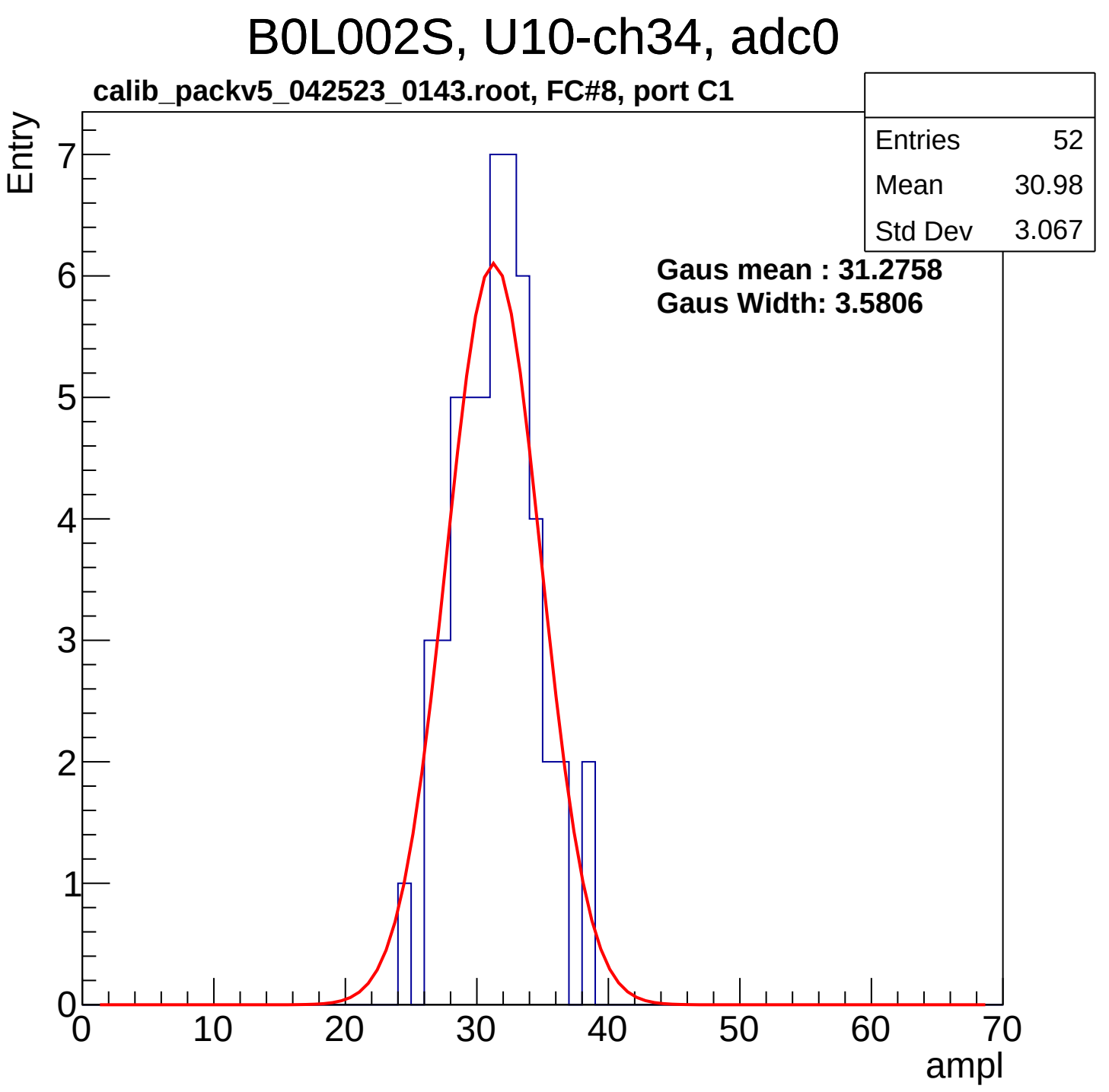

B0L002S, U10-ch33, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.92
Std Dev 2.847
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

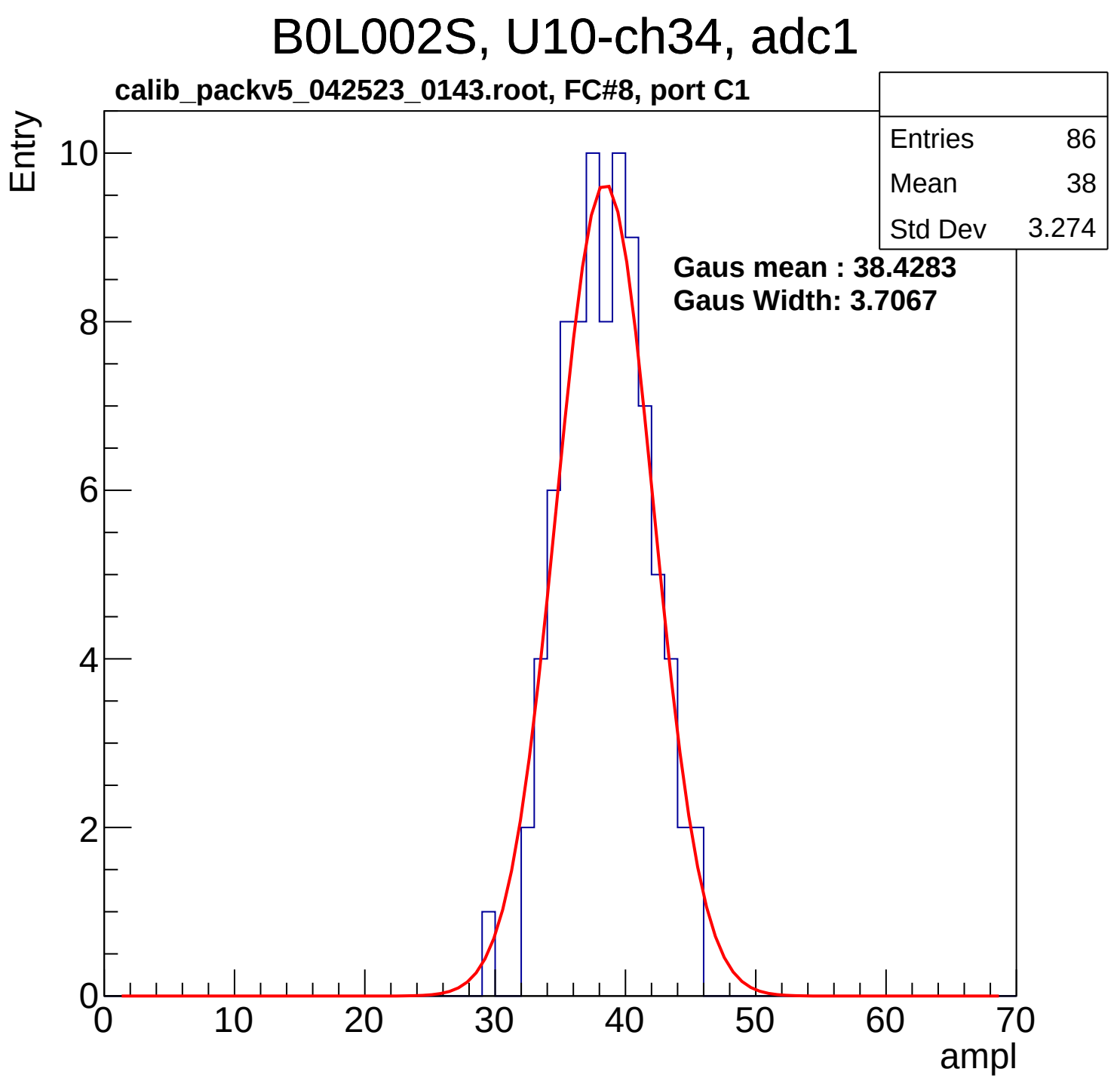

B0L002S, U10-ch33, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 57.93
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

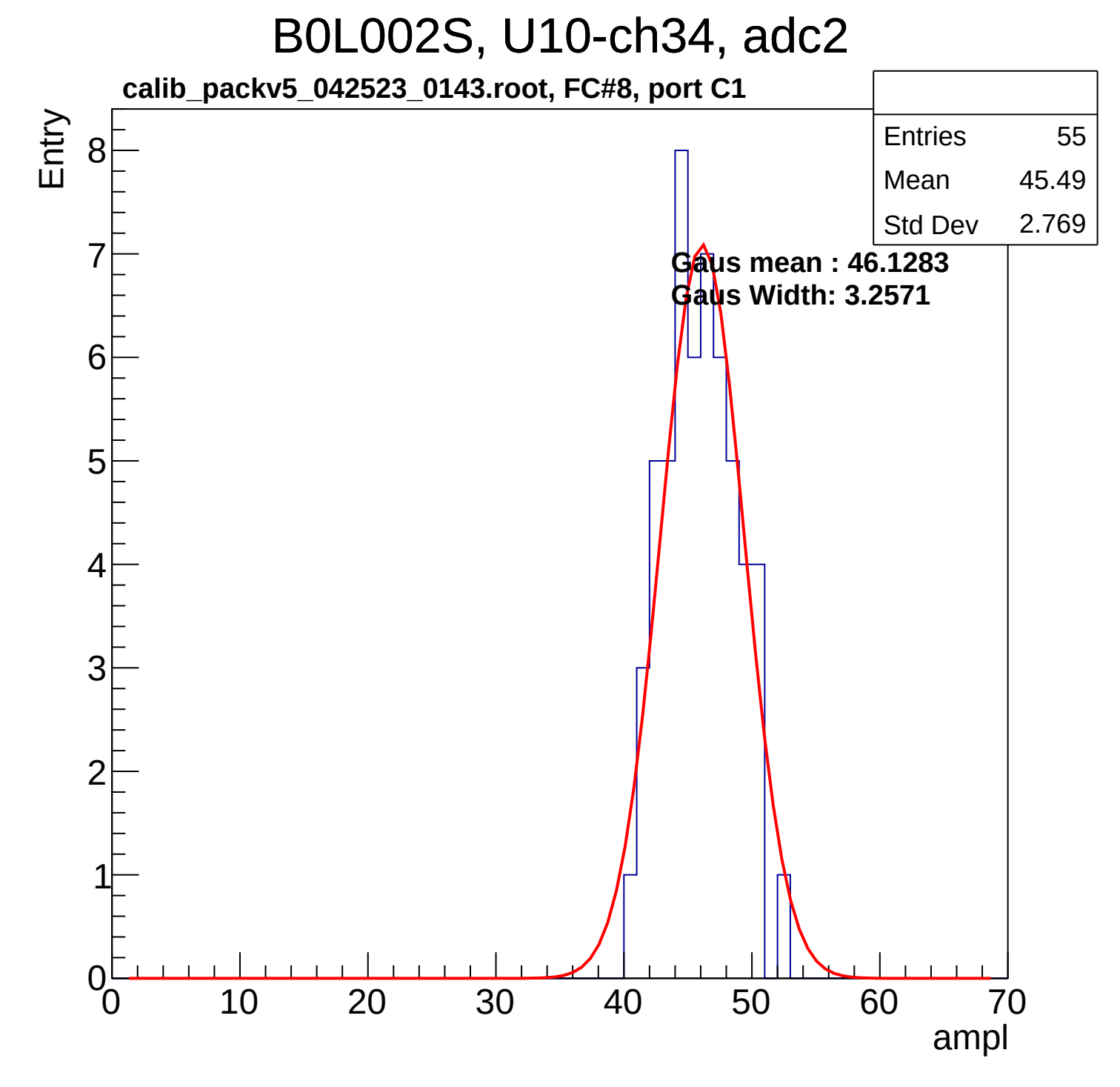

B0L002S, U10-ch33, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 58.5
Std Dev 12.84
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

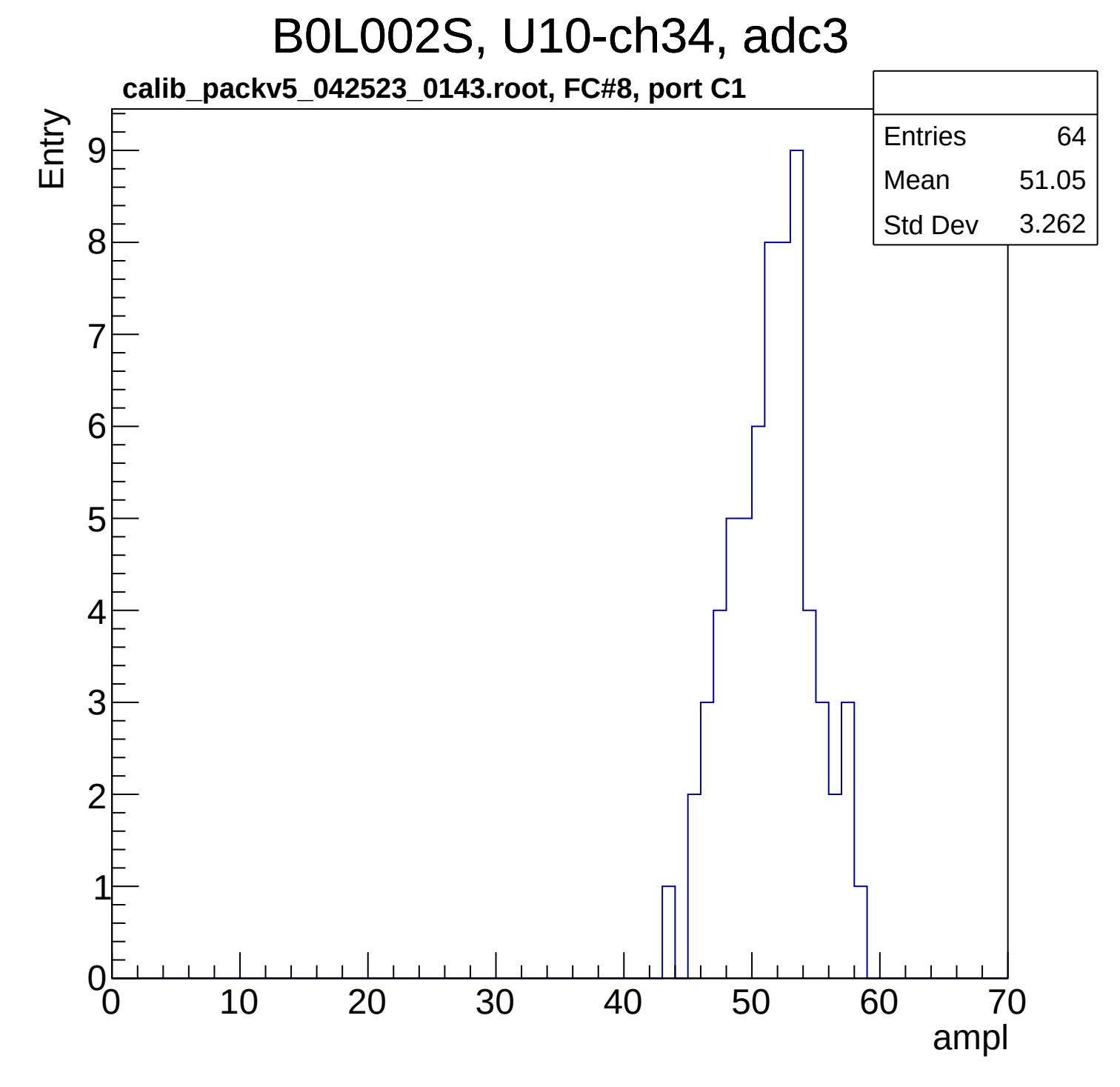

B0L002S, U10-ch33, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

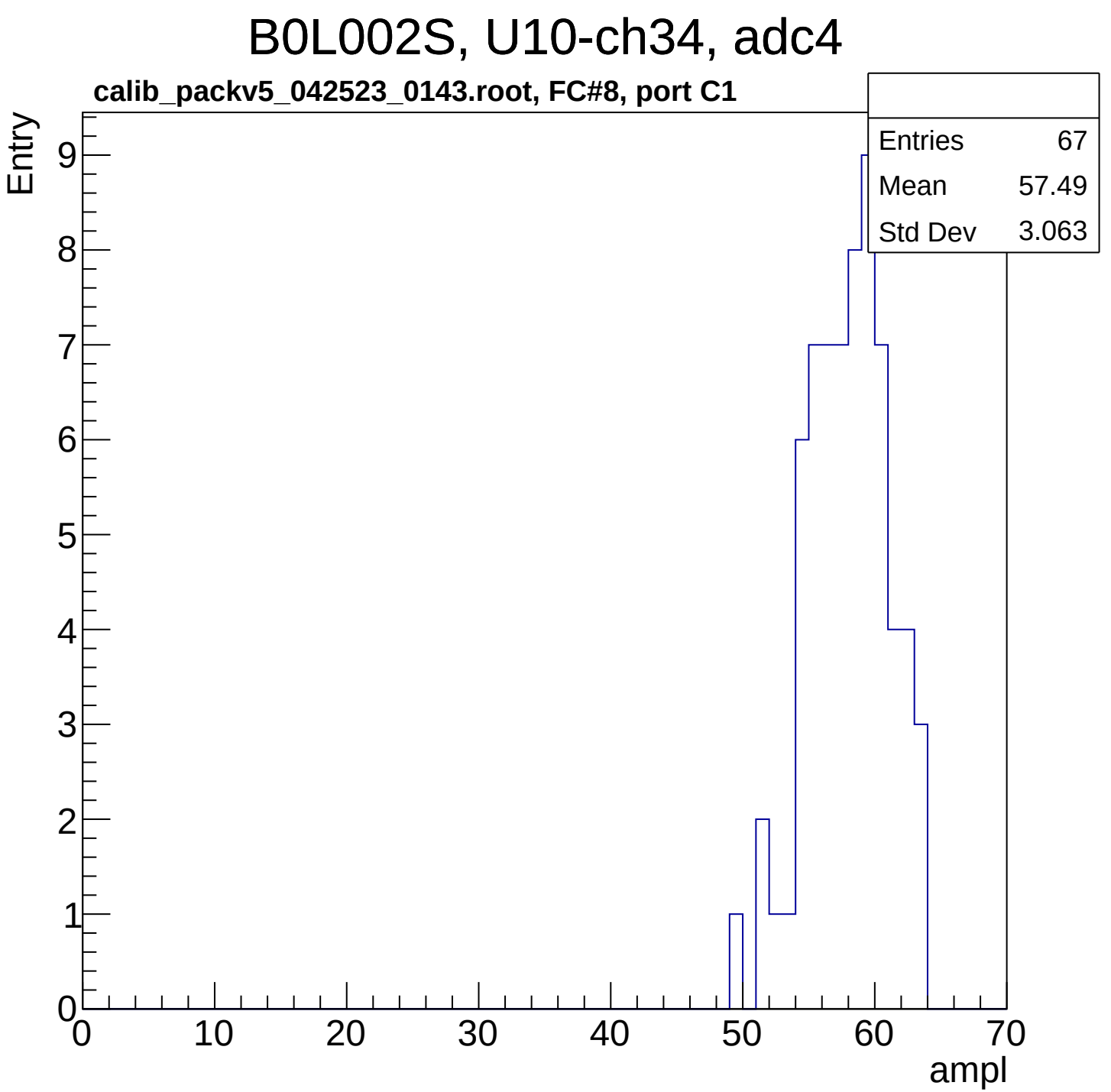

B0L002S, U10-ch33, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

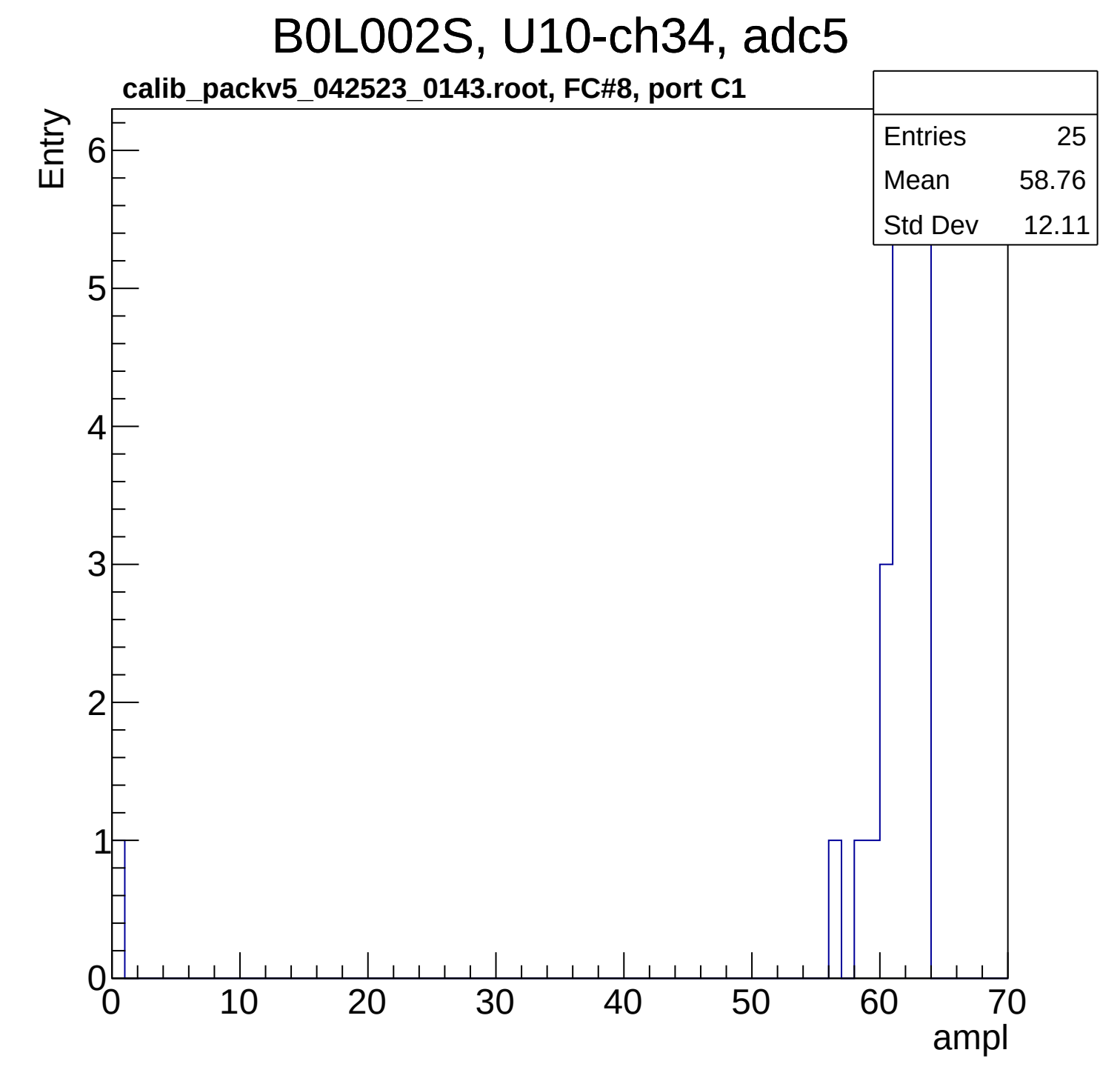

B0L002S, U10-ch34, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 30.98
Std Dev 3.067
Gaus mean : 31.2758
Gaus Width: 3.5806
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

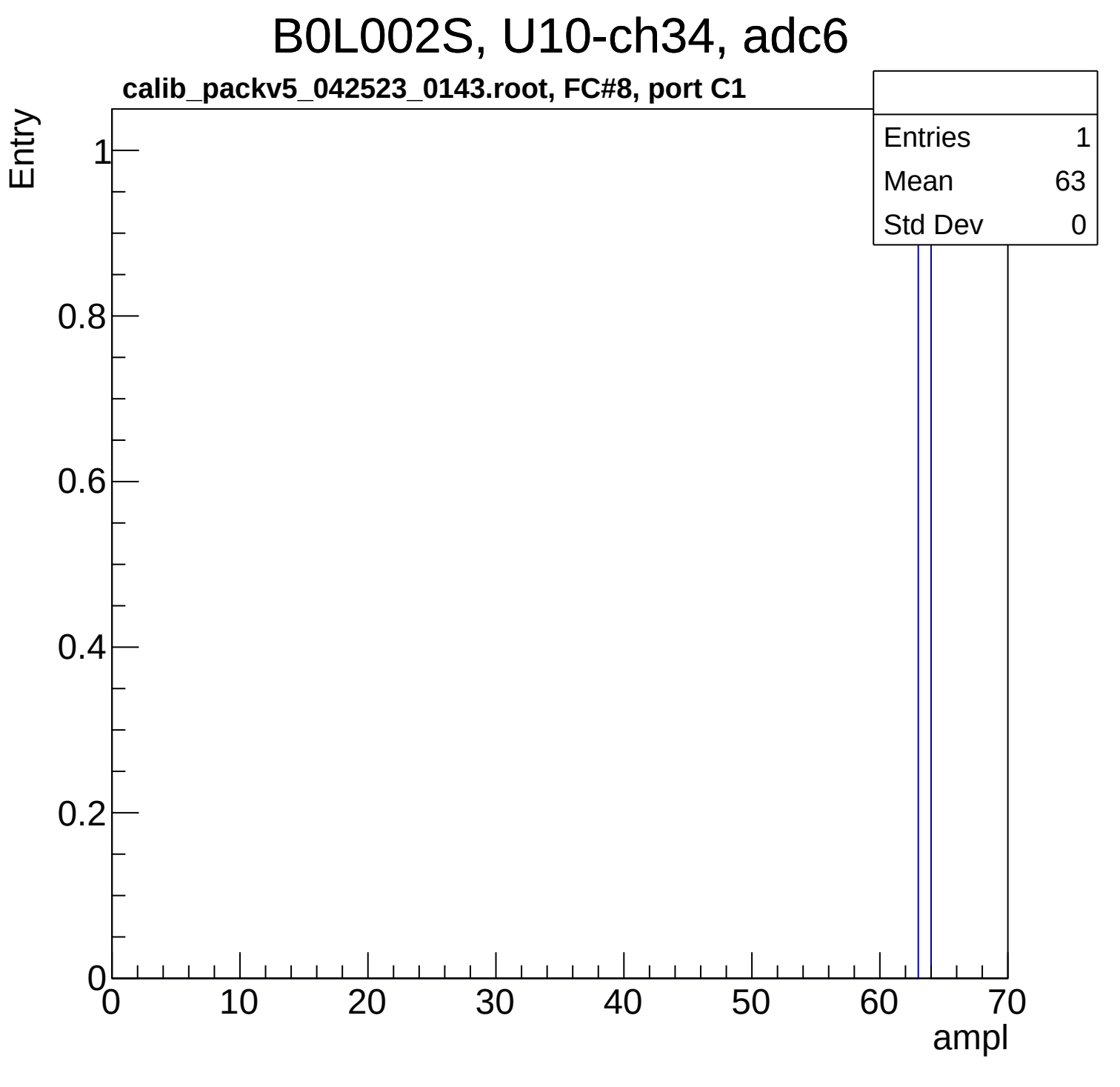

B0L002S, U10-ch34, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 38
Std Dev 3.274
Gaus mean : 38.4283
Gaus Width: 3.7067
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

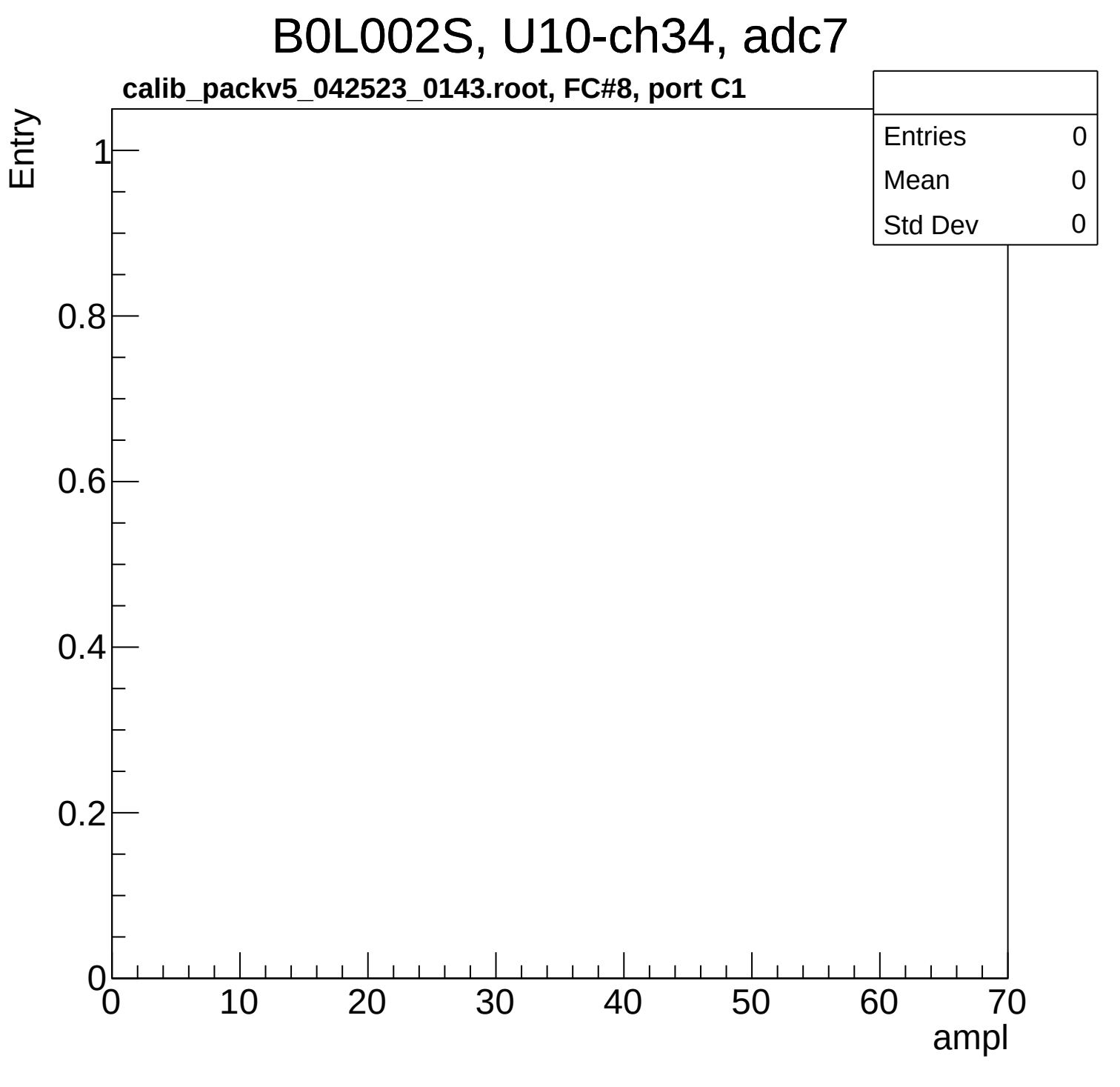

B0L002S, U10-ch34, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 45.49
Std Dev 2.769
Gaus mean : 46.1283
Gaus Width: 3.2571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

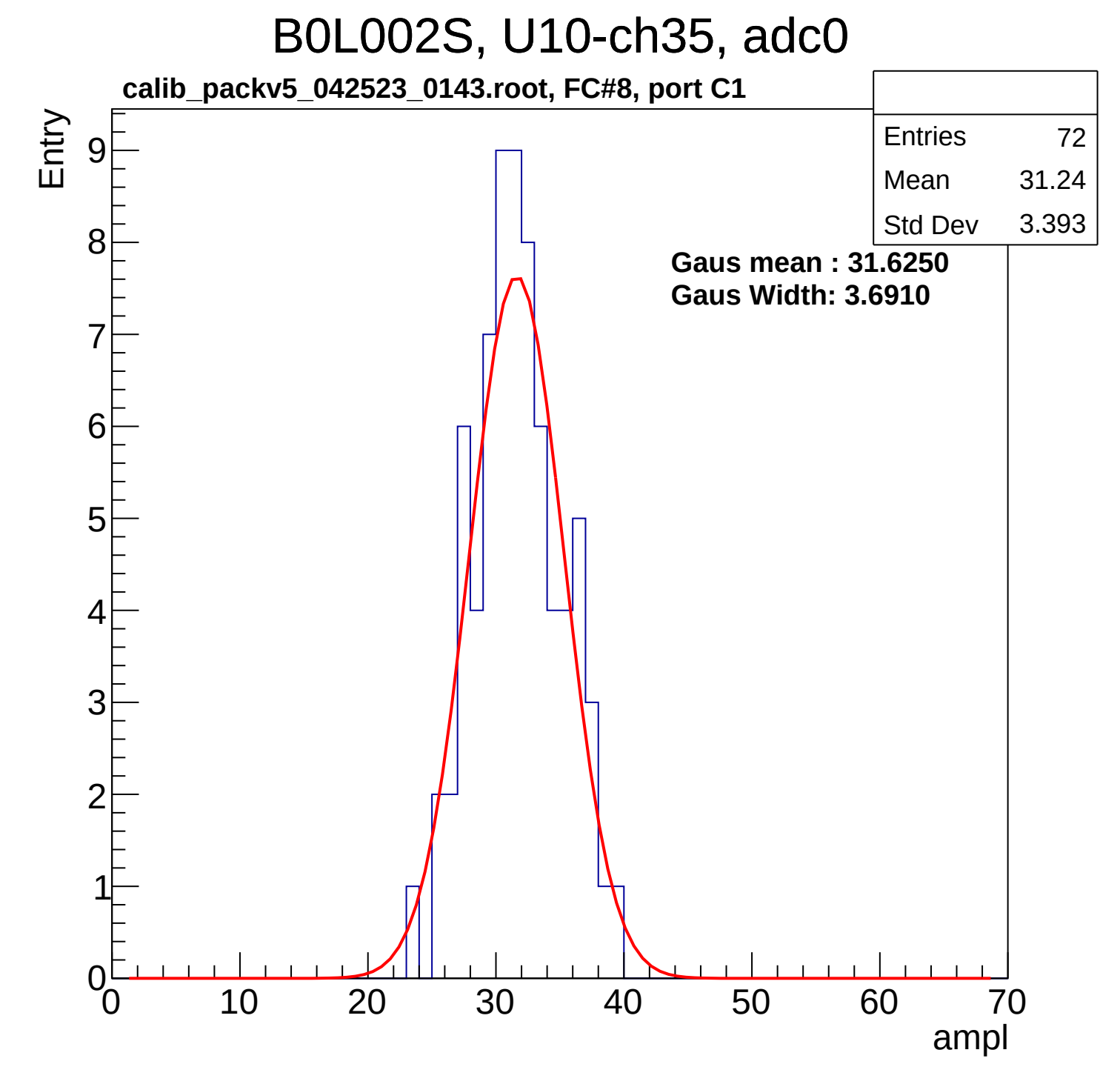

B0L002S, U10-ch34, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.05
Std Dev 3.262
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

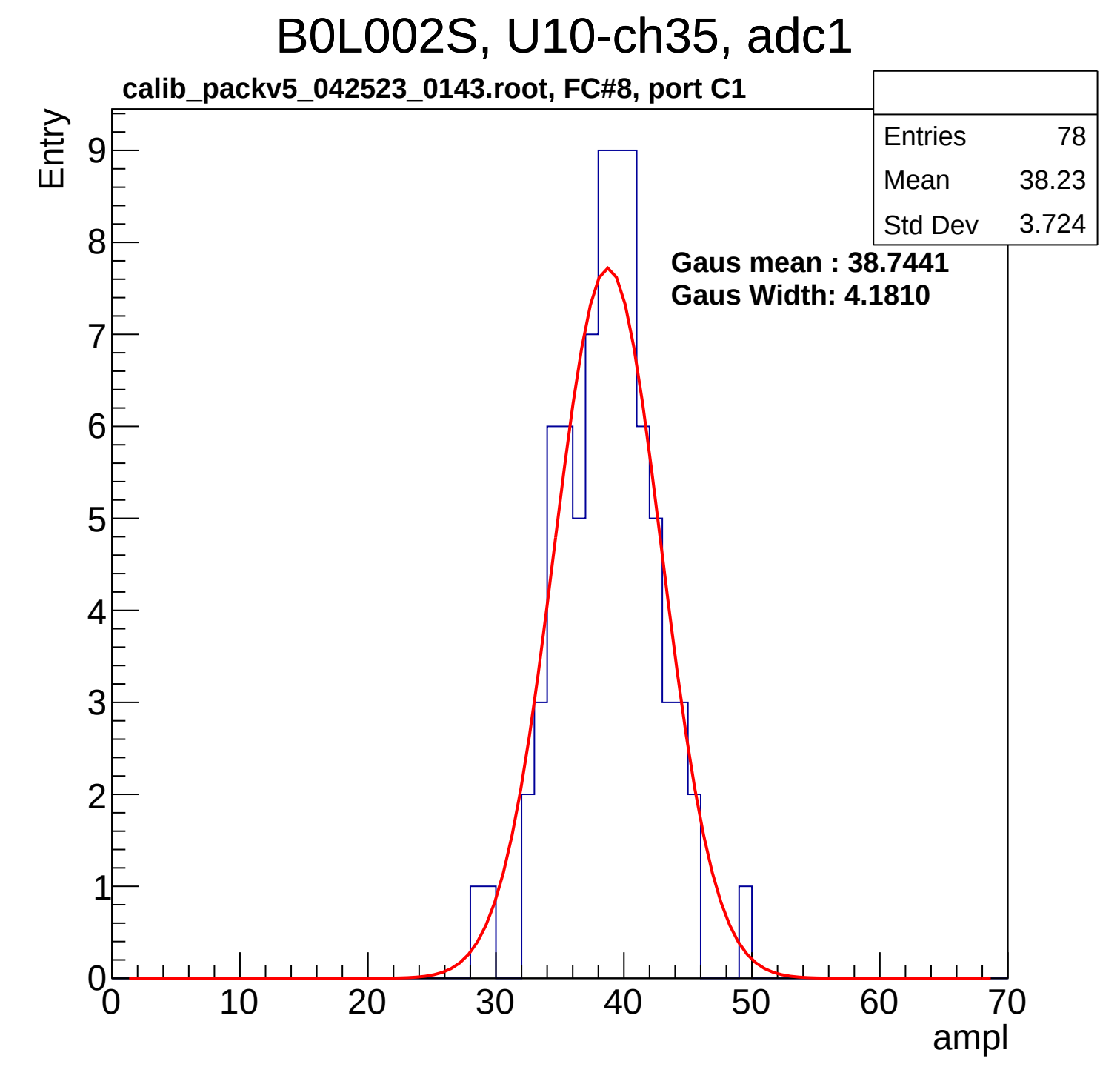

B0L002S, U10-ch34, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 57.49
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

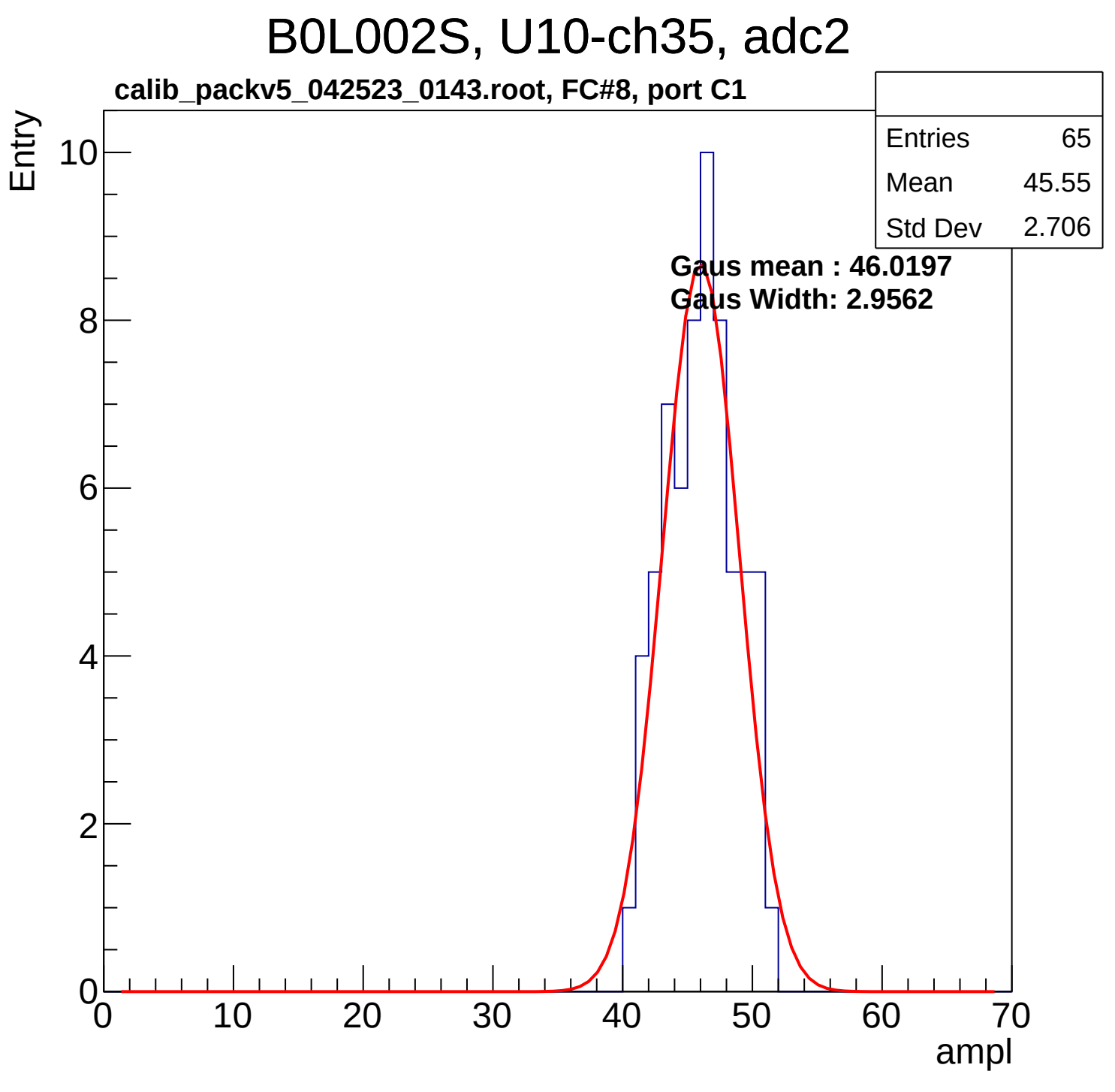

B0L002S, U10-ch34, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.76
Std Dev 12.11
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

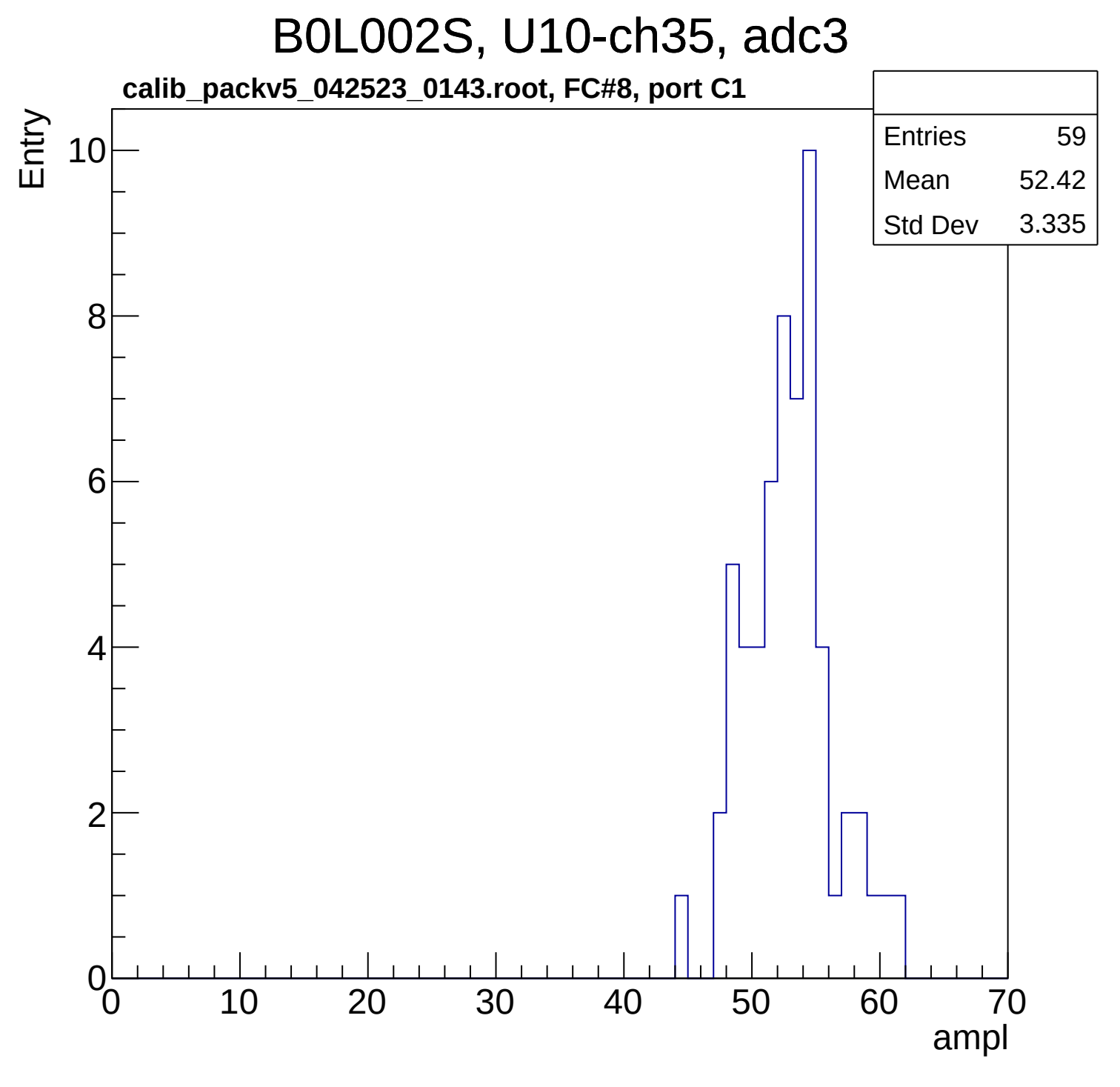

B0L002S, U10-ch34, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

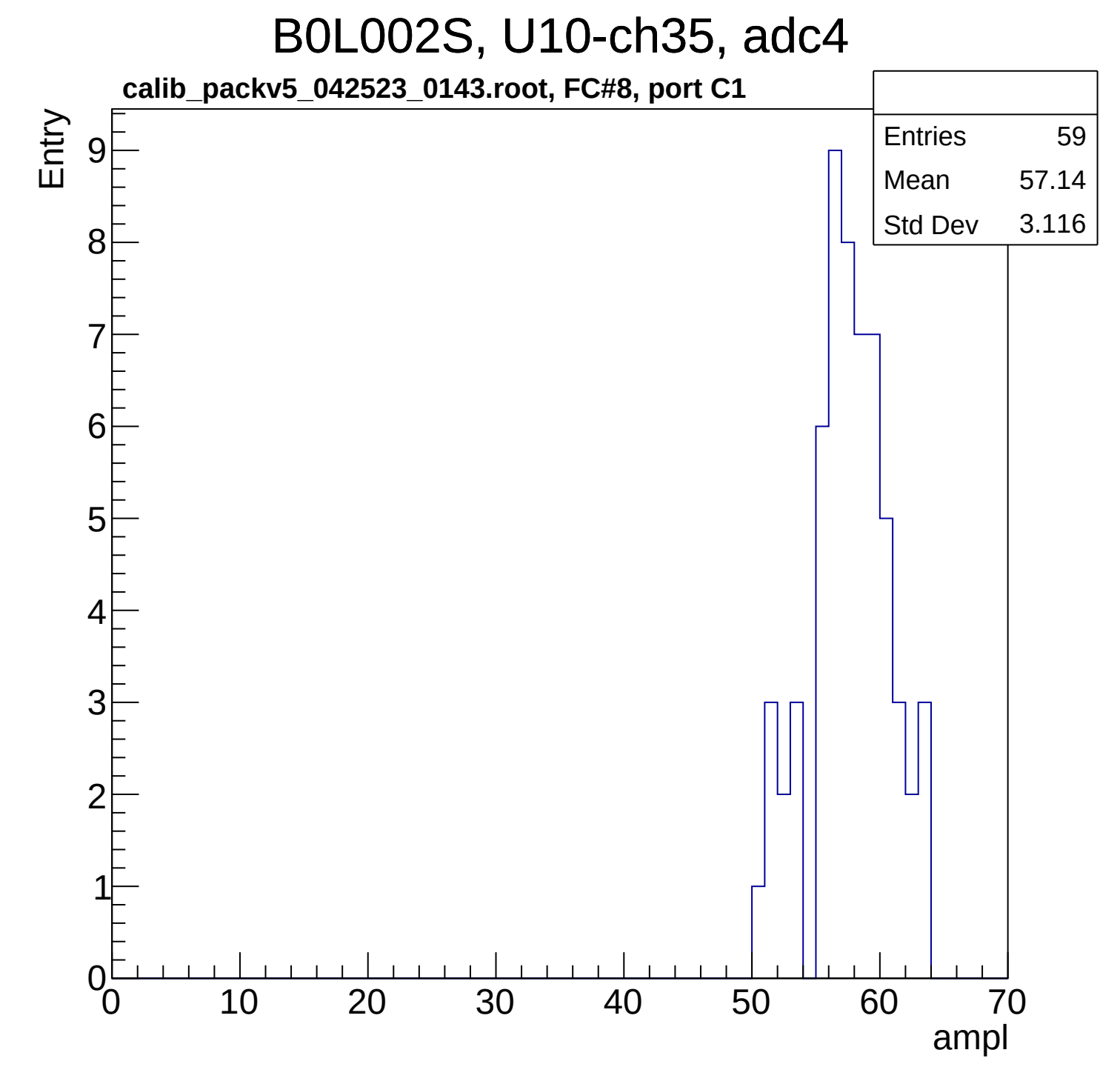

B0L002S, U10-ch34, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

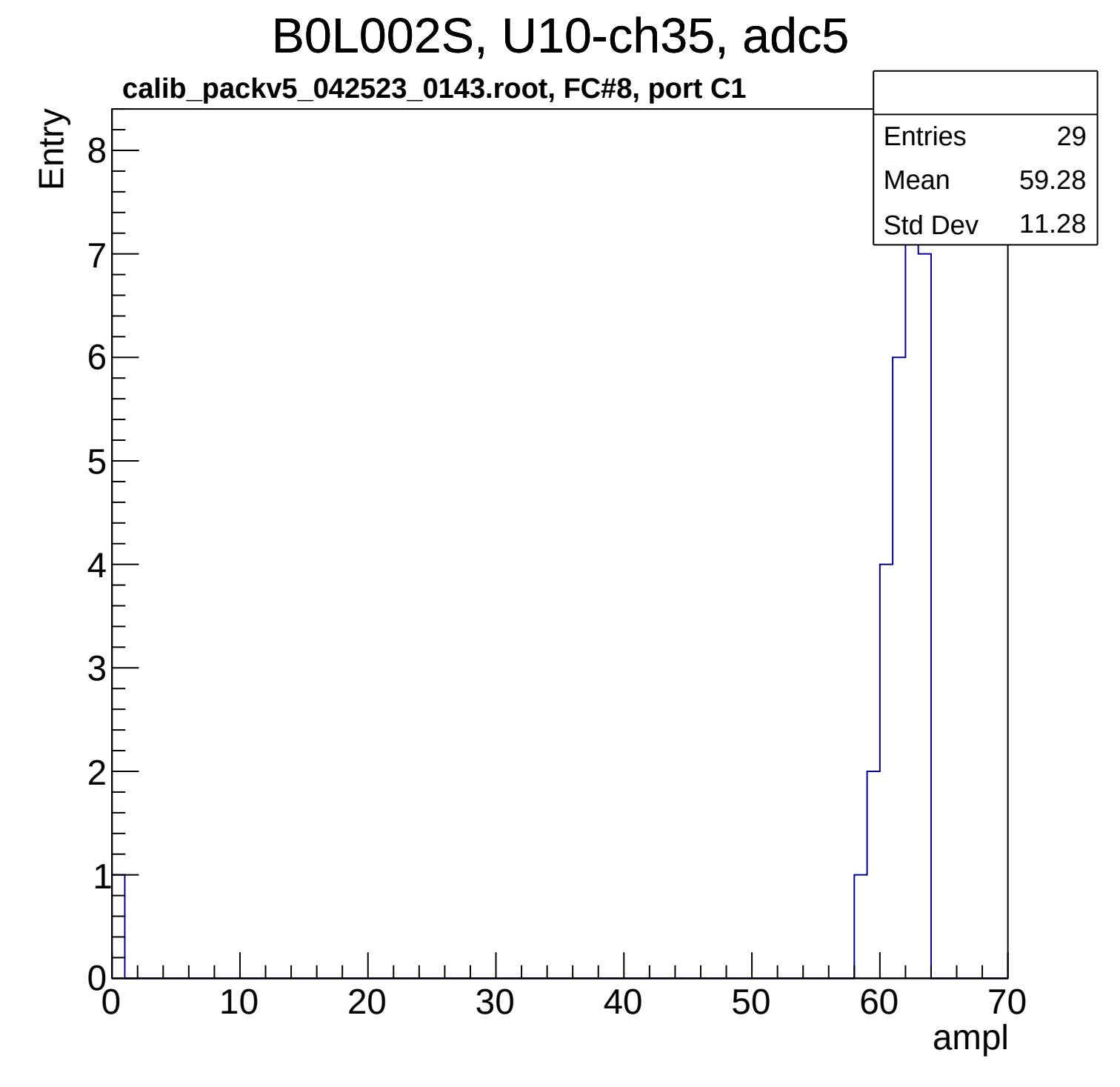

B0L002S, U10-ch35, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.24
Std Dev 3.393
Gaus mean : 31.6250
Gaus Width: 3.6910
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

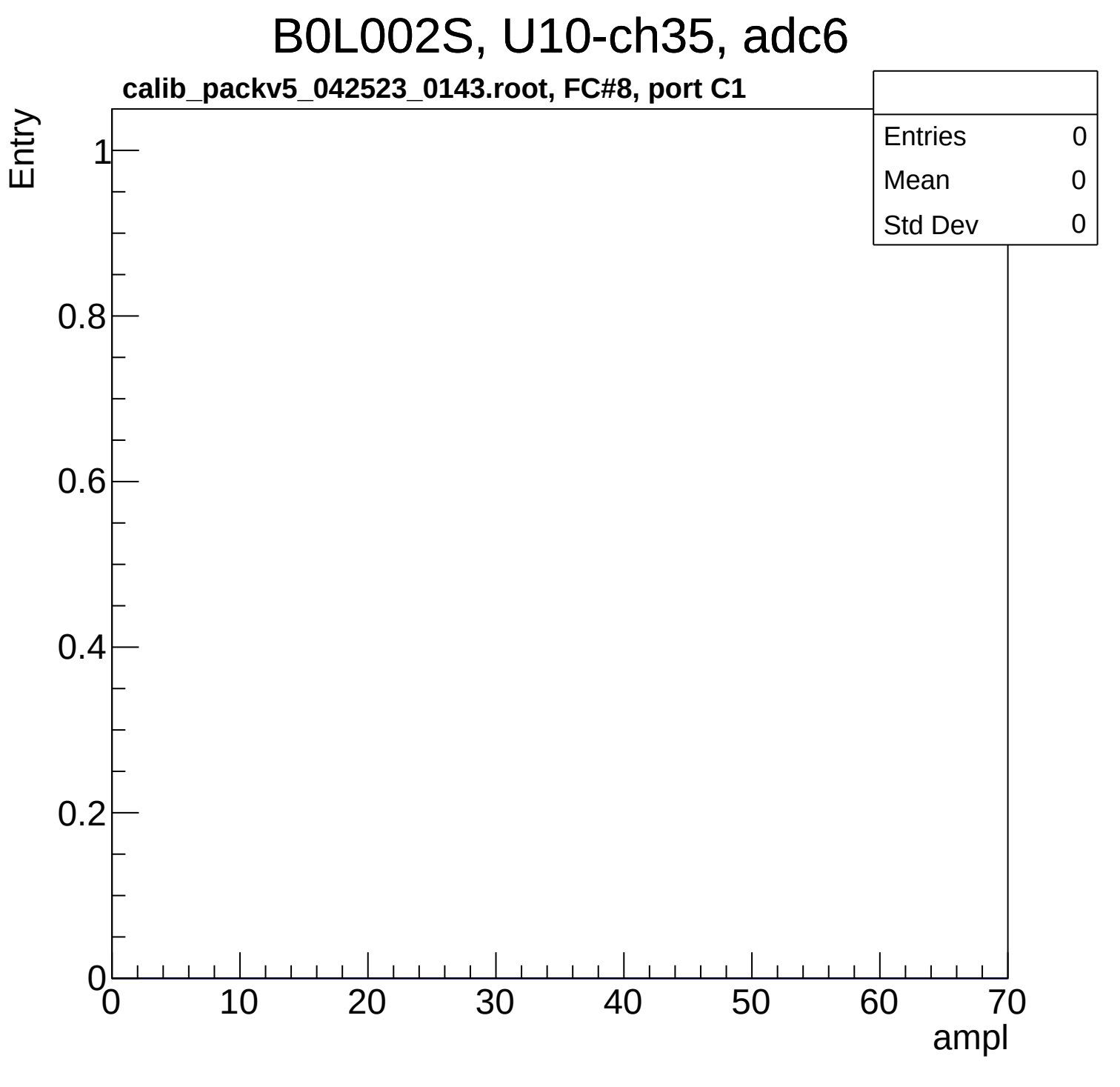

B0L002S, U10-ch35, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.23
Std Dev 3.724
Gaus mean : 38.7441
Gaus Width: 4.1810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

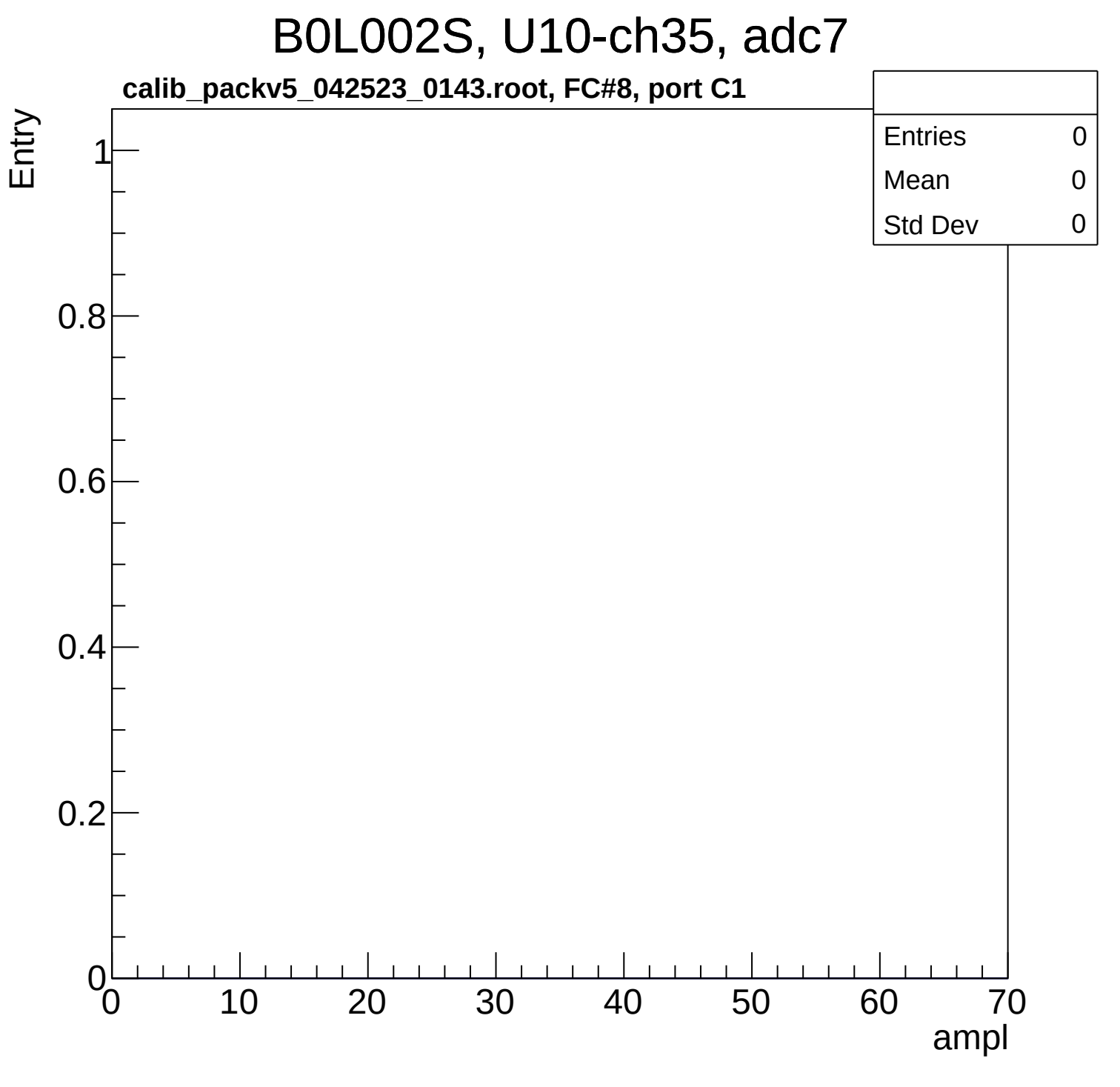

B0L002S, U10-ch35, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.55
Std Dev 2.706
Gaus mean : 46.0197
Gaus Width: 2.9562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

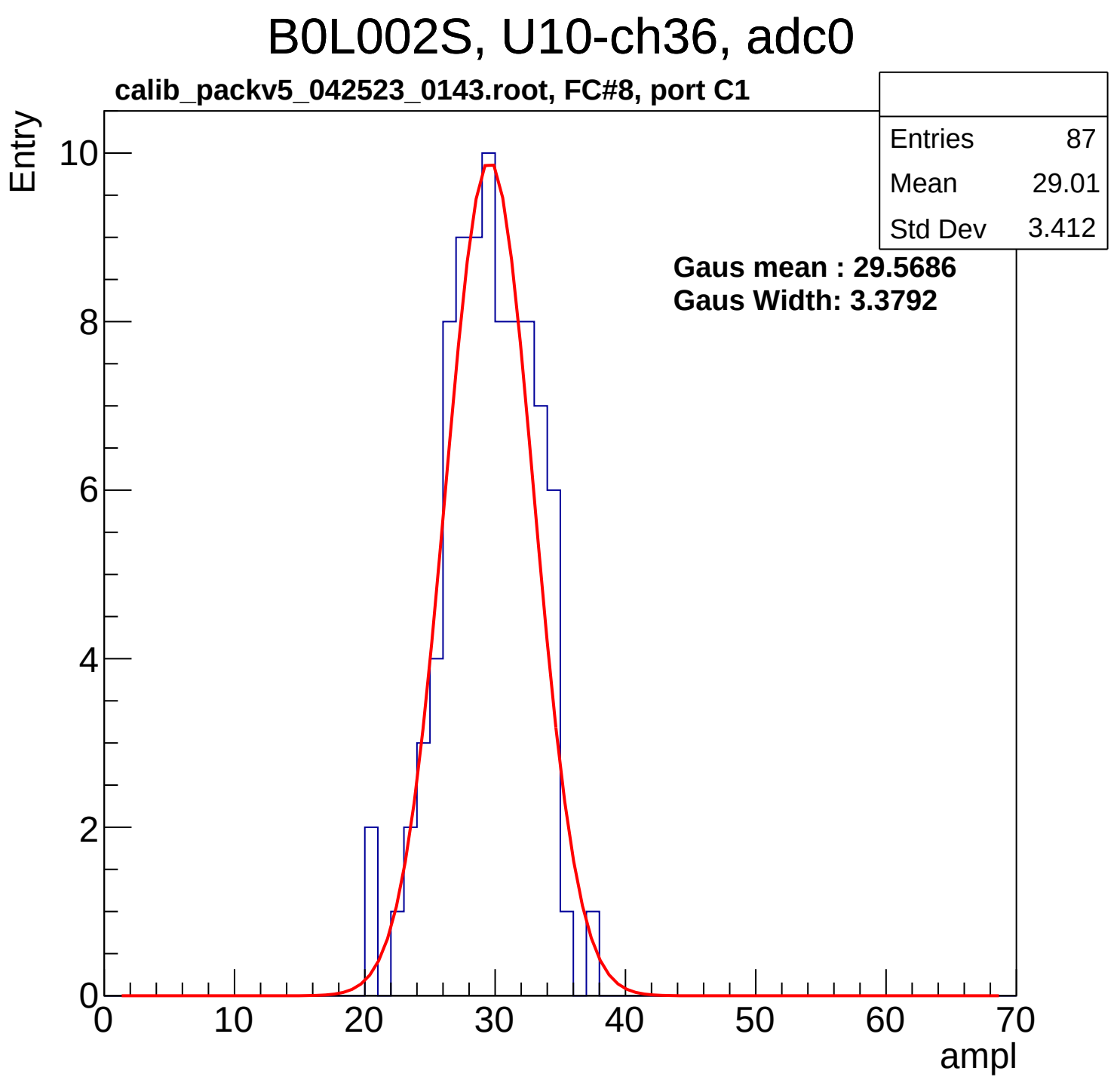

B0L002S, U10-ch35, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 52.42
Std Dev 3.335
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

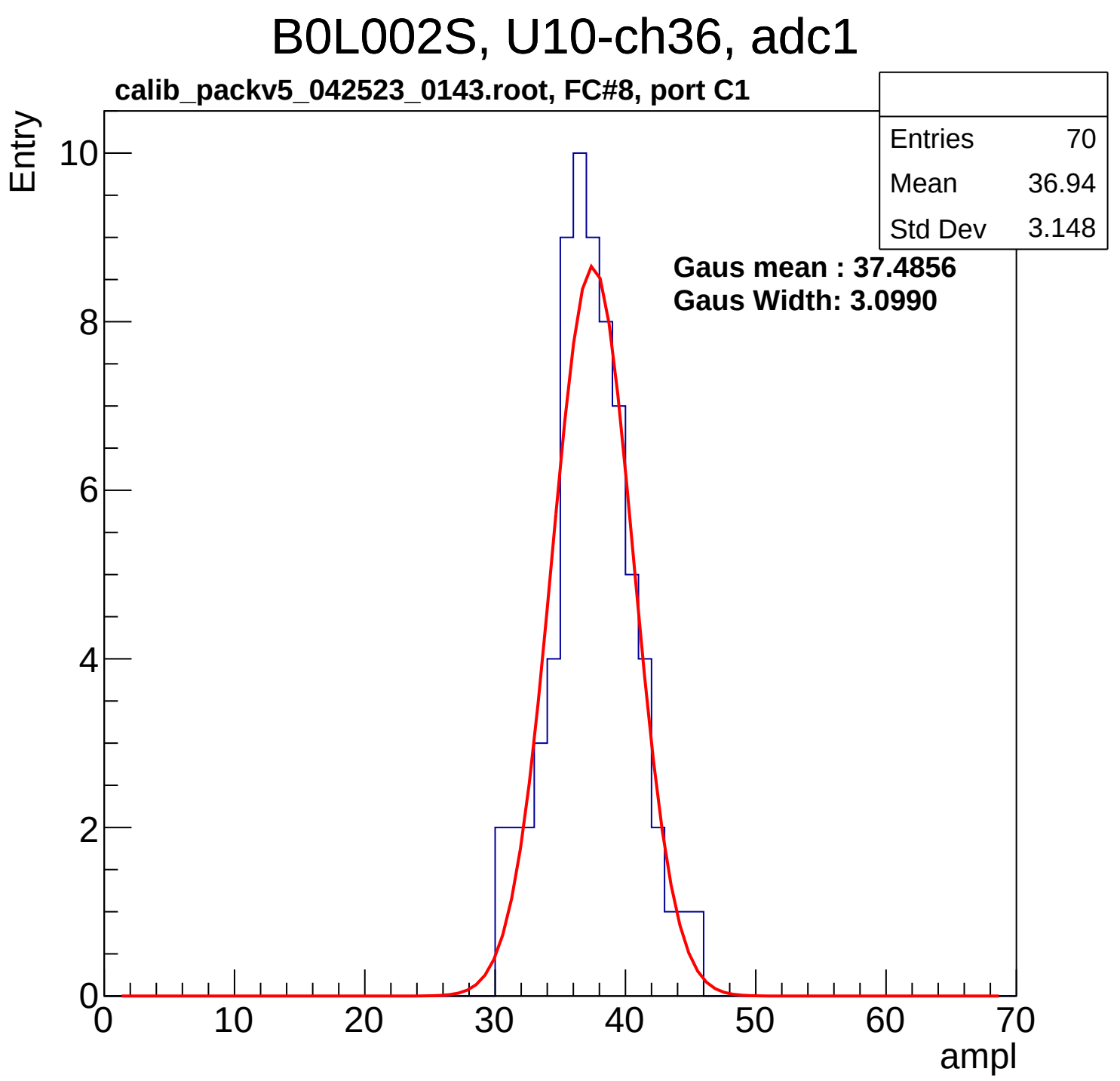

B0L002S, U10-ch35, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.14
Std Dev 3.116
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

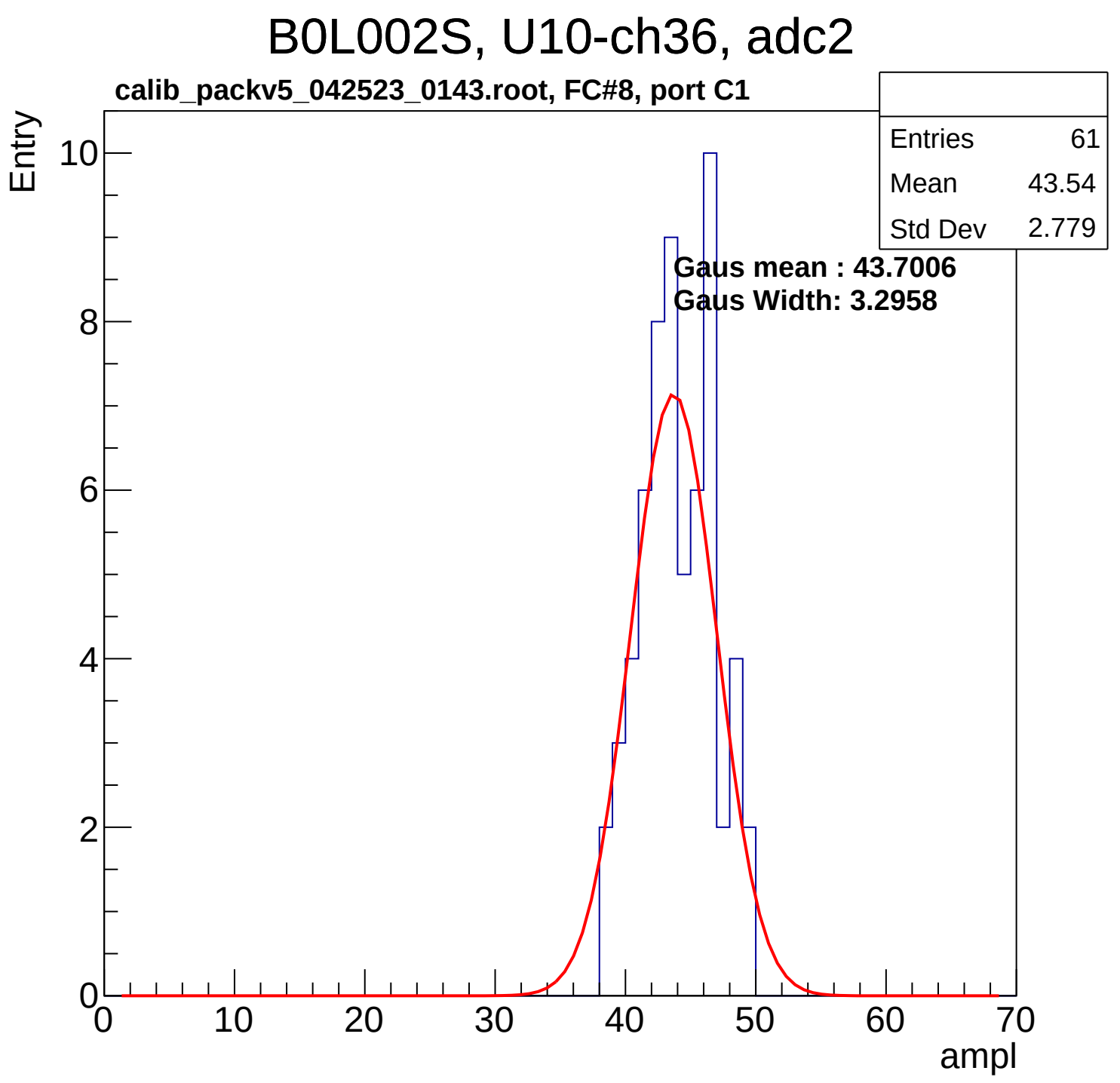

B0L002S, U10-ch35, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.28
Std Dev 11.28
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

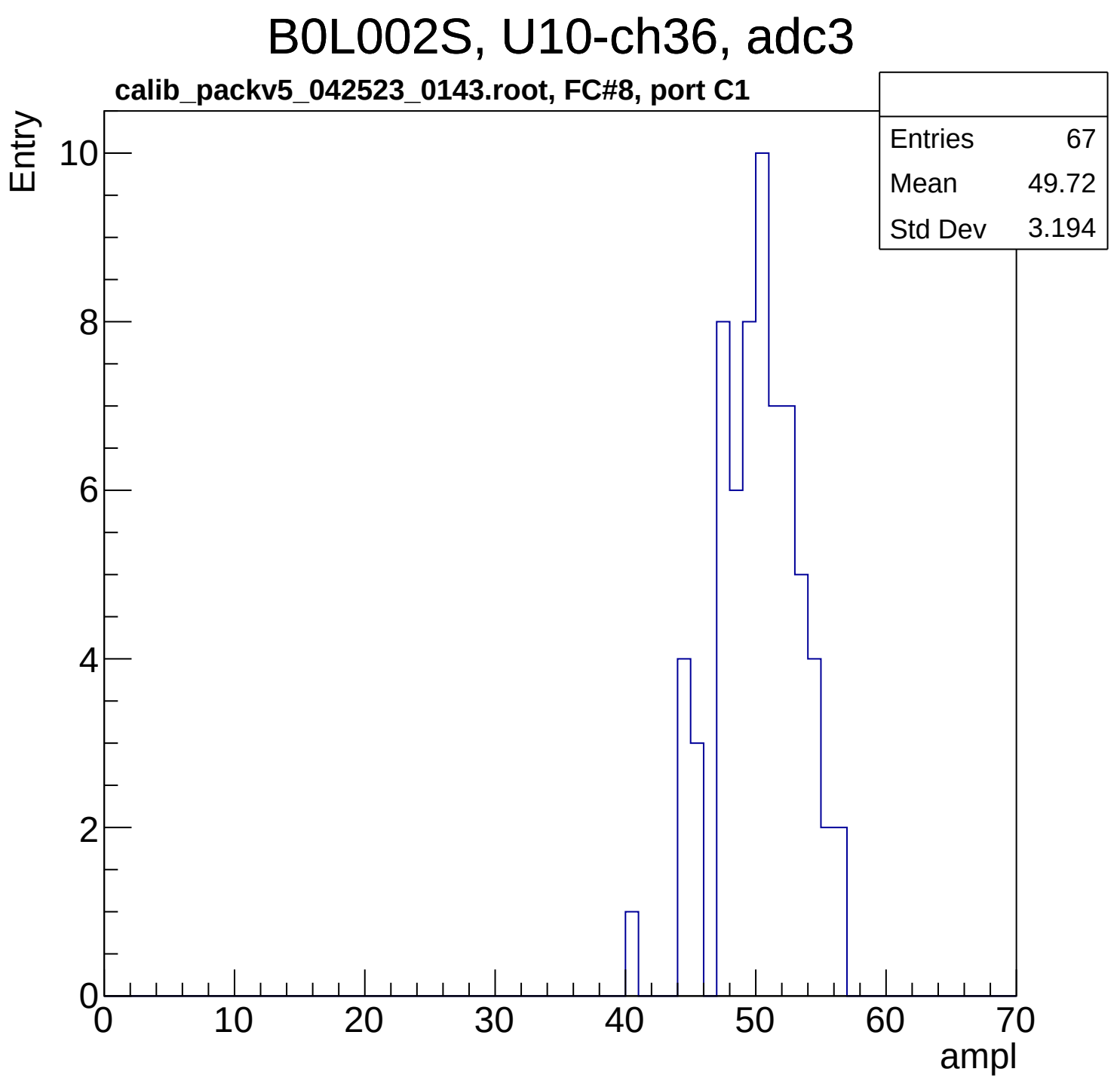

B0L002S, U10-ch35, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

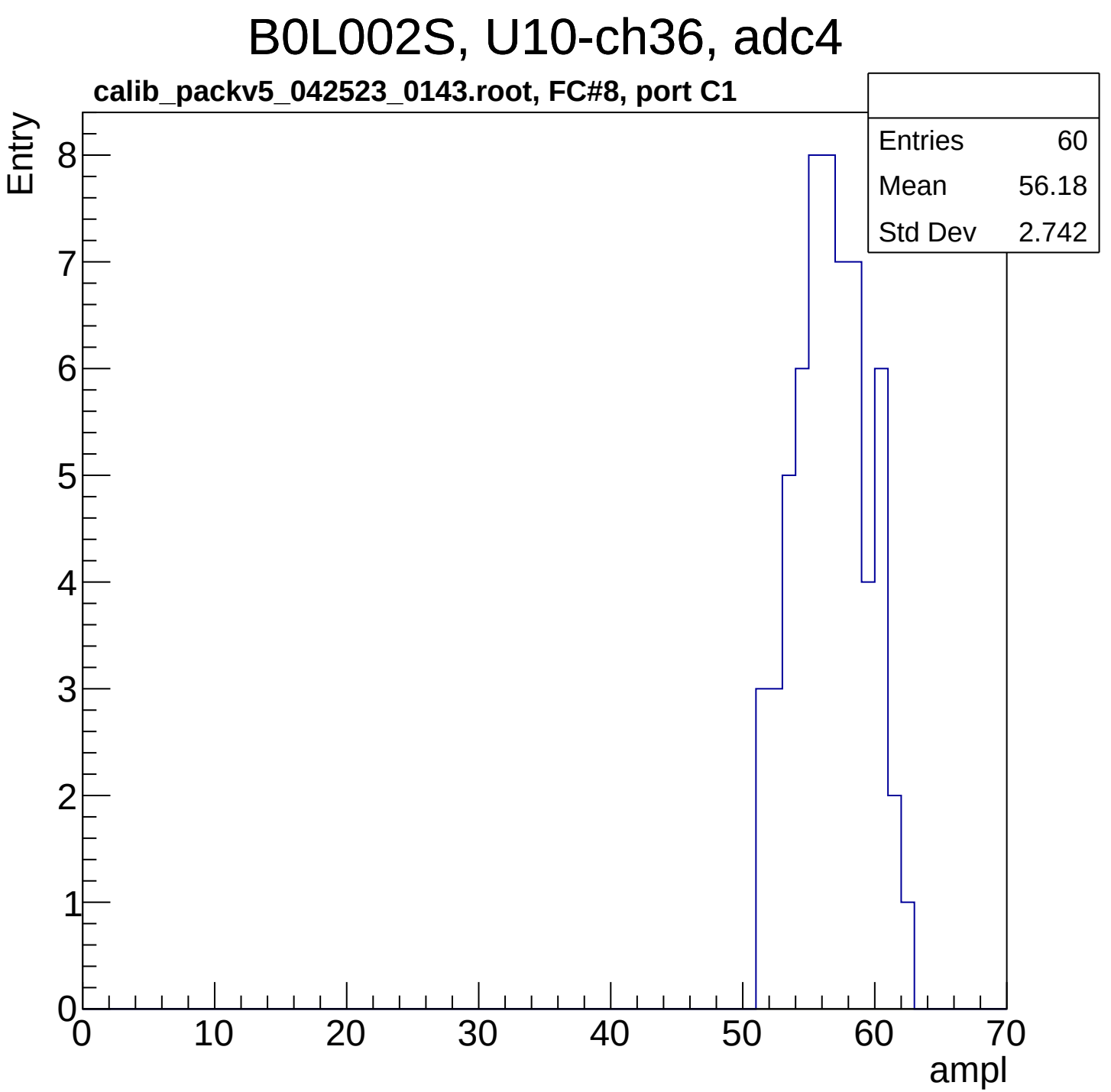

B0L002S, U10-ch35, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

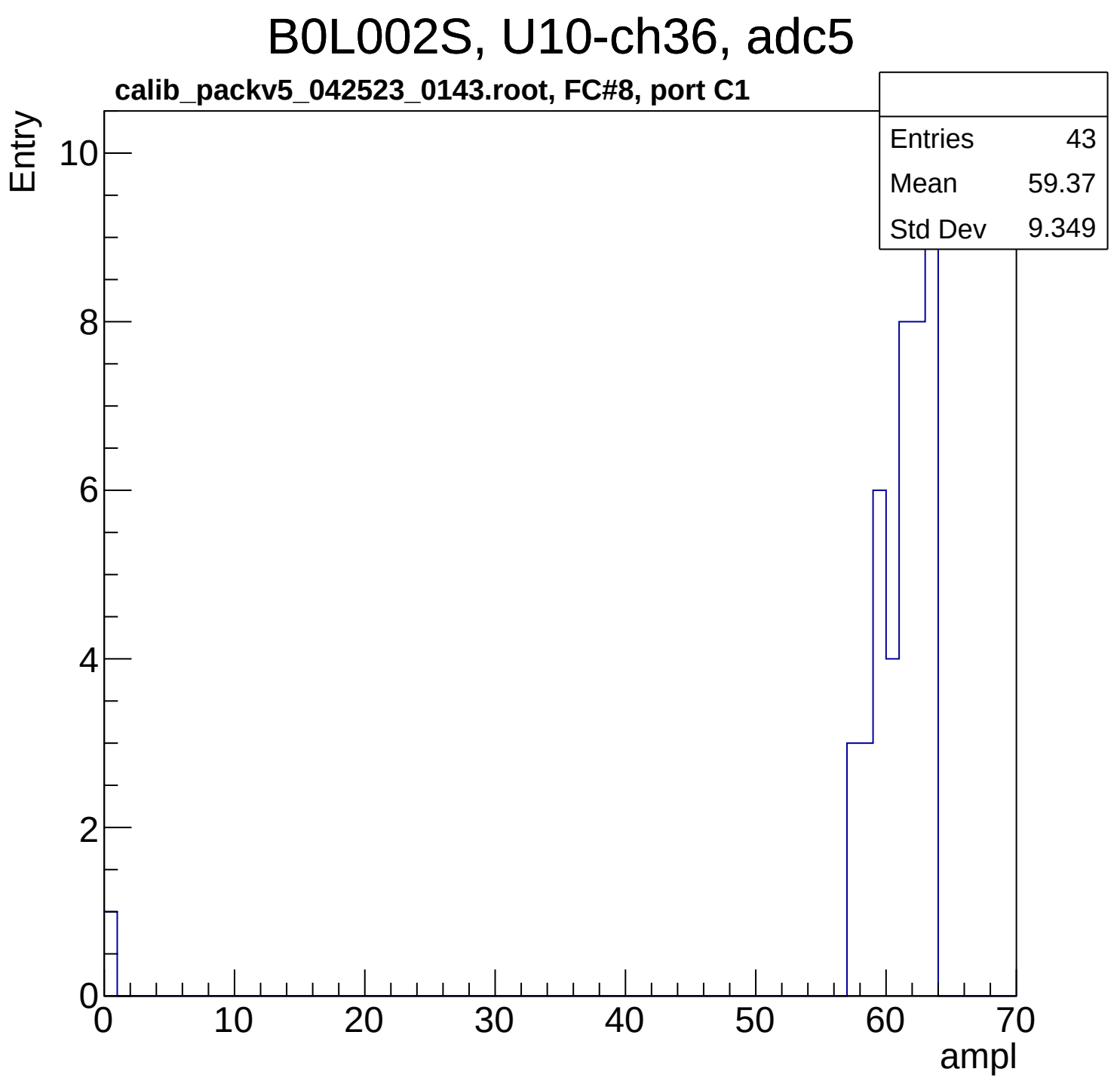

B0L002S, U10-ch36, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 29.01
Std Dev 3.412
Gaus mean : 29.5686
Gaus Width: 3.3792
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

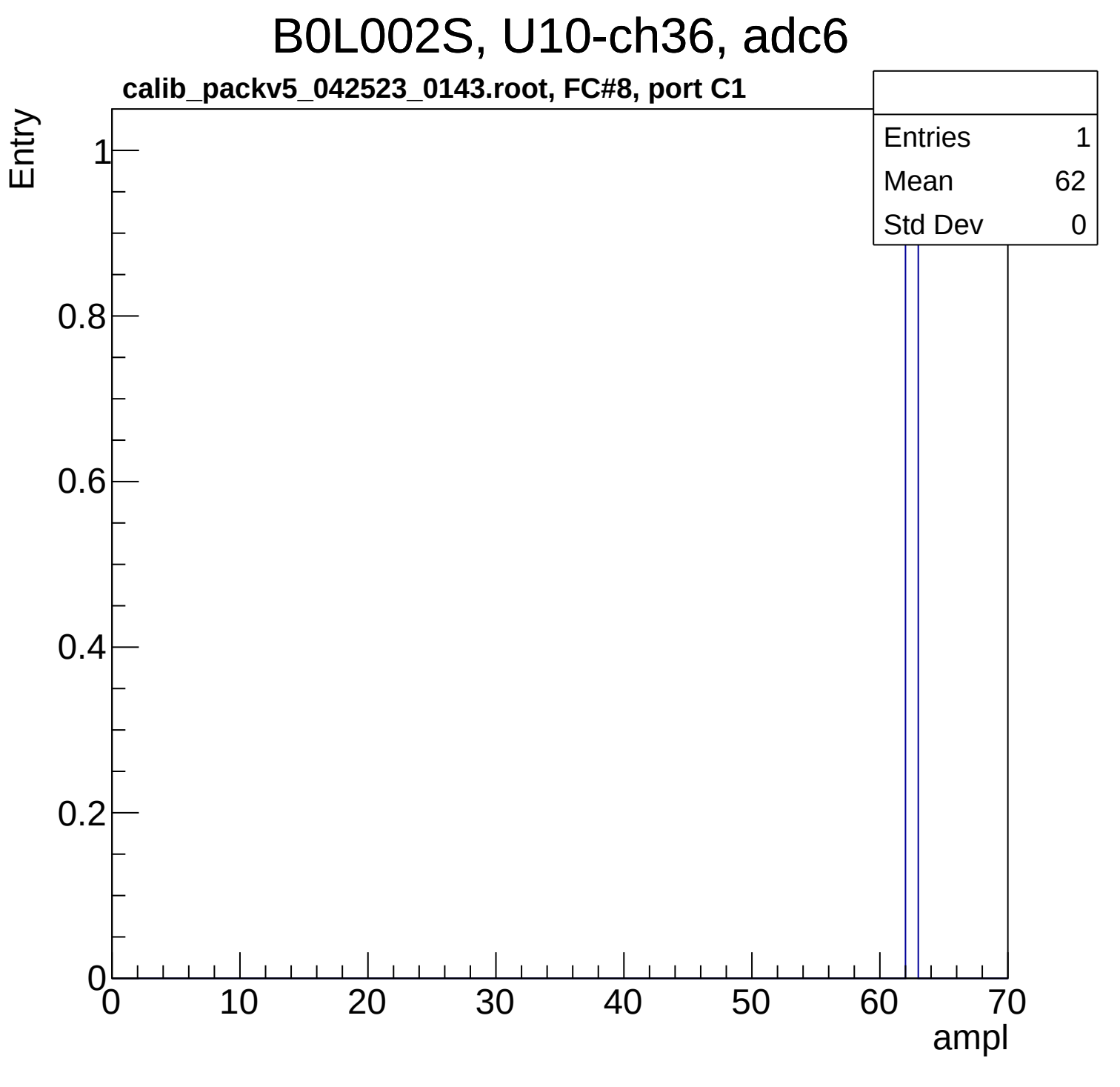

B0L002S, U10-ch36, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 36.94
Std Dev 3.148
Gaus mean : 37.4856
Gaus Width: 3.0990
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

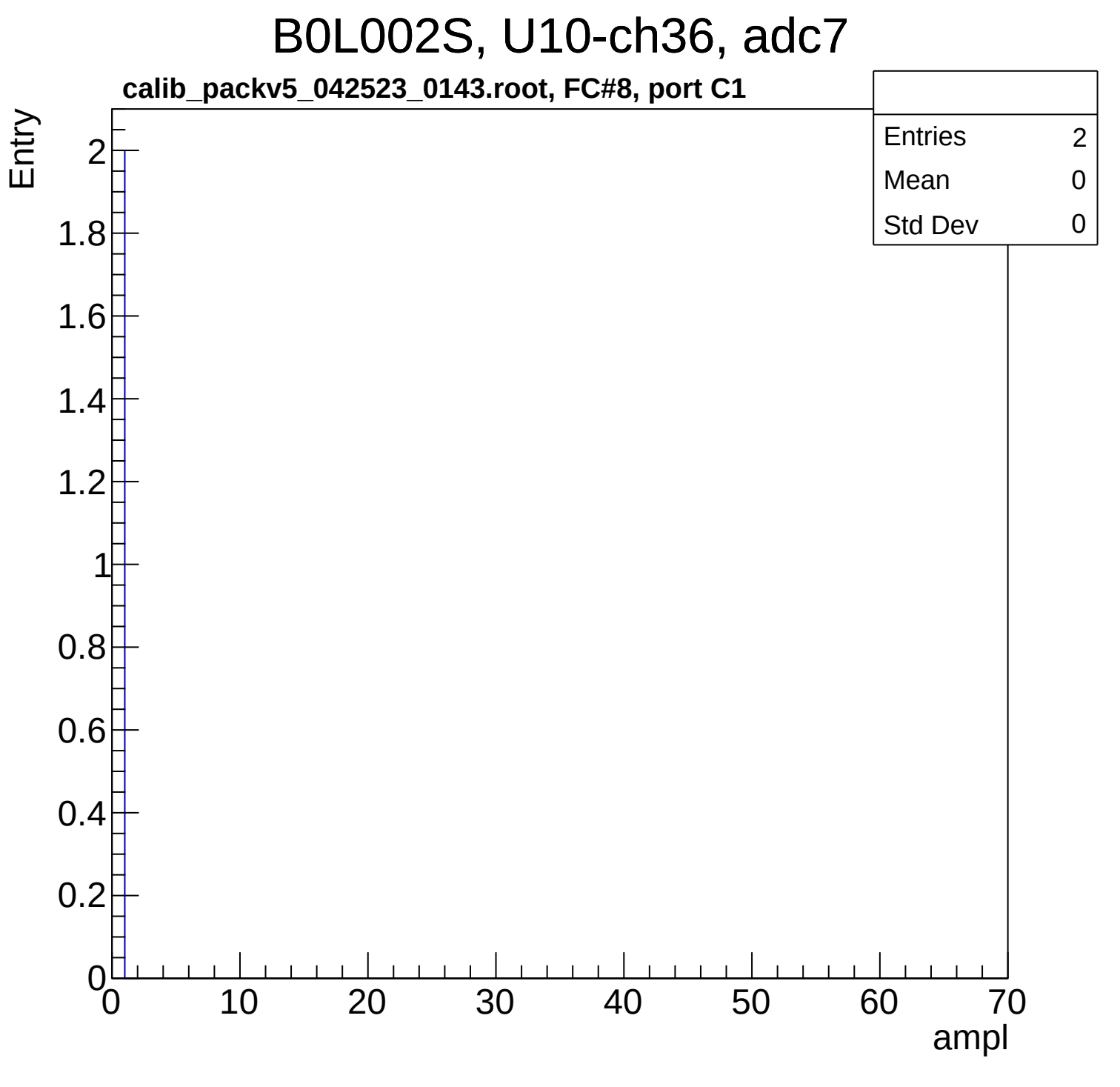

B0L002S, U10-ch36, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.54
Std Dev 2.779
Gaus mean : 43.7006
Gaus Width: 3.2958
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

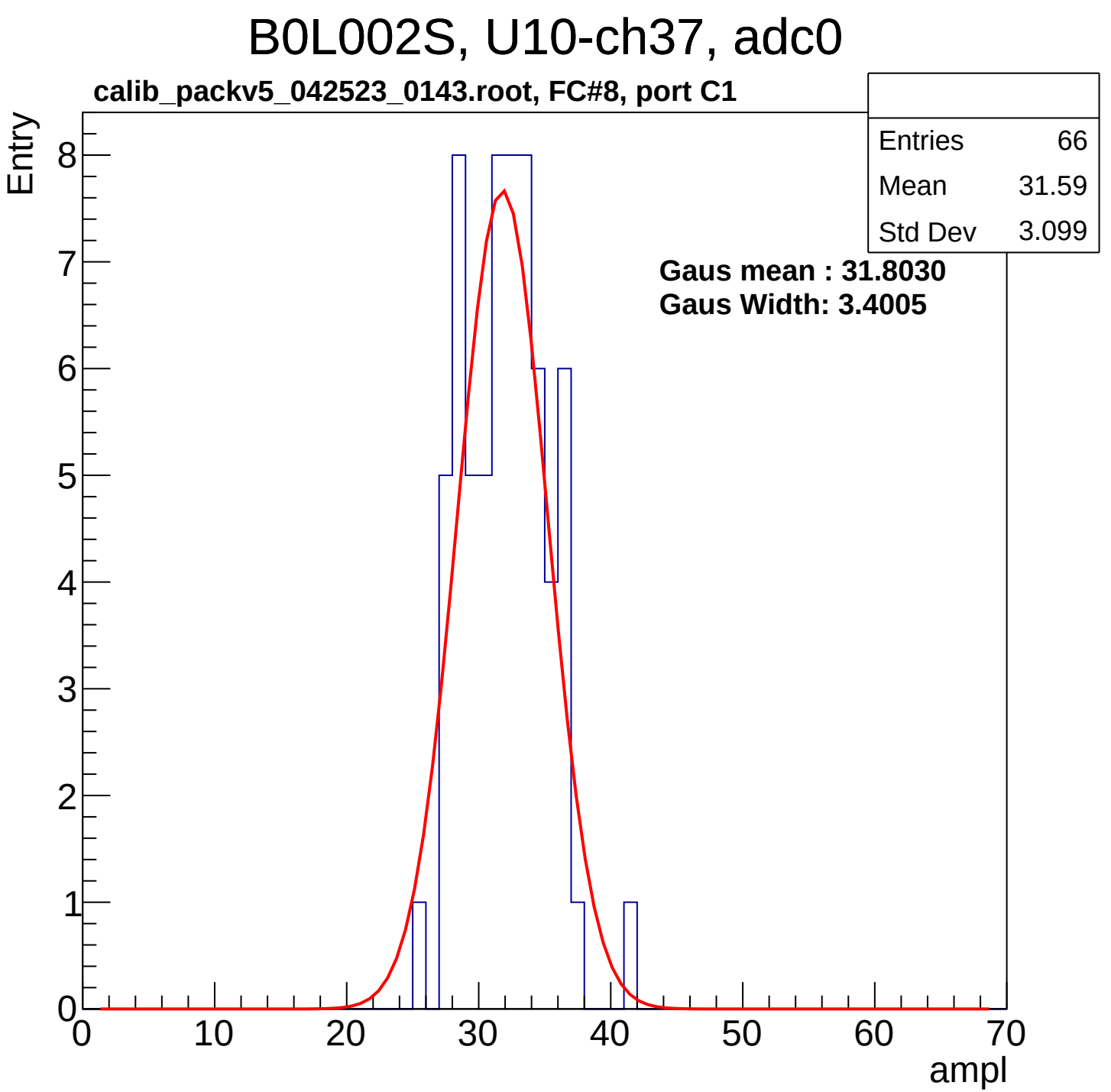

B0L002S, U10-ch36, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 49.72
Std Dev 3.194
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

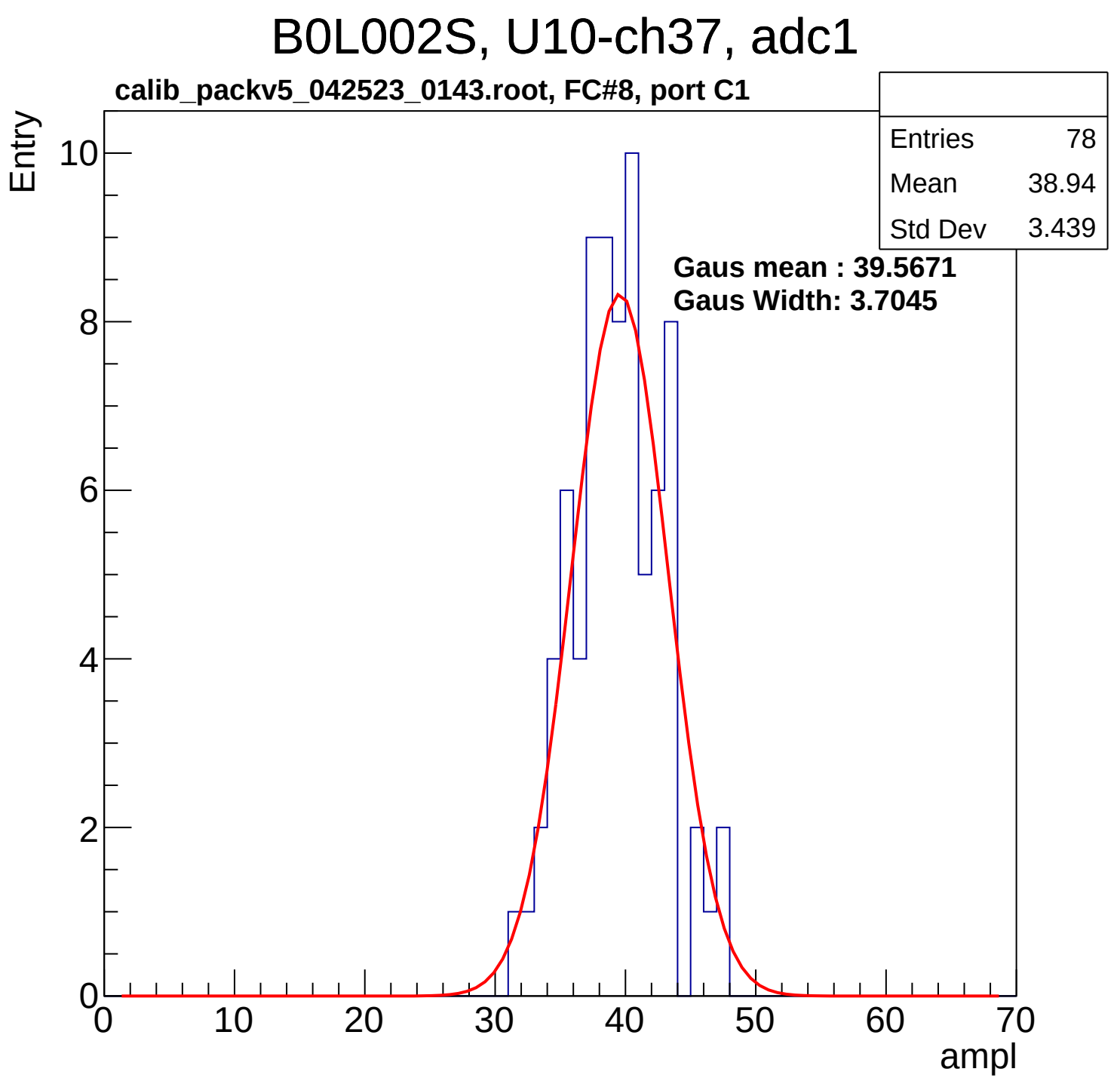

B0L002S, U10-ch36, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.18
Std Dev 2.742
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

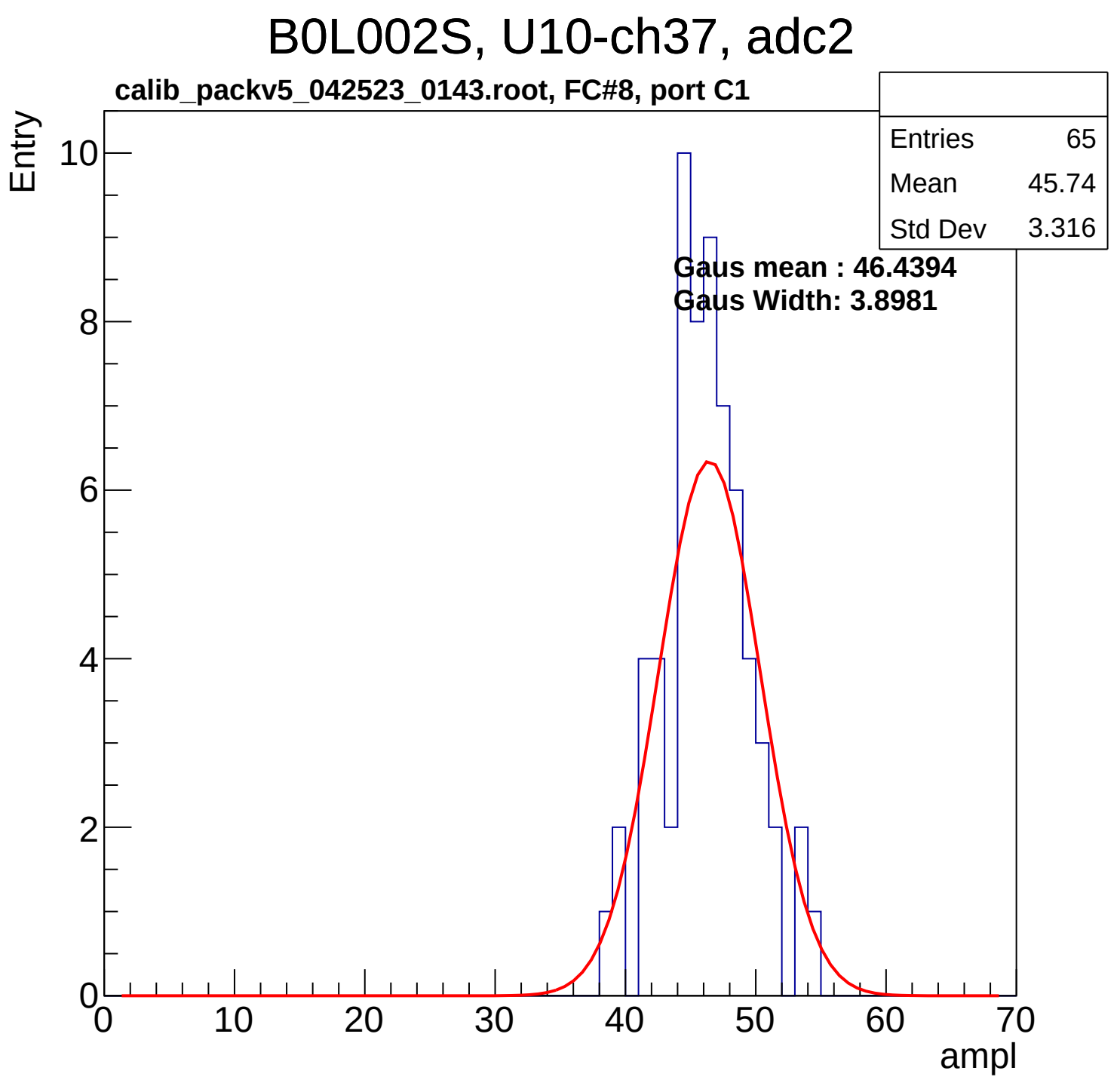

B0L002S, U10-ch36, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.37
Std Dev 9.349
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

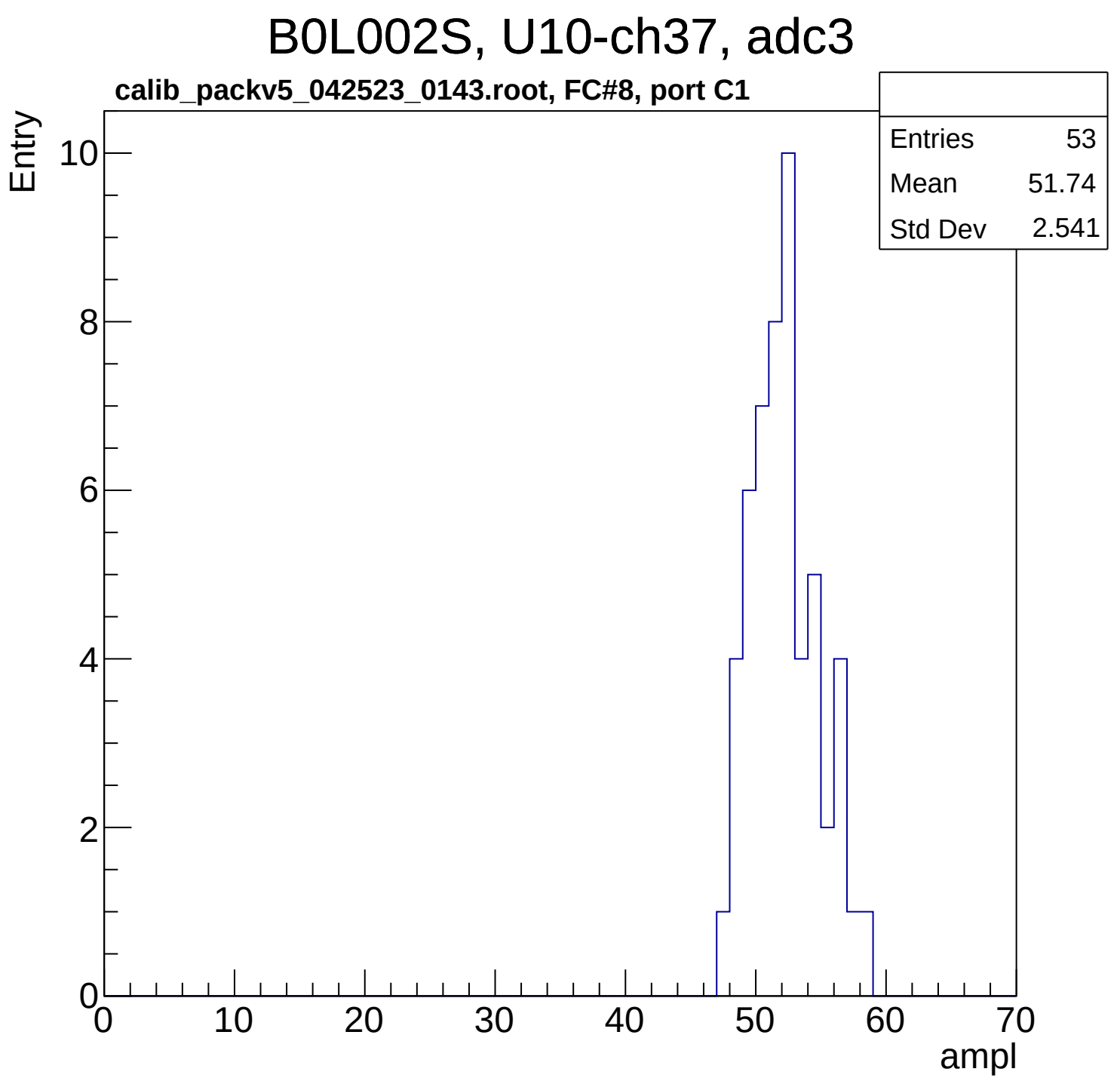

B0L002S, U10-ch36, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

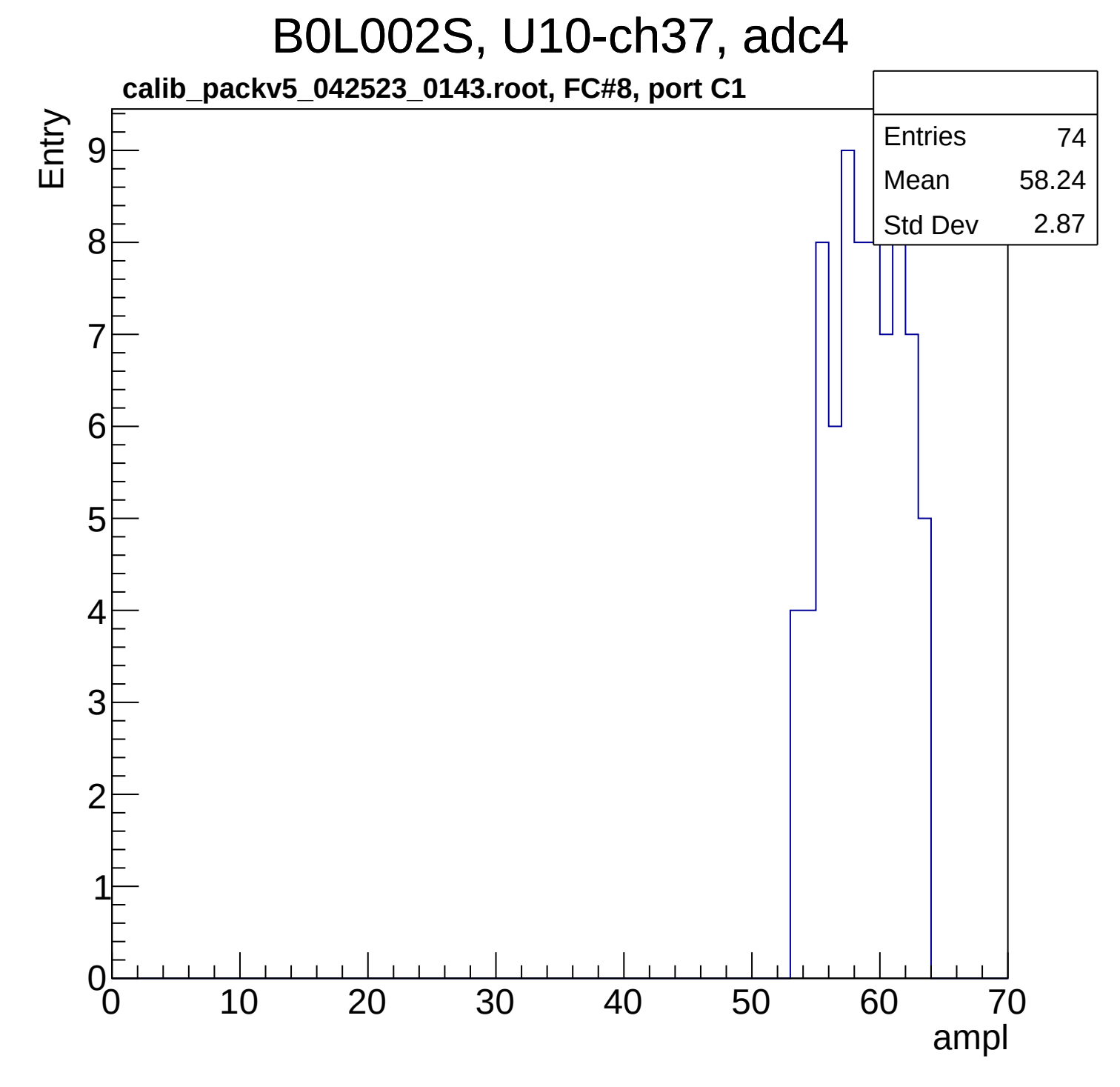

B0L002S, U10-ch36, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

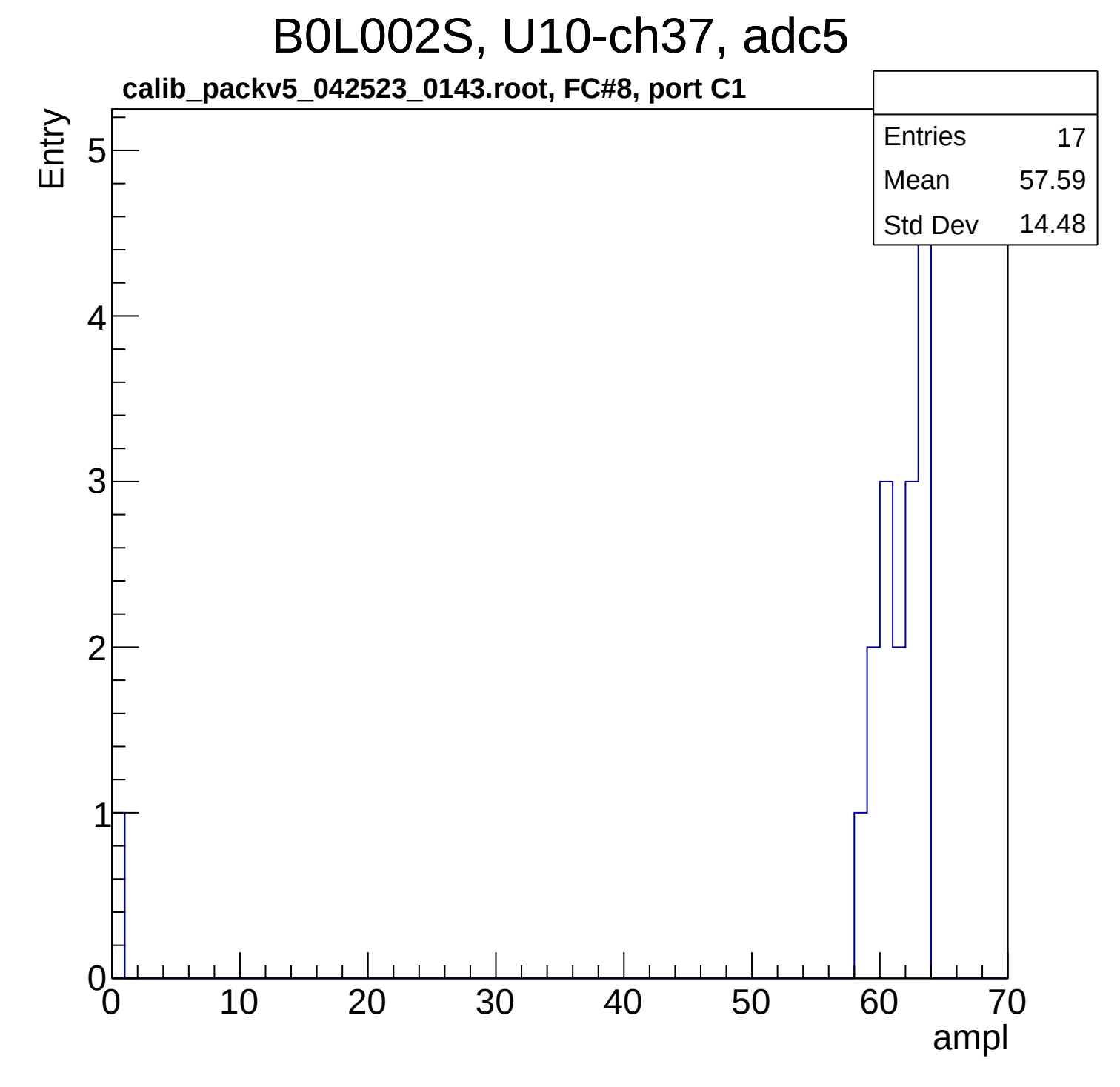

B0L002S, U10-ch37, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 31.59
Std Dev 3.099
Gaus mean : 31.8030
Gaus Width: 3.4005
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

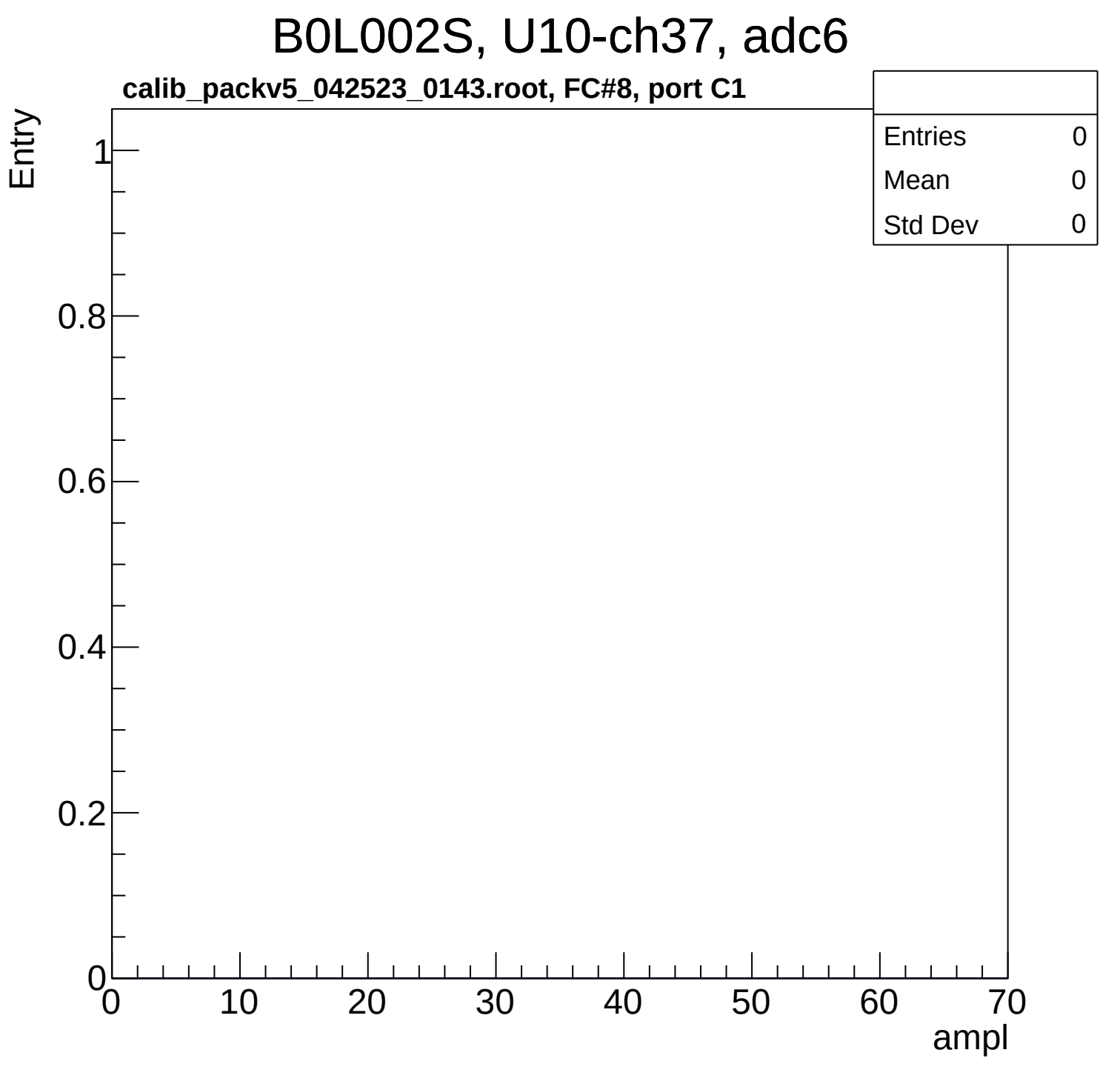

B0L002S, U10-ch37, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.94
Std Dev 3.439
Gaus mean : 39.5671
Gaus Width: 3.7045
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

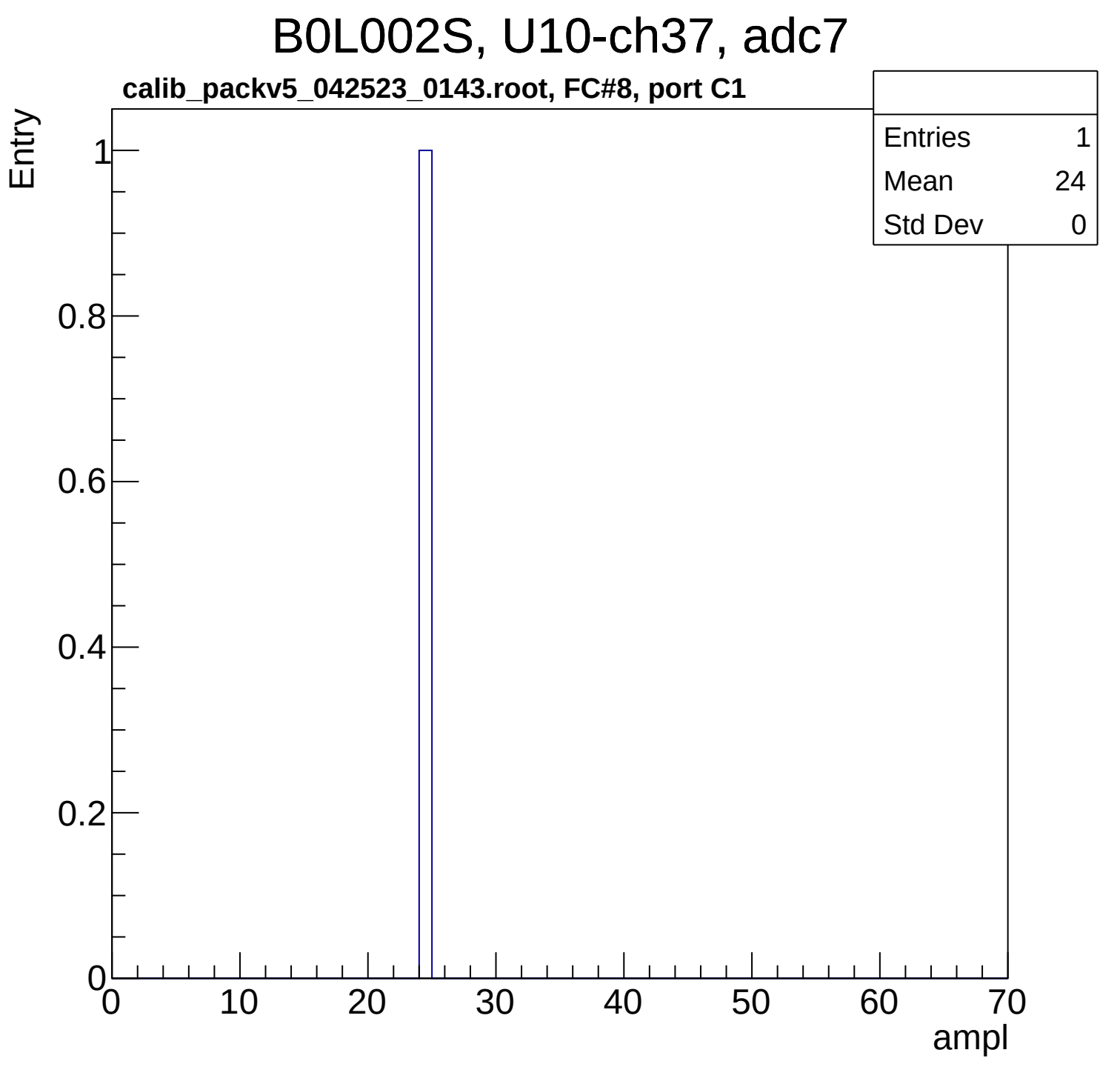

B0L002S, U10-ch37, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 45.74
Std Dev 3.316
Gaus mean : 46.4394
Gaus Width: 3.8981
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

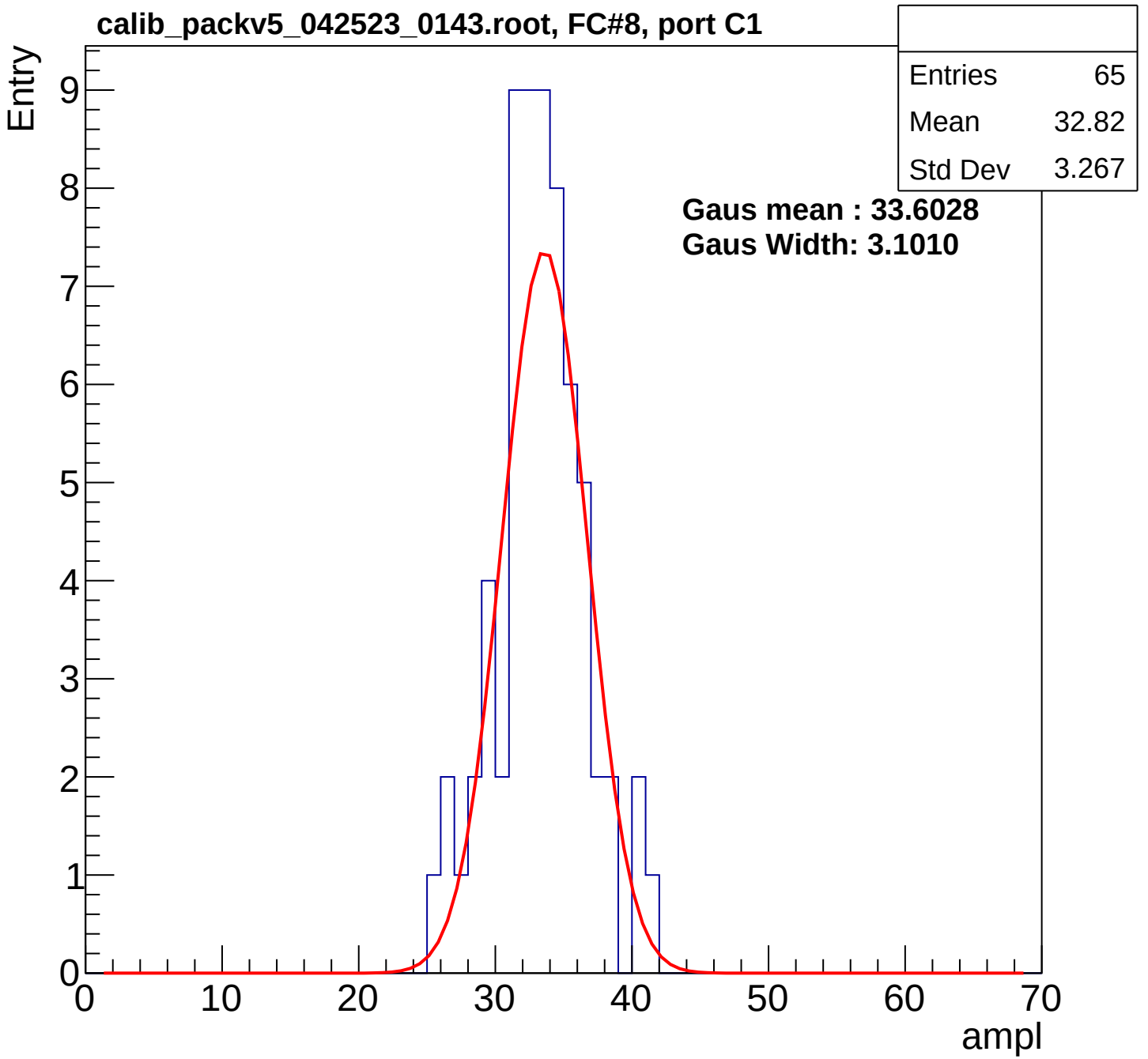

B0L002S, U10-ch37, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.74
Std Dev 2.541
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

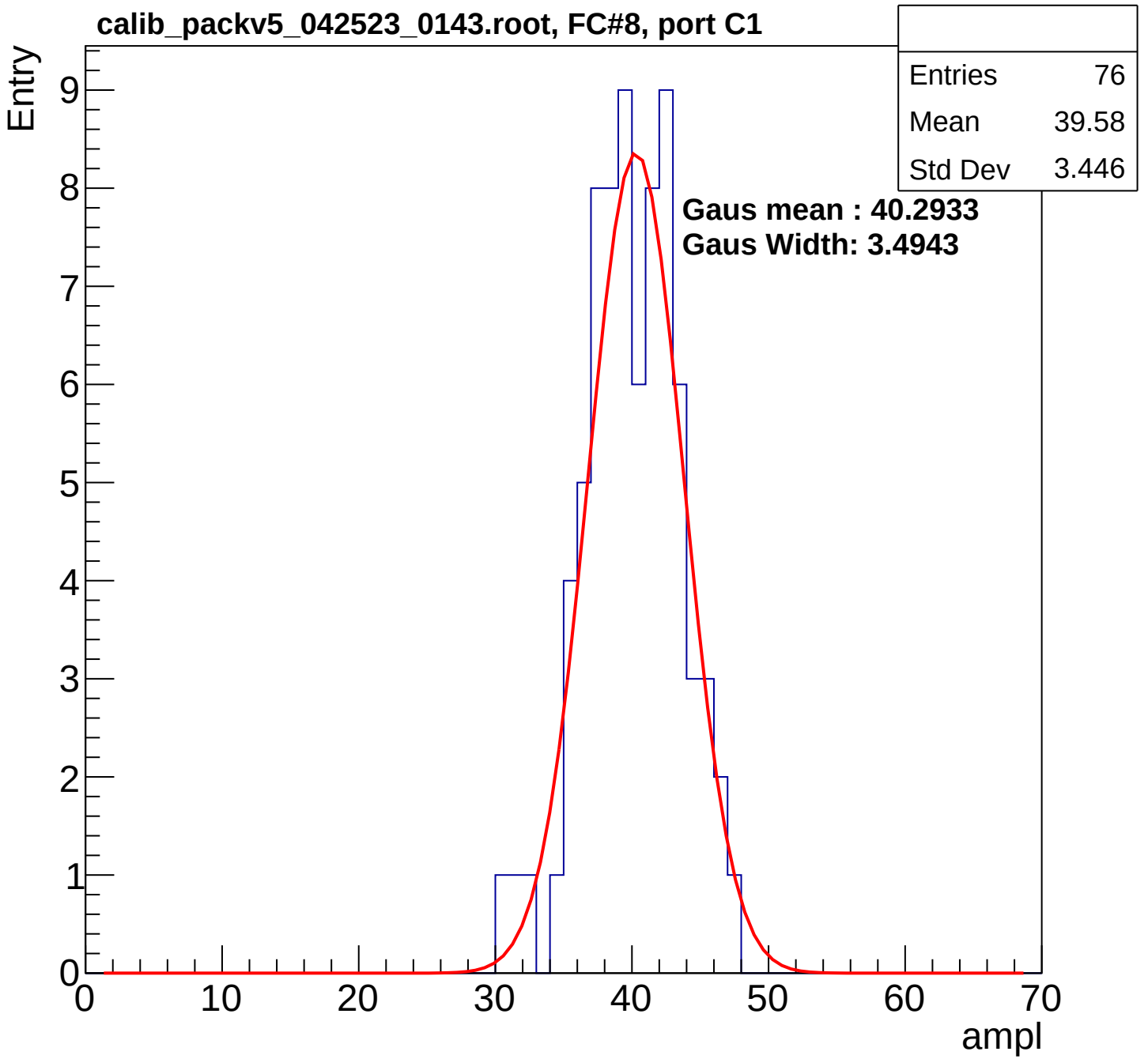

B0L002S, U10-ch37, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 58.24
Std Dev 2.87
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

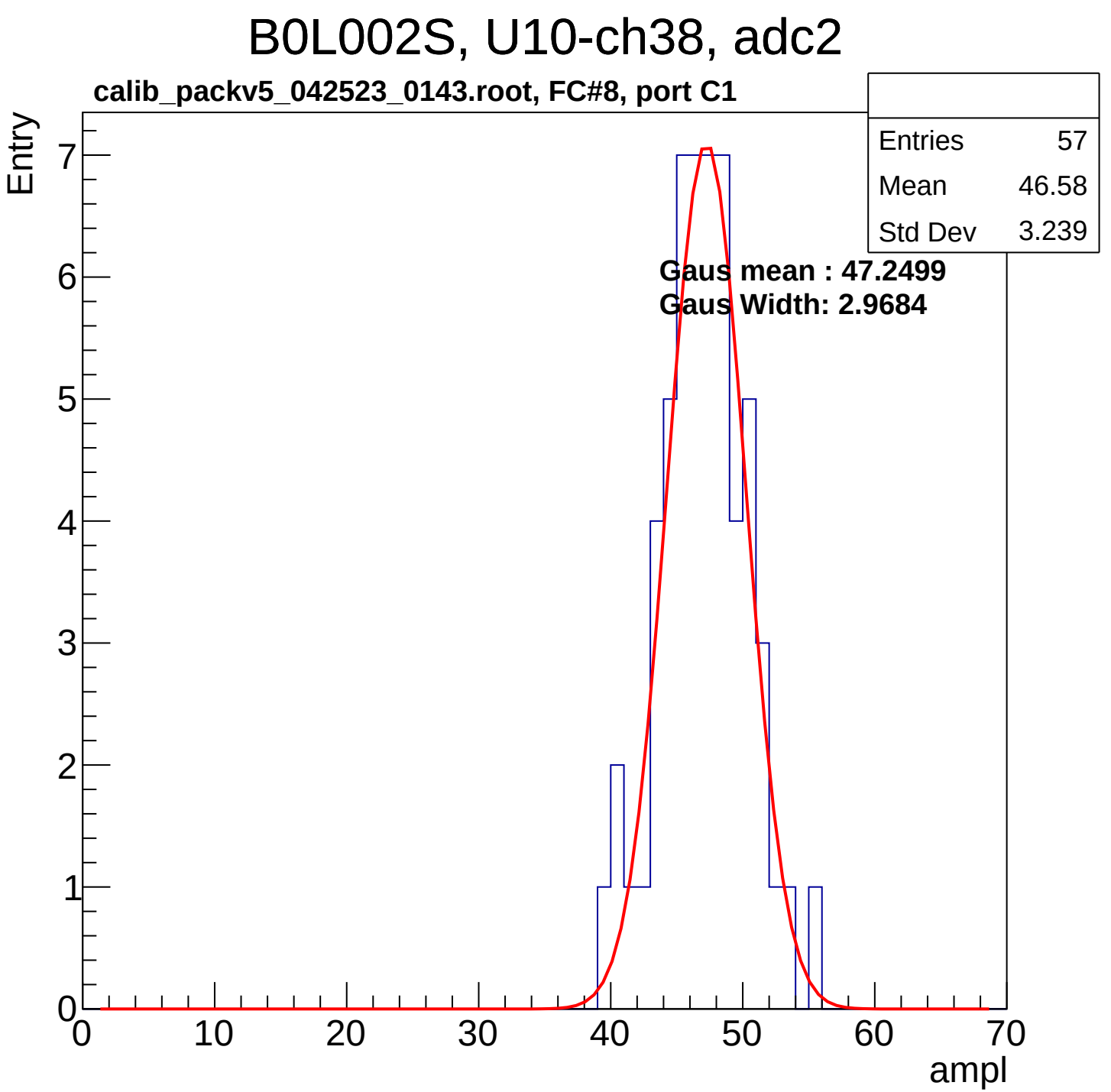

B0L002S, U10-ch37, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 17
Mean 57.59
Std Dev 14.48
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

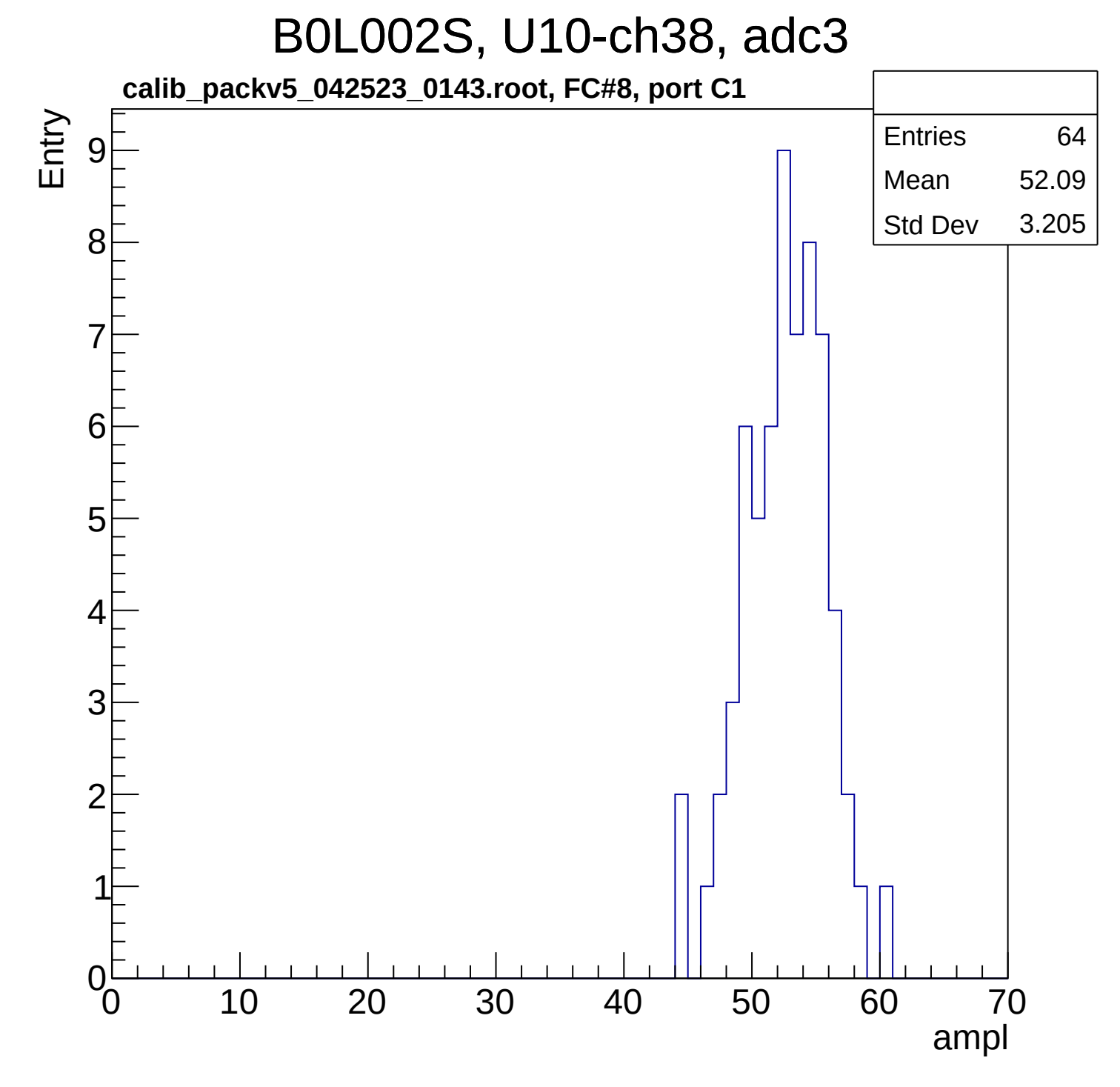

B0L002S, U10-ch37, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

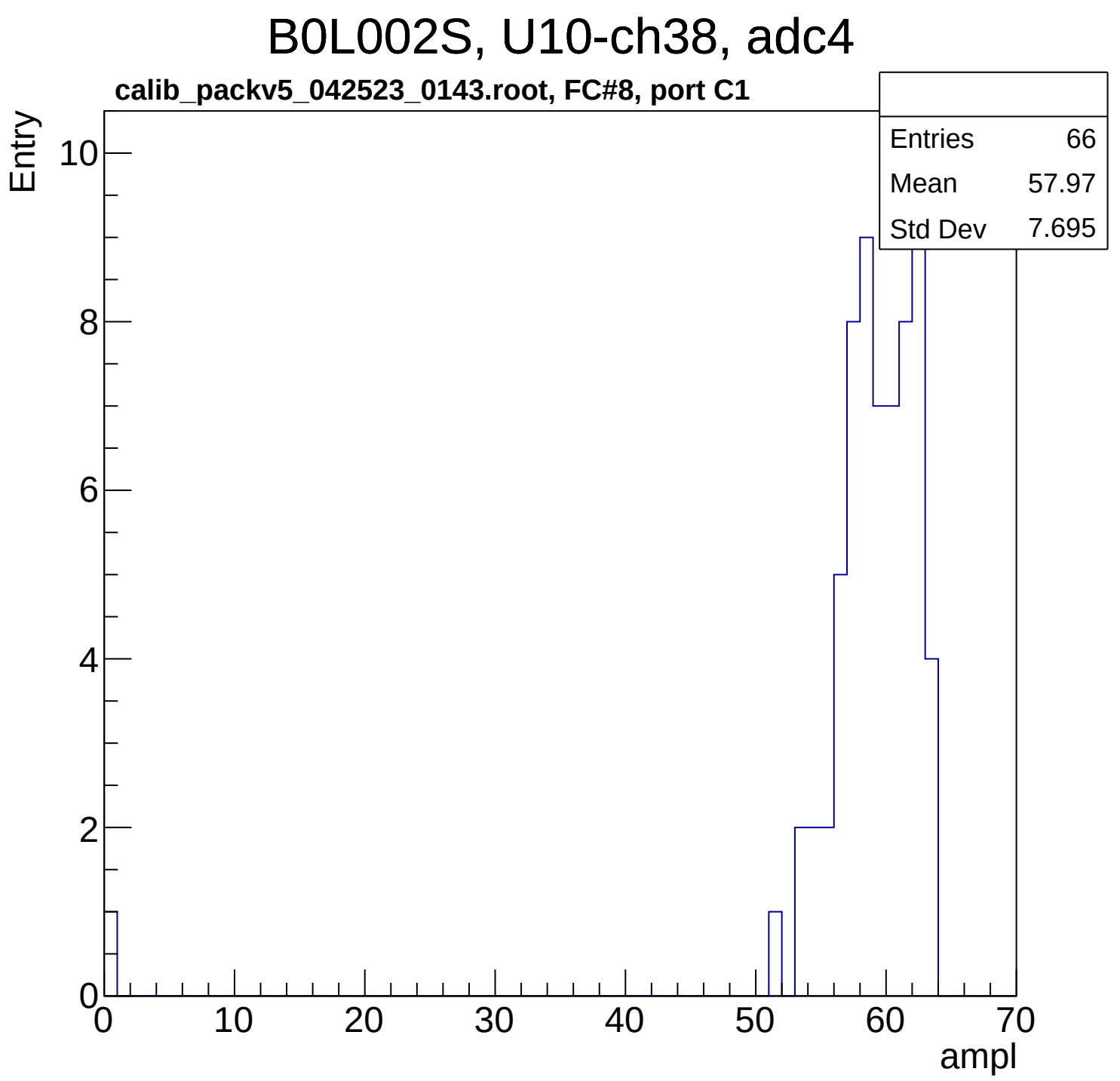

B0L002S, U10-ch37, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

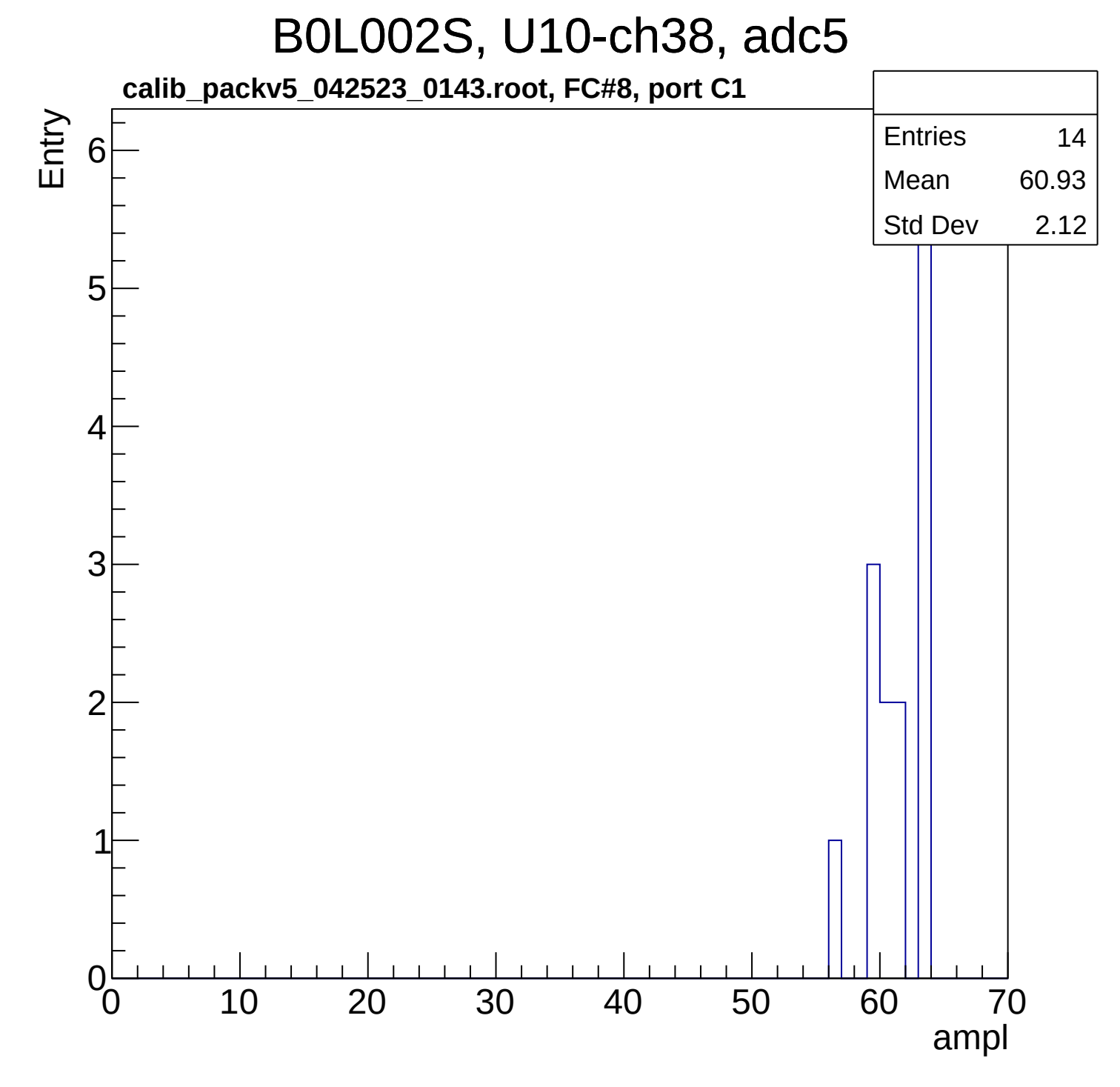

B0L002S, U10-ch38, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 32.82
Std Dev 3.267
Gaus mean : 33.6028
Gaus Width: 3.1010
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

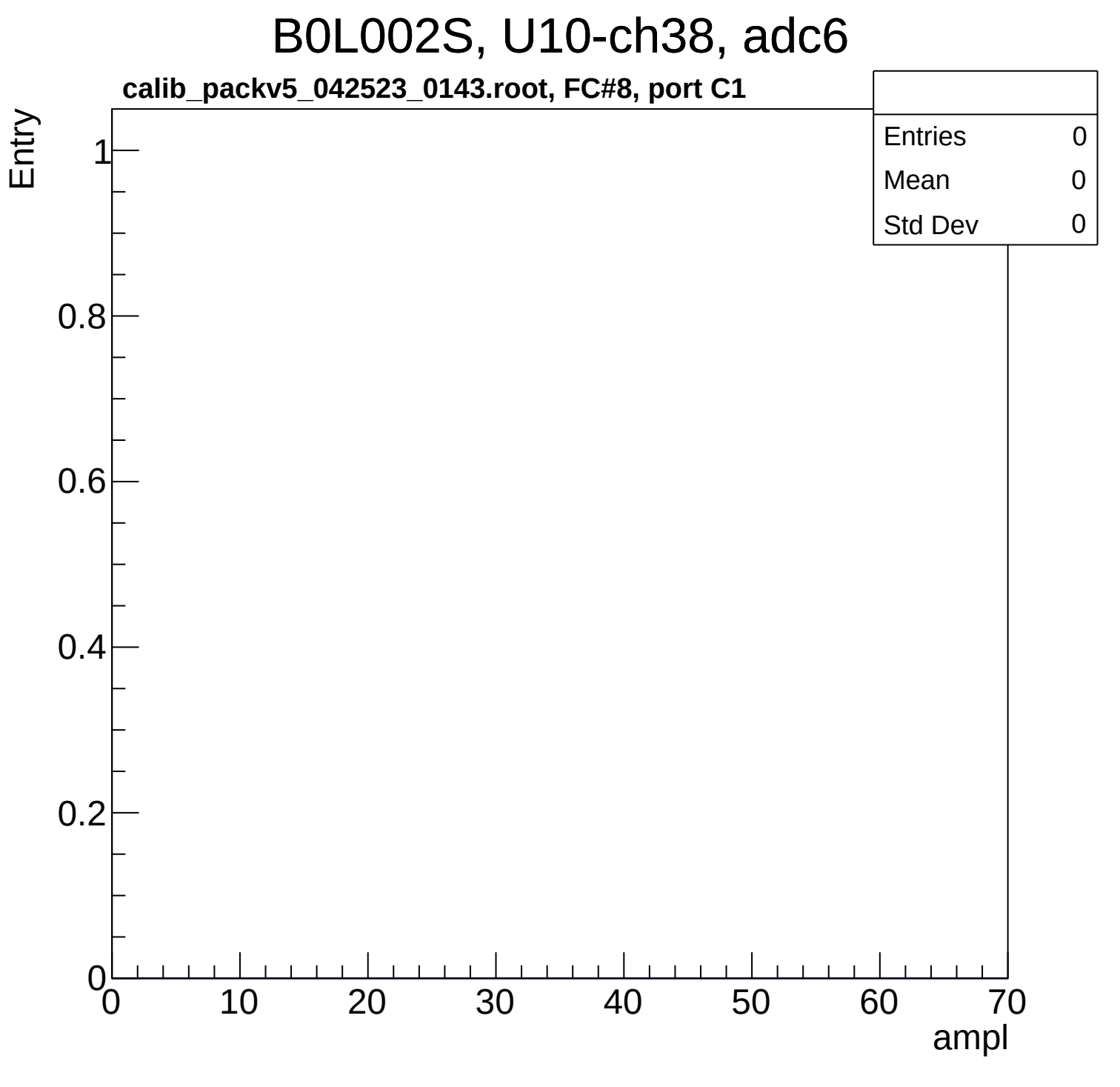

B0L002S, U10-ch38, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 39.58
Std Dev 3.446
Gaus mean : 40.2933
Gaus Width: 3.4943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

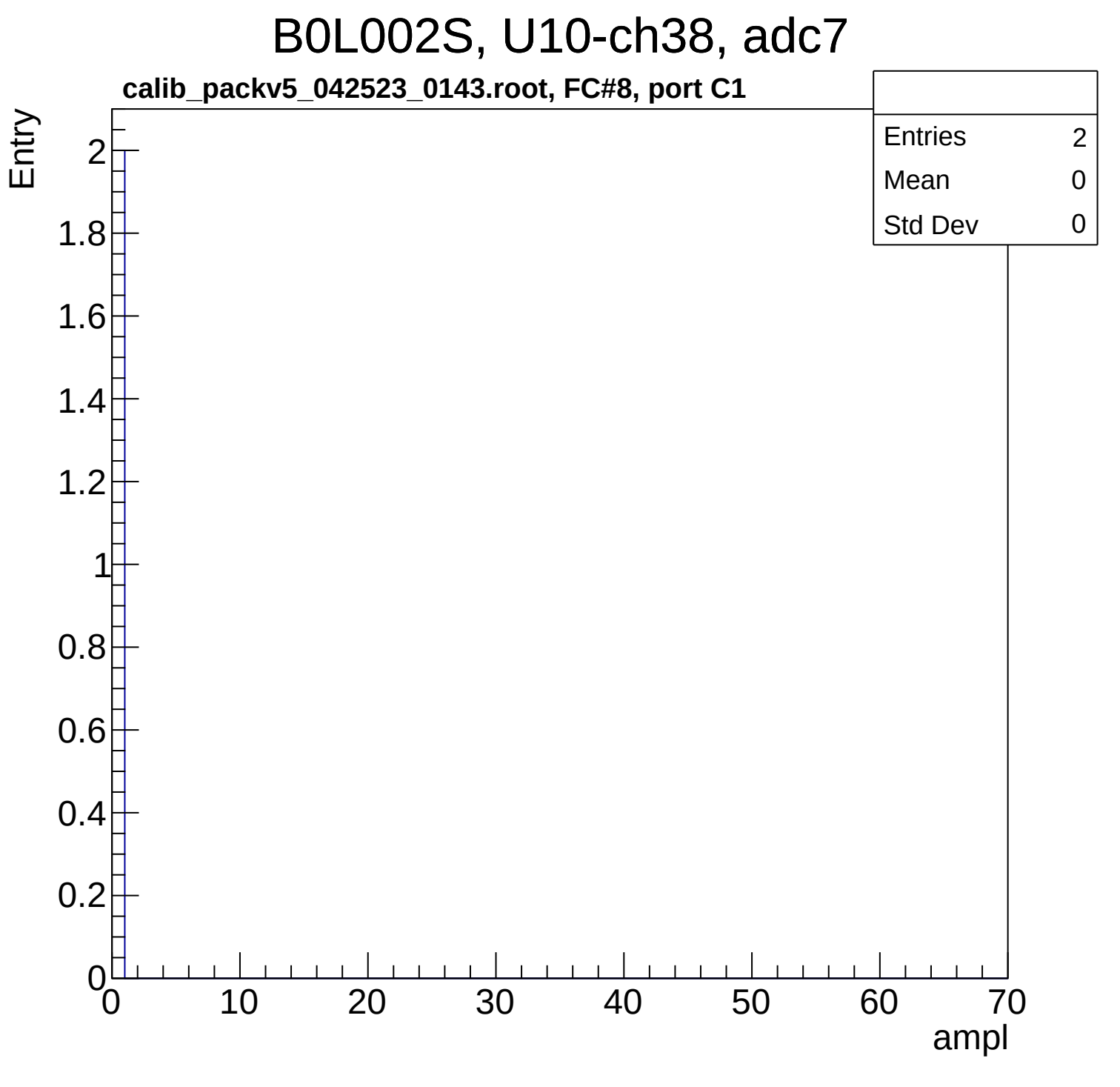

B0L002S, U10-ch38, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 46.58
Std Dev 3.239
Gaus mean : 47.2499
Gaus Width: 2.9684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

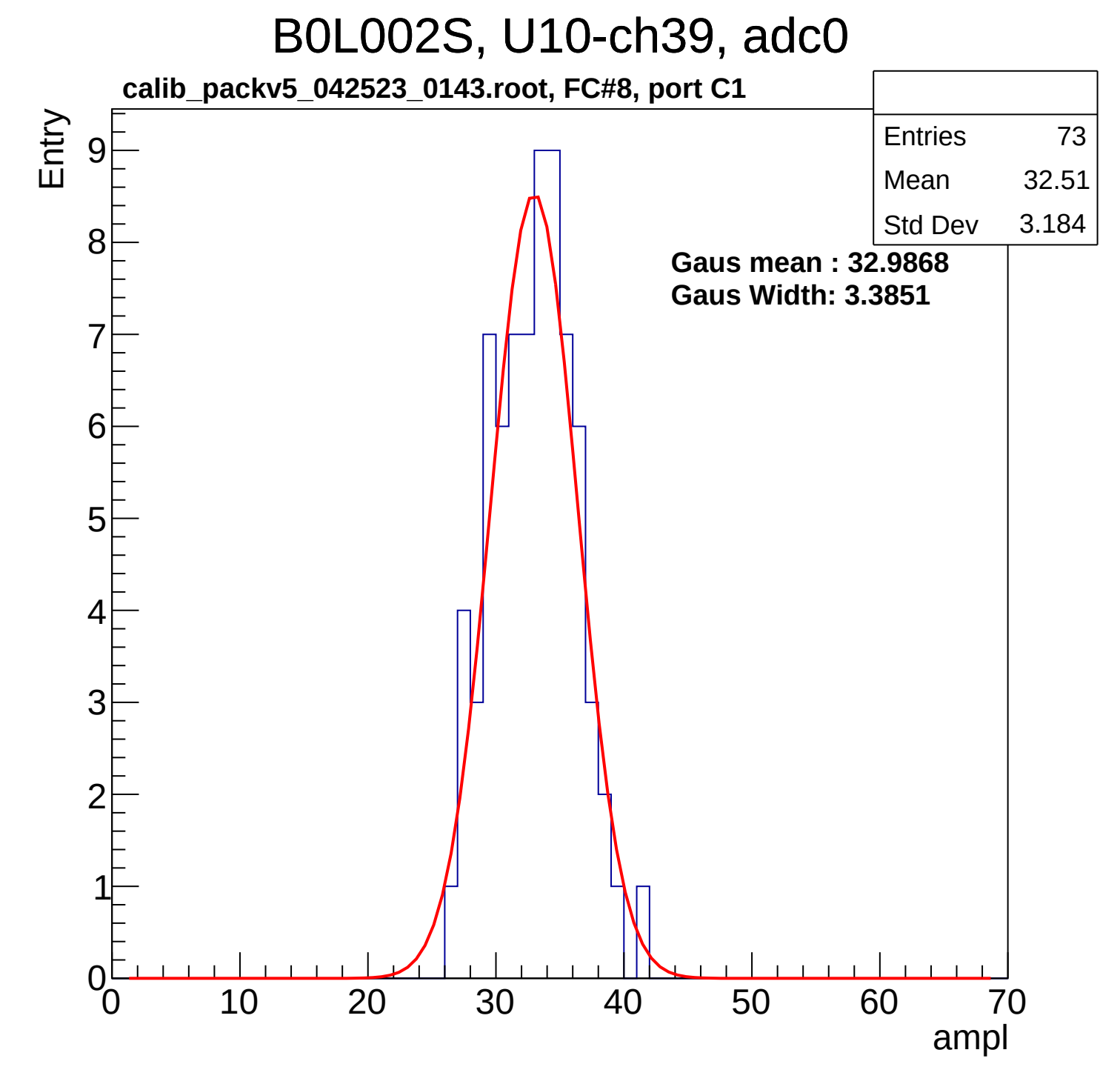

B0L002S, U10-ch38, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 52.09
Std Dev 3.205
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

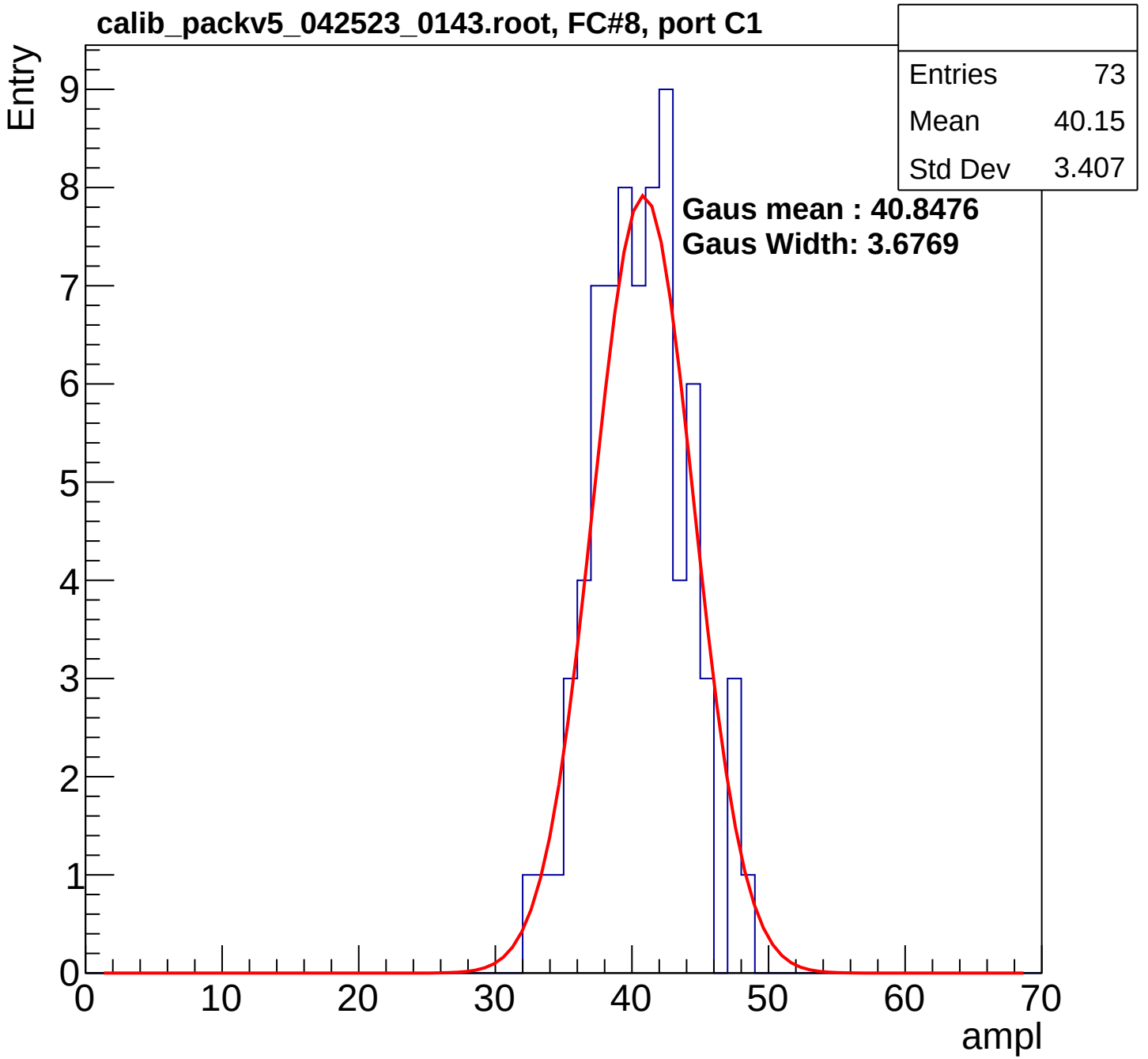

B0L002S, U10-ch38, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.97
Std Dev 7.695
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

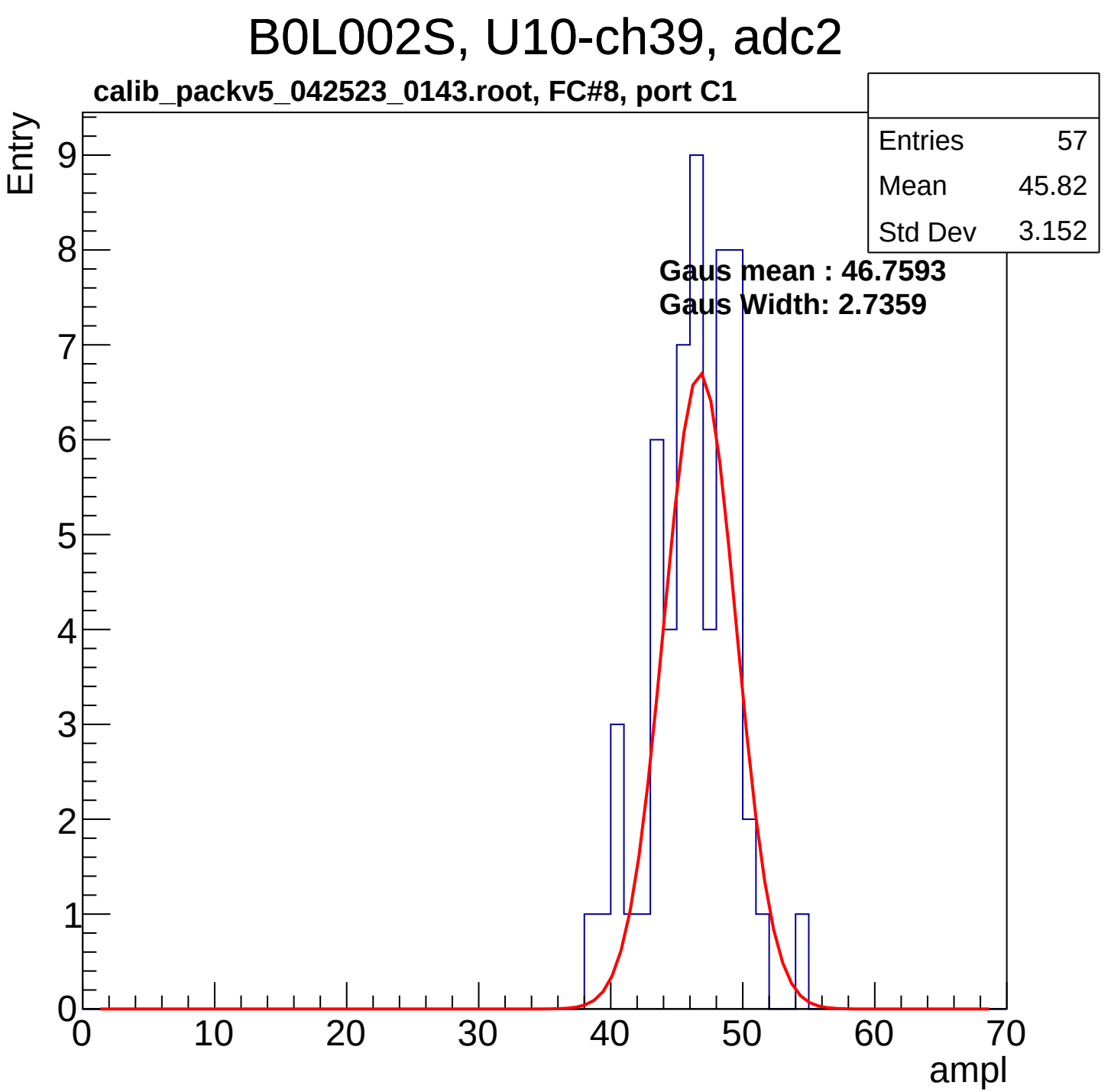

B0L002S, U10-ch38, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 60.93
Std Dev 2.12
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

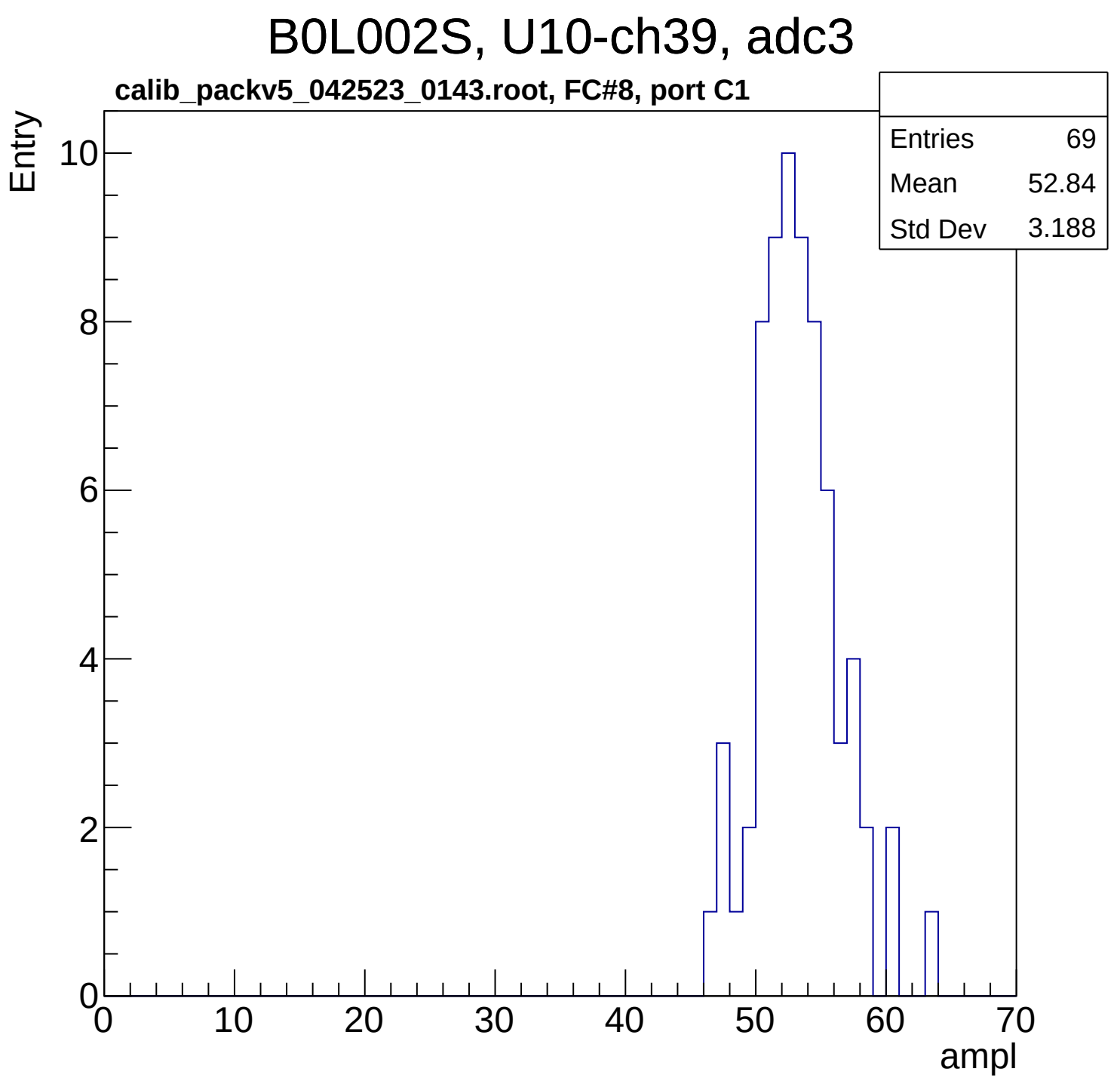

B0L002S, U10-ch38, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

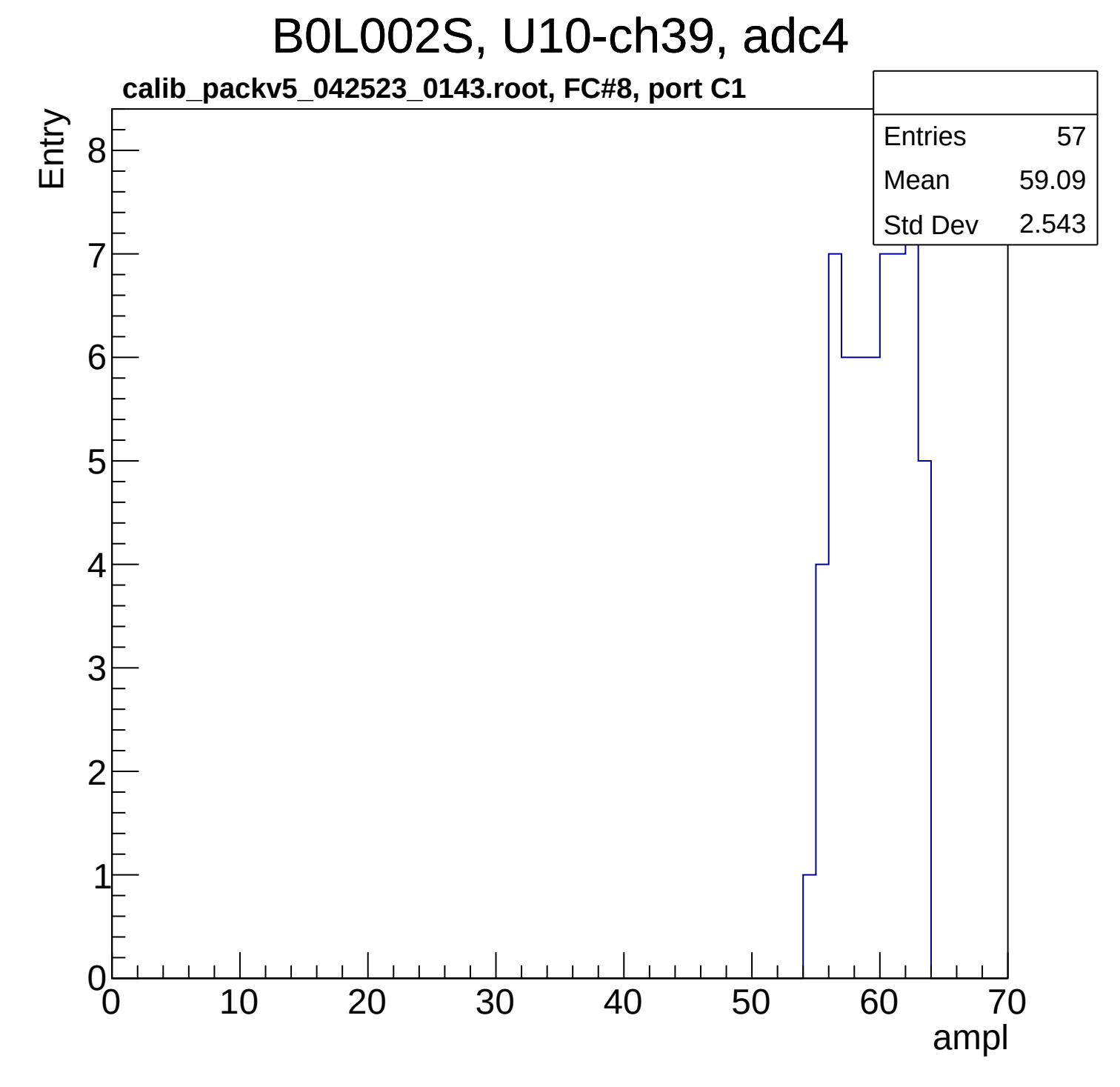

B0L002S, U10-ch38, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

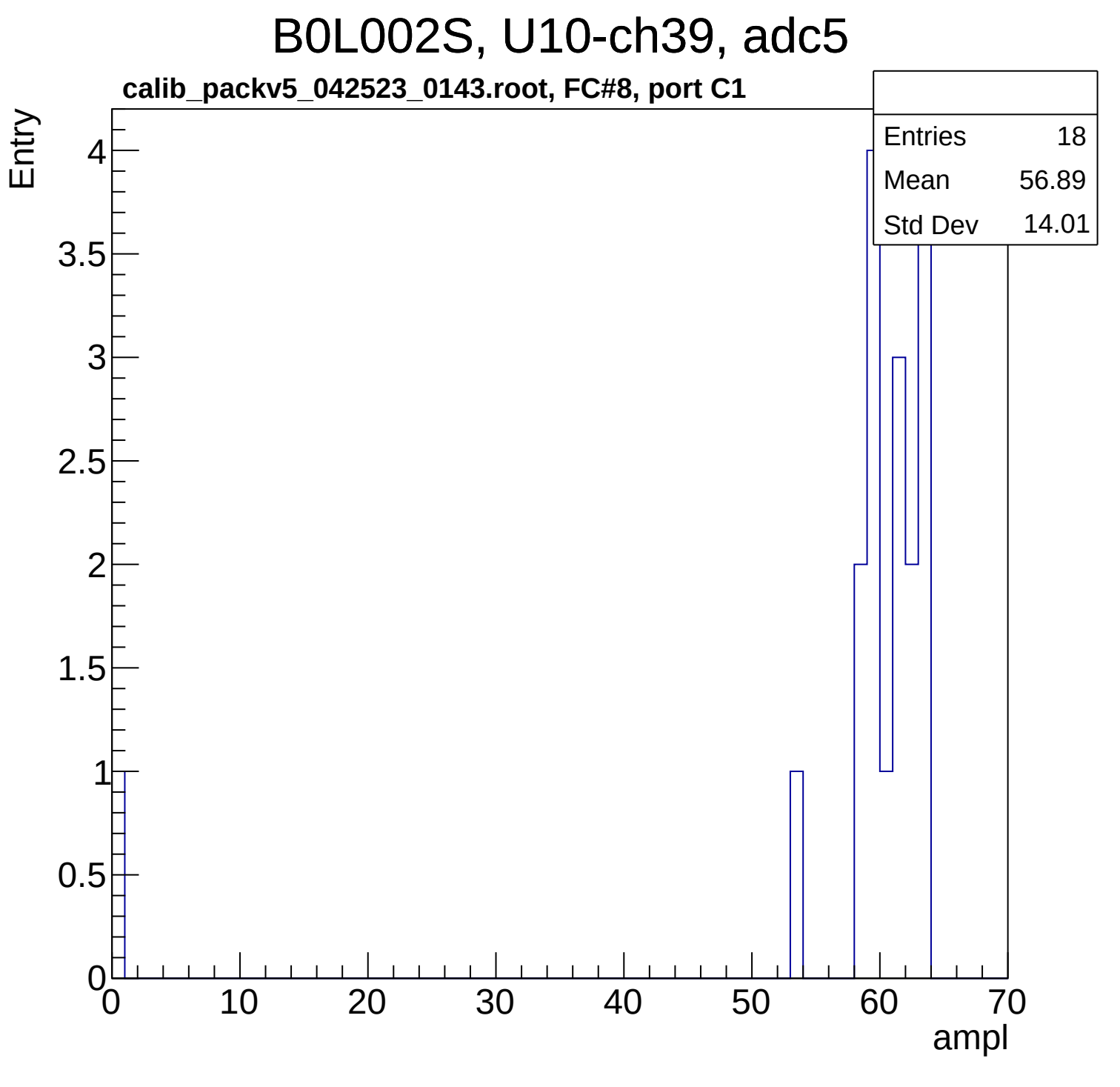

B0L002S, U10-ch39, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 32.51
Std Dev 3.184
Gaus mean : 32.9868
Gaus Width: 3.3851
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

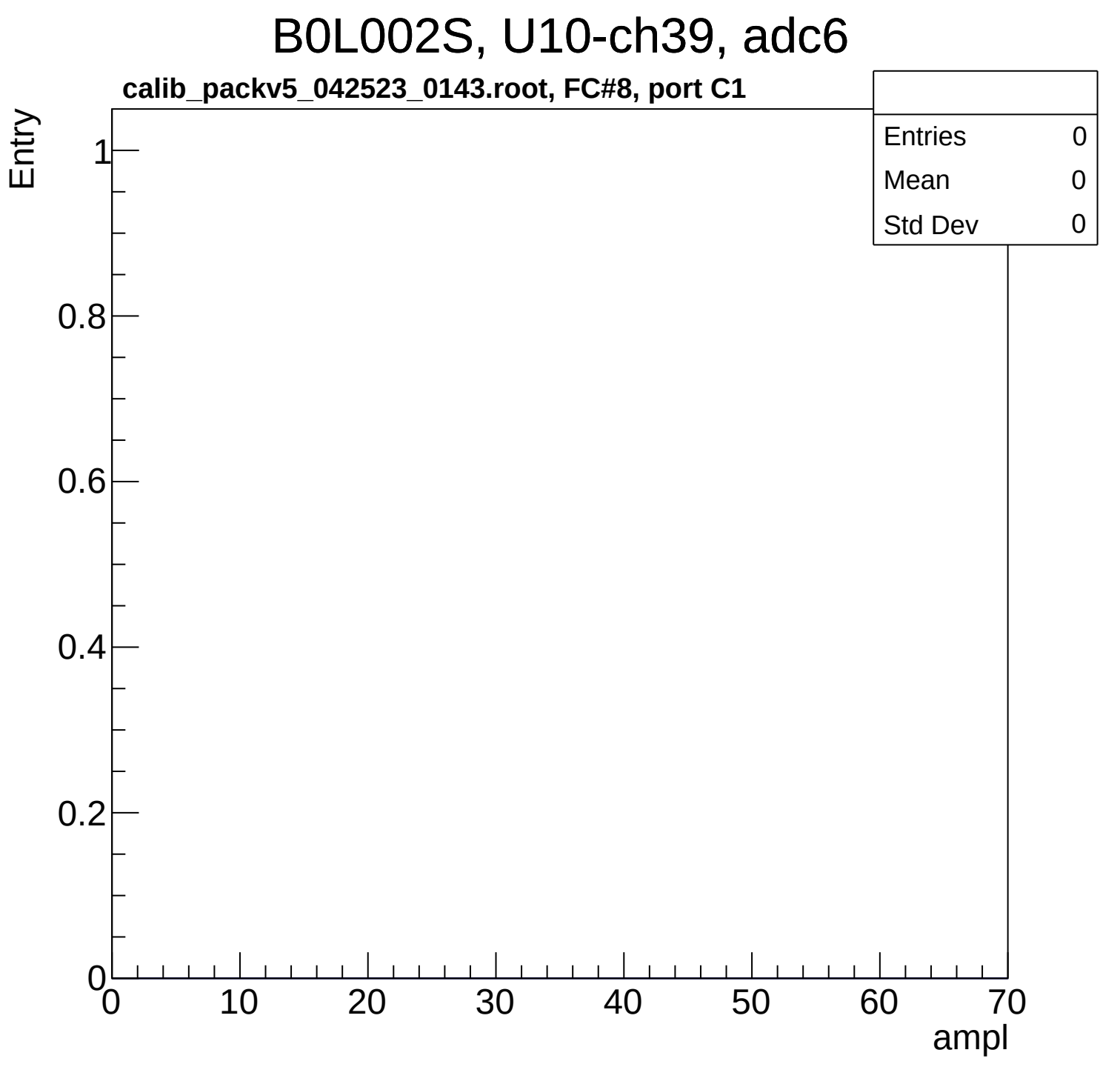

B0L002S, U10-ch39, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 40.15
Std Dev 3.407
Gaus mean : 40.8476
Gaus Width: 3.6769
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

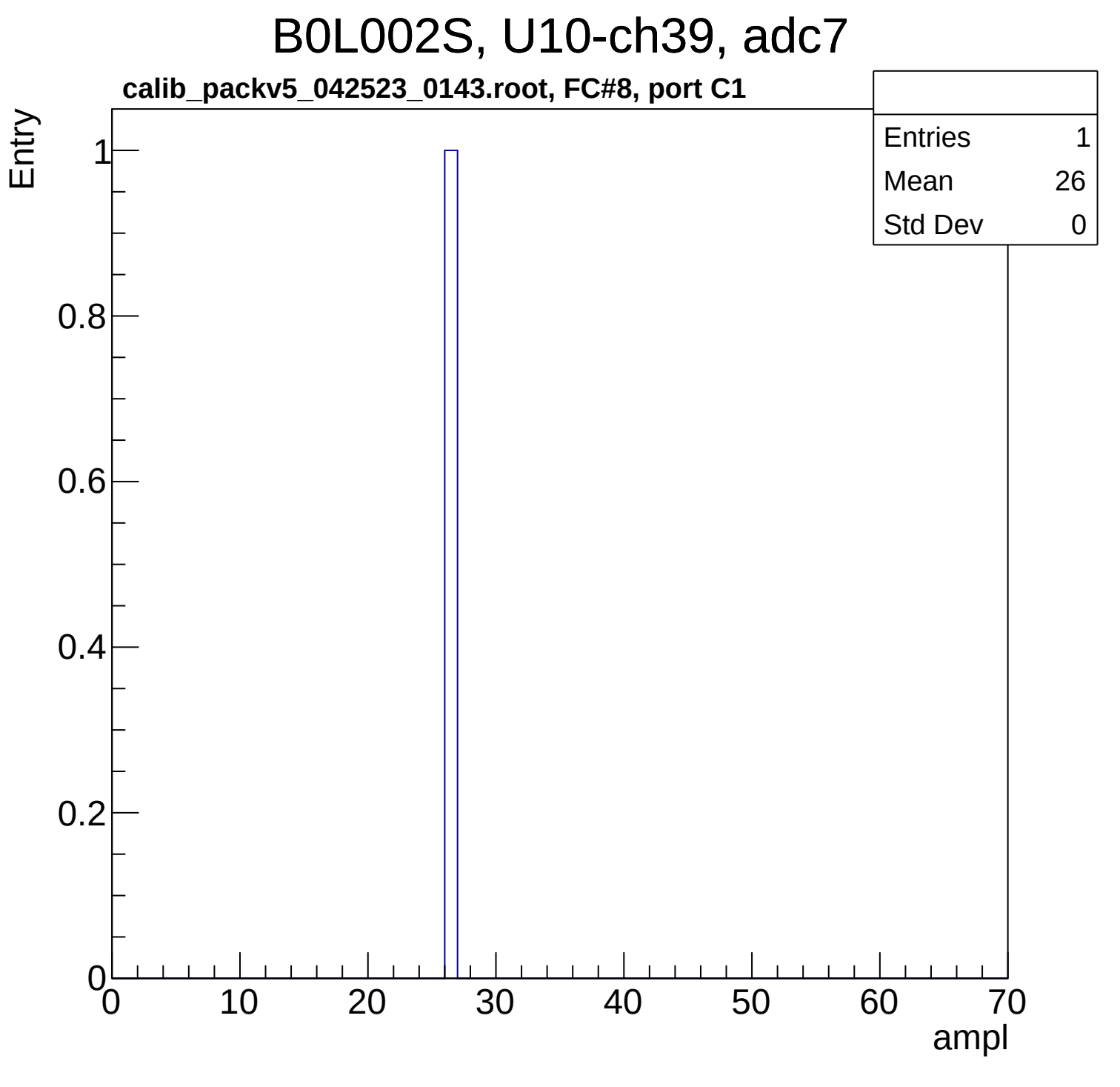

B0L002S, U10-ch39, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 45.82
Std Dev 3.152
Gaus mean : 46.7593
Gaus Width: 2.7359
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

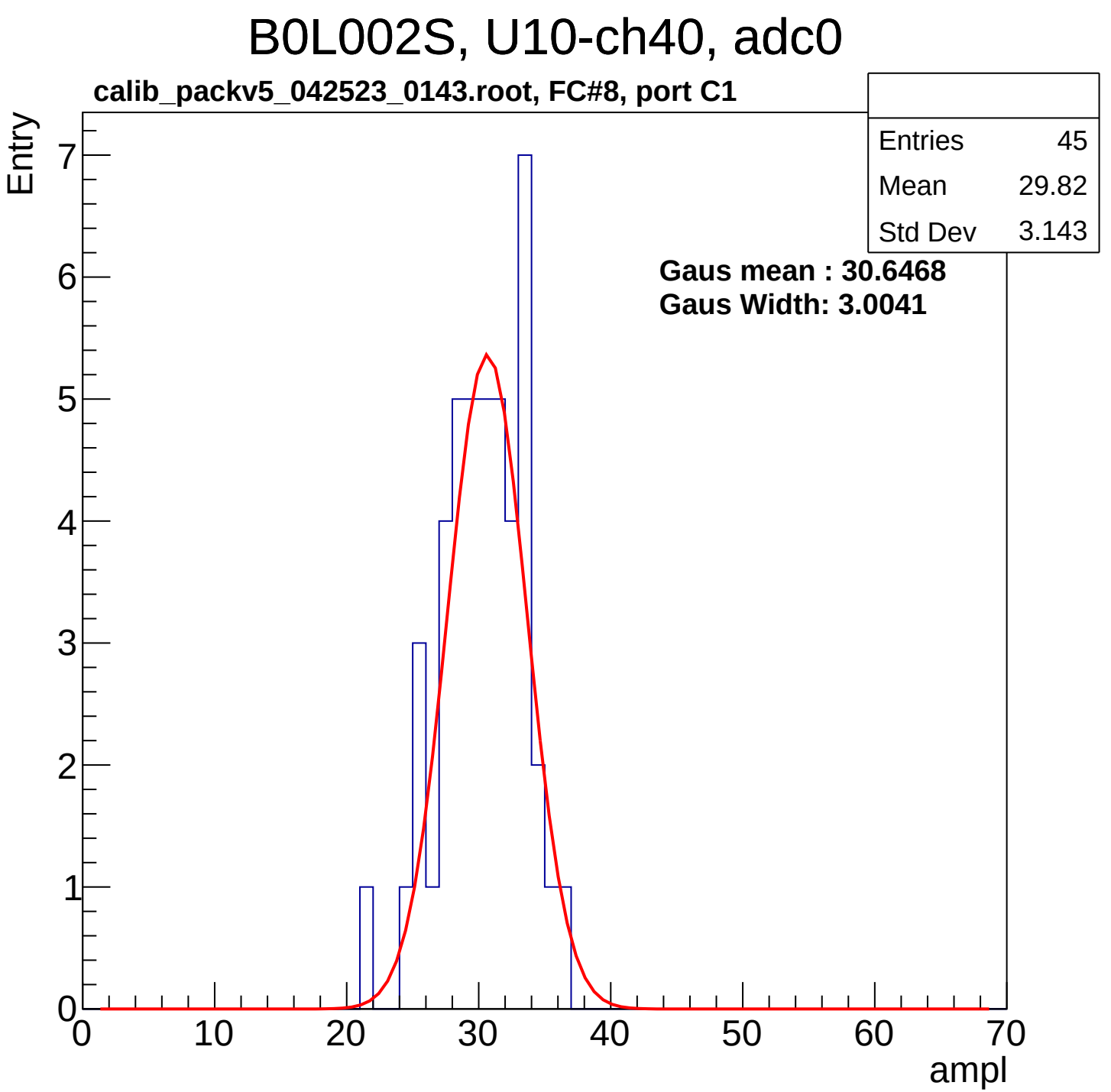

B0L002S, U10-ch39, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 52.84
Std Dev 3.188
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

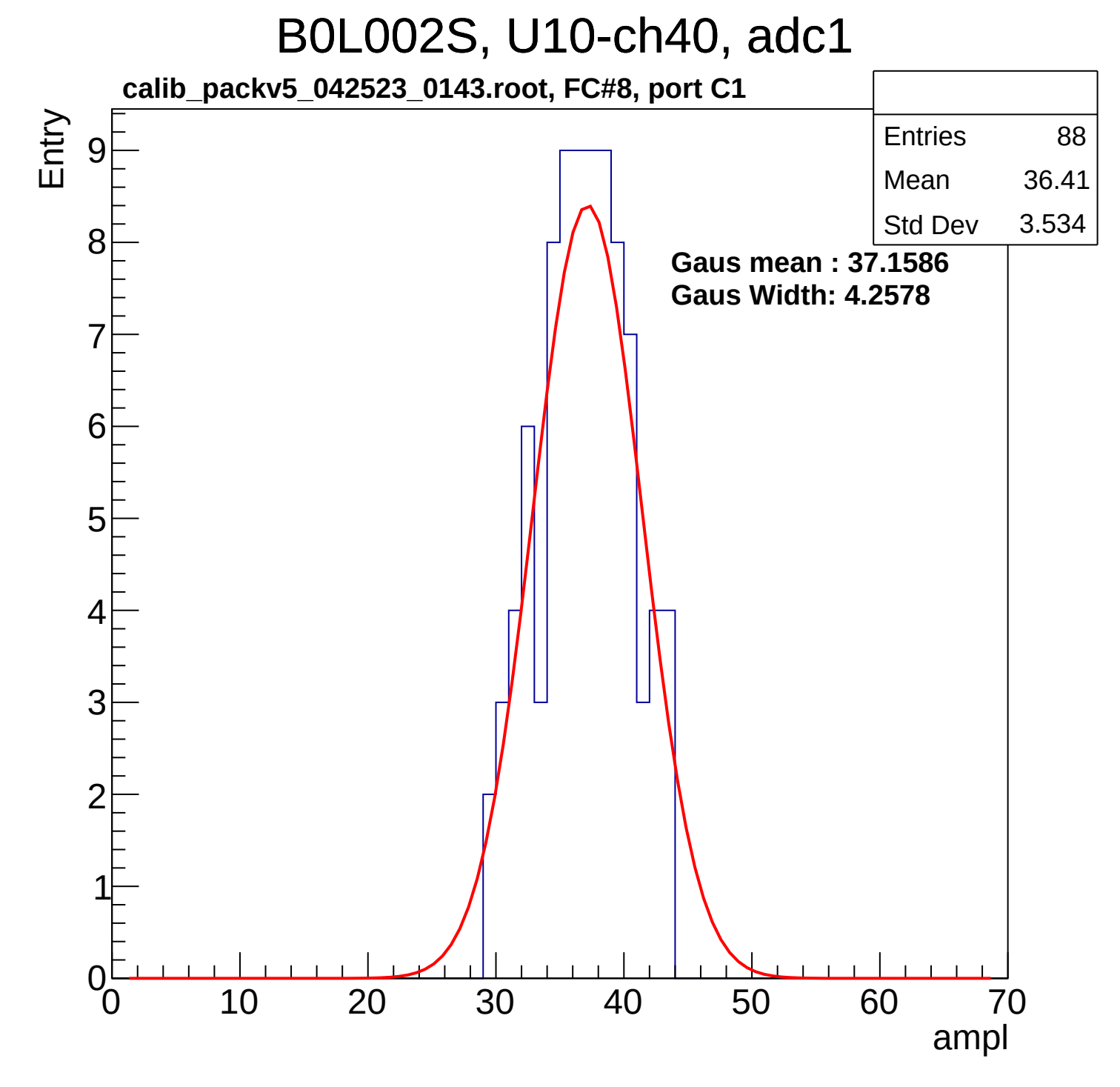

B0L002S, U10-ch39, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 59.09
Std Dev 2.543
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

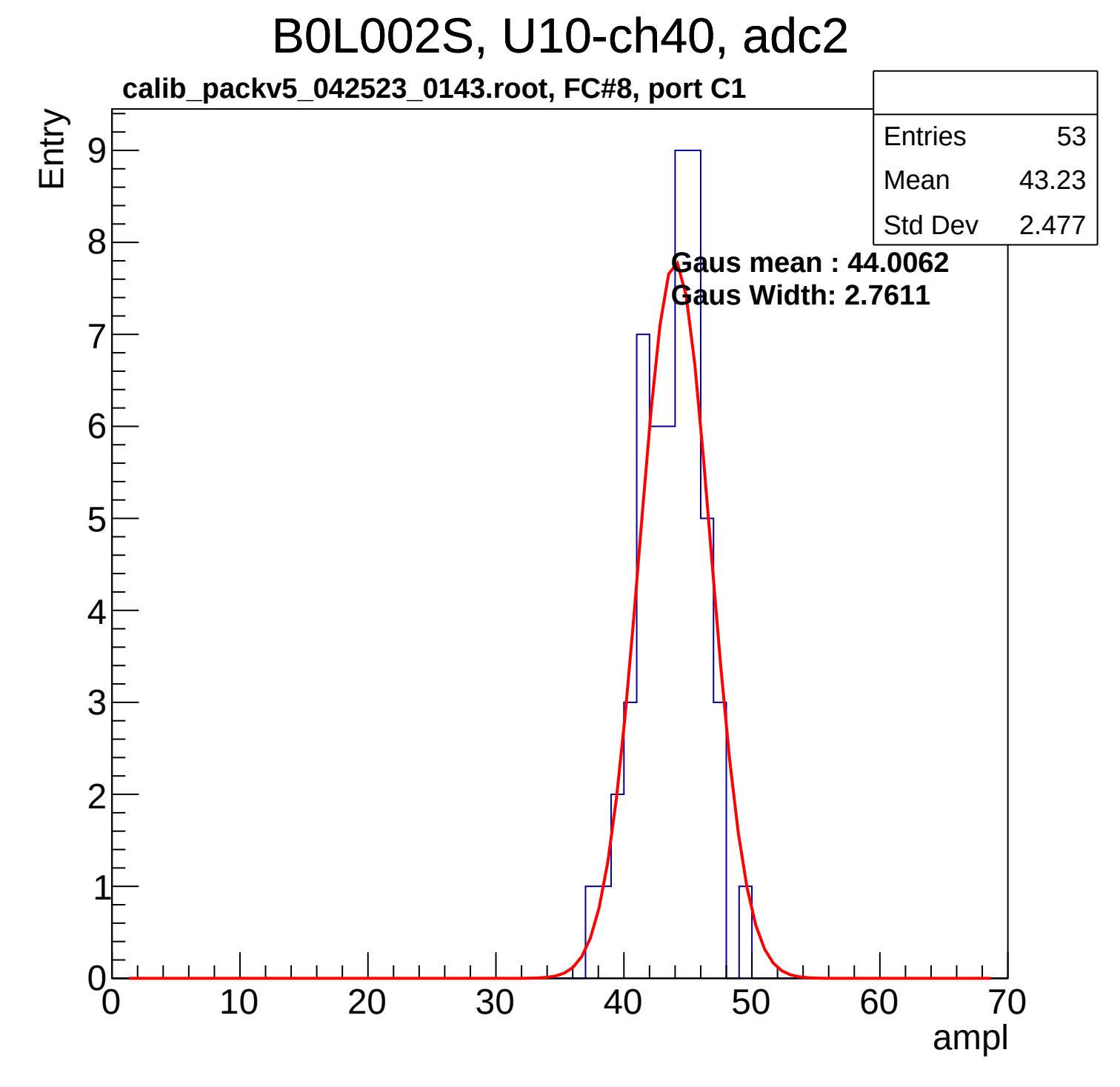

B0L002S, U10-ch39, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 56.89
Std Dev 14.01
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

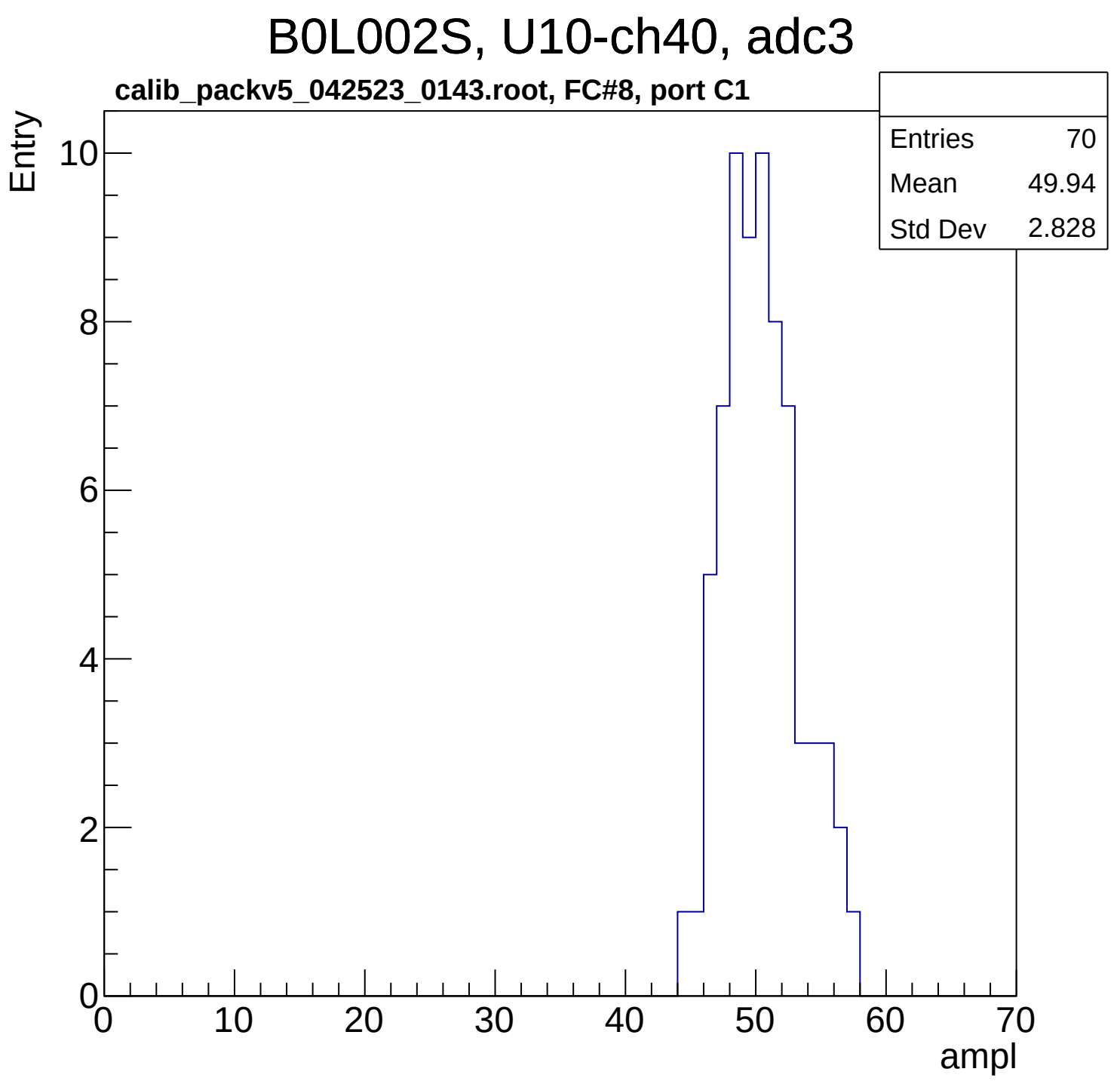

B0L002S, U10-ch39, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

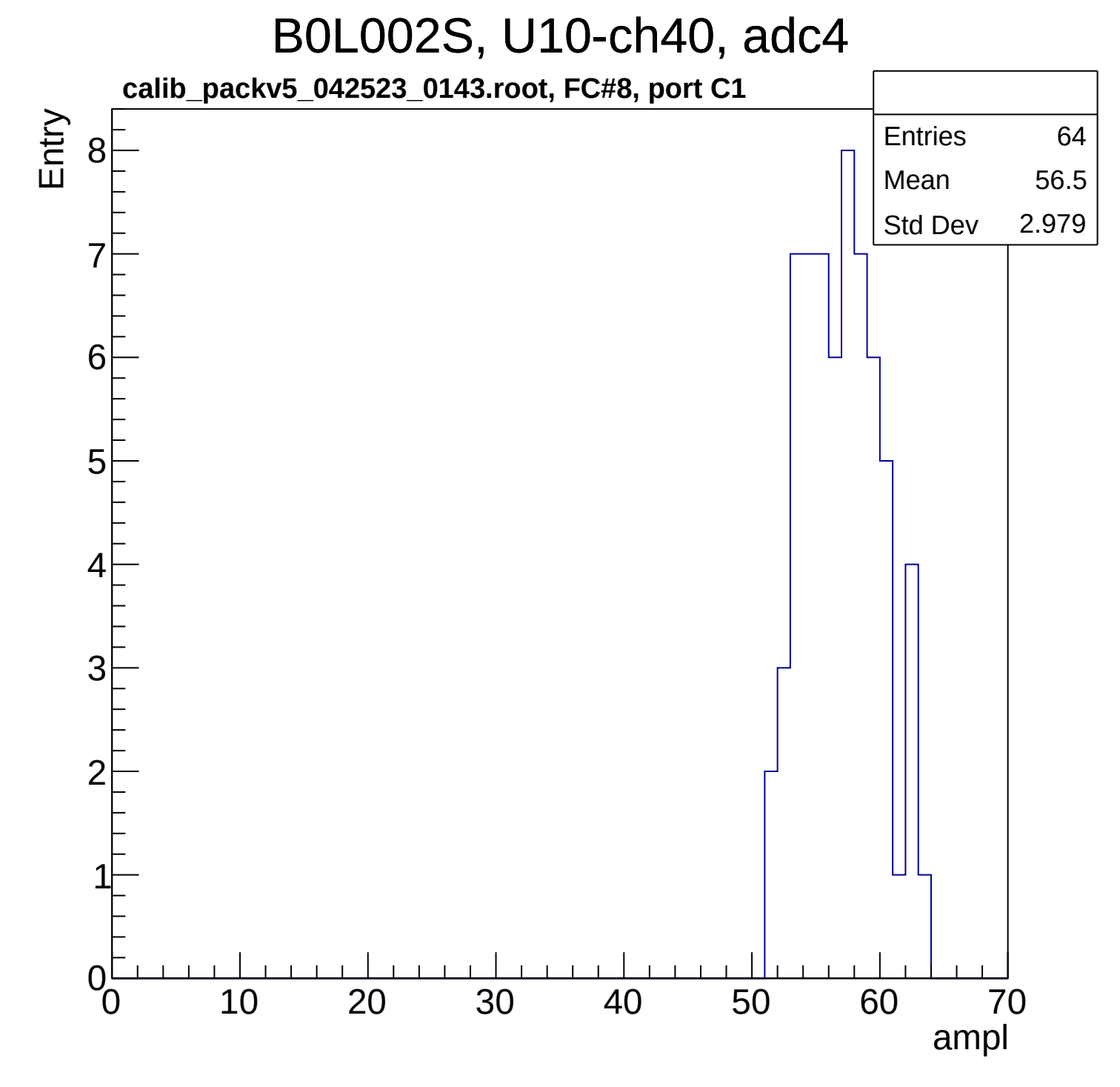

B0L002S, U10-ch39, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

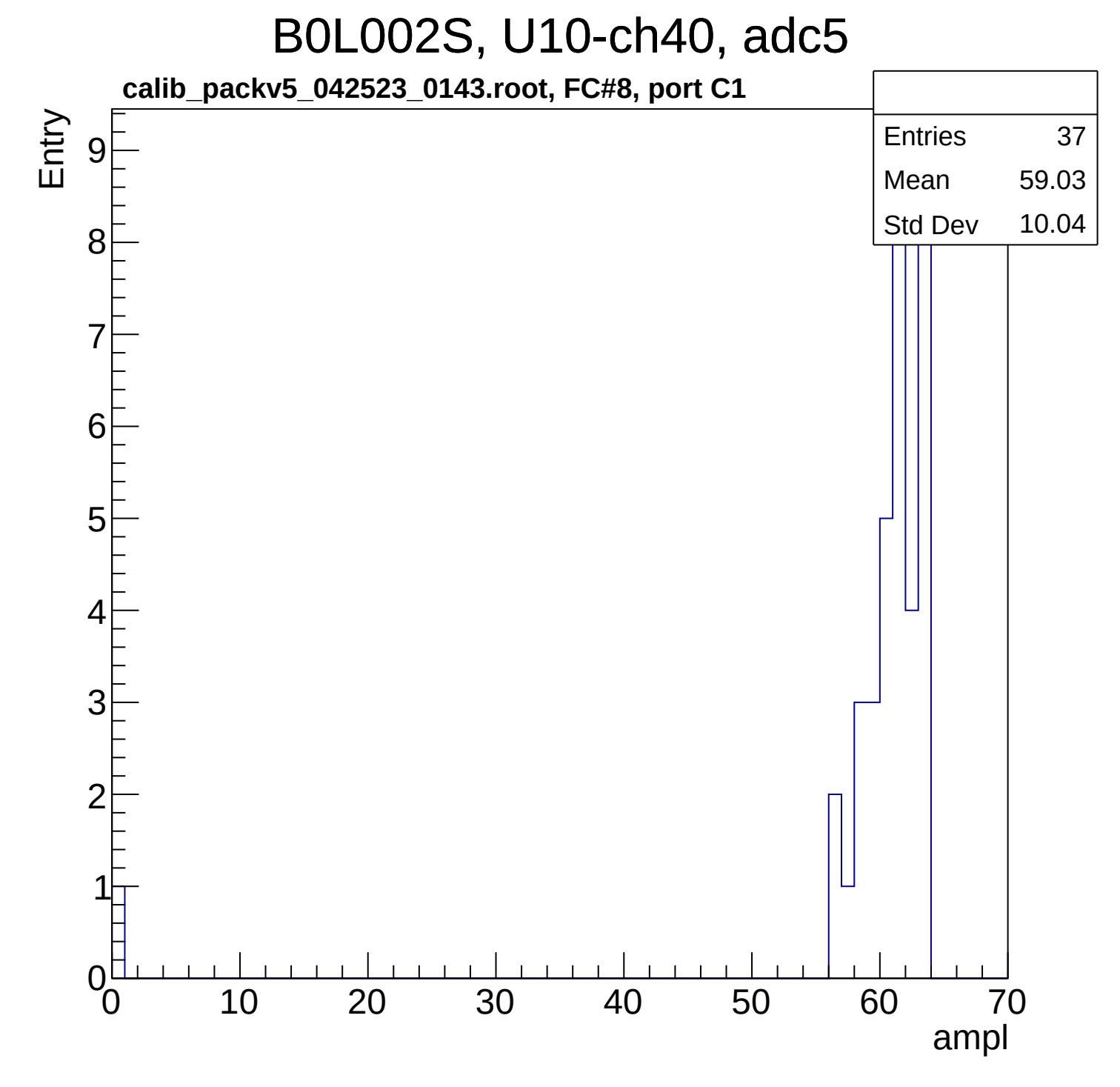

B0L002S, U10-ch40, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 29.82
Std Dev 3.143
Gaus mean : 30.6468
Gaus Width: 3.0041
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

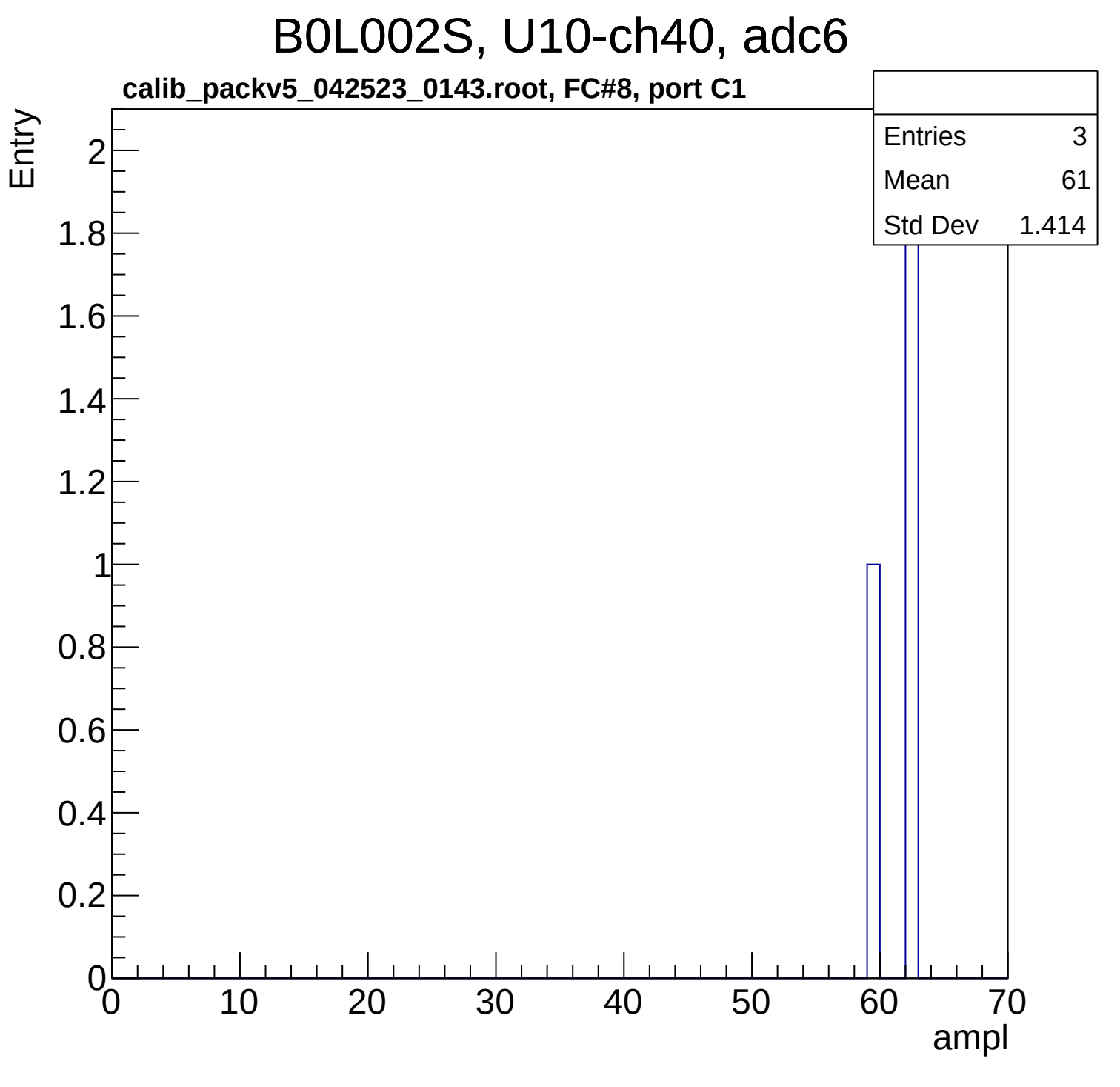

B0L002S, U10-ch40, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 36.41
Std Dev 3.534
Gaus mean : 37.1586
Gaus Width: 4.2578
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

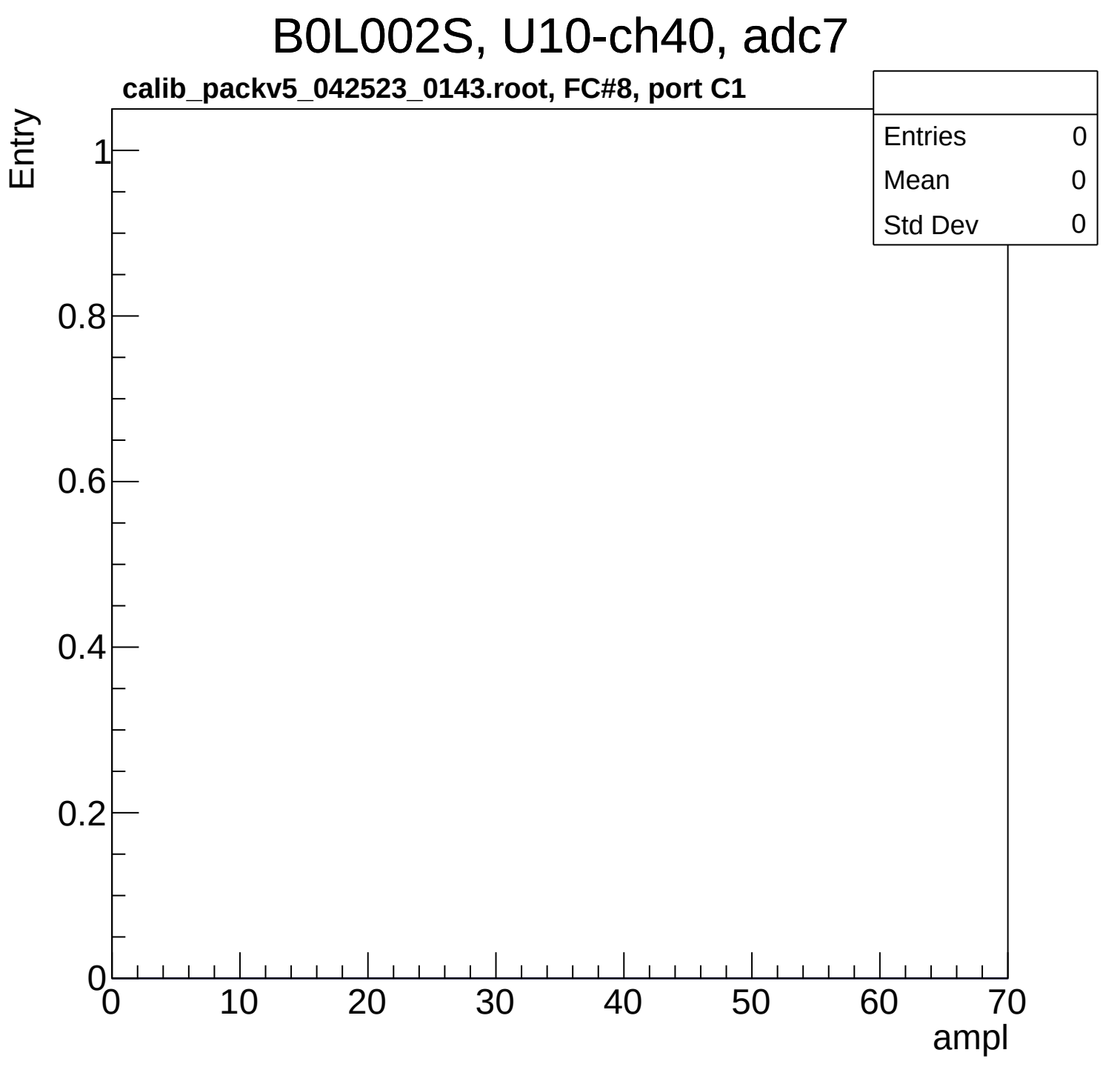

B0L002S, U10-ch40, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 43.23
Std Dev 2.477
Gaus mean : 44.0062
Gaus Width: 2.7611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

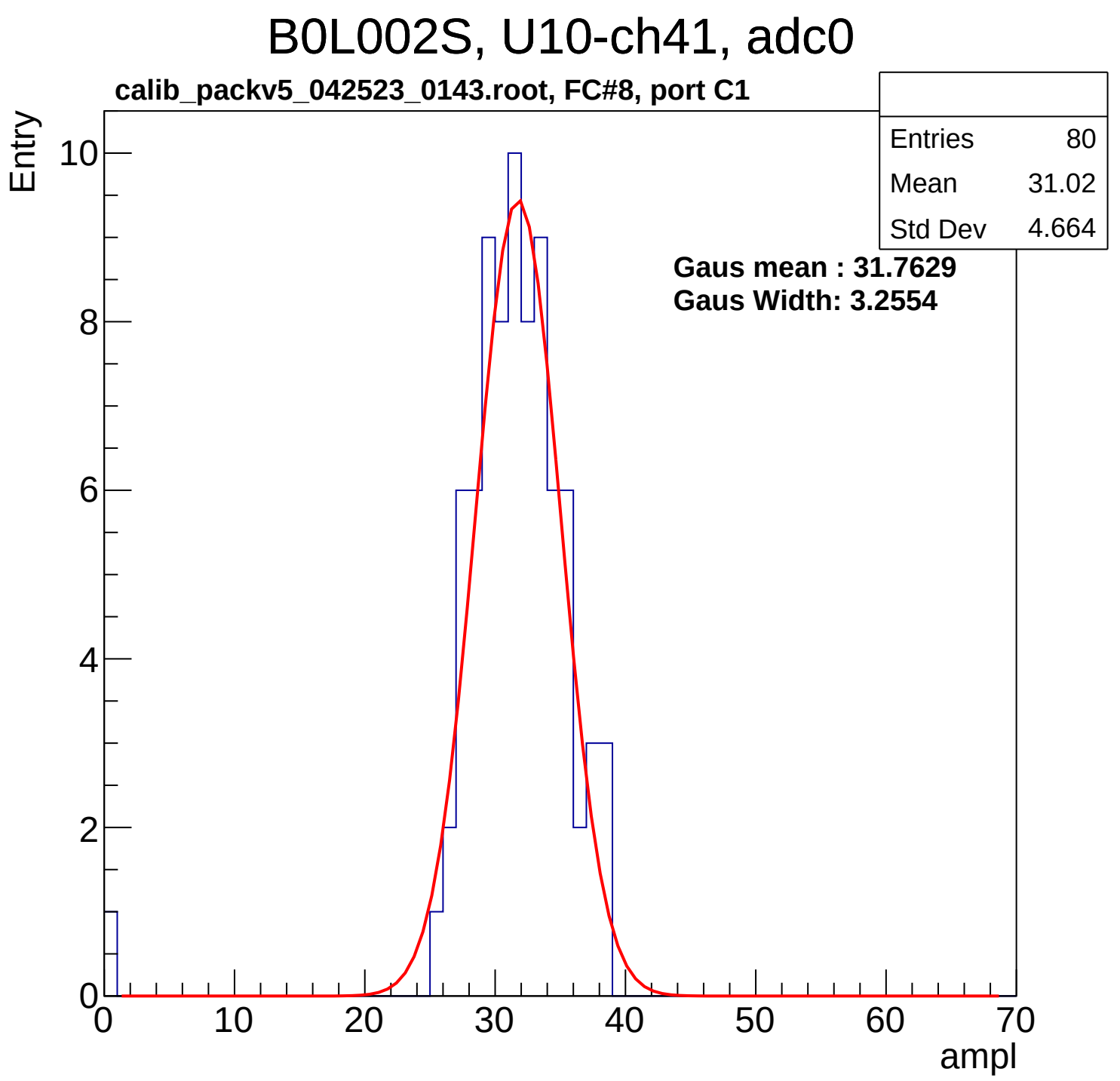

B0L002S, U10-ch40, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 49.94
Std Dev 2.828
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

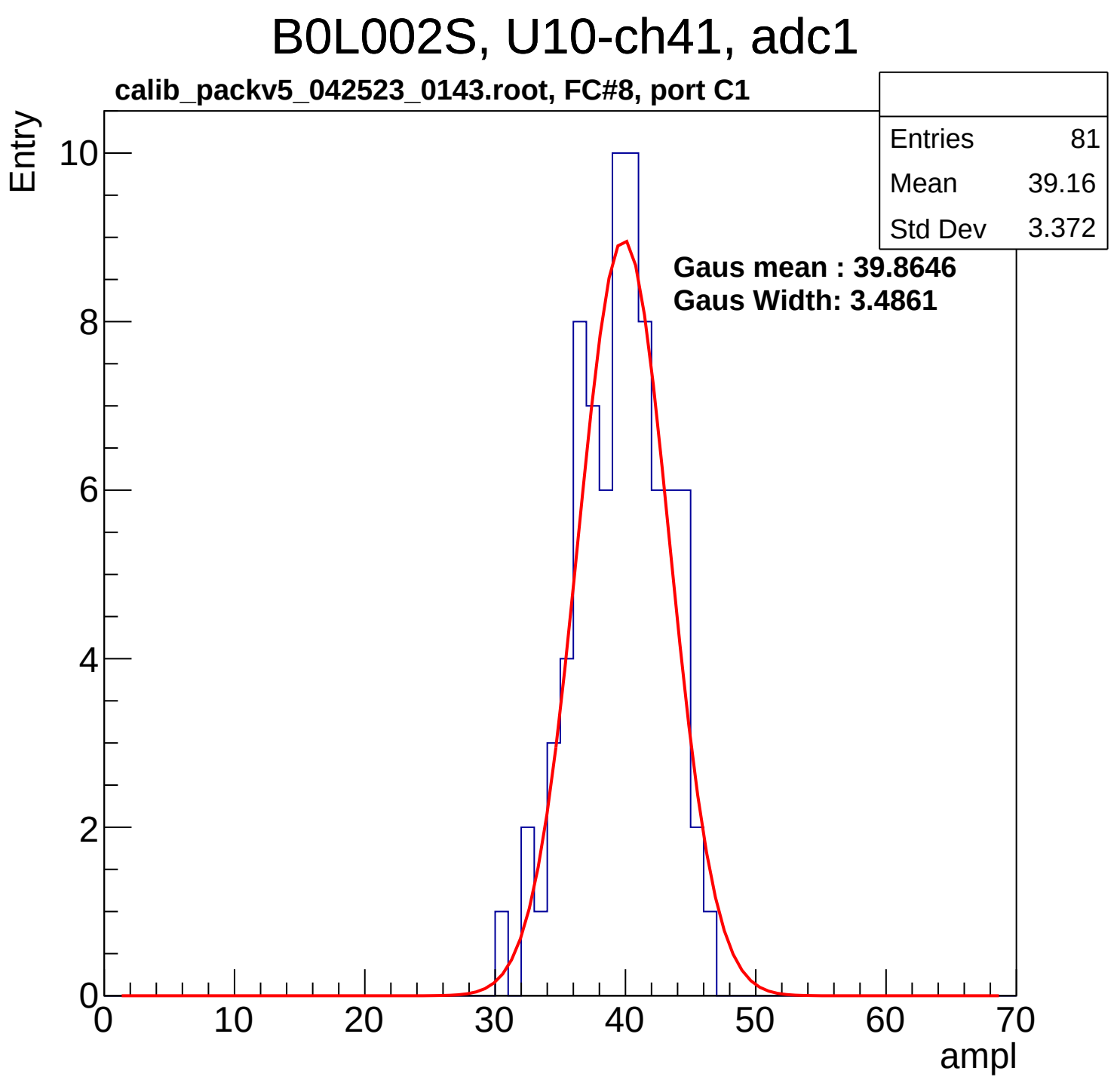

B0L002S, U10-ch40, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.5
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

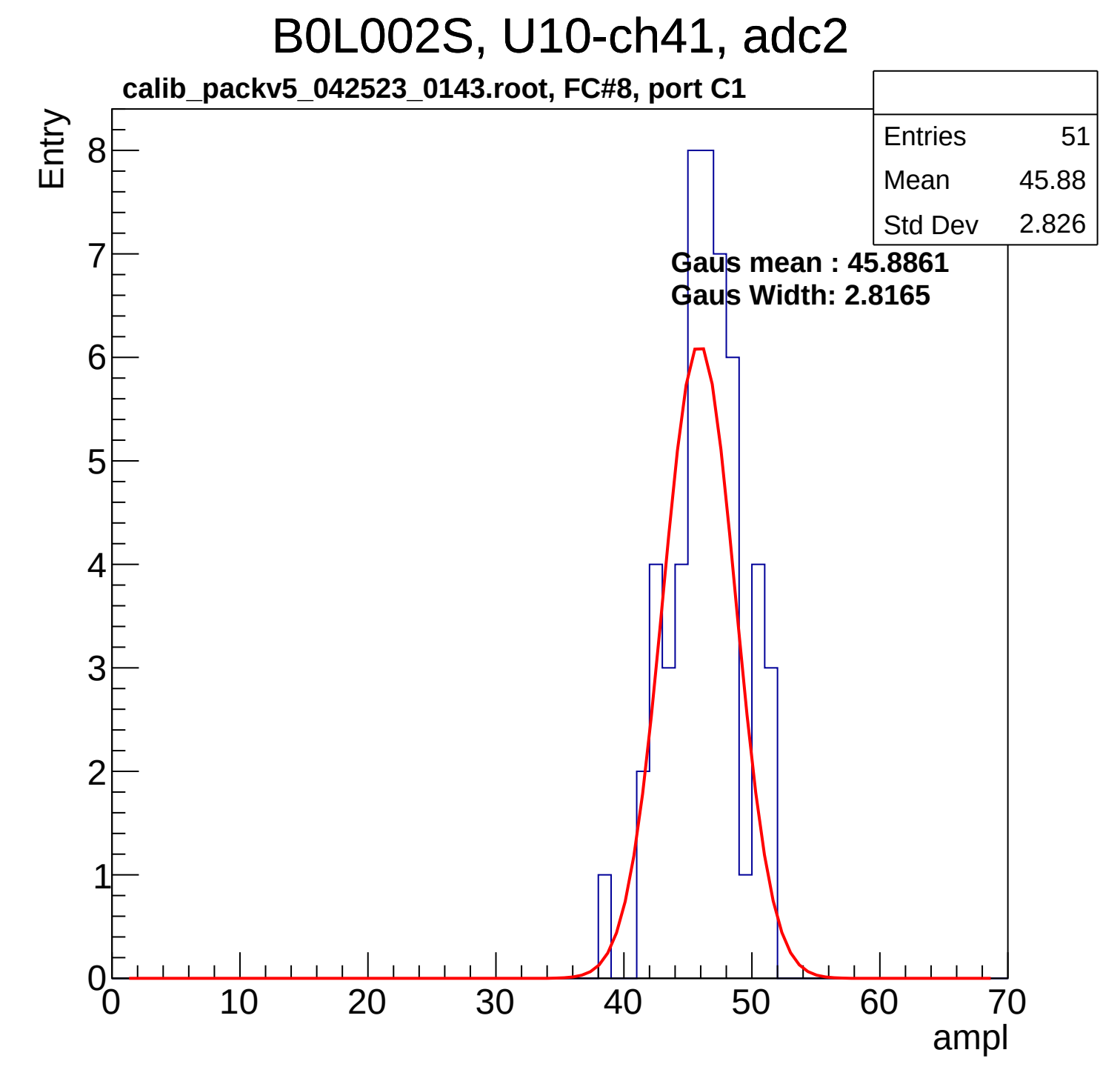

B0L002S, U10-ch40, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.03
Std Dev 10.04
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

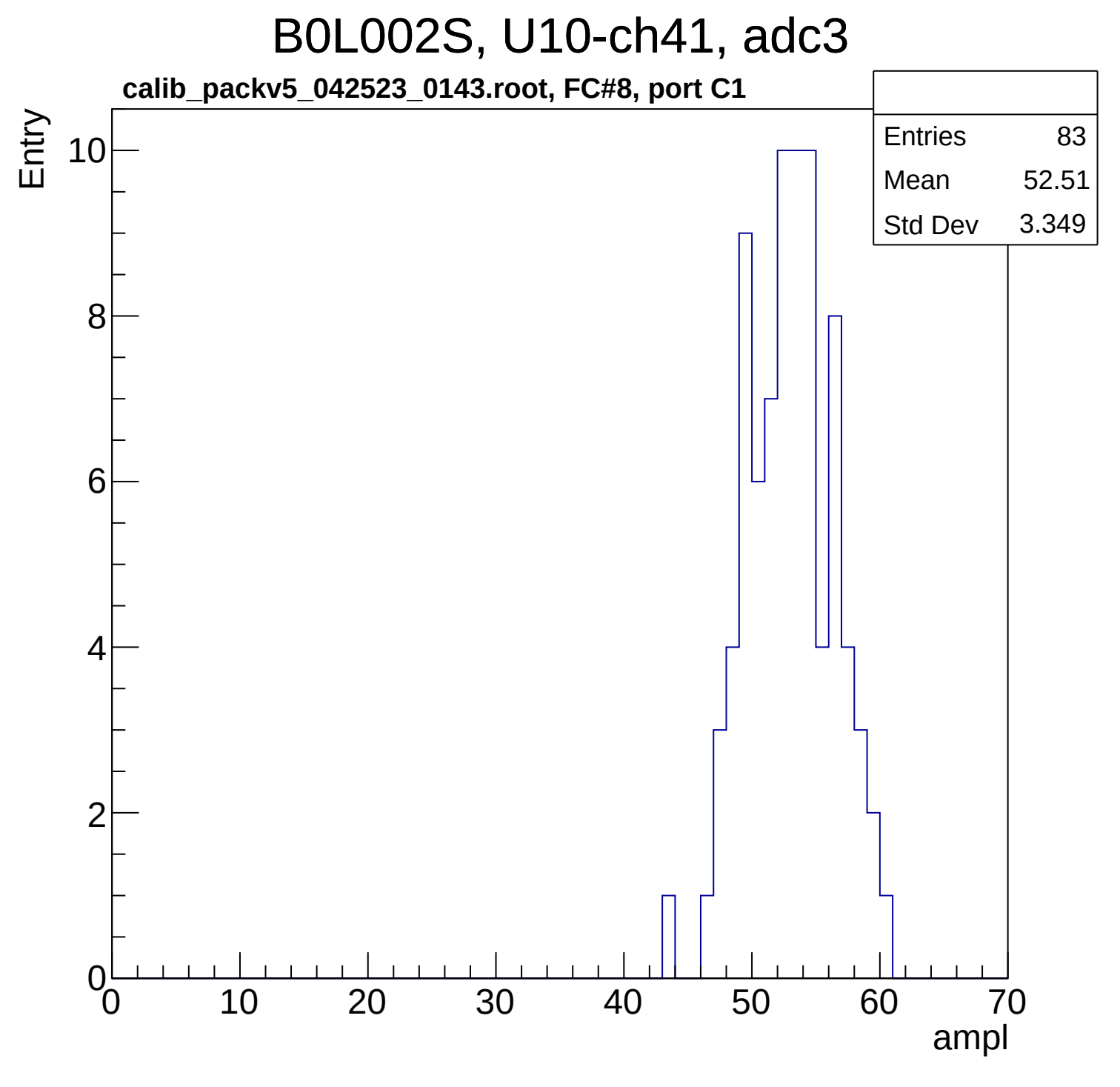

B0L002S, U10-ch40, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

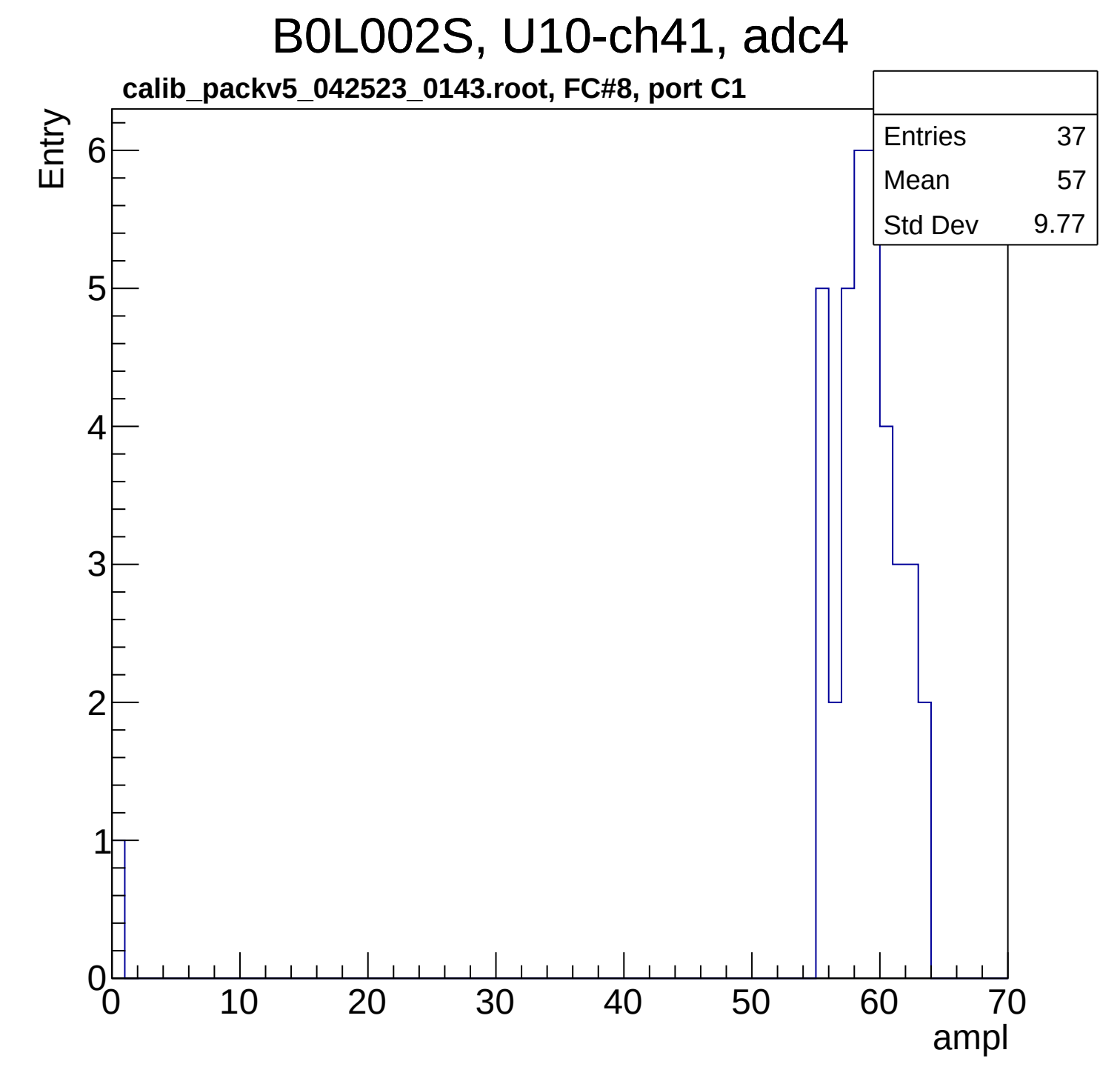

B0L002S, U10-ch40, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

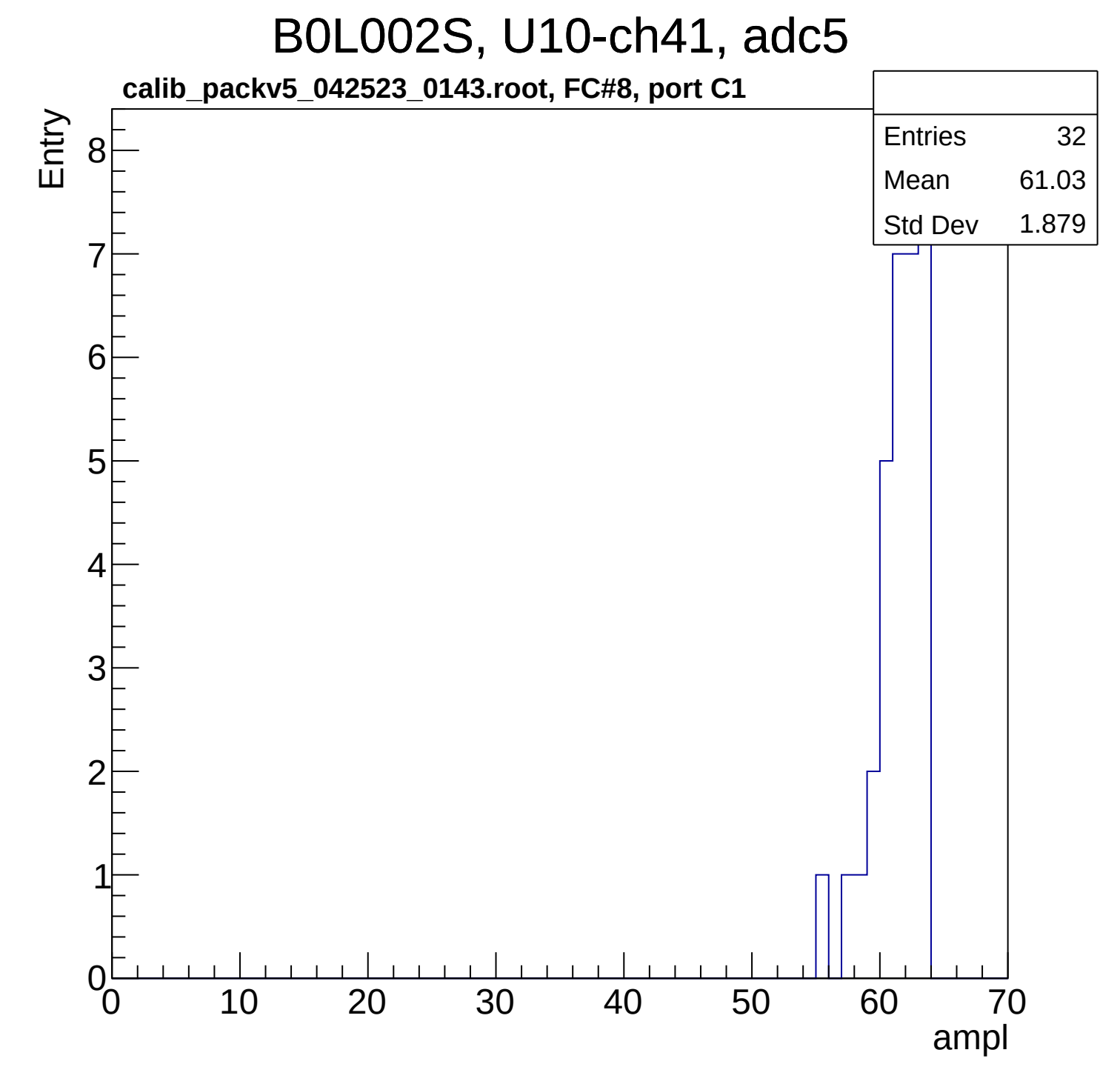

B0L002S, U10-ch41, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 31.02
Std Dev 4.664
Gaus mean : 31.7629
Gaus Width: 3.2554
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

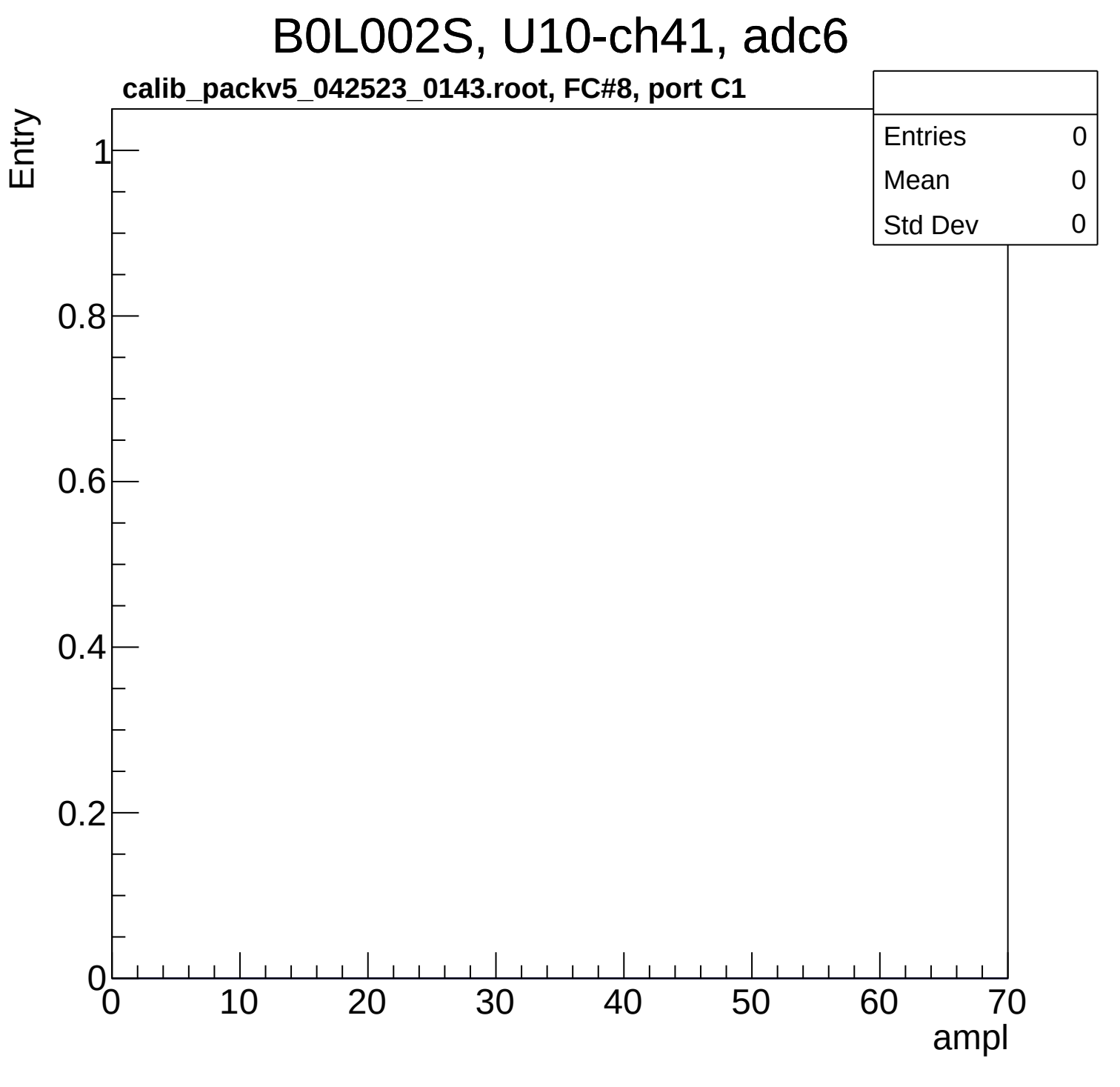

B0L002S, U10-ch41, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 39.16
Std Dev 3.372
Gaus mean : 39.8646
Gaus Width: 3.4861
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

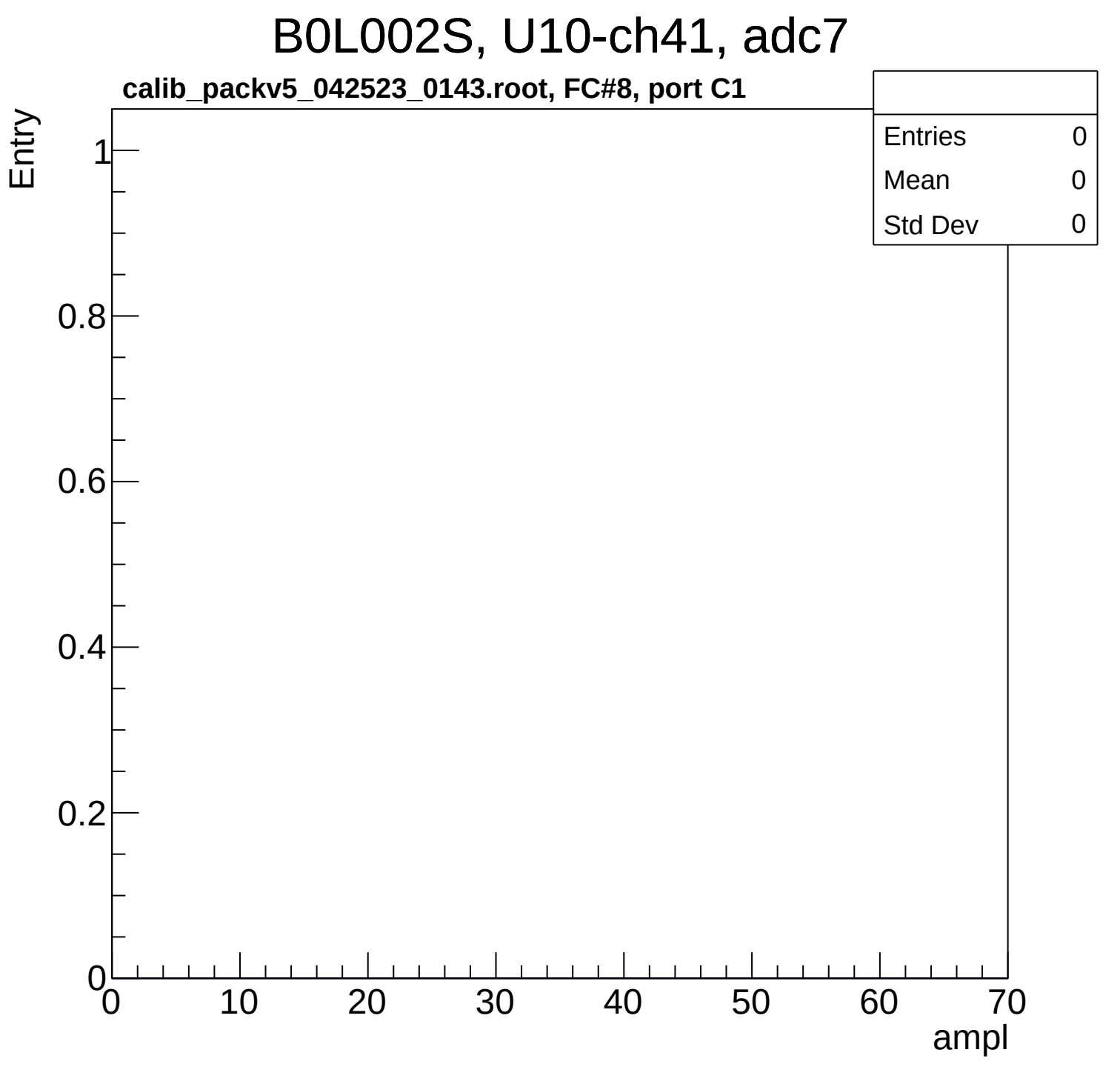

B0L002S, U10-ch41, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 45.88
Std Dev 2.826
Gaus mean : 45.8861
Gaus Width: 2.8165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

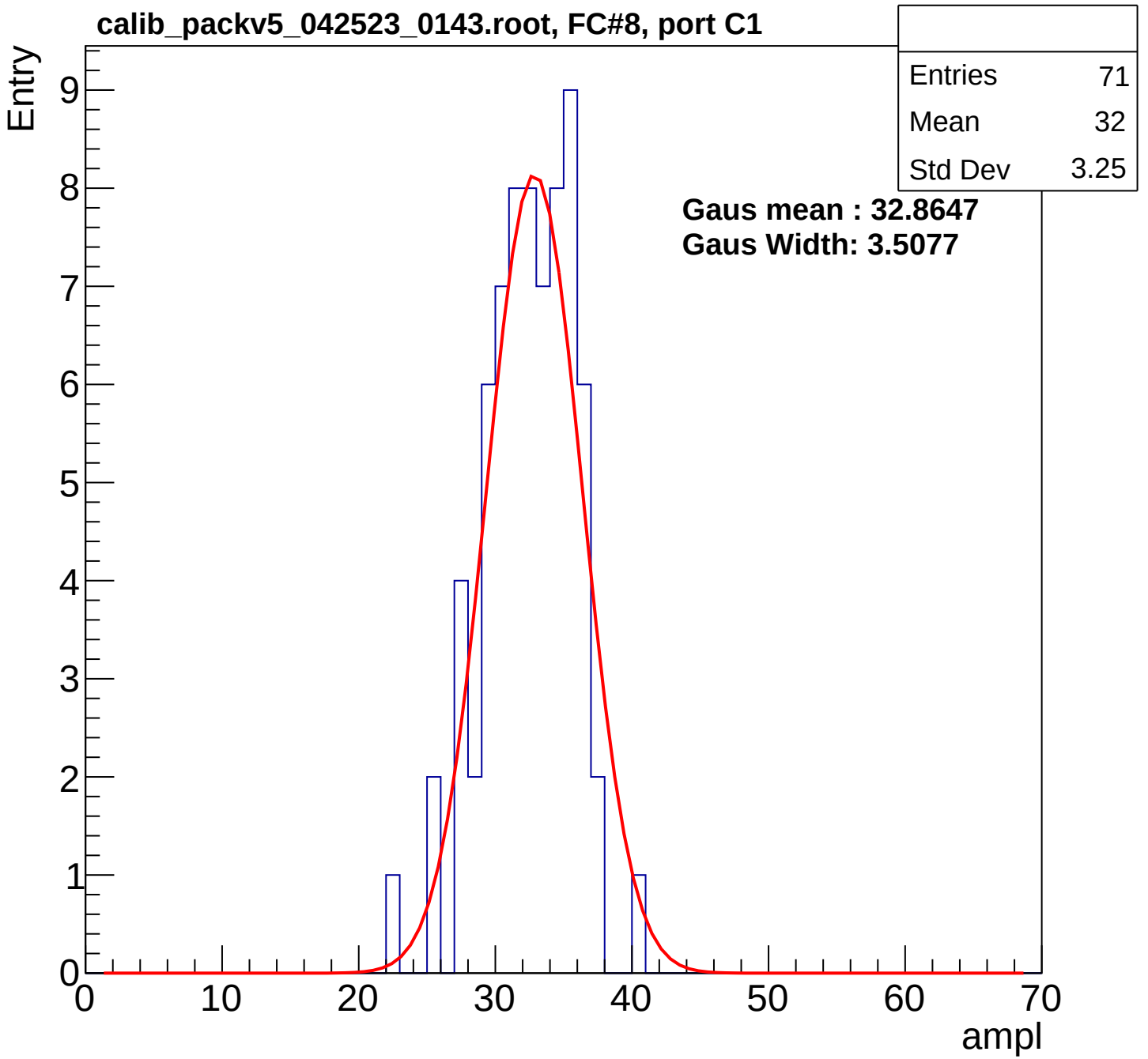

B0L002S, U10-ch41, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 52.51
Std Dev 3.349
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

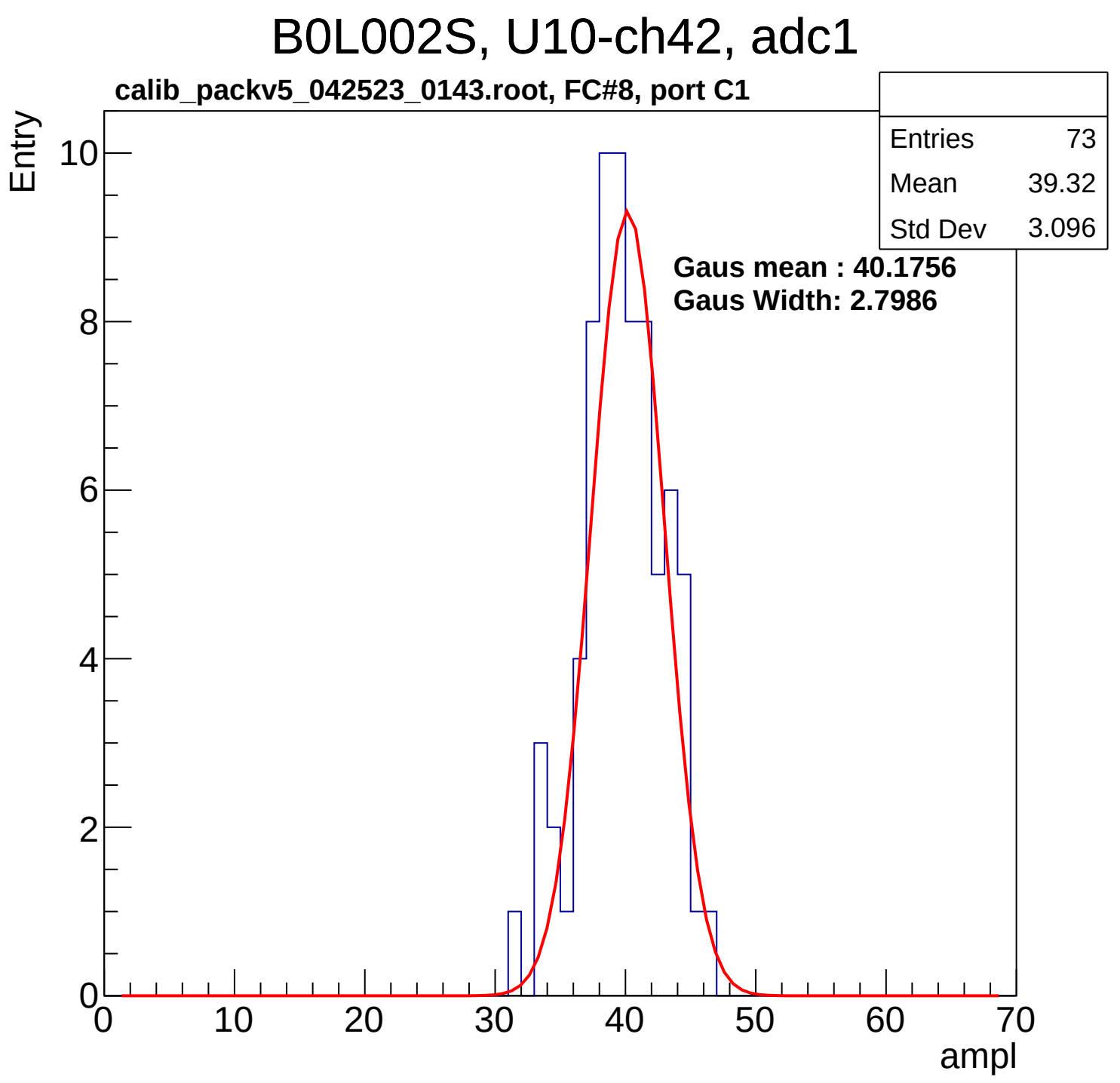

B0L002S, U10-ch41, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 57
Std Dev 9.77
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

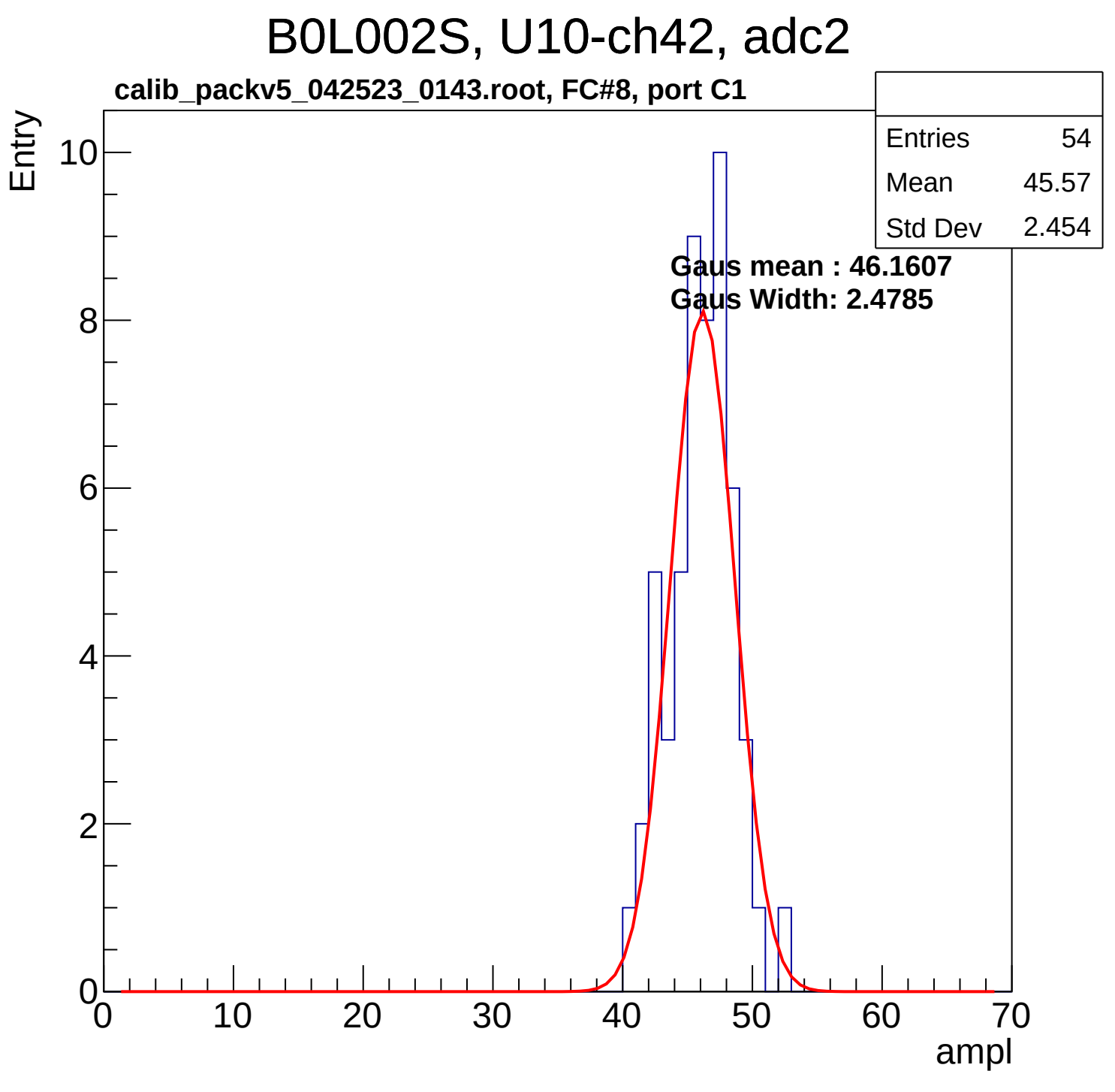

B0L002S, U10-ch41, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 61.03
Std Dev 1.879
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

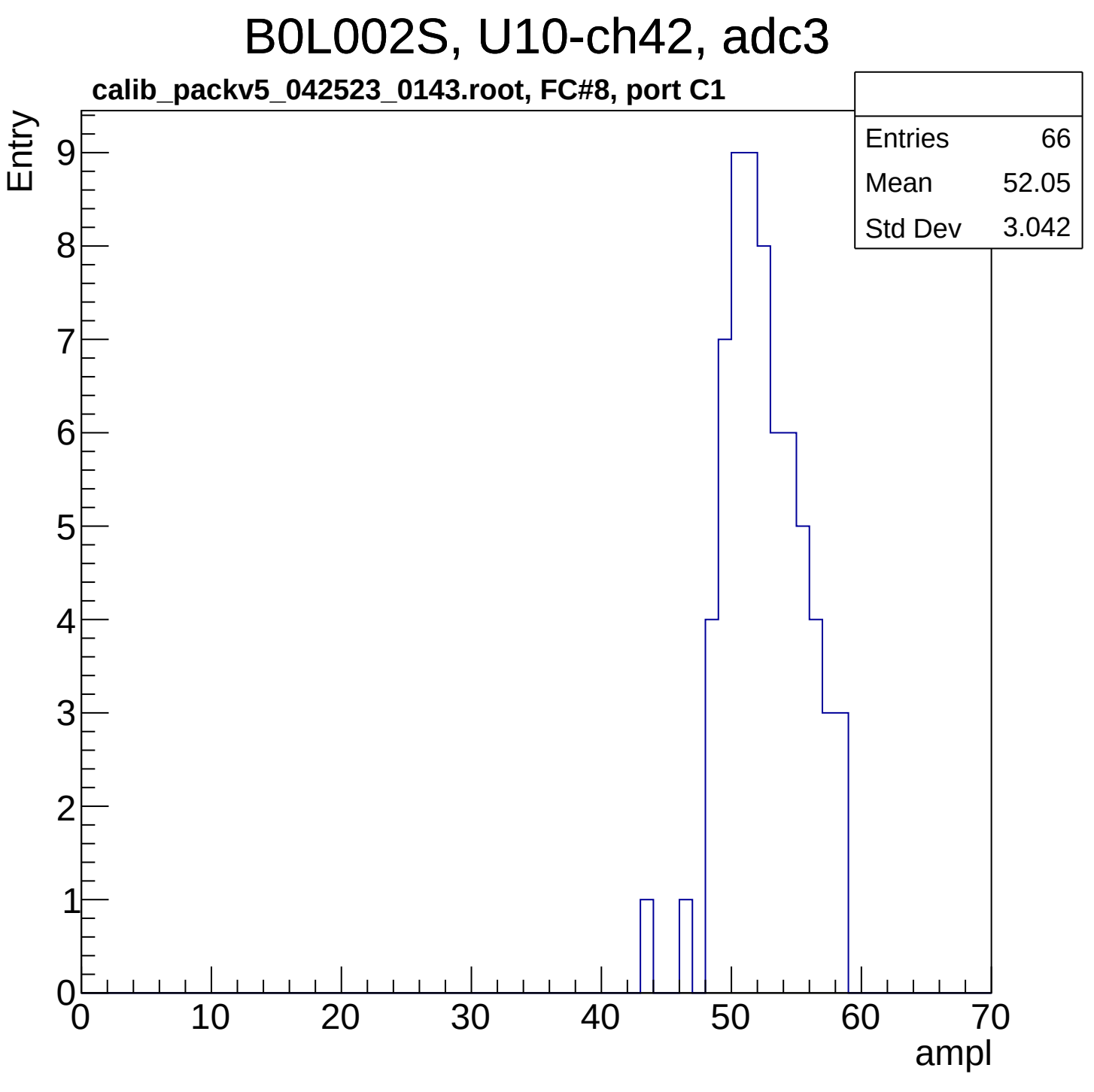

B0L002S, U10-ch41, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

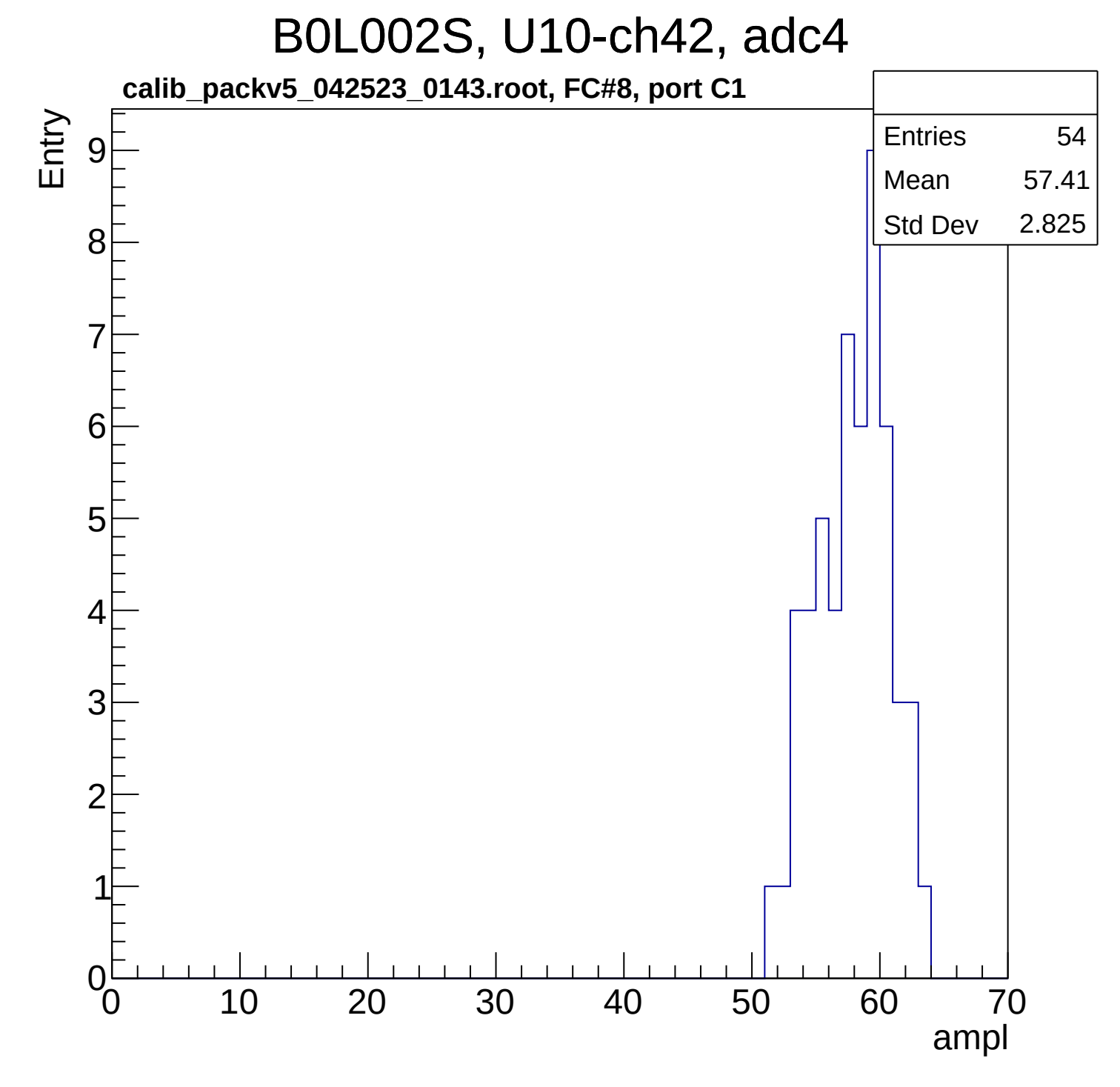

B0L002S, U10-ch41, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

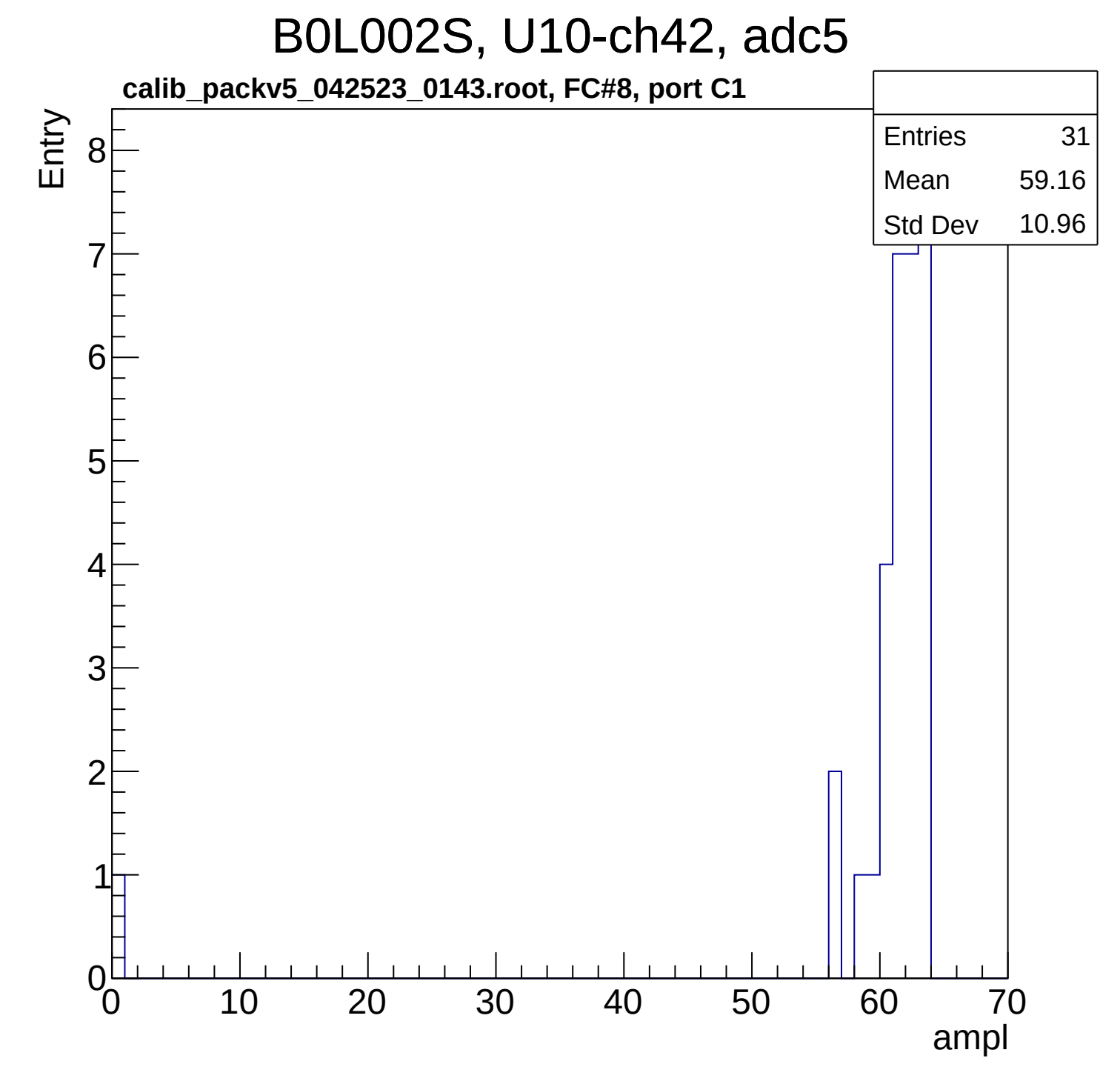

B0L002S, U10-ch42, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 32
Std Dev 3.25
Gaus mean : 32.8647
Gaus Width: 3.5077
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

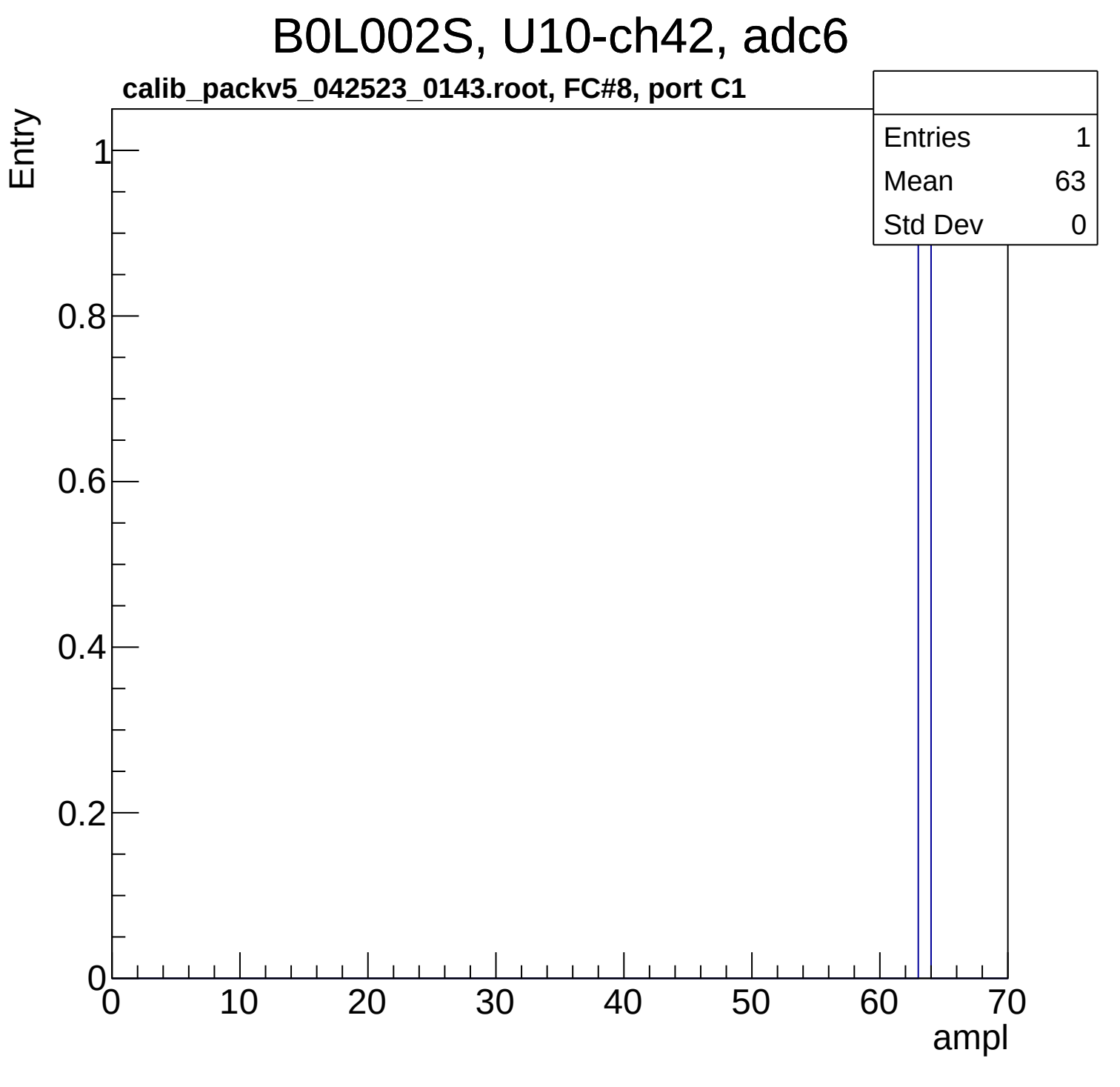

B0L002S, U10-ch42, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 39.32
Std Dev 3.096
Gaus mean : 40.1756
Gaus Width: 2.7986
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

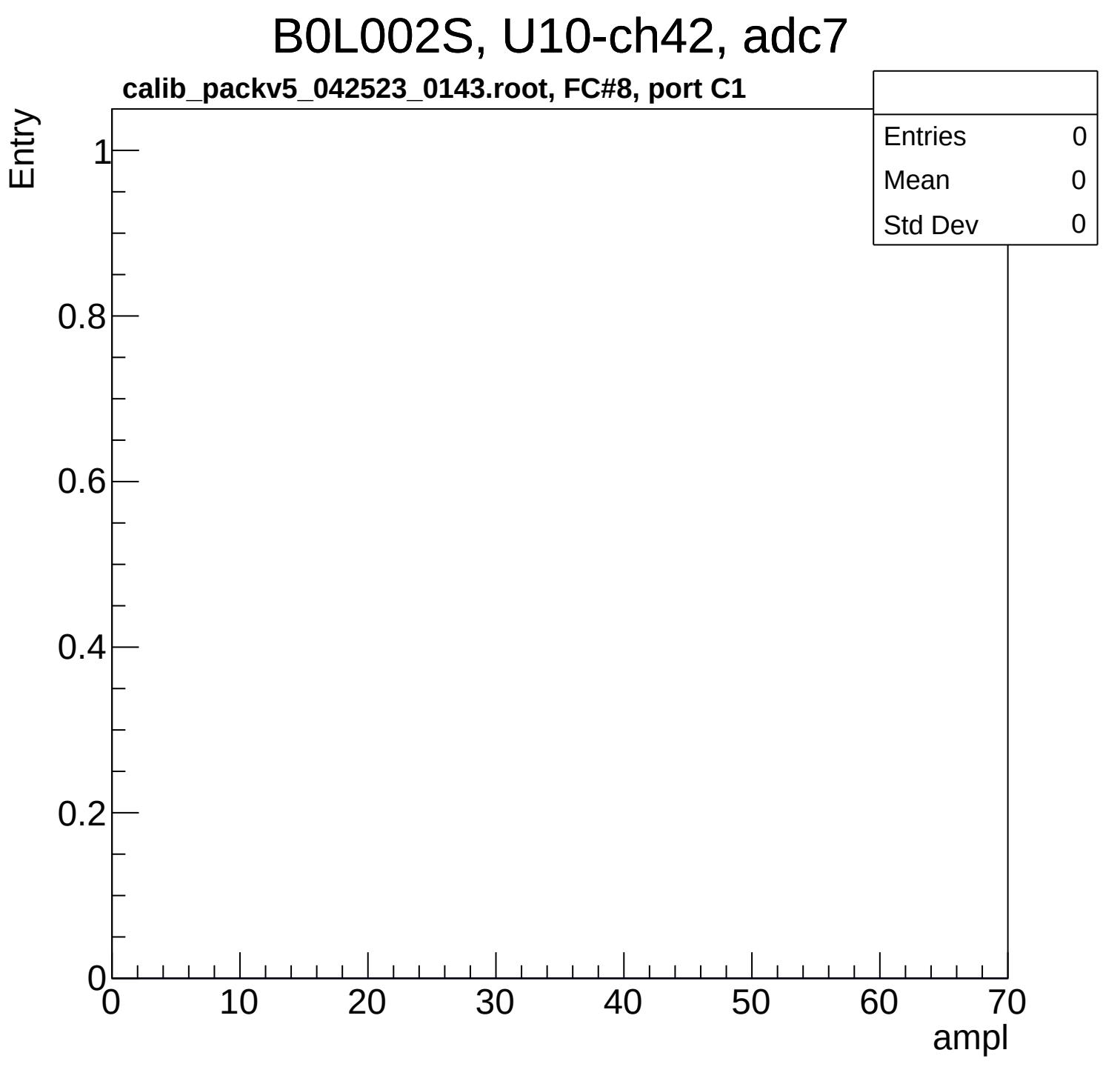

B0L002S, U10-ch42, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 45.57
Std Dev 2.454
Gaus mean : 46.1607
Gaus Width: 2.4785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

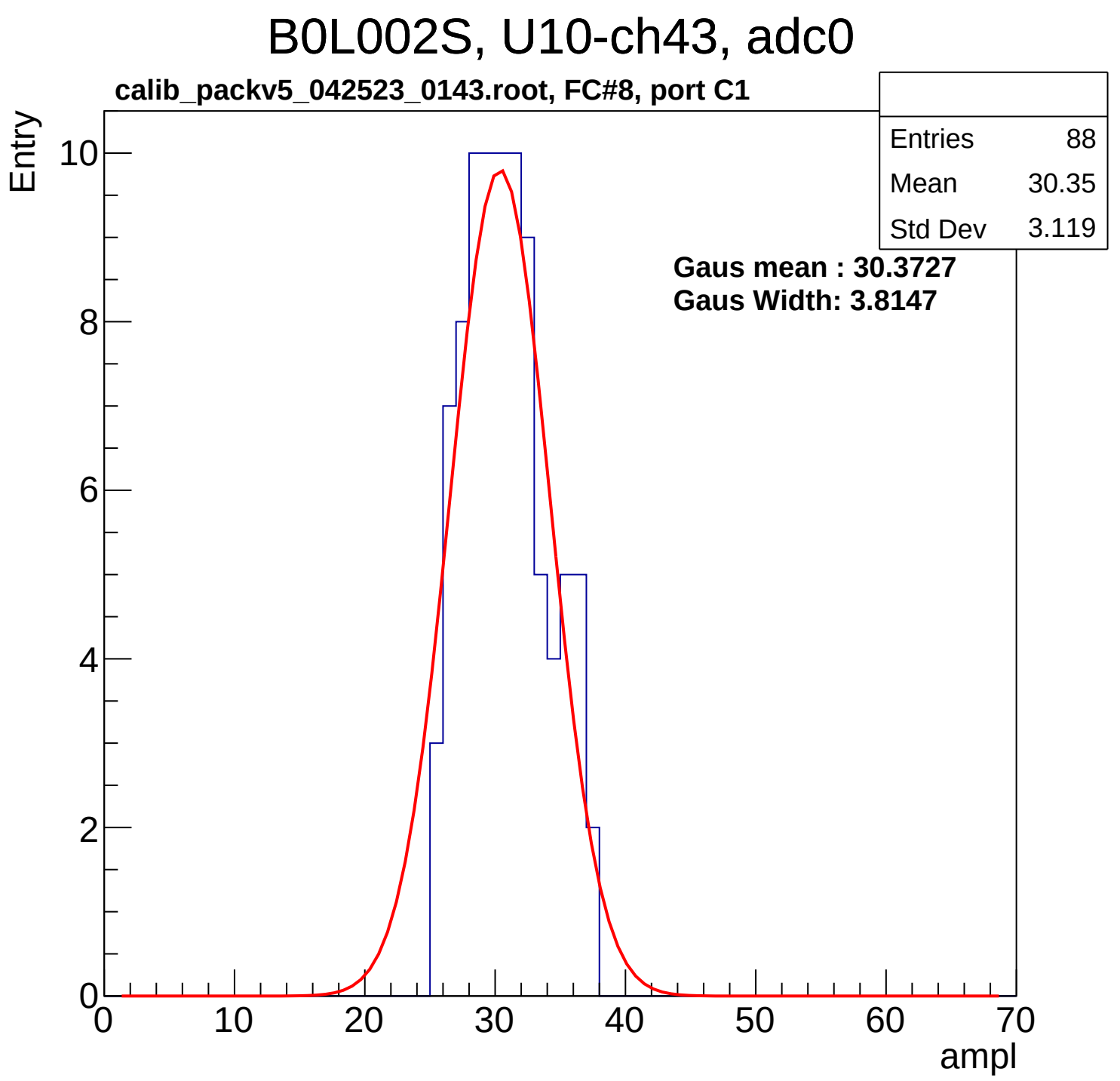

B0L002S, U10-ch42, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 52.05
Std Dev 3.042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

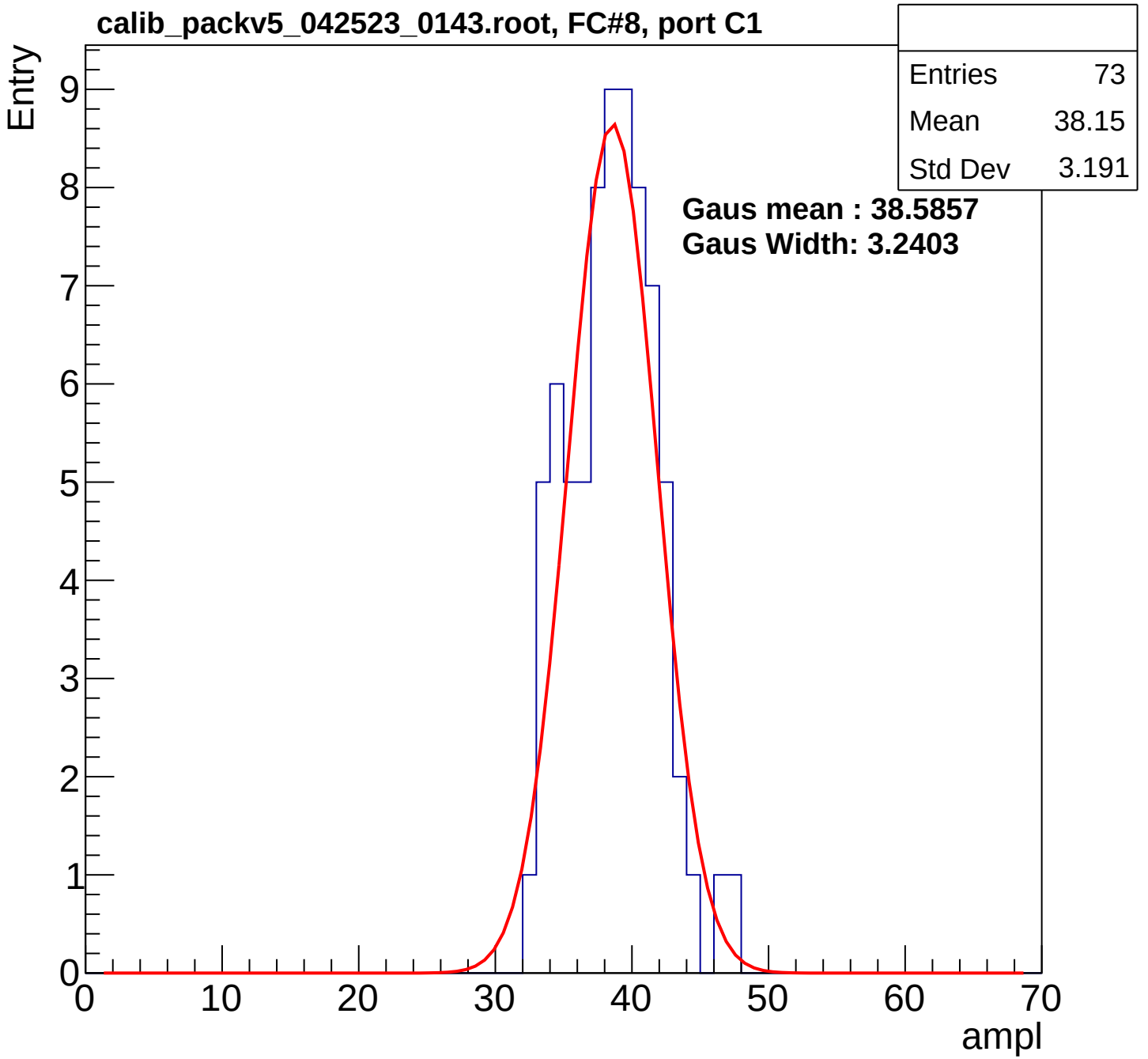

B0L002S, U10-ch42, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.41
Std Dev 2.825
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

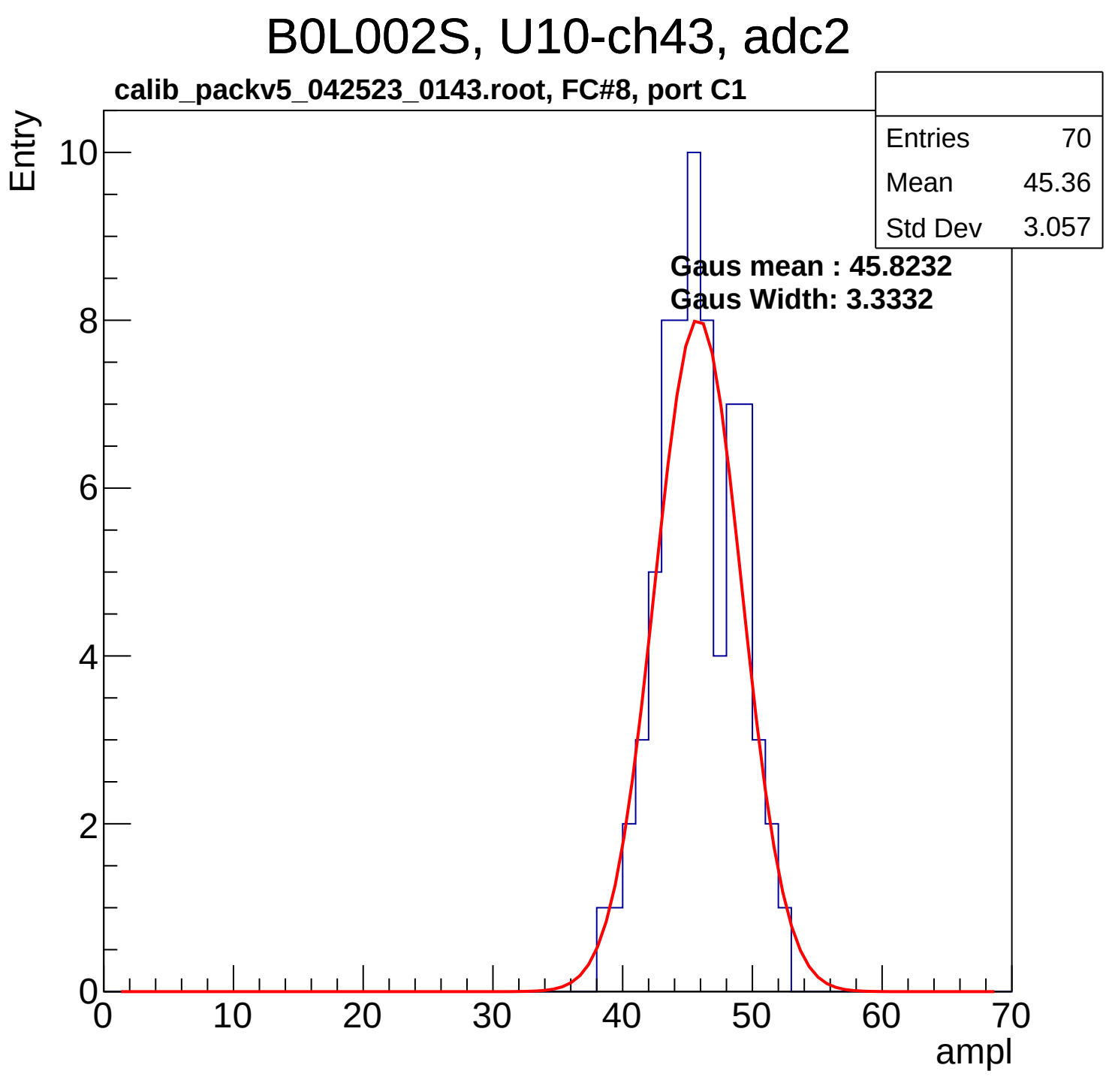

B0L002S, U10-ch42, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.16
Std Dev 10.96
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

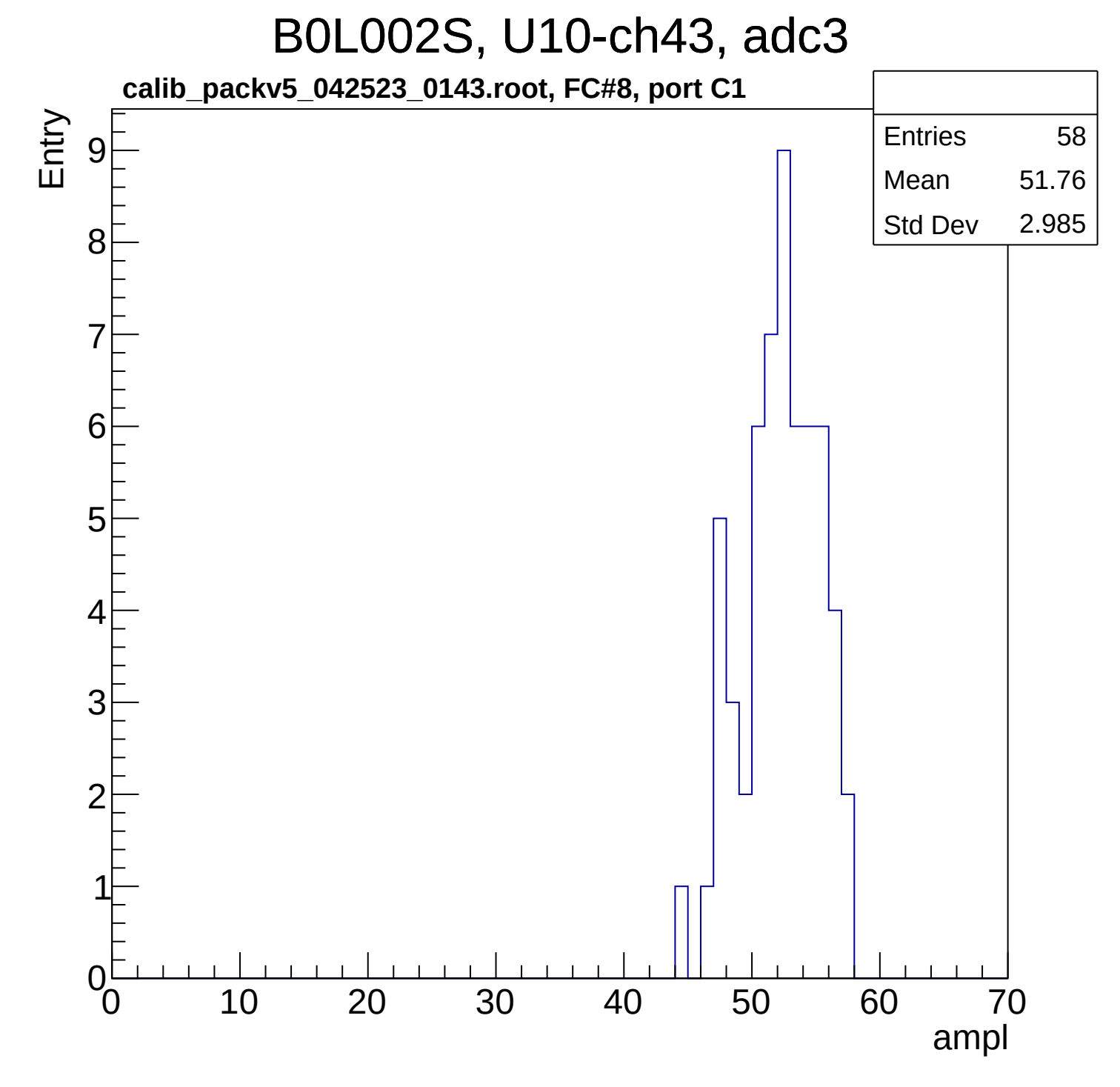

B0L002S, U10-ch42, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

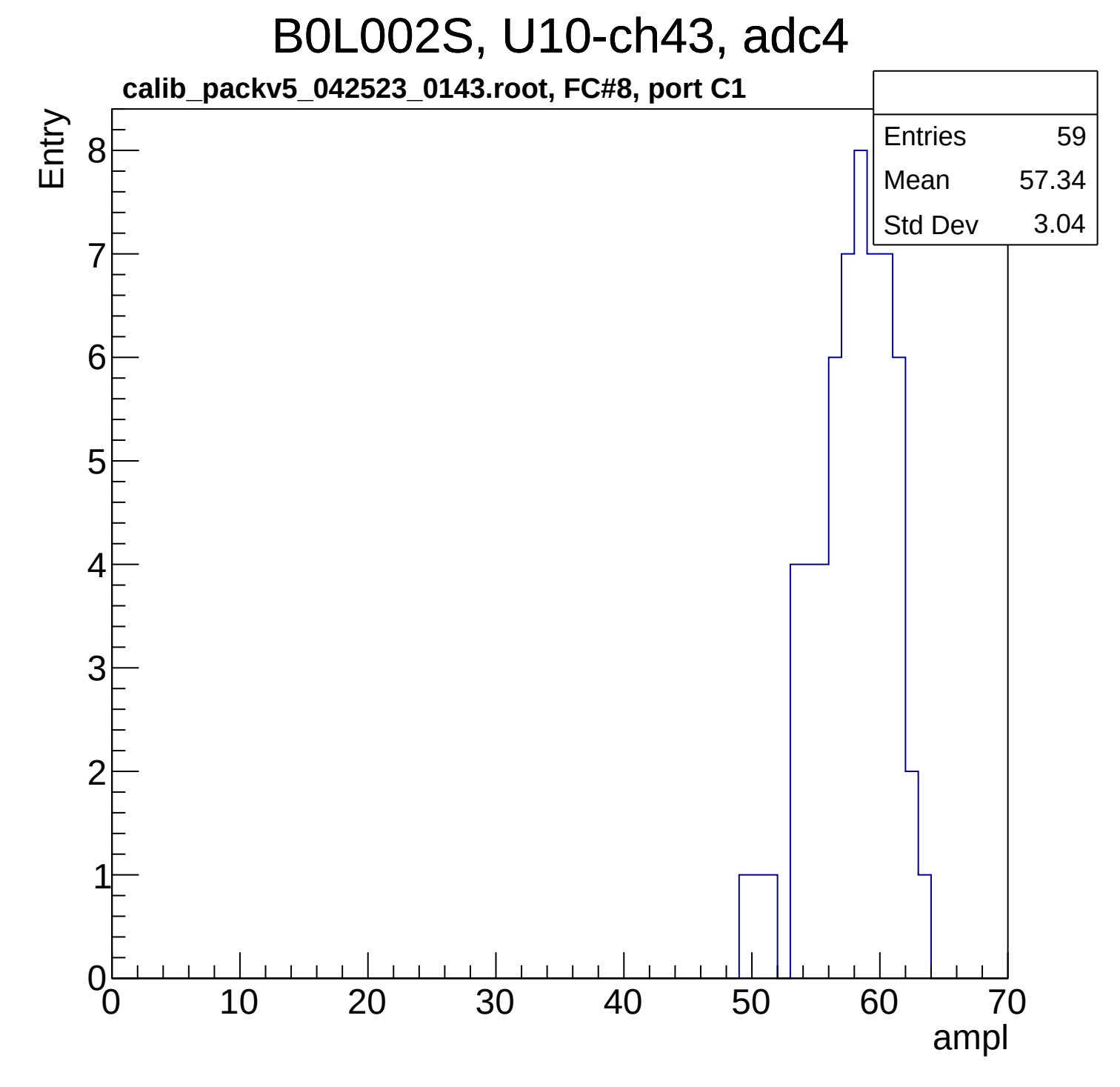

B0L002S, U10-ch42, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

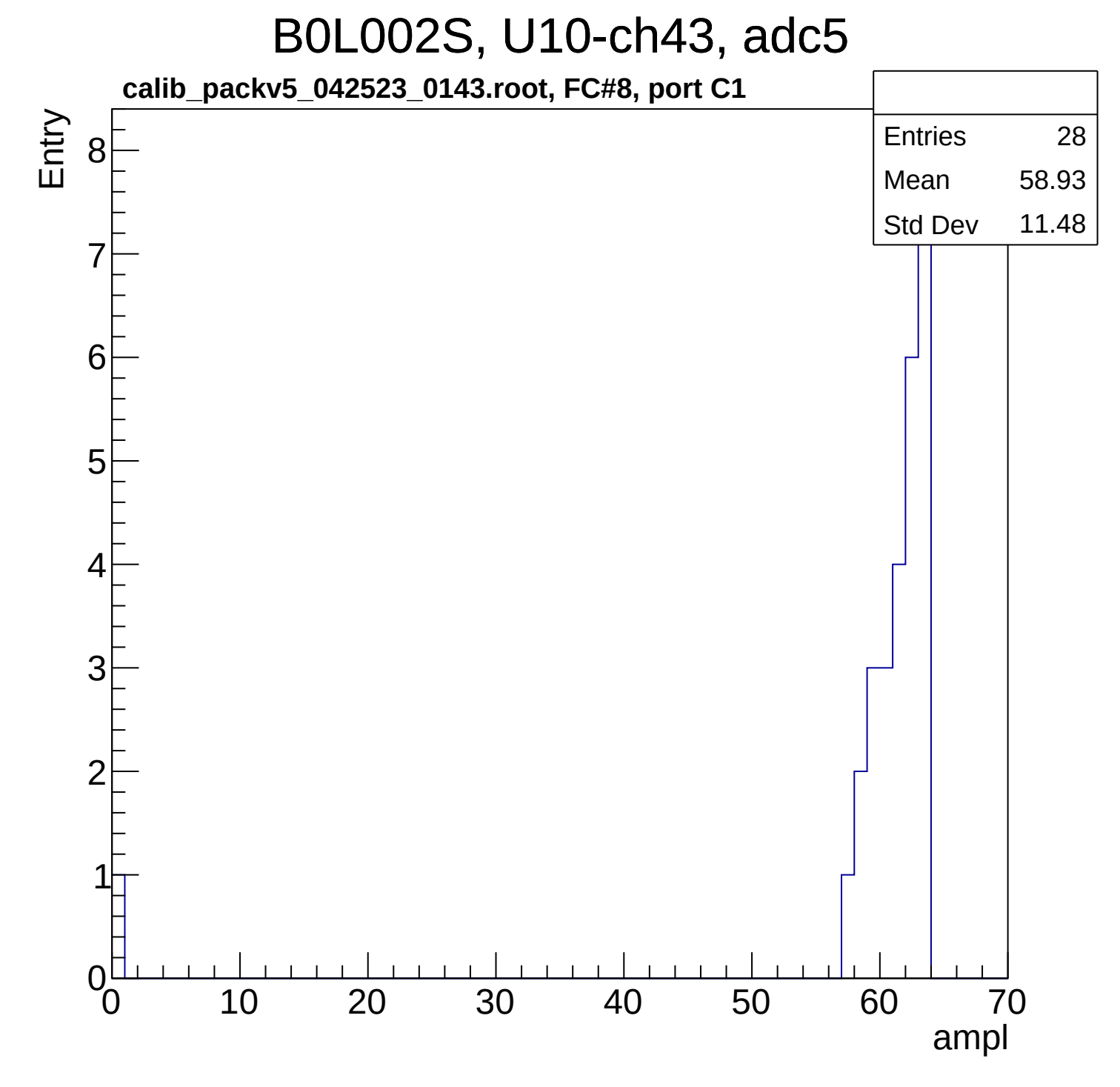

B0L002S, U10-ch43, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 30.35
Std Dev 3.119
Gaus mean : 30.3727
Gaus Width: 3.8147
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

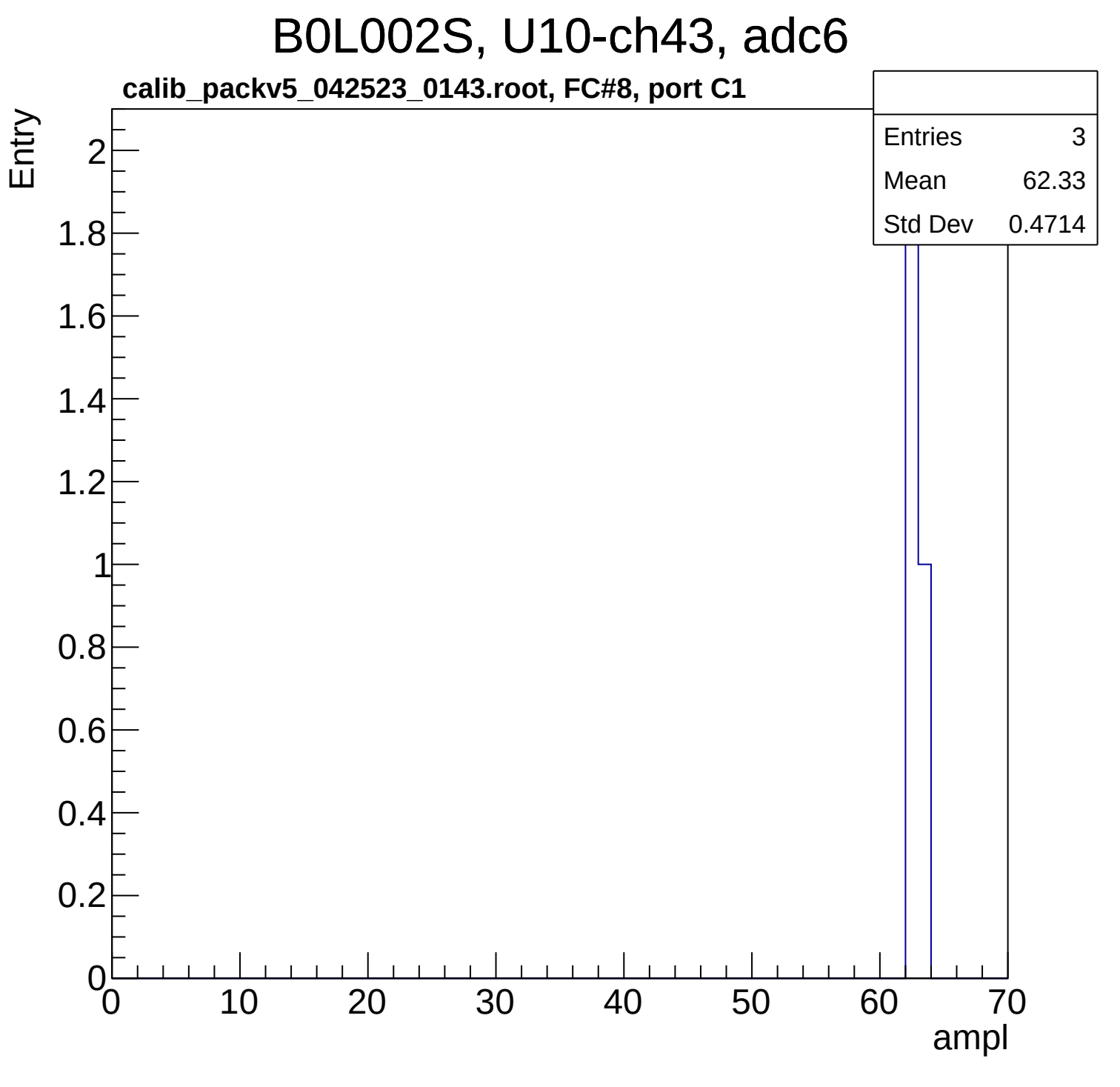

B0L002S, U10-ch43, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.15
Std Dev 3.191
Gaus mean : 38.5857
Gaus Width: 3.2403
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

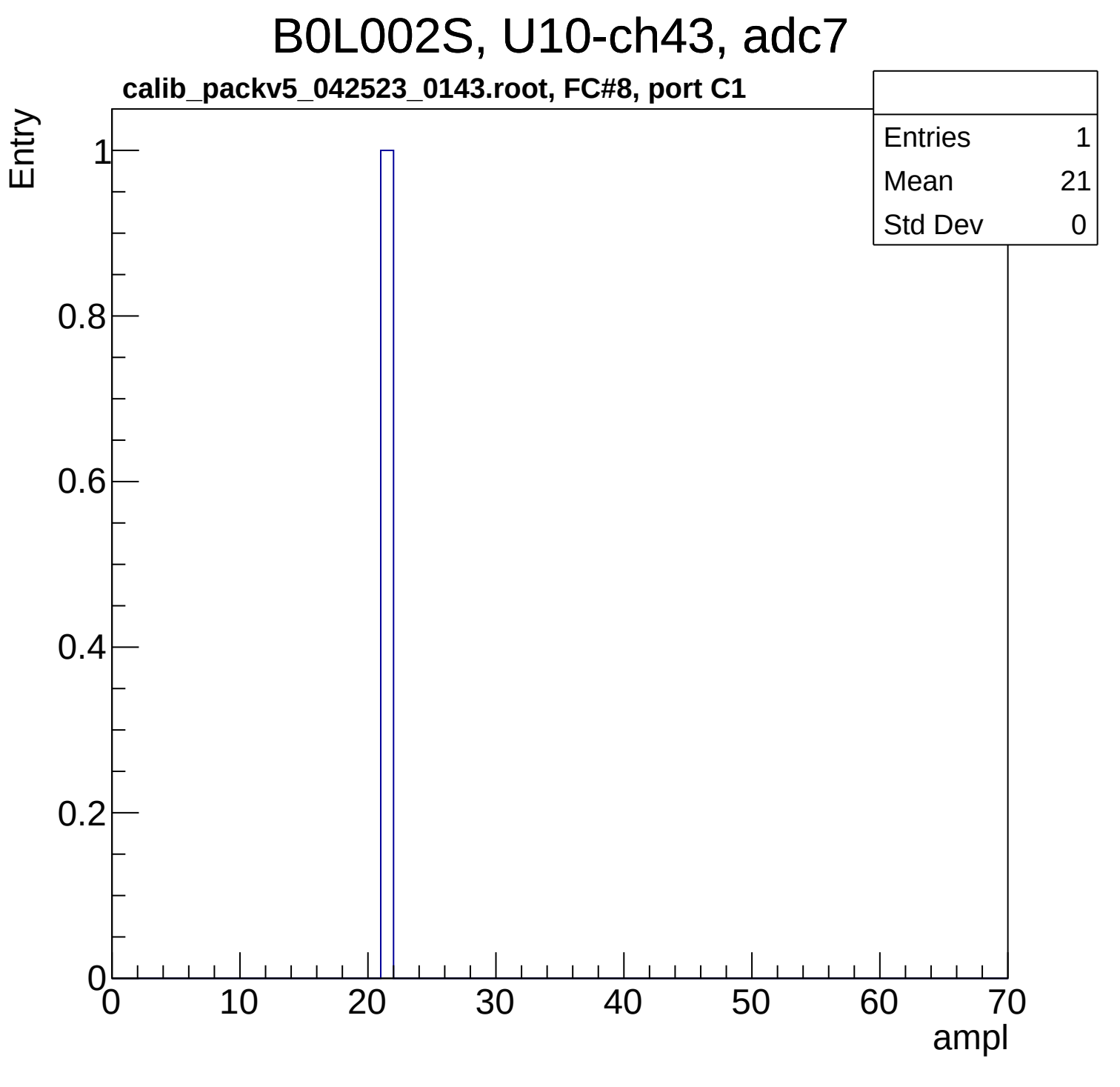

B0L002S, U10-ch43, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 45.36
Std Dev 3.057
Gaus mean : 45.8232
Gaus Width: 3.3332
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

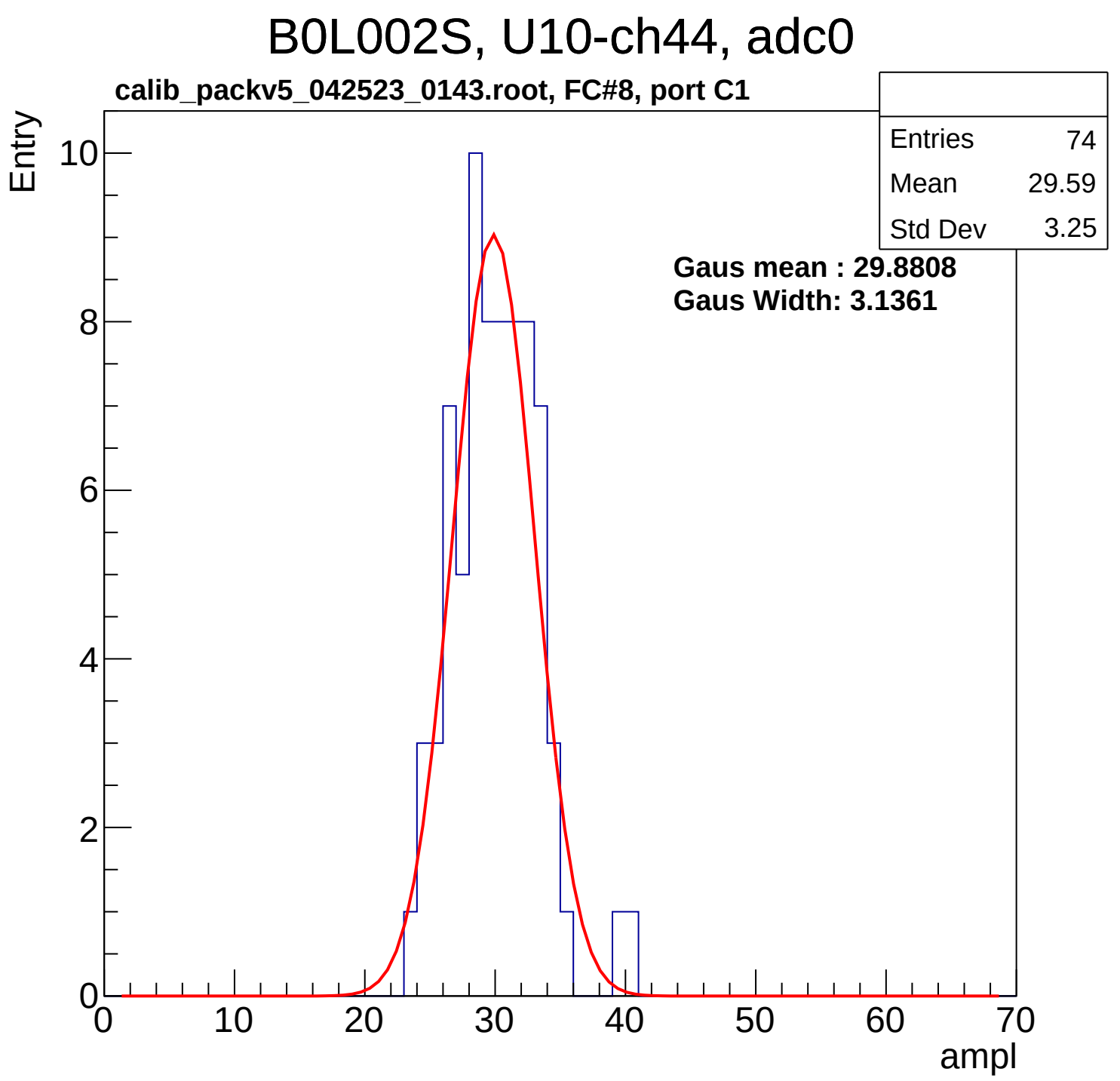

B0L002S, U10-ch43, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.76
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

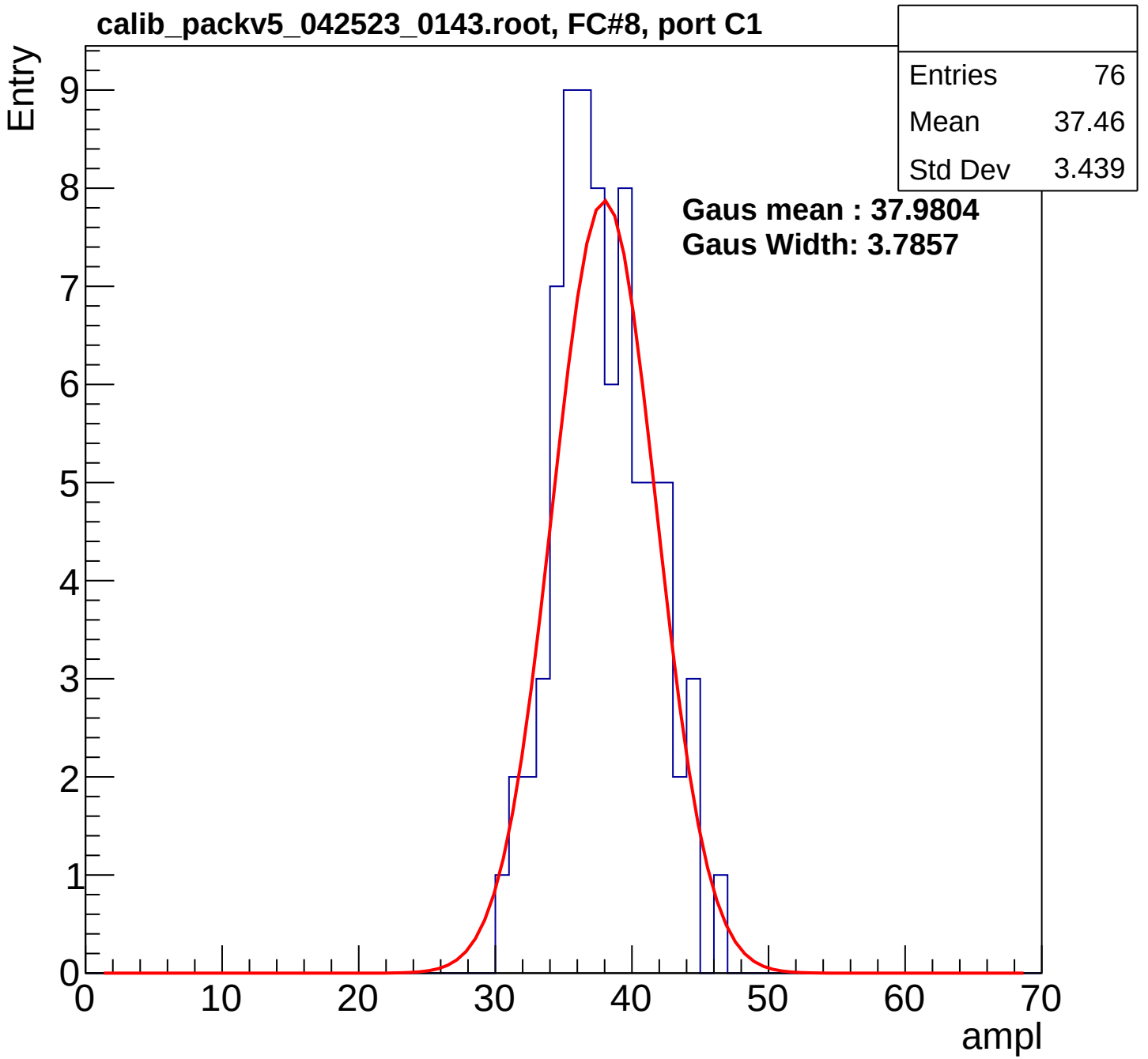

B0L002S, U10-ch43, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 57.34
Std Dev 3.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

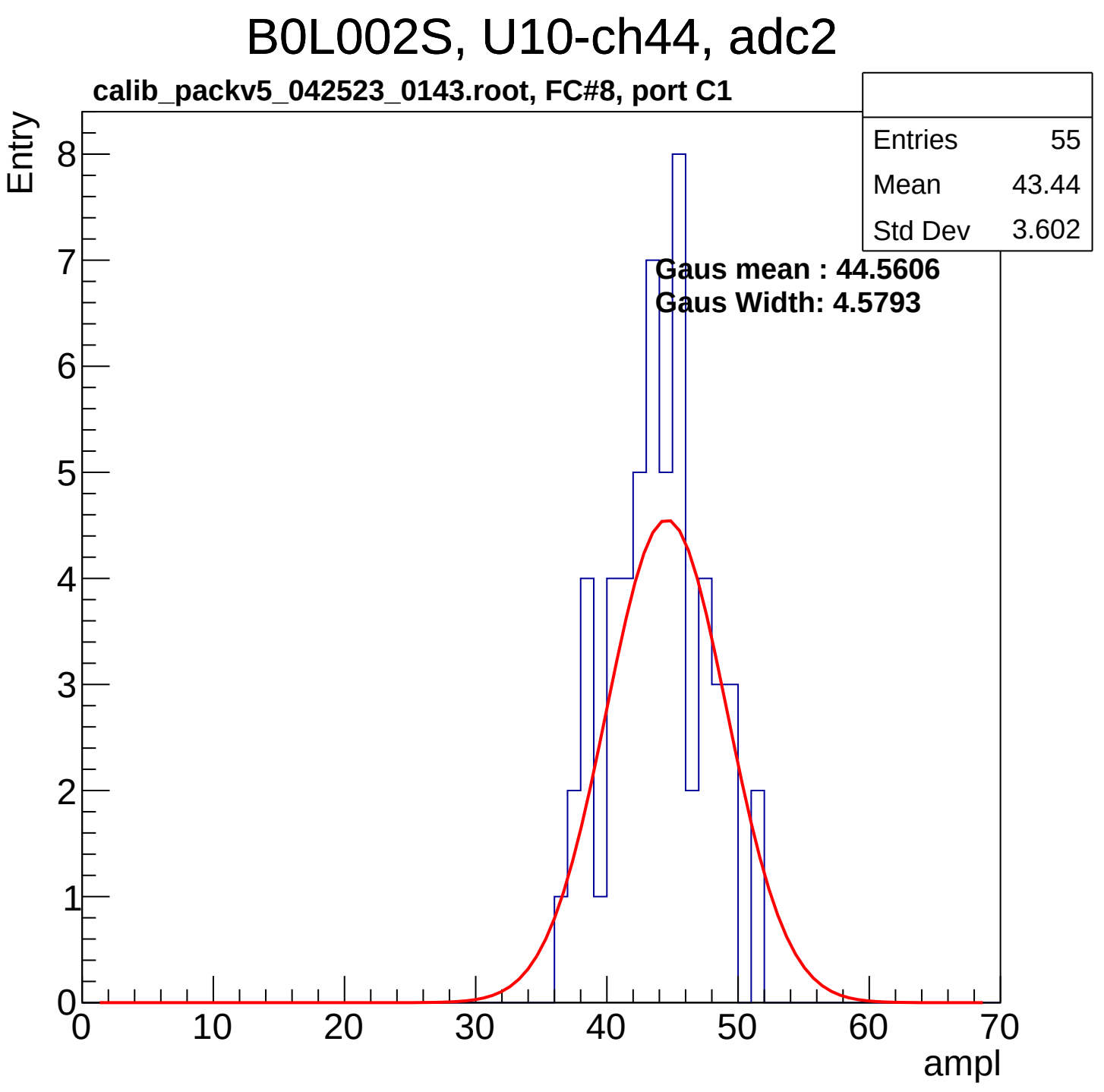

B0L002S, U10-ch43, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.93
Std Dev 11.48
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

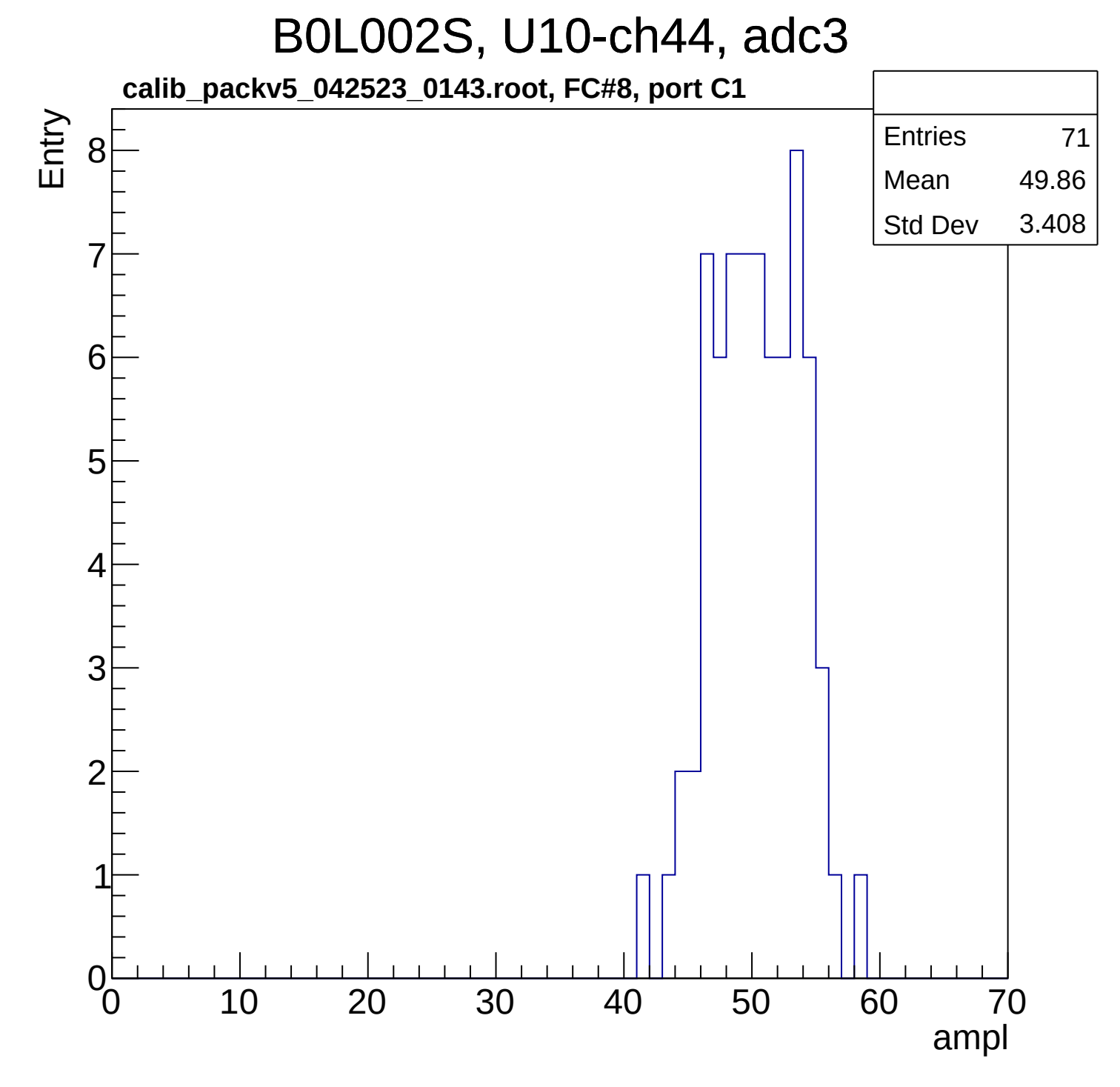

B0L002S, U10-ch43, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

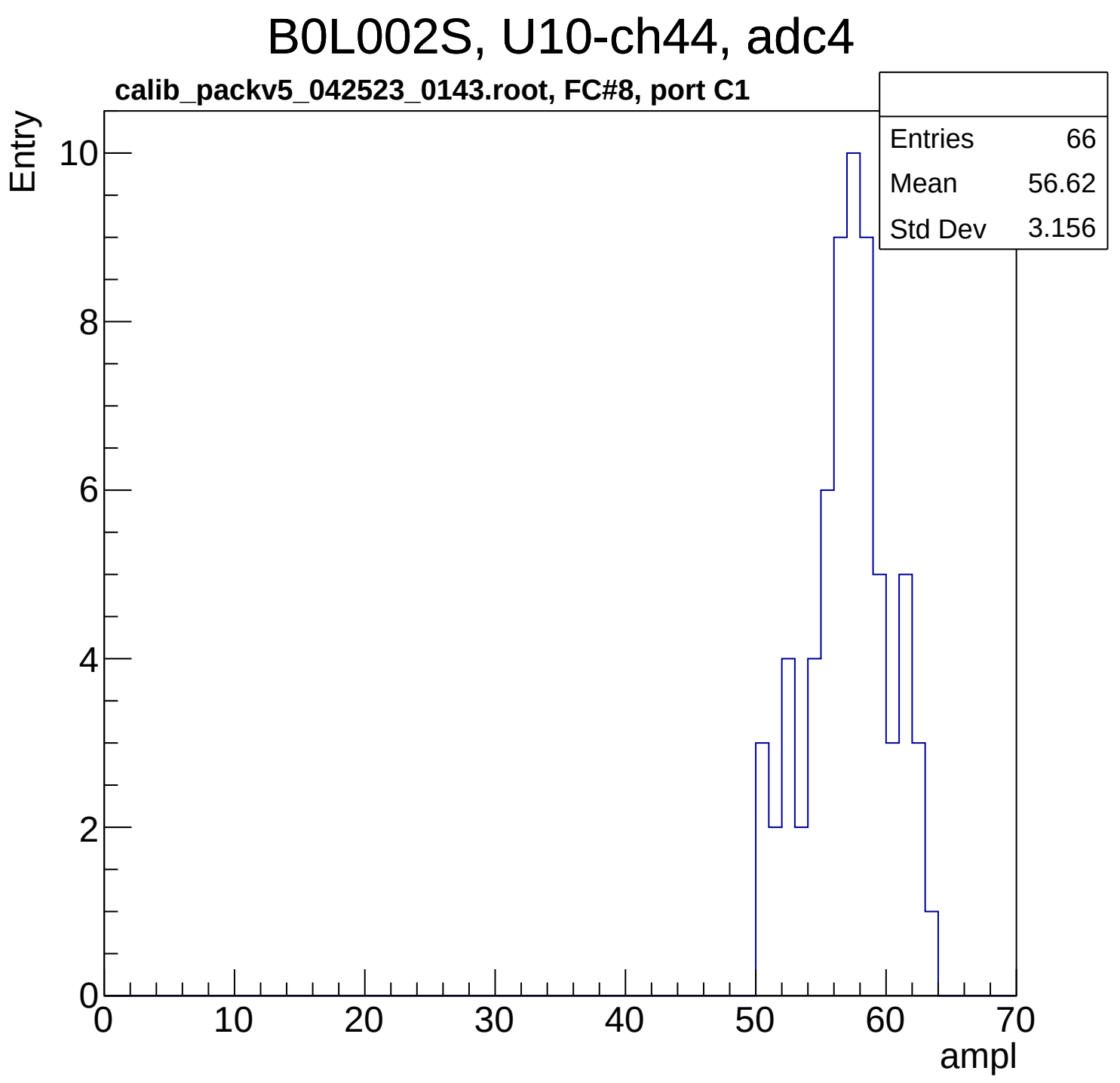

B0L002S, U10-ch43, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

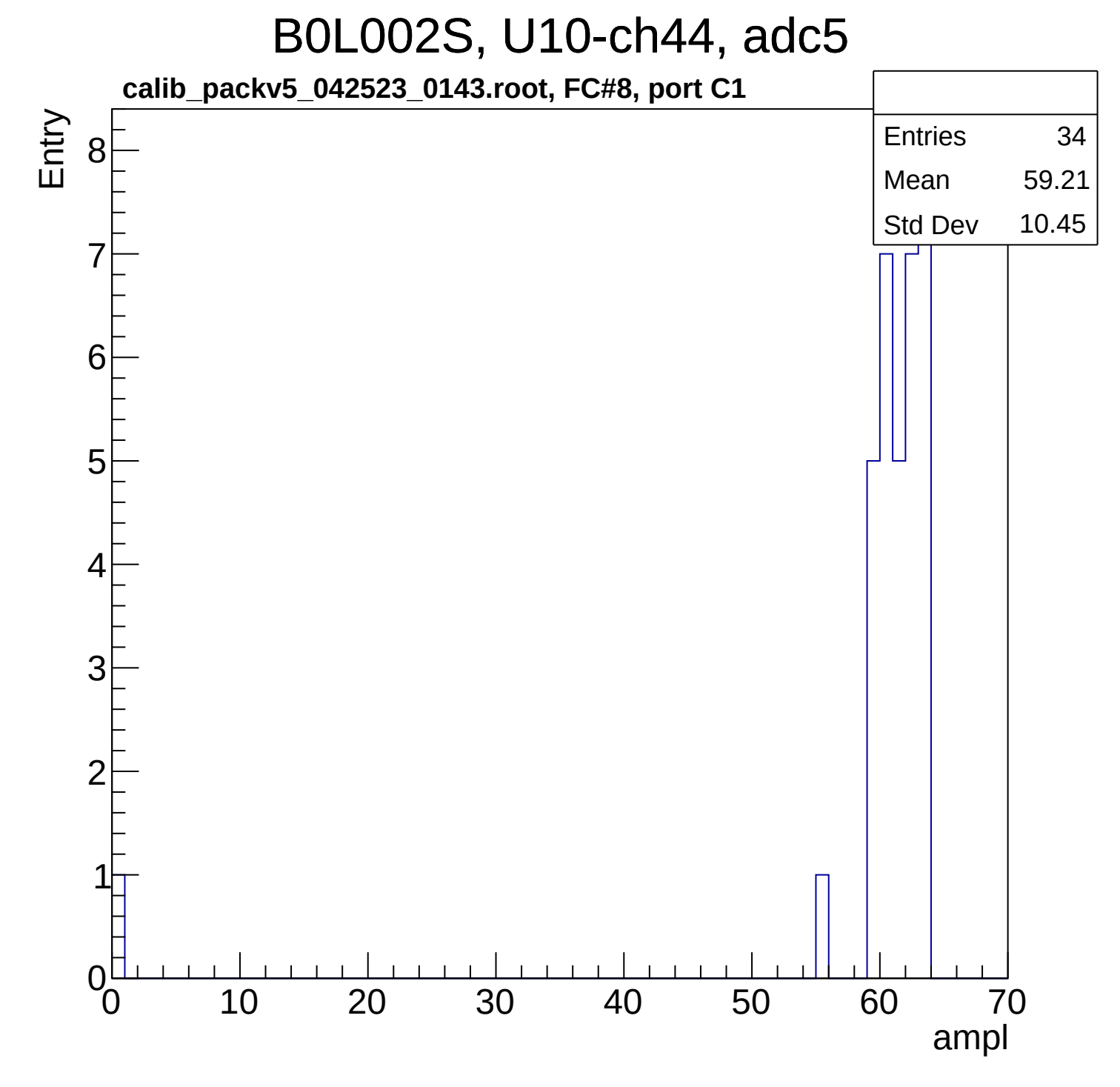

B0L002S, U10-ch44, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.59
Std Dev 3.25
Gaus mean : 29.8808
Gaus Width: 3.1361
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

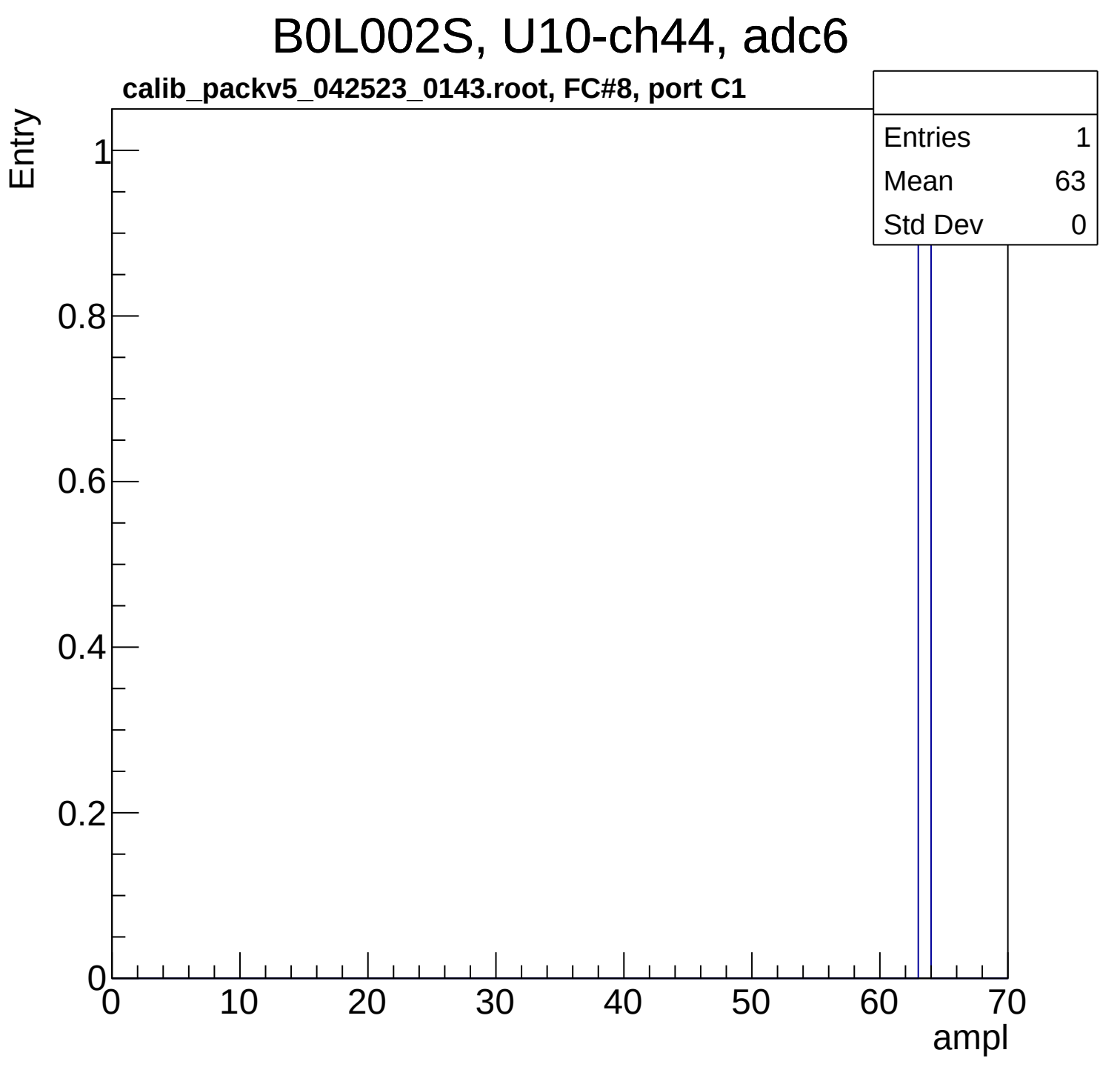

B0L002S, U10-ch44, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 37.46
Std Dev 3.439
Gaus mean : 37.9804
Gaus Width: 3.7857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

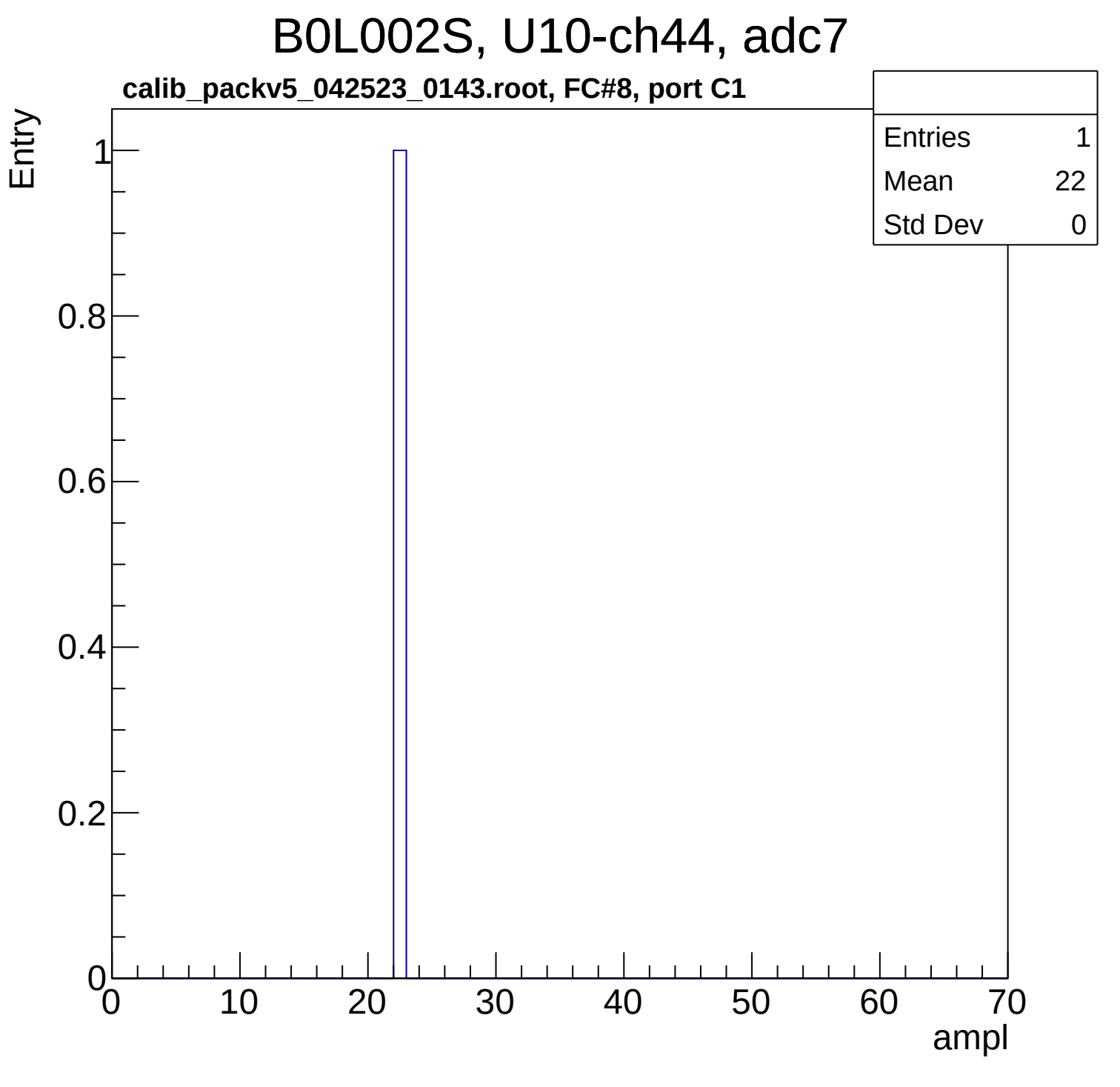

B0L002S, U10-ch44, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 43.44
Std Dev 3.602
Gaus mean : 44.5606
Gaus Width: 4.5793
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

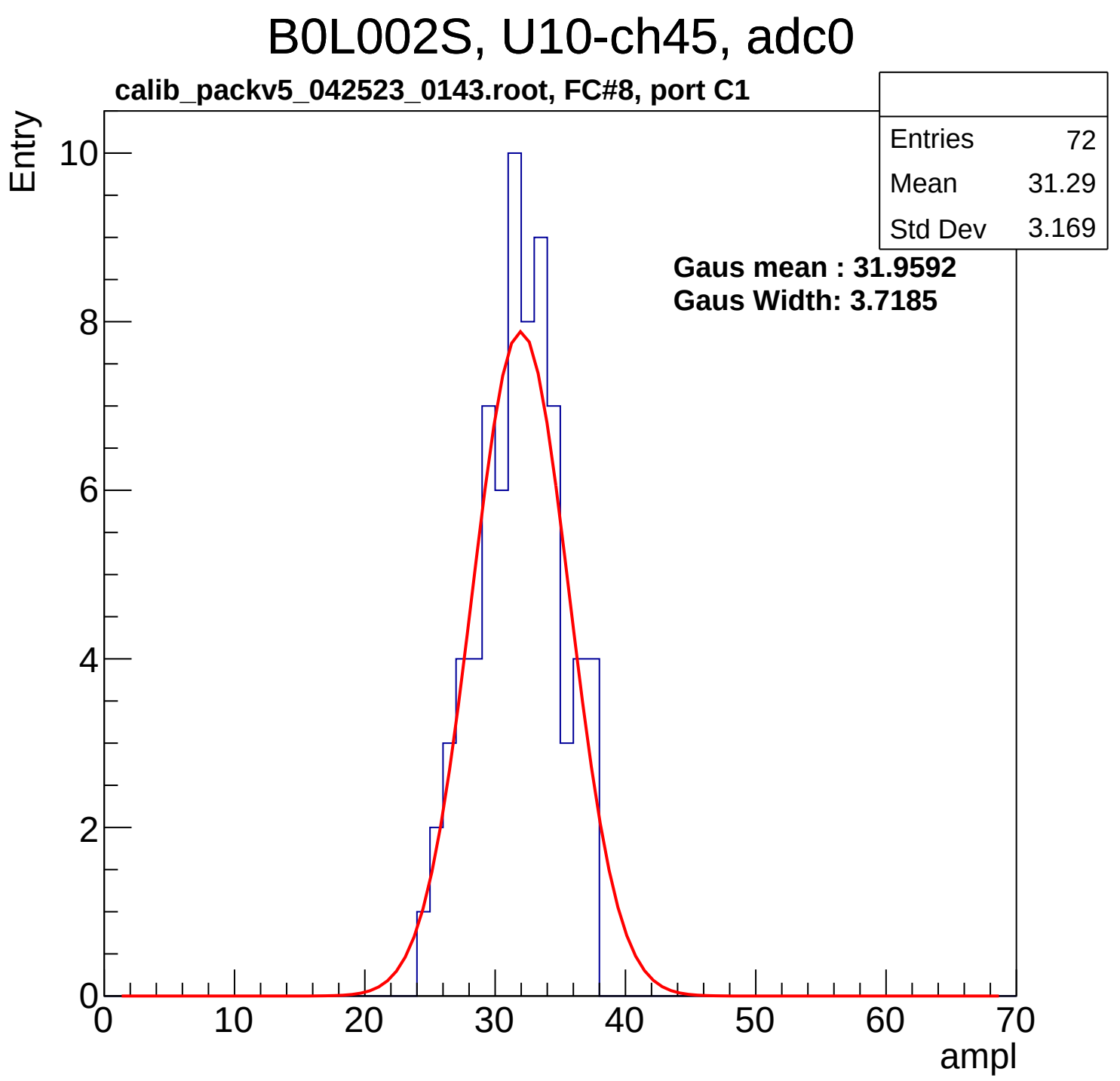

B0L002S, U10-ch44, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 49.86
Std Dev 3.408
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

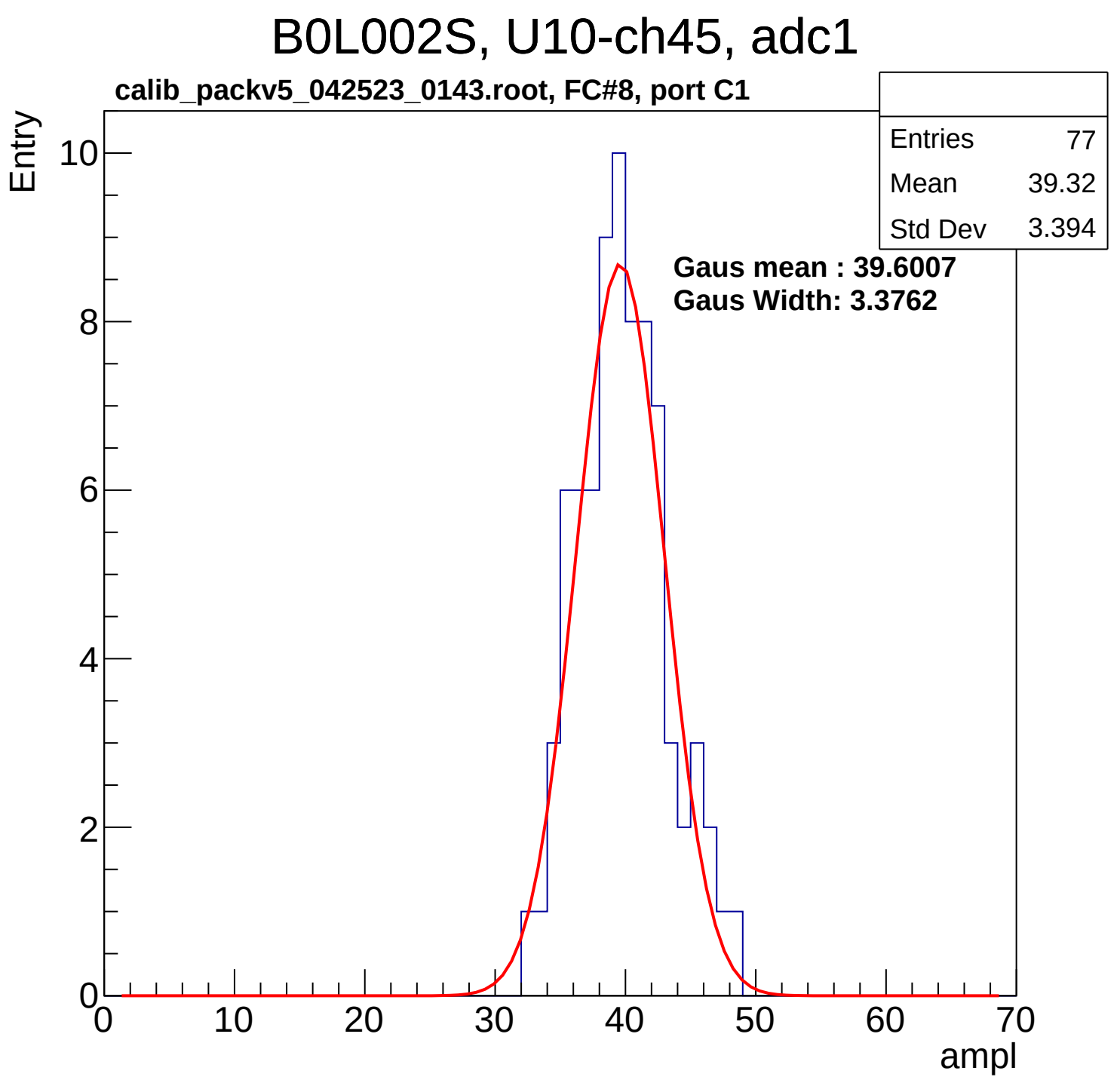

B0L002S, U10-ch44, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 56.62
Std Dev 3.156
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

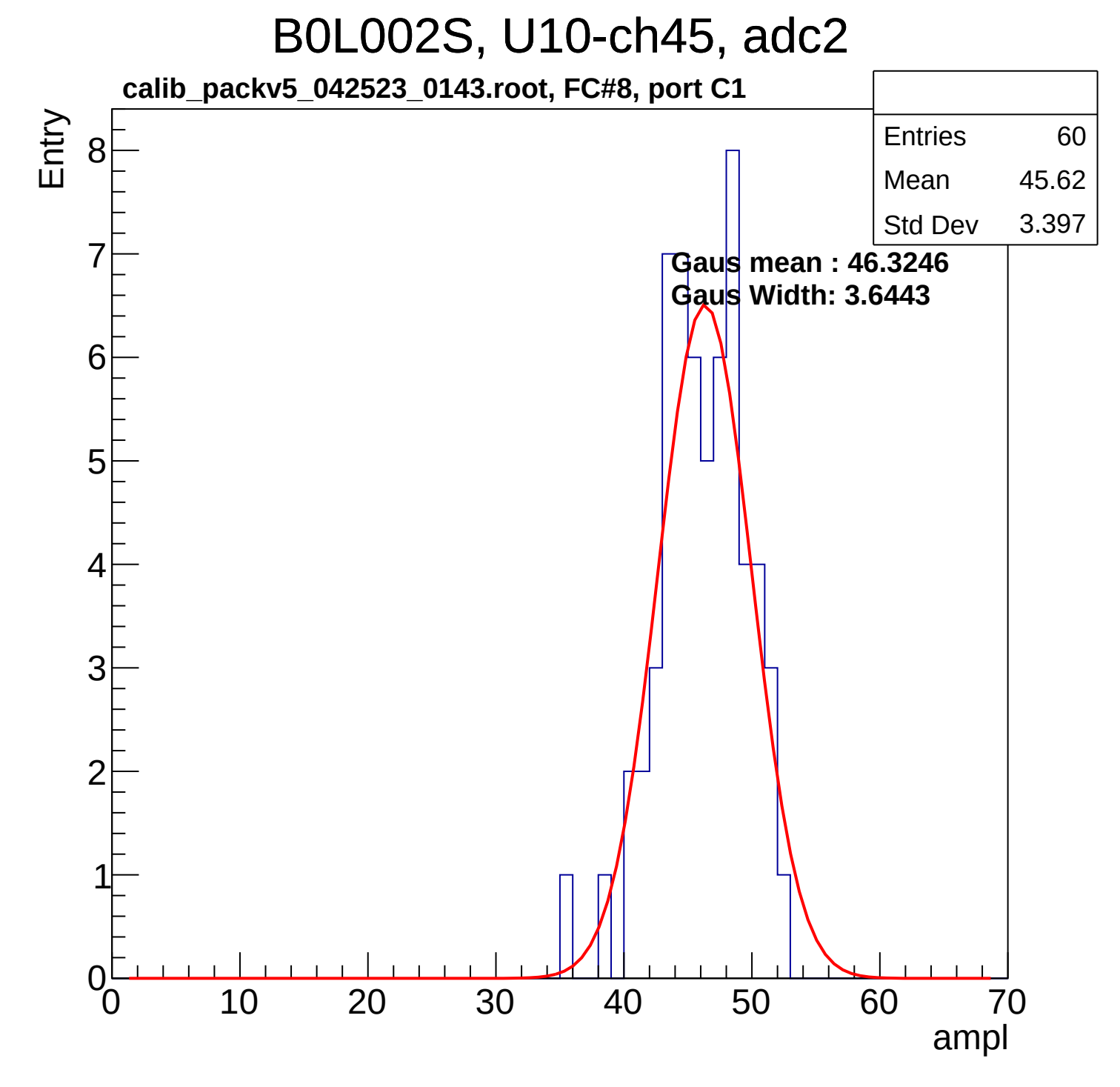

B0L002S, U10-ch44, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 59.21
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

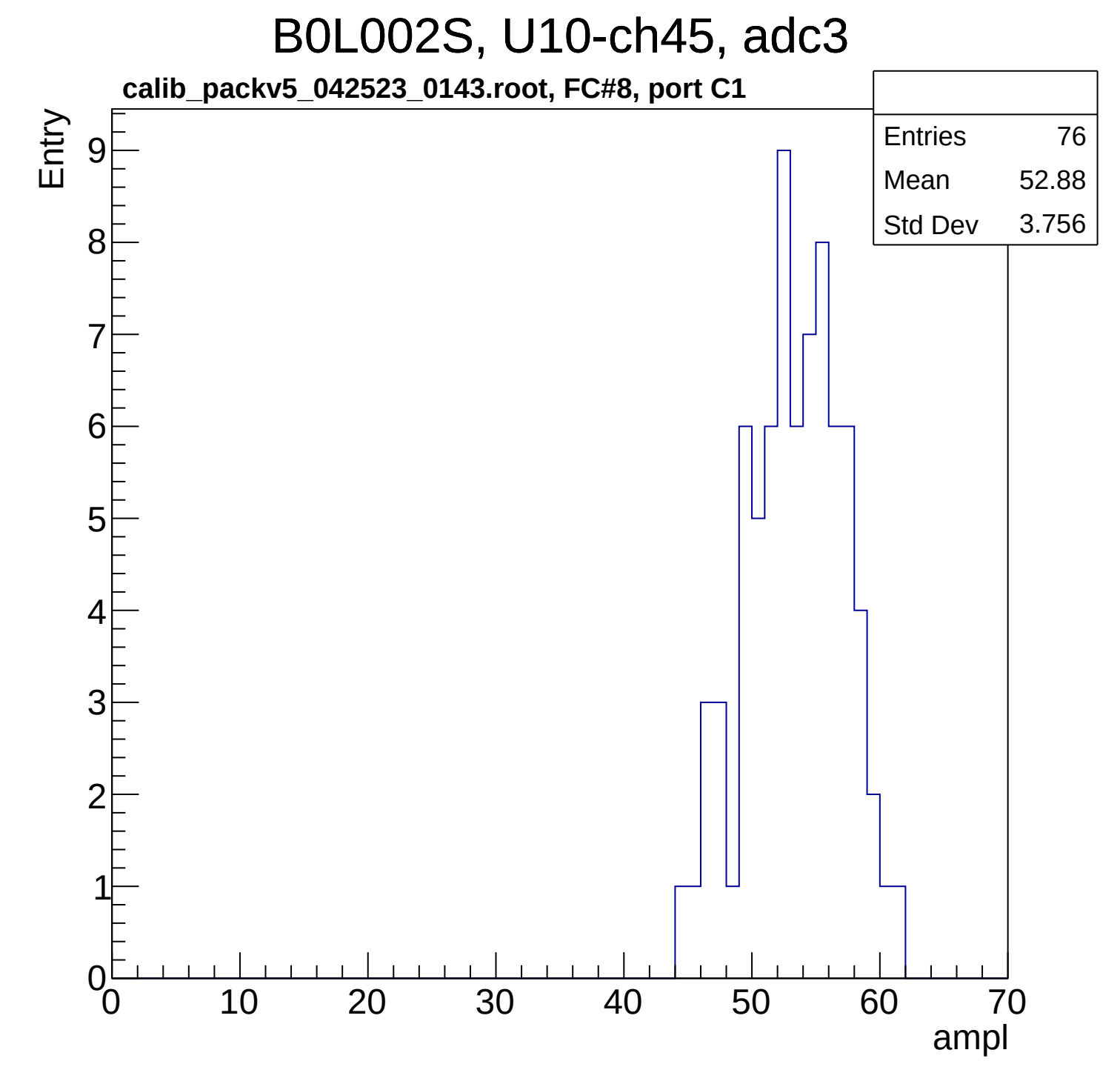

B0L002S, U10-ch44, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

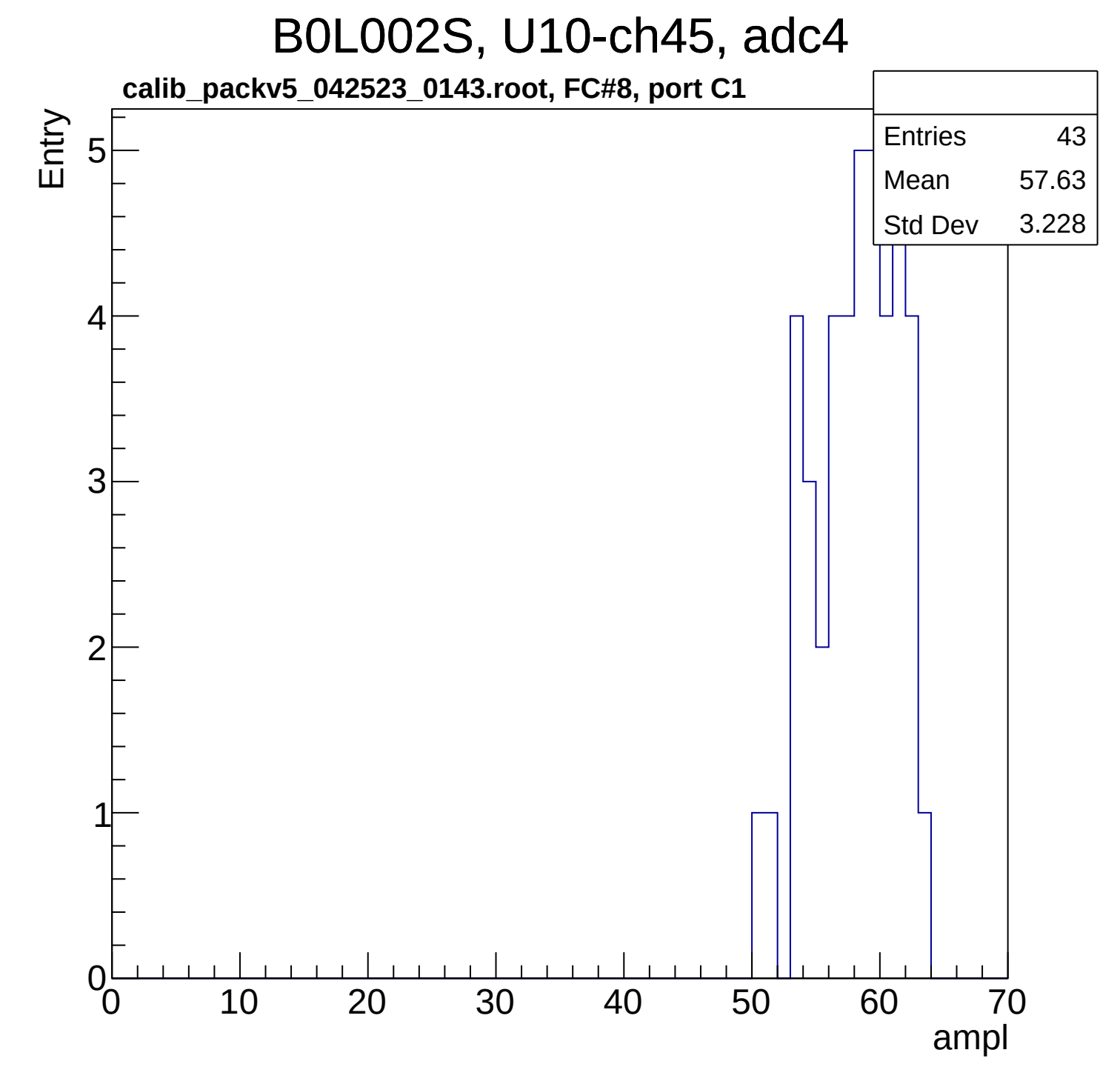

B0L002S, U10-ch44, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

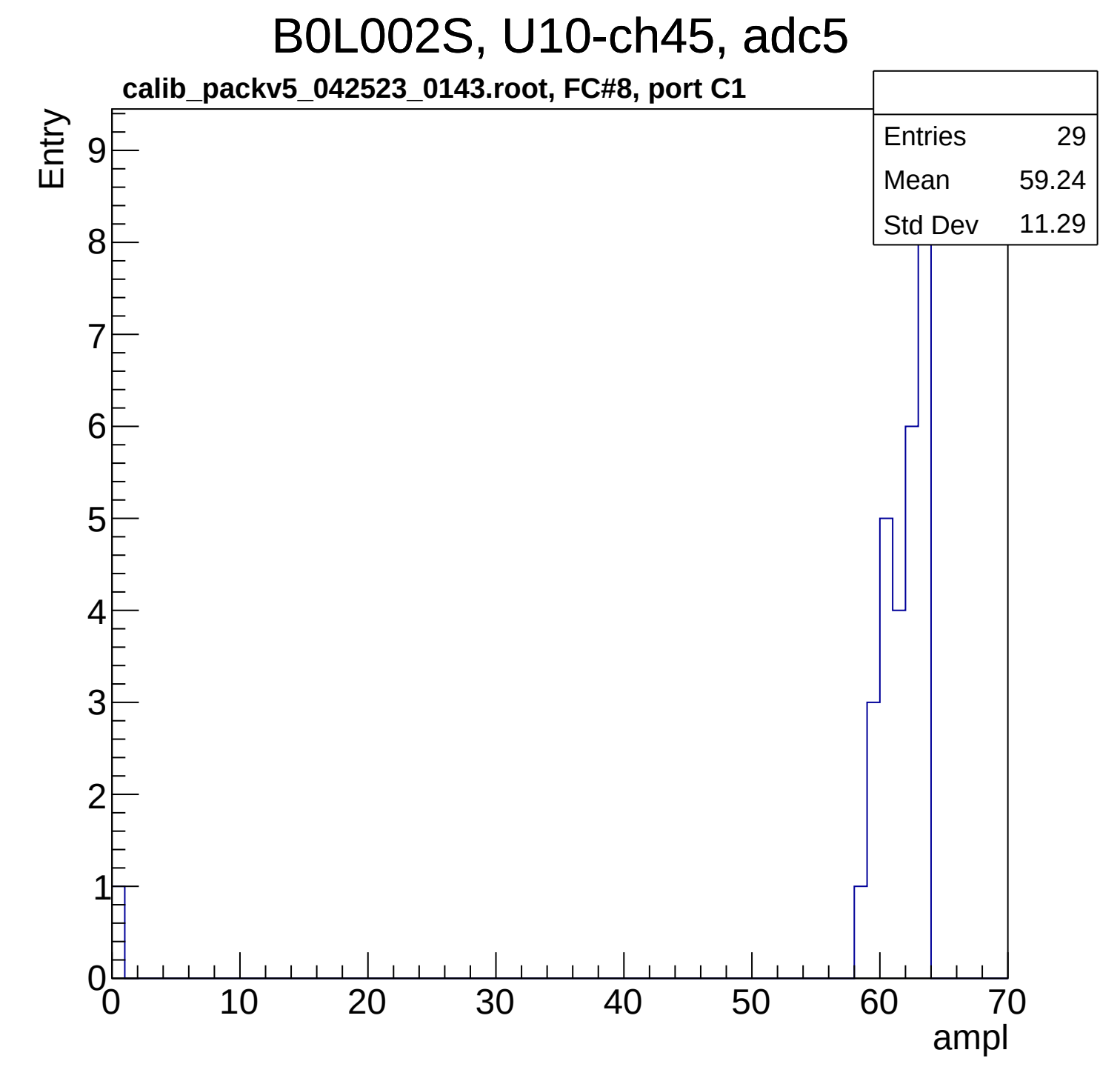

B0L002S, U10-ch45, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 31.29
Std Dev 3.169
Gaus mean : 31.9592
Gaus Width: 3.7185
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

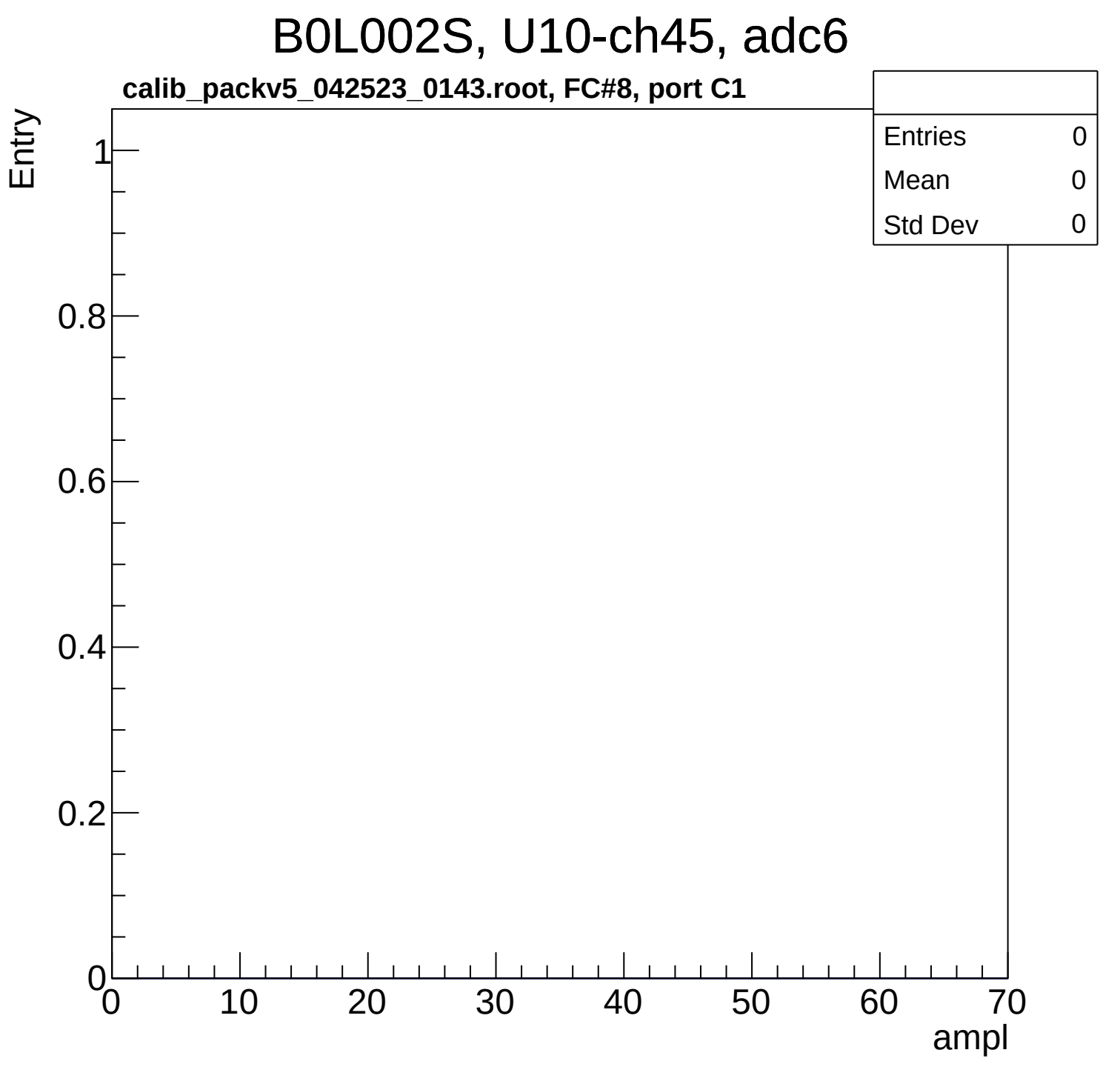

B0L002S, U10-ch45, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.32
Std Dev 3.394
Gaus mean : 39.6007
Gaus Width: 3.3762
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

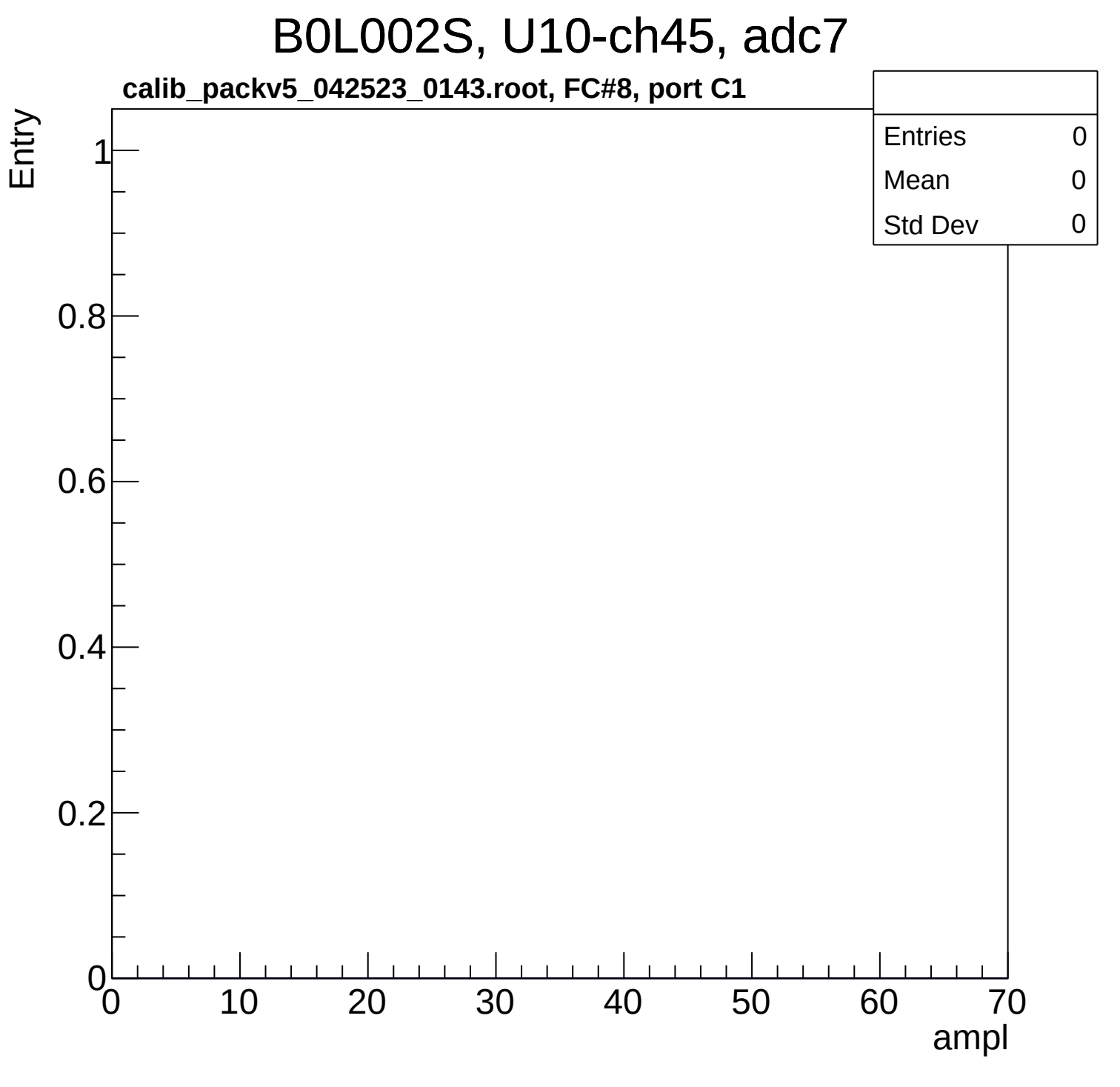

B0L002S, U10-ch45, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.62
Std Dev 3.397
Gaus mean : 46.3246
Gaus Width: 3.6443
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

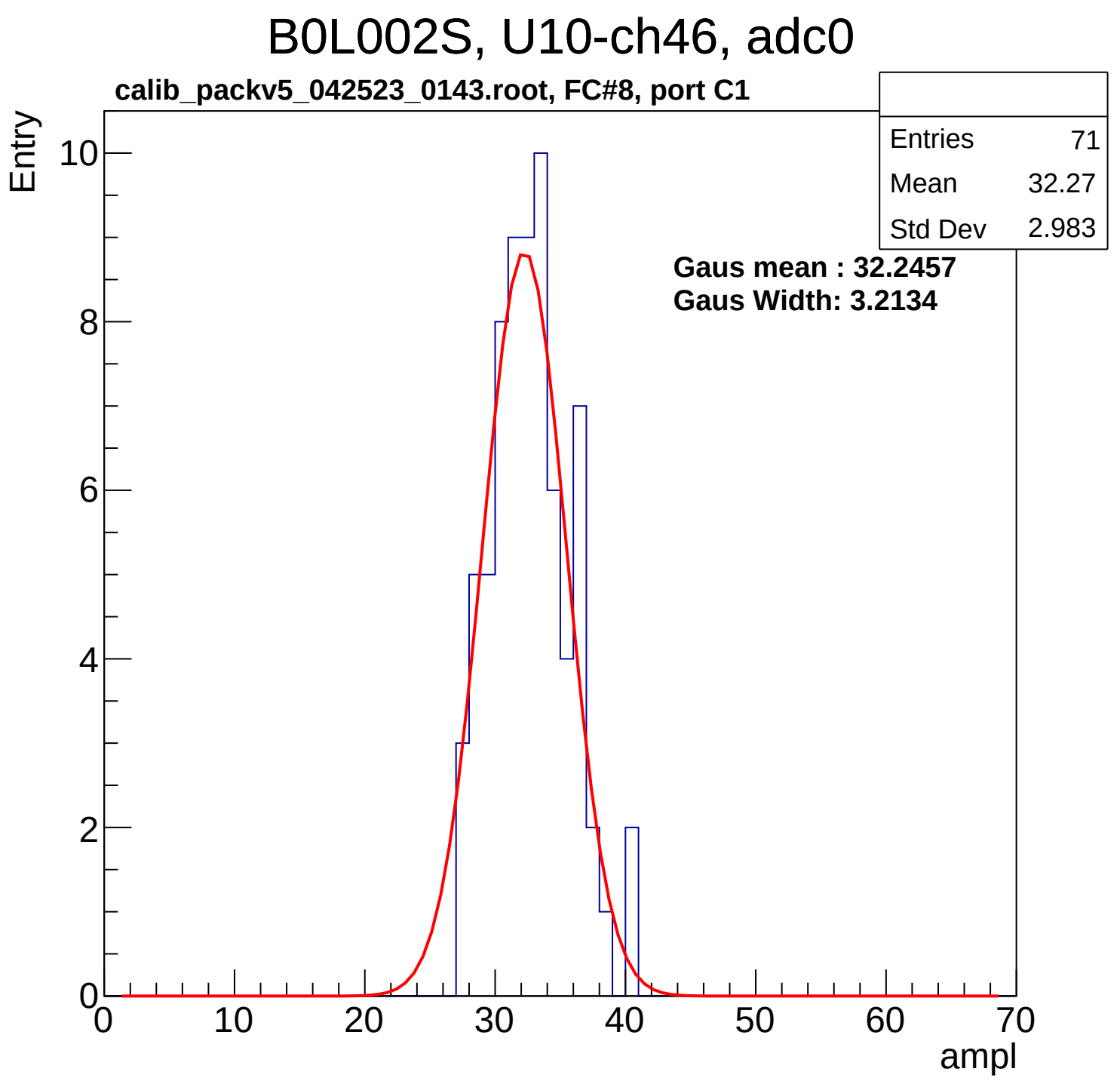

B0L002S, U10-ch45, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 52.88
Std Dev 3.756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

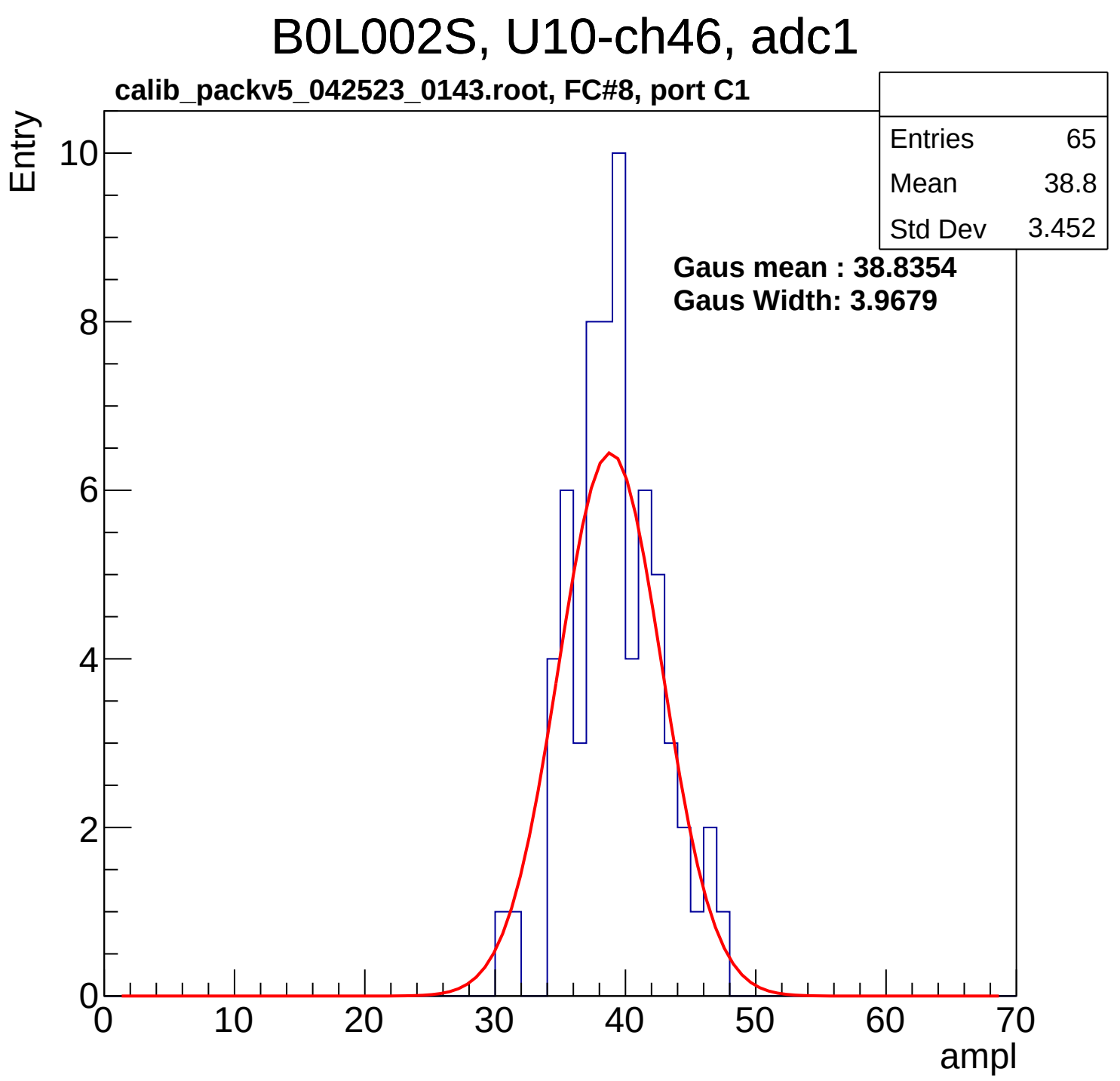

B0L002S, U10-ch45, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 57.63
Std Dev 3.228
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

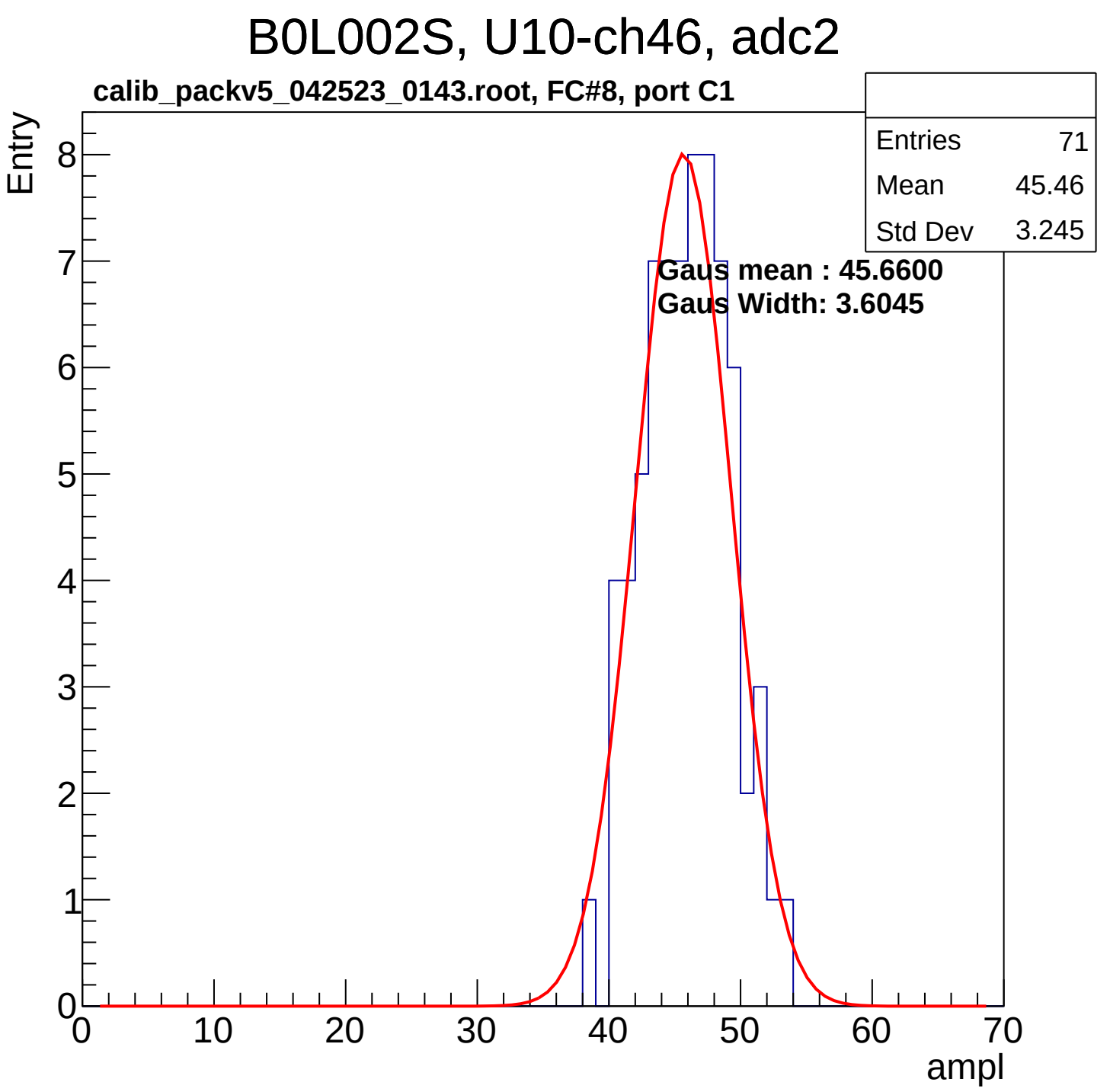

B0L002S, U10-ch45, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 59.24
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

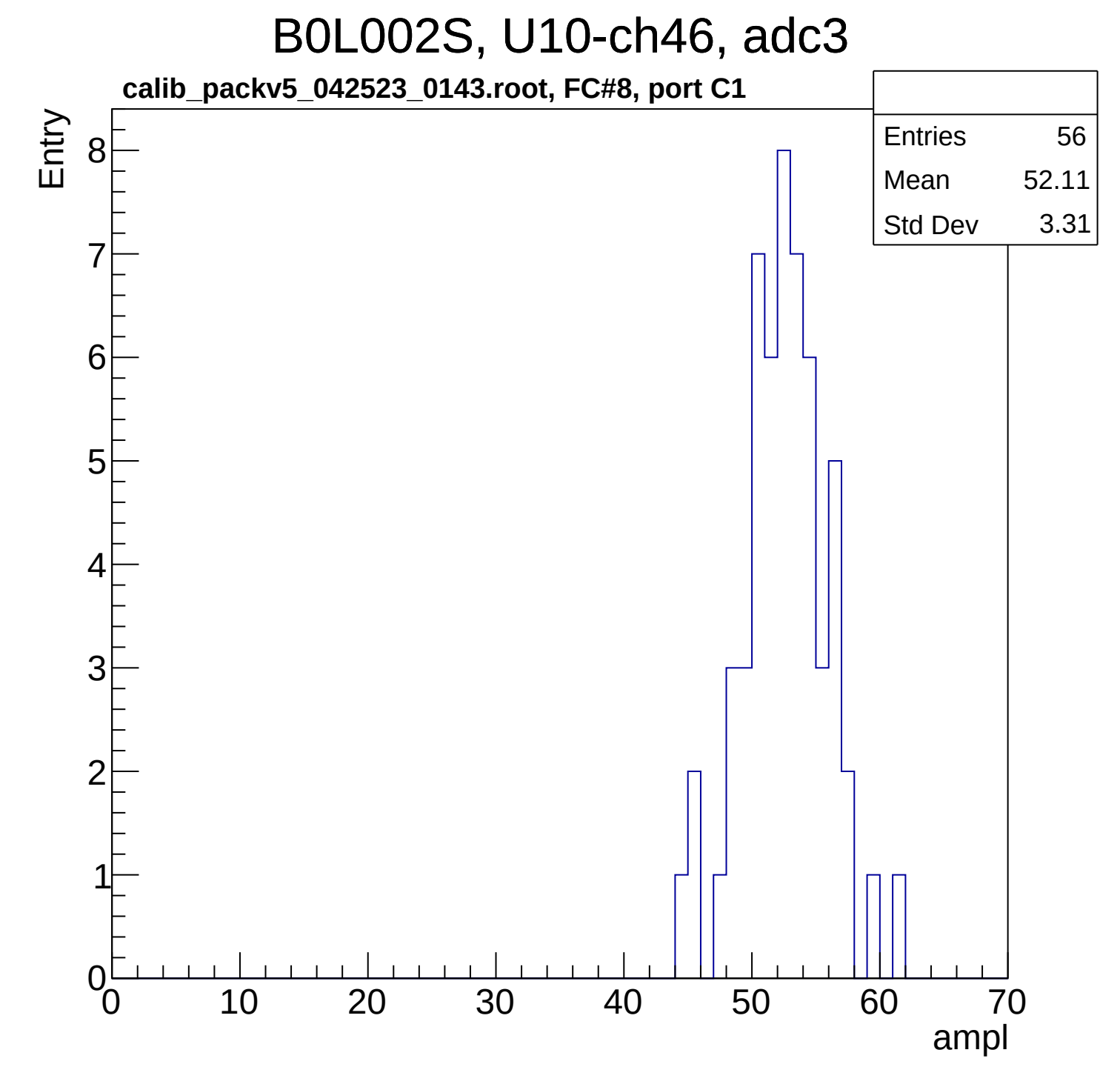

B0L002S, U10-ch45, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

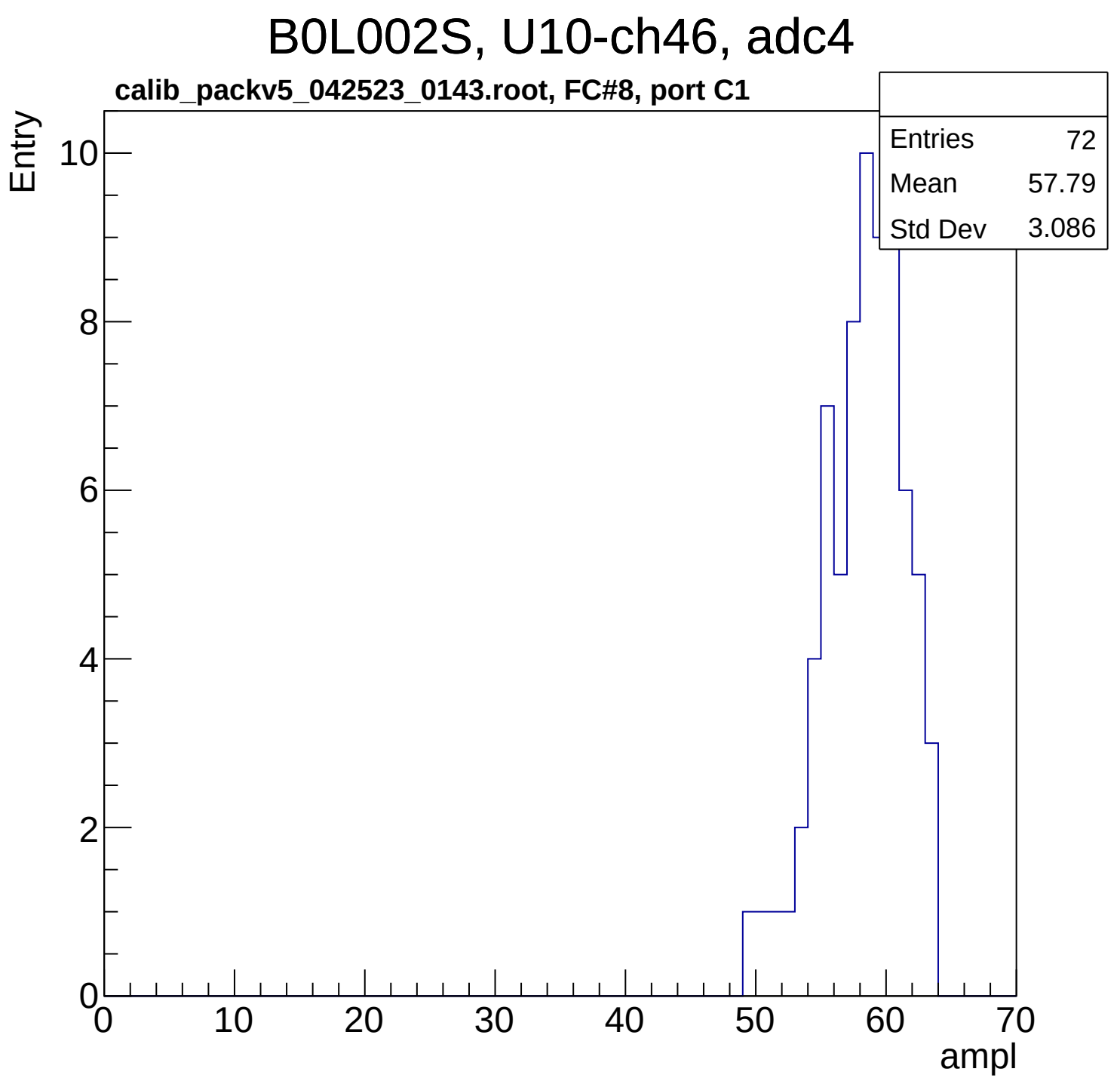

B0L002S, U10-ch45, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

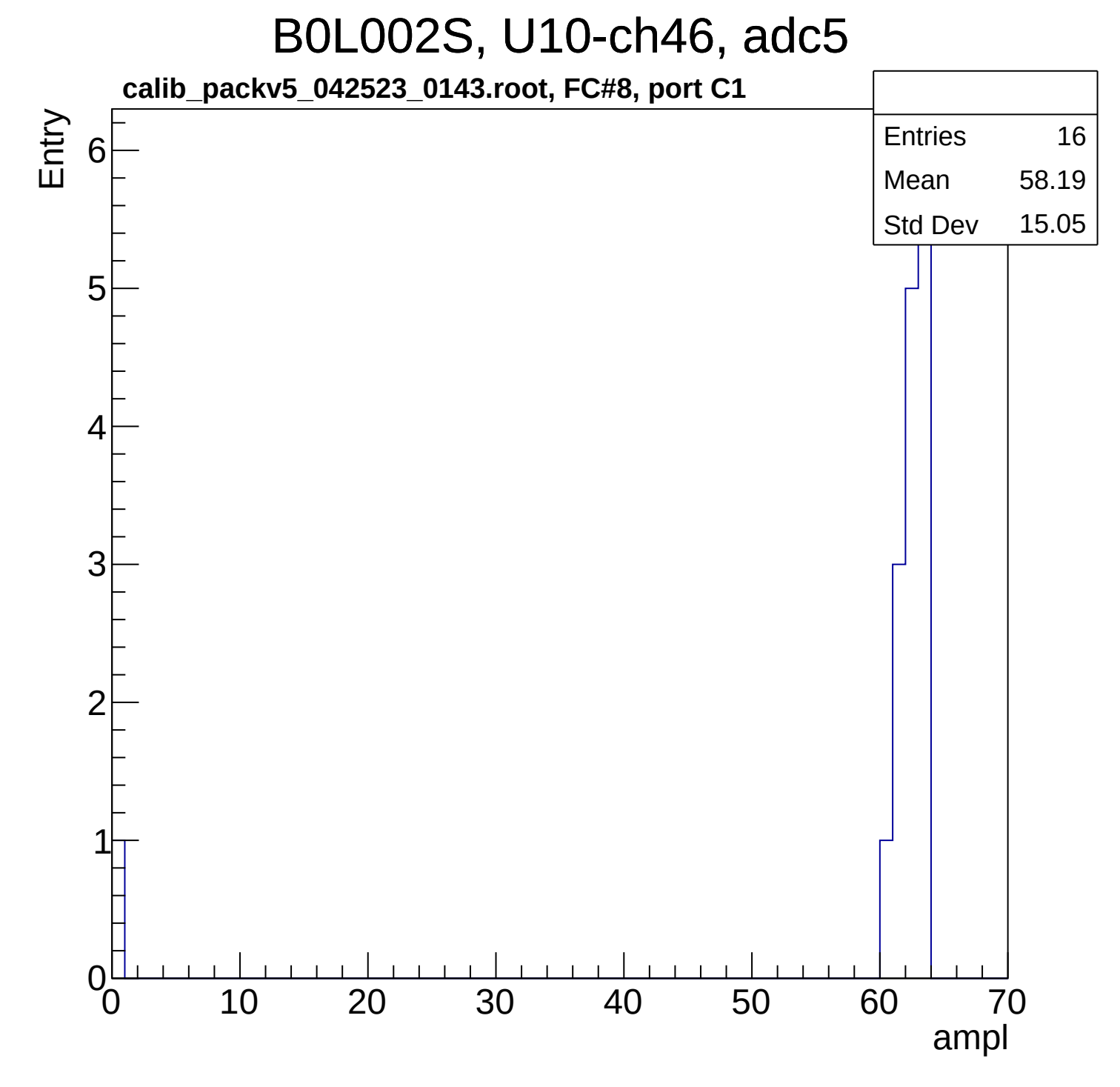

B0L002S, U10-ch46, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 32.27
Std Dev 2.983
Gaus mean : 32.2457
Gaus Width: 3.2134
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

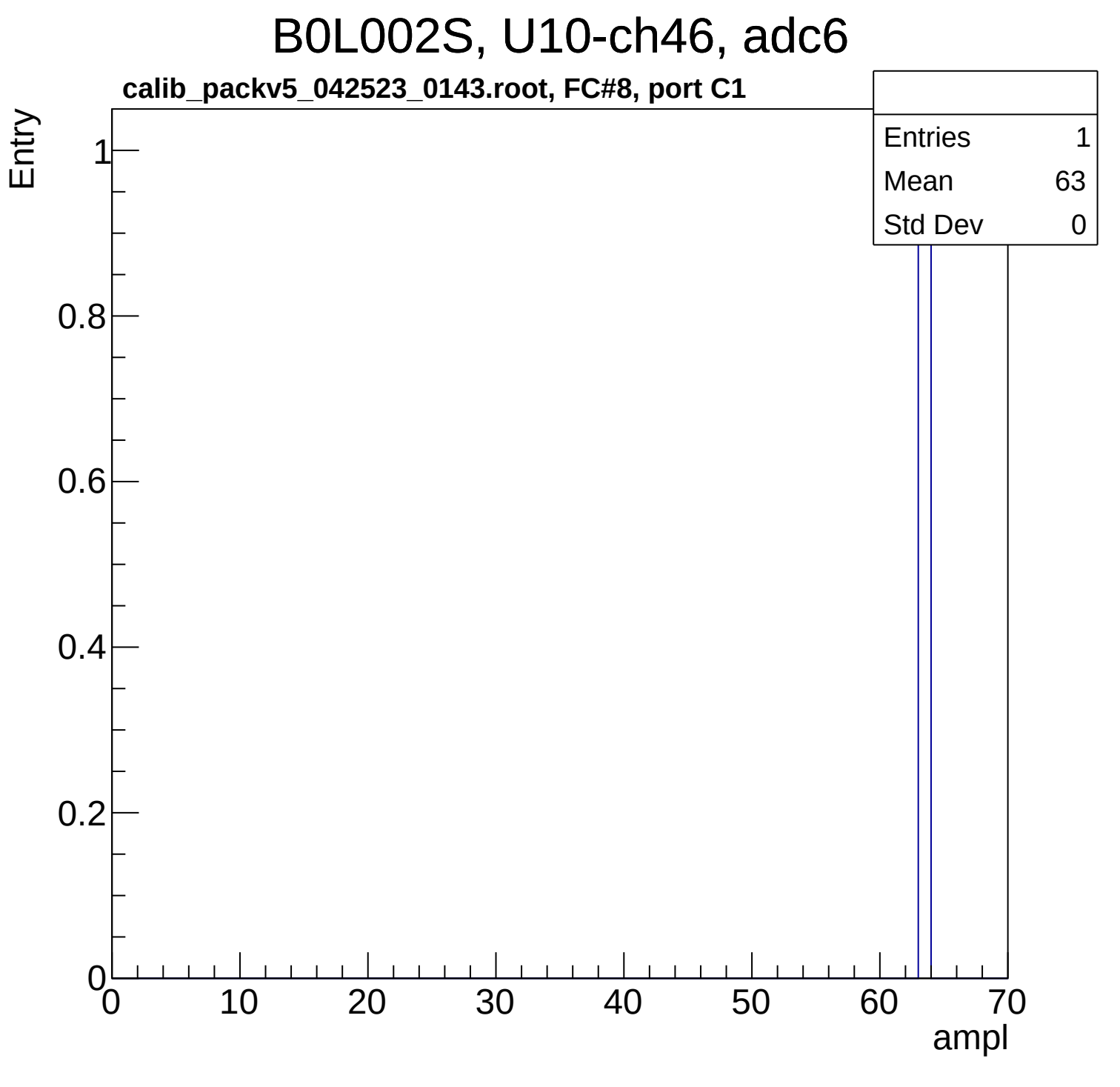

B0L002S, U10-ch46, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 38.8
Std Dev 3.452
Gaus mean : 38.8354
Gaus Width: 3.9679
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

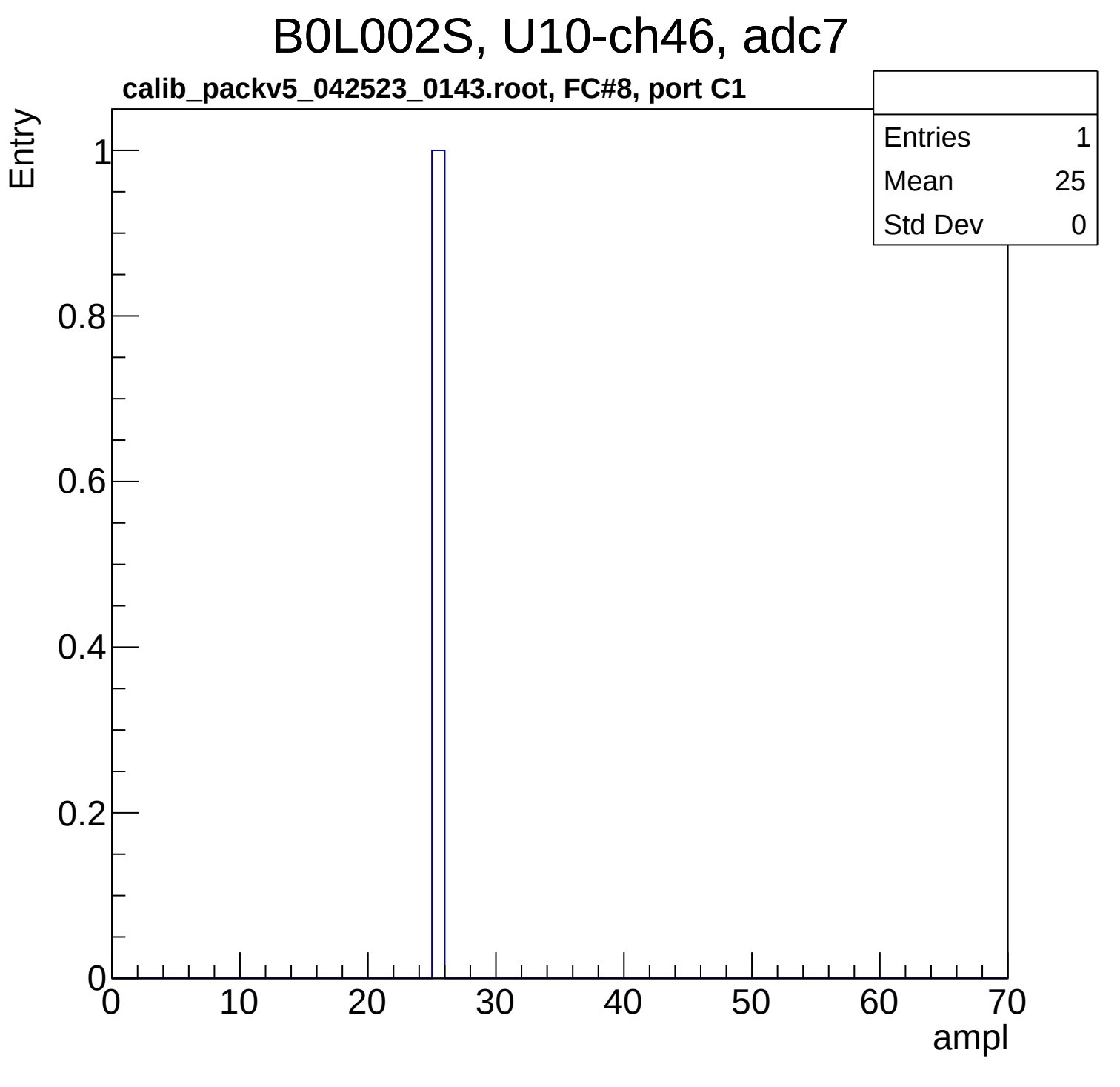

B0L002S, U10-ch46, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 45.46
Std Dev 3.245
Gaus mean : 45.6600
Gaus Width: 3.6045
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

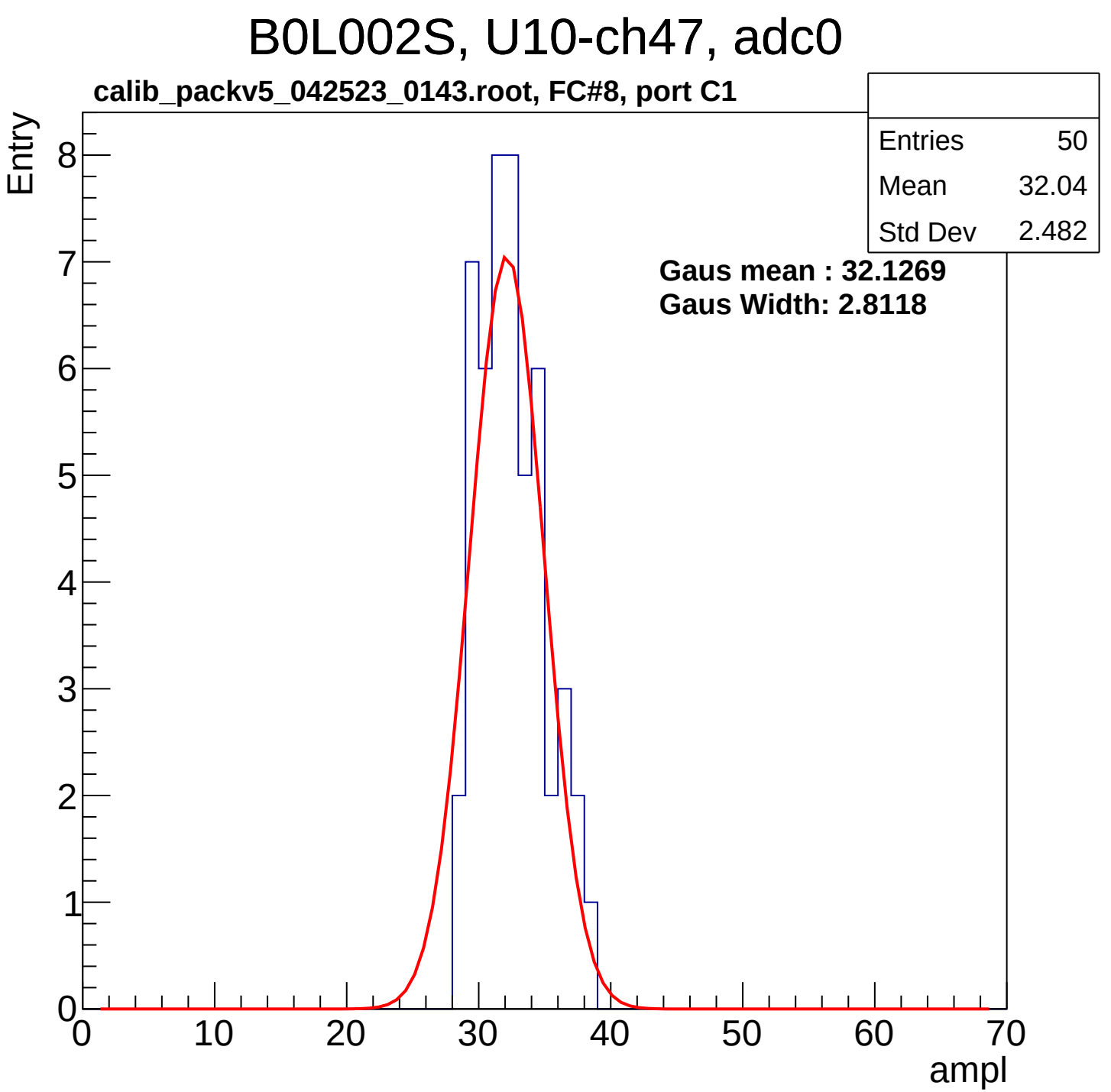

B0L002S, U10-ch46, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 52.11
Std Dev 3.31
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

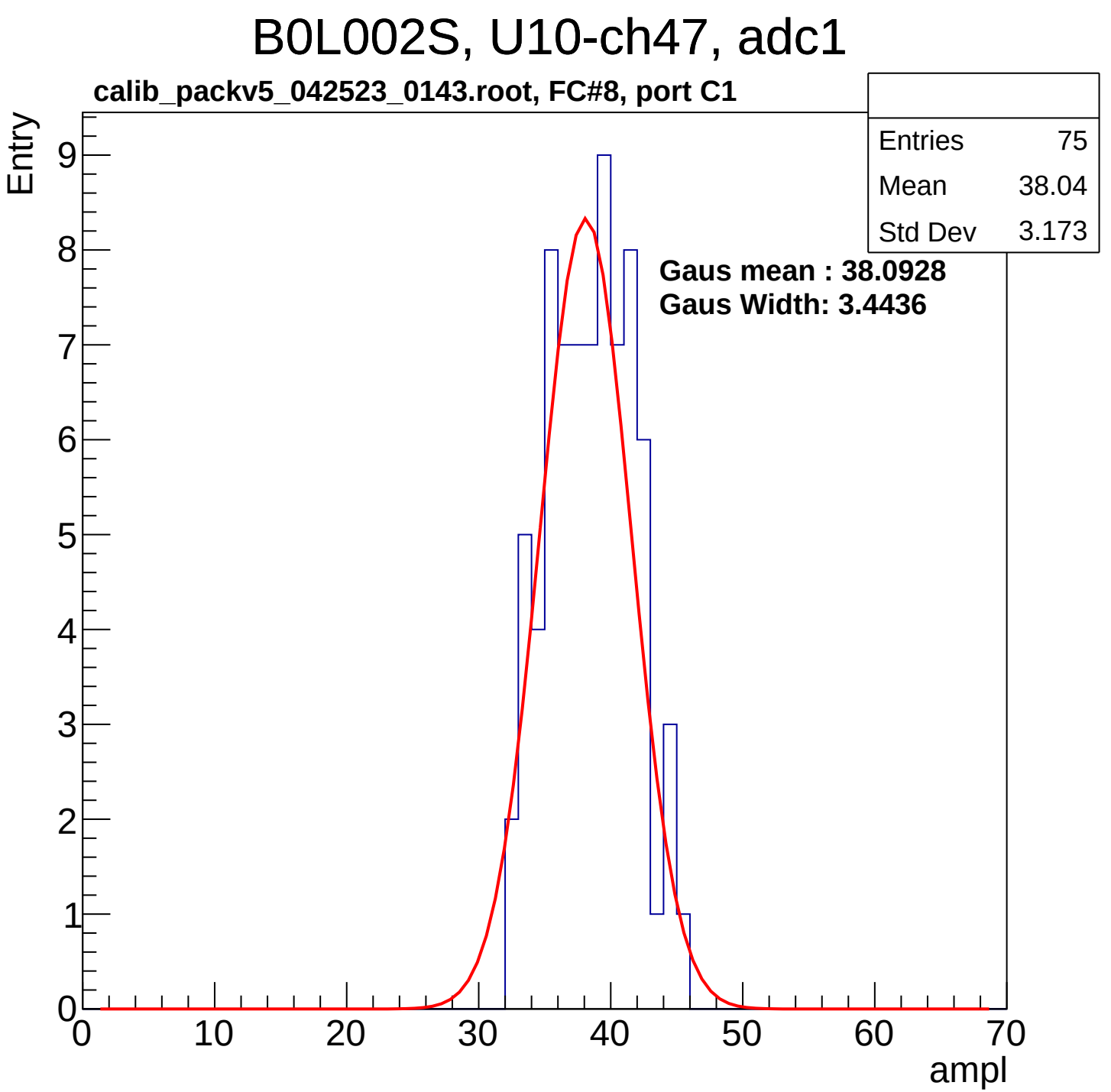

B0L002S, U10-ch46, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 57.79
Std Dev 3.086
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

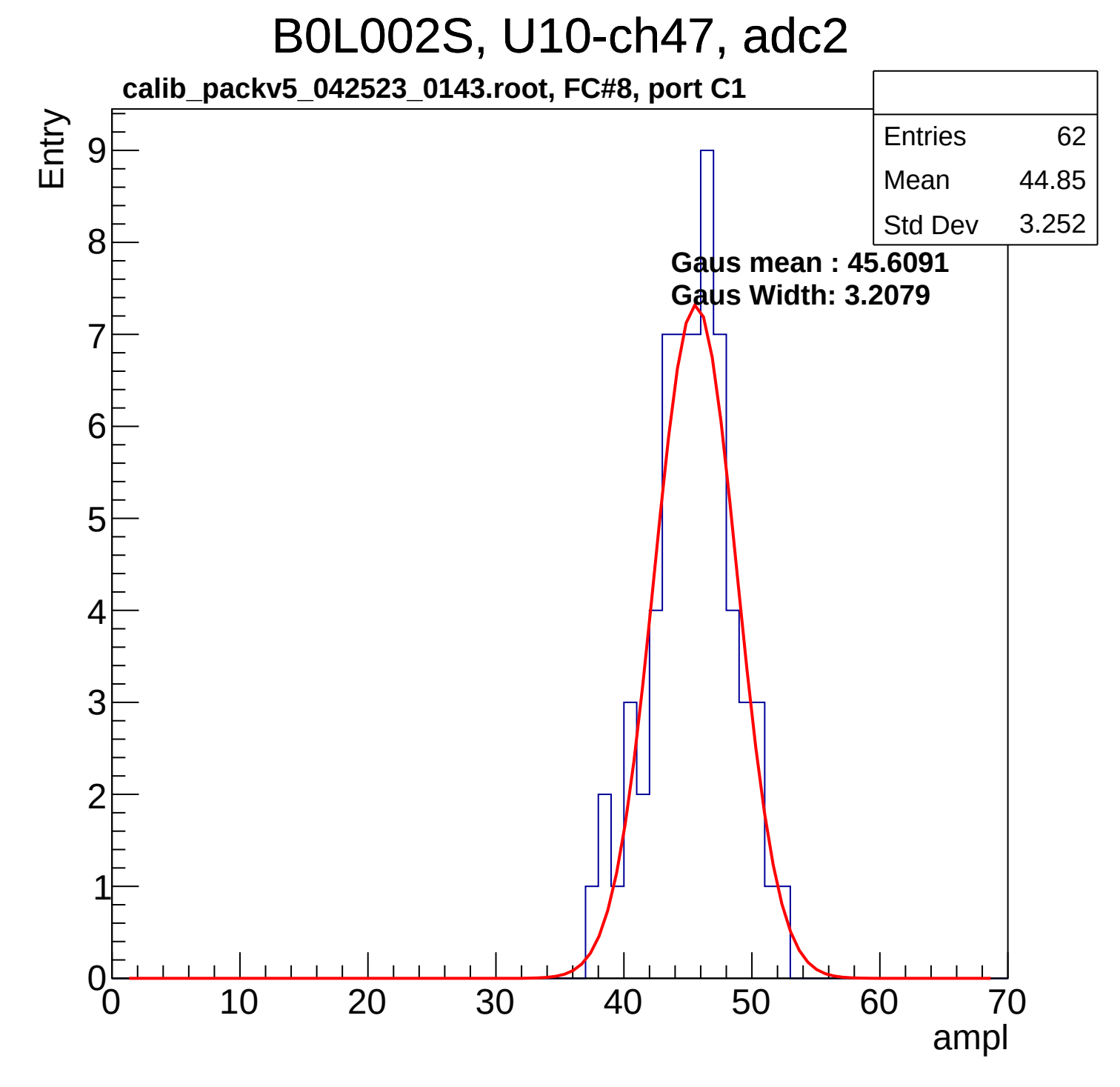

B0L002S, U10-ch46, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 58.19
Std Dev 15.05
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

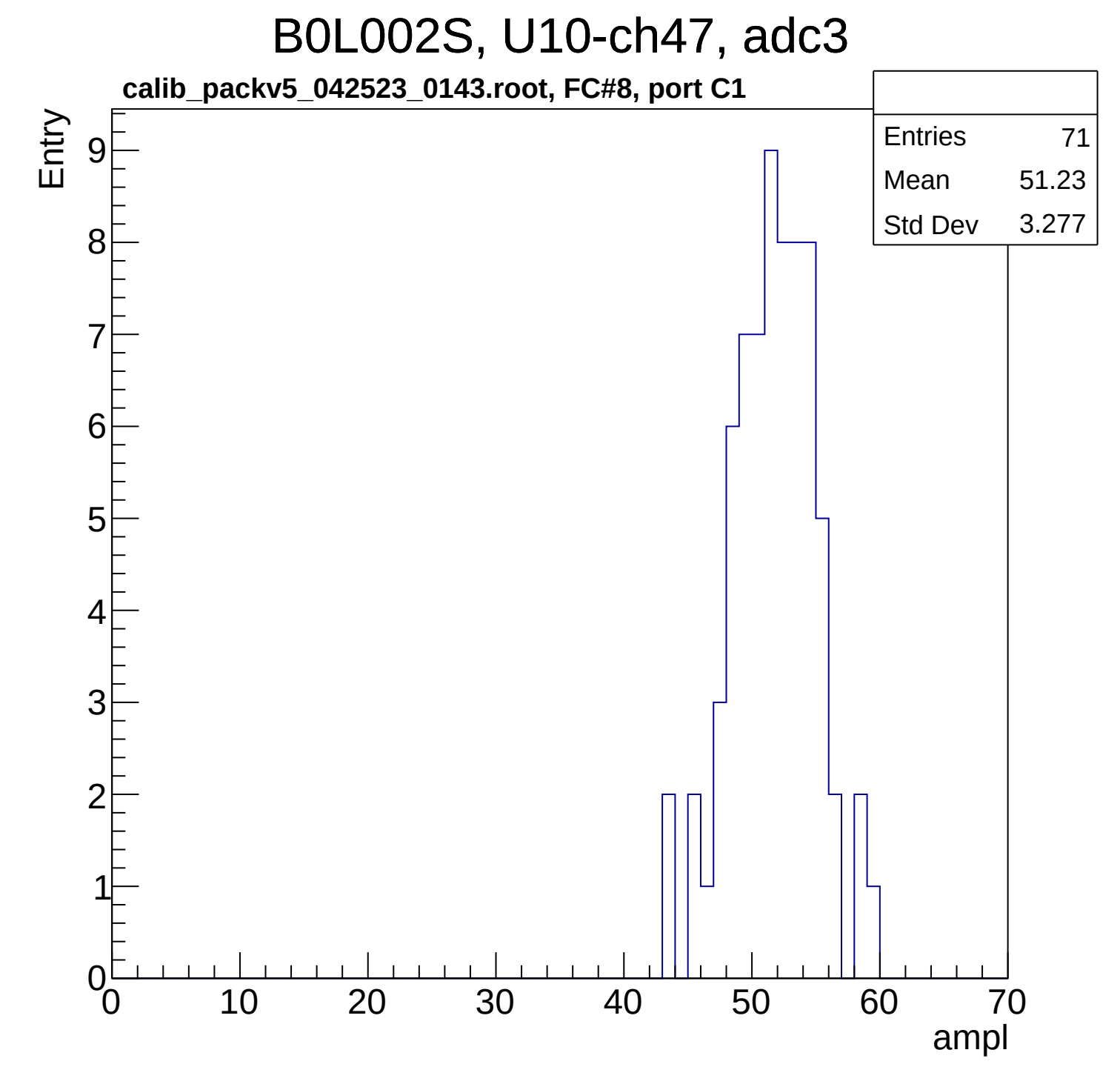

B0L002S, U10-ch46, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

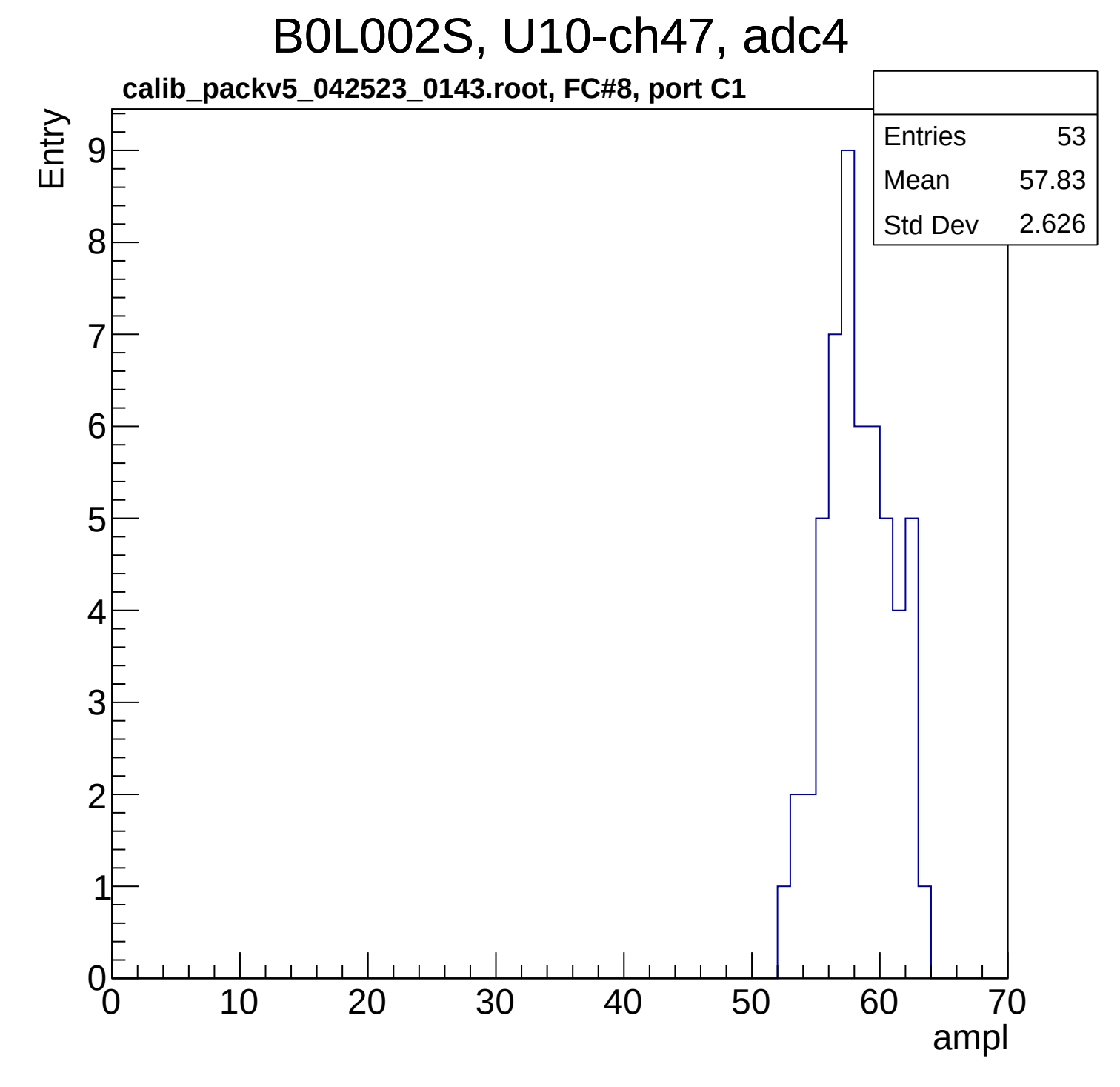

B0L002S, U10-ch46, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

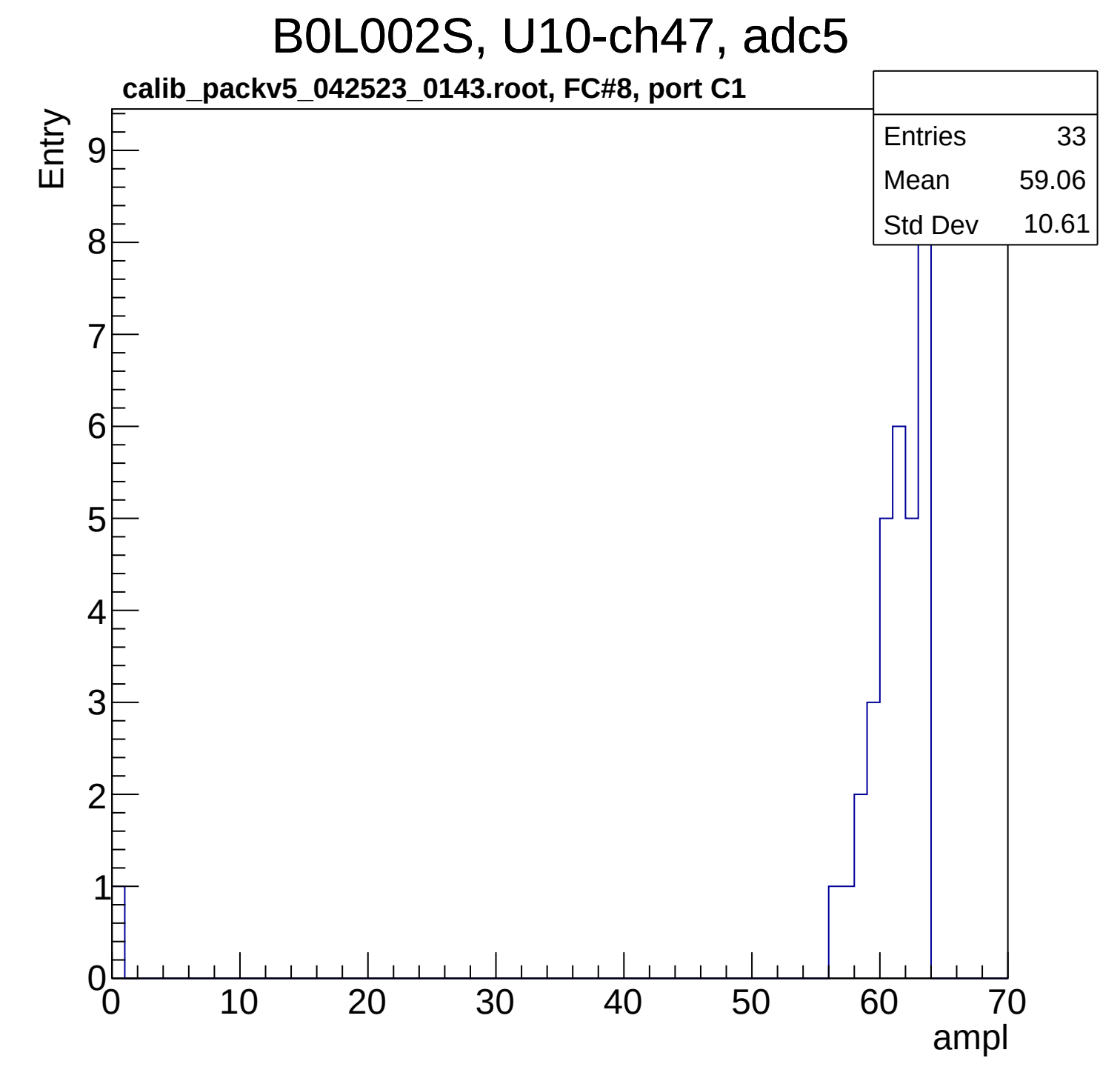

B0L002S, U10-ch47, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 32.04
Std Dev 2.482
Gaus mean : 32.1269
Gaus Width: 2.8118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

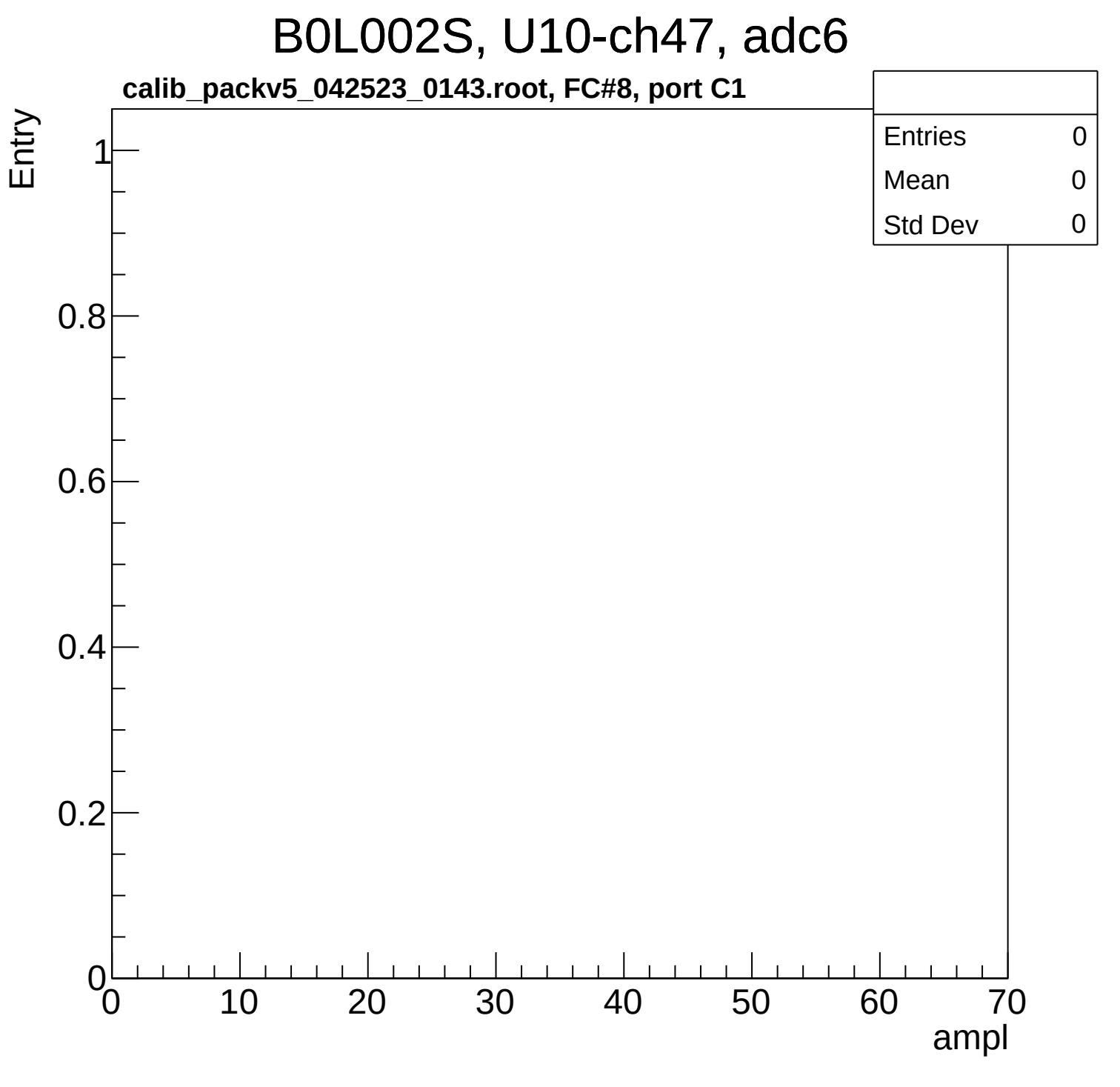

B0L002S, U10-ch47, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 38.04
Std Dev 3.173
Gaus mean : 38.0928
Gaus Width: 3.4436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

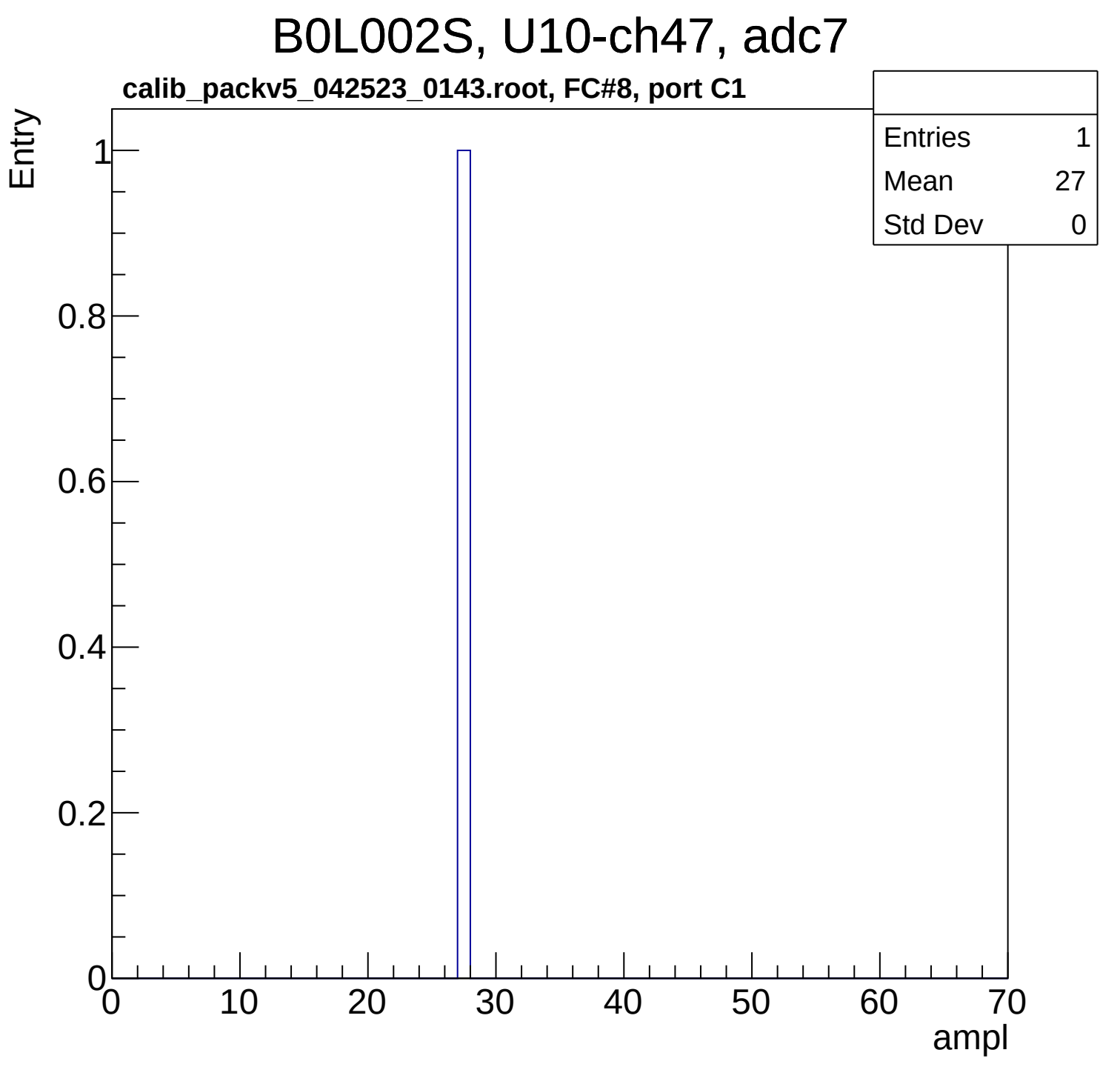

B0L002S, U10-ch47, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.85
Std Dev 3.252
Gaus mean : 45.6091
Gaus Width: 3.2079
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

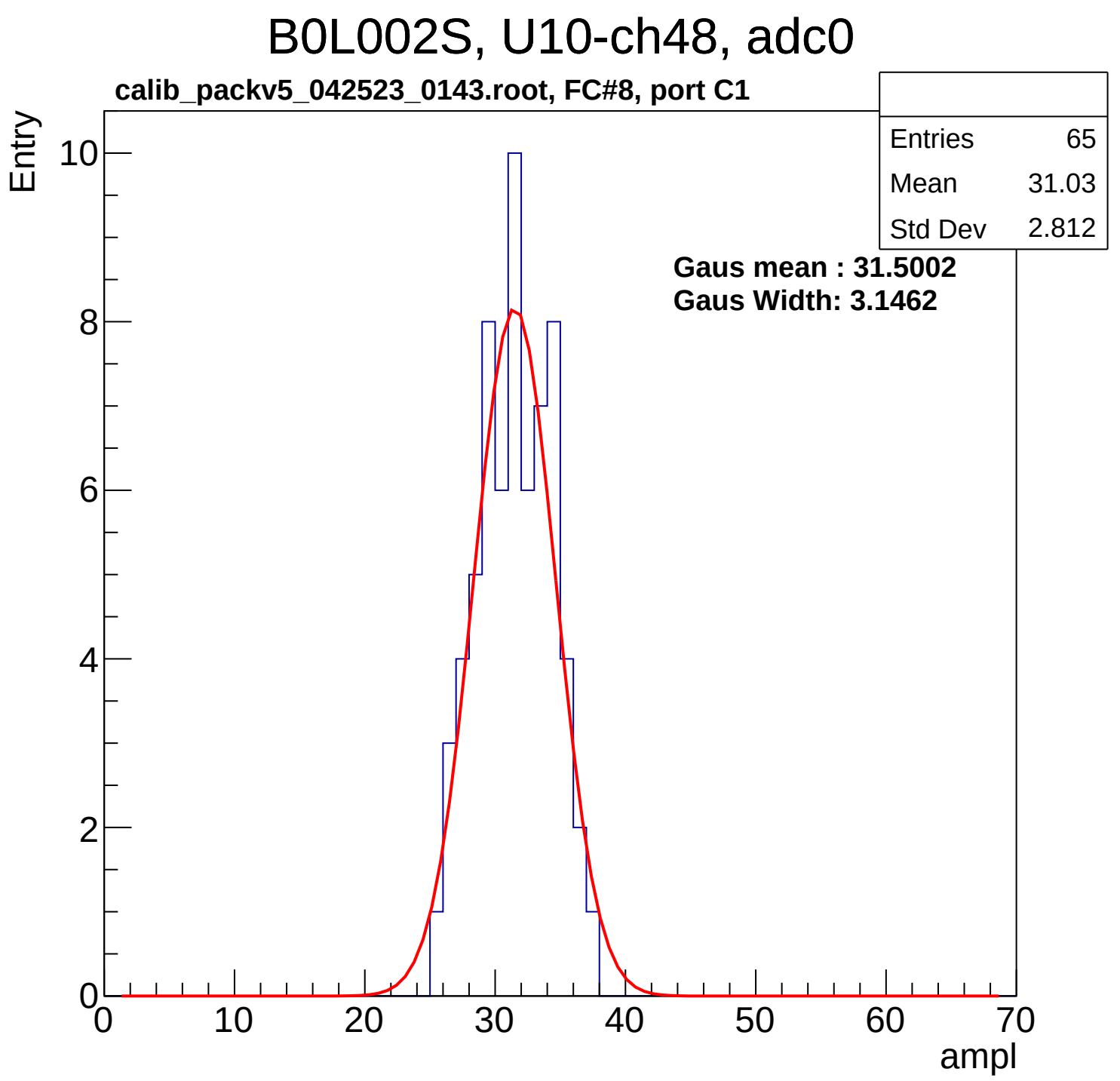

B0L002S, U10-ch47, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.23
Std Dev 3.277
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

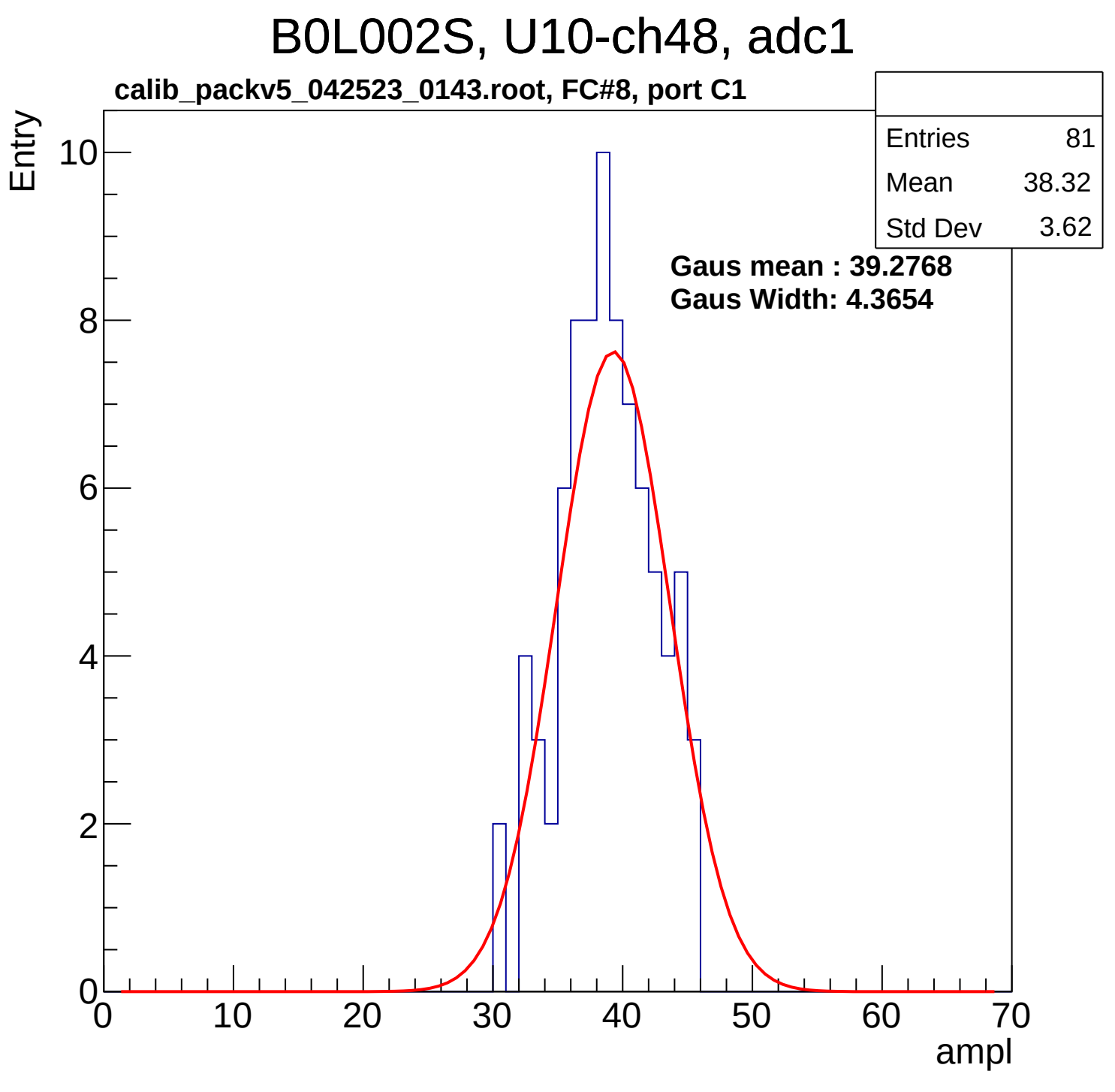

B0L002S, U10-ch47, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.83
Std Dev 2.626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

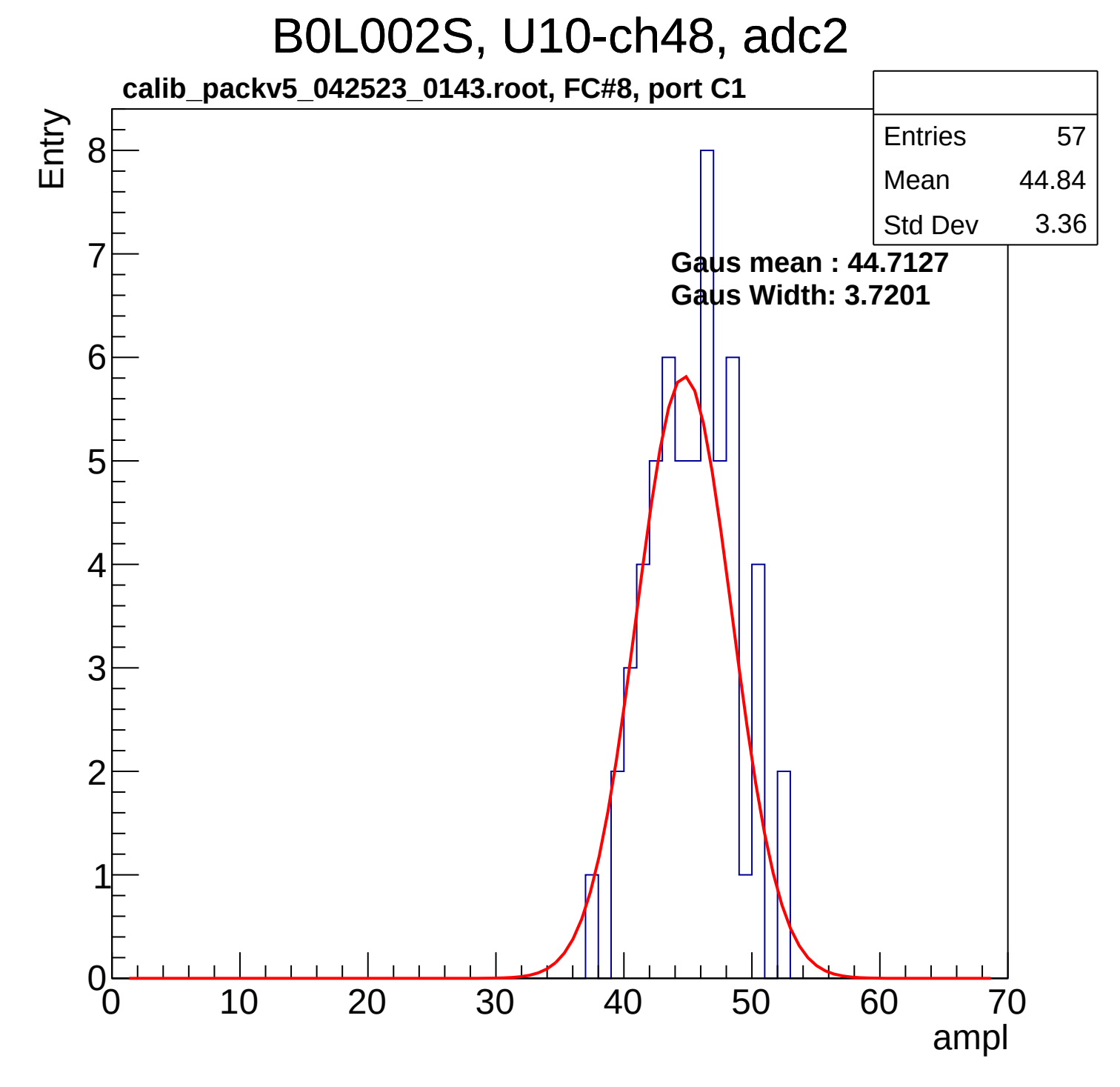

B0L002S, U10-ch47, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.06
Std Dev 10.61
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

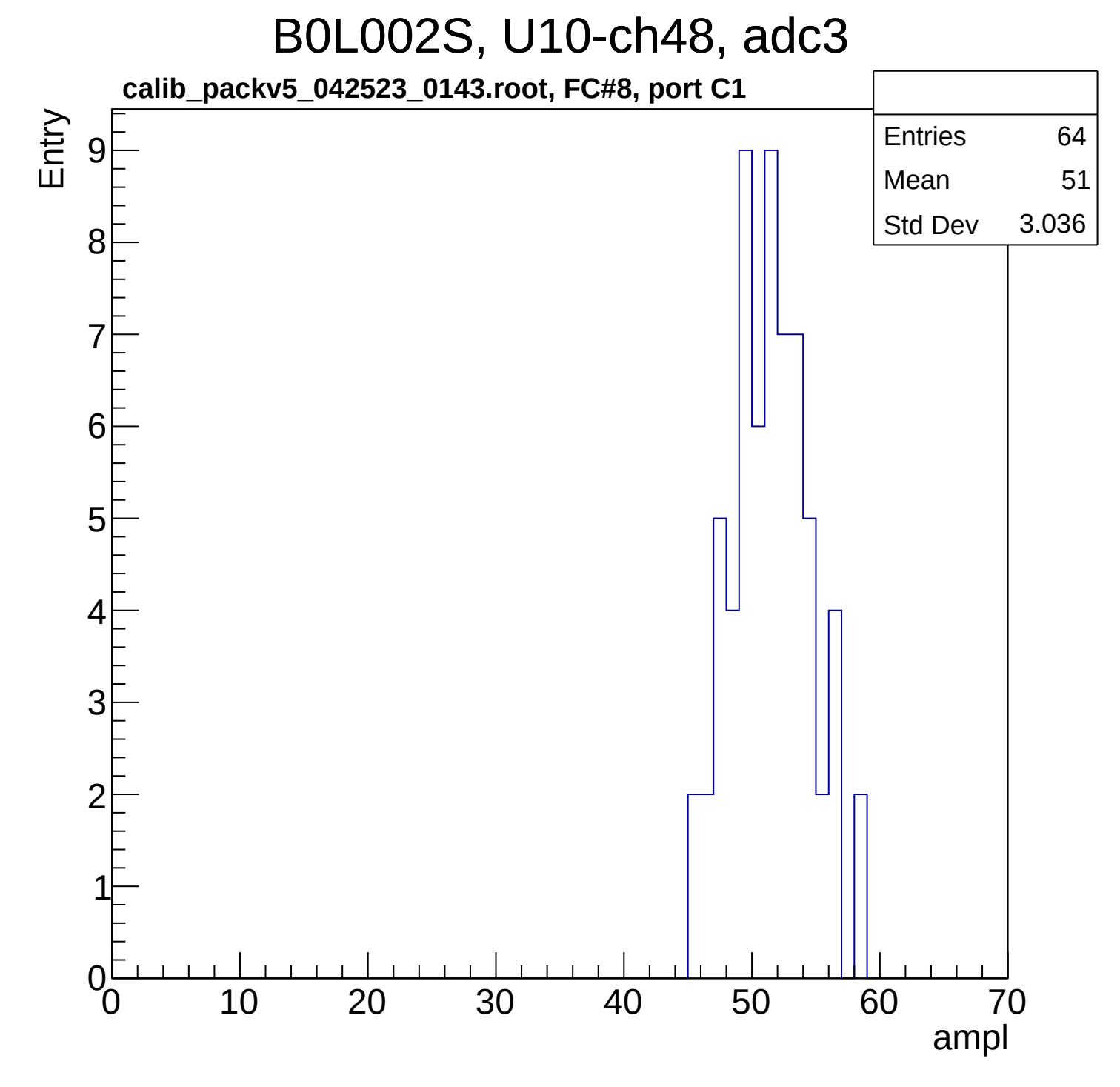

B0L002S, U10-ch47, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

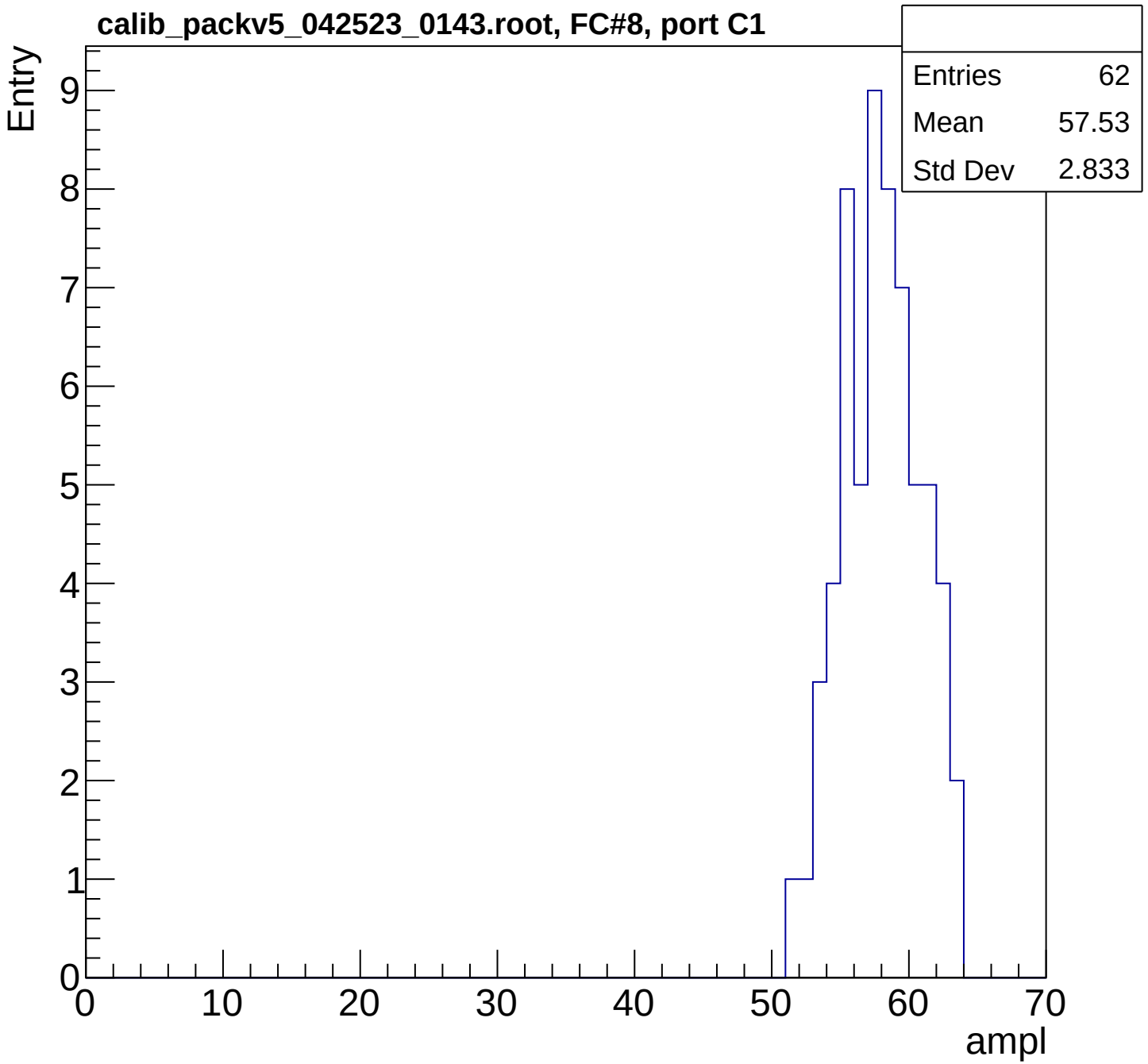

B0L002S, U10-ch47, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 27
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

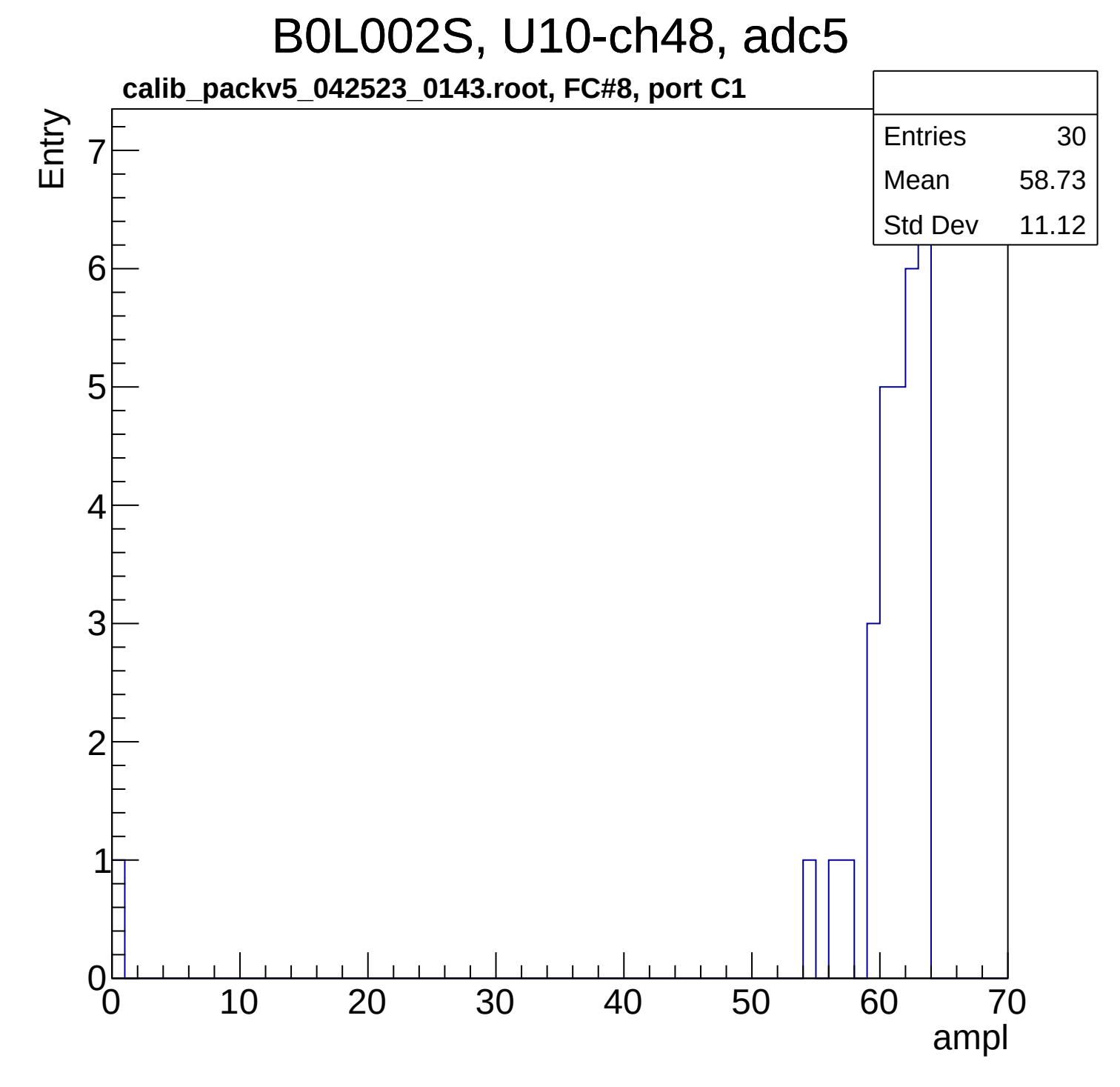

B0L002S, U10-ch48, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 31.03
Std Dev 2.812
Gaus mean : 31.5002
Gaus Width: 3.1462
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

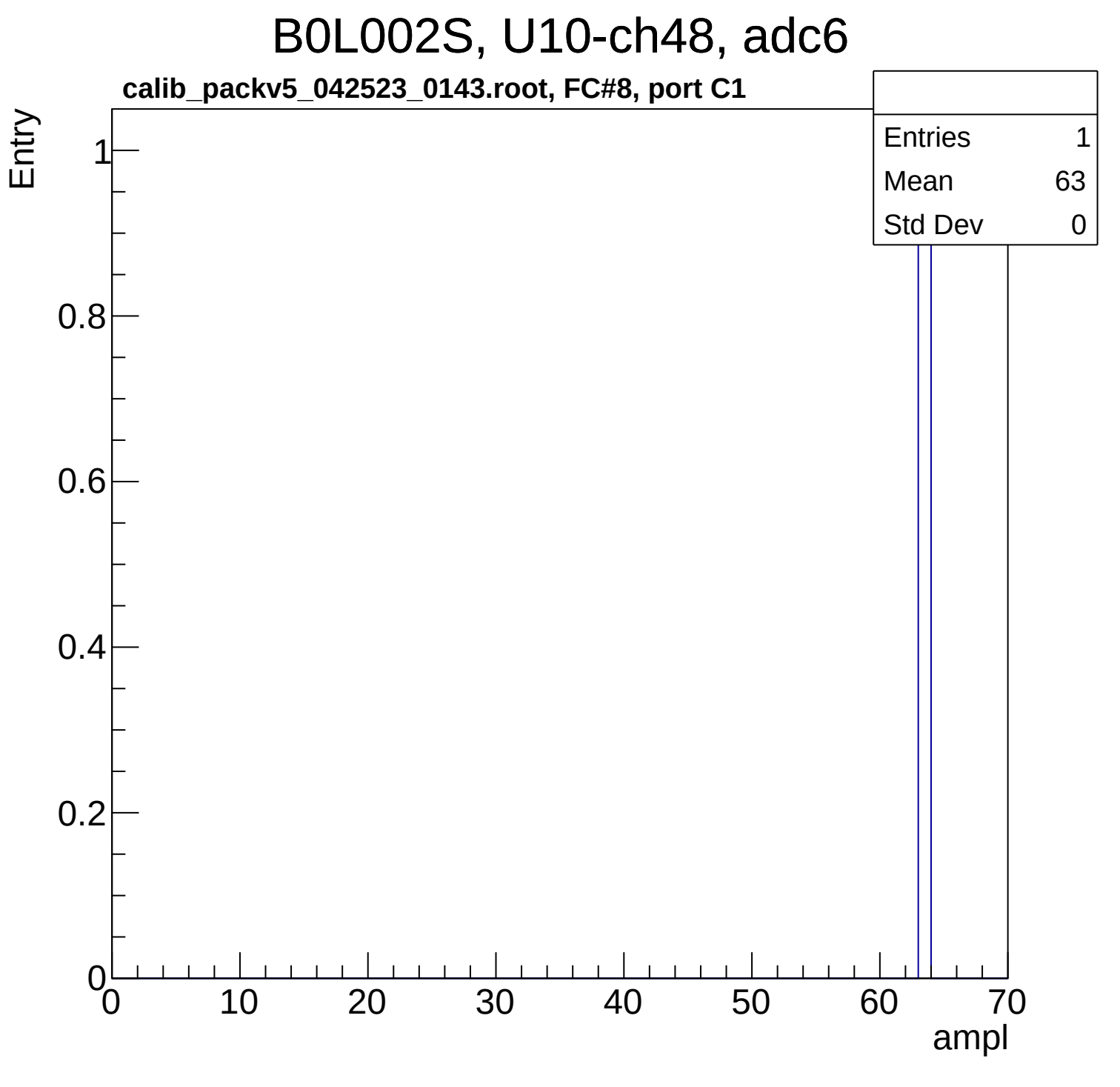

B0L002S, U10-ch48, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 38.32
Std Dev 3.62
Gaus mean : 39.2768
Gaus Width: 4.3654
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

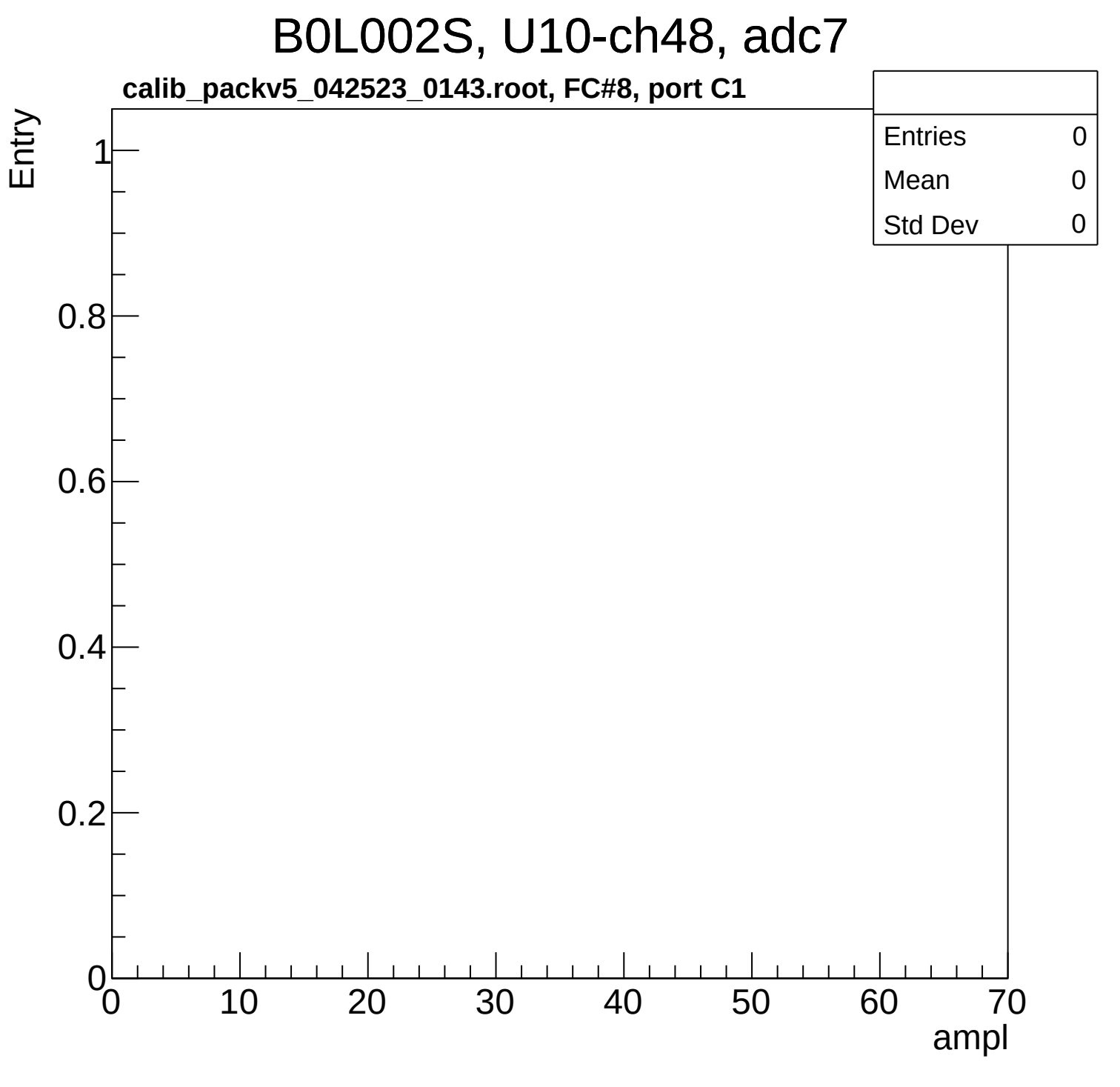

B0L002S, U10-ch48, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 44.84
Std Dev 3.36
Gaus mean : 44.7127
Gaus Width: 3.7201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

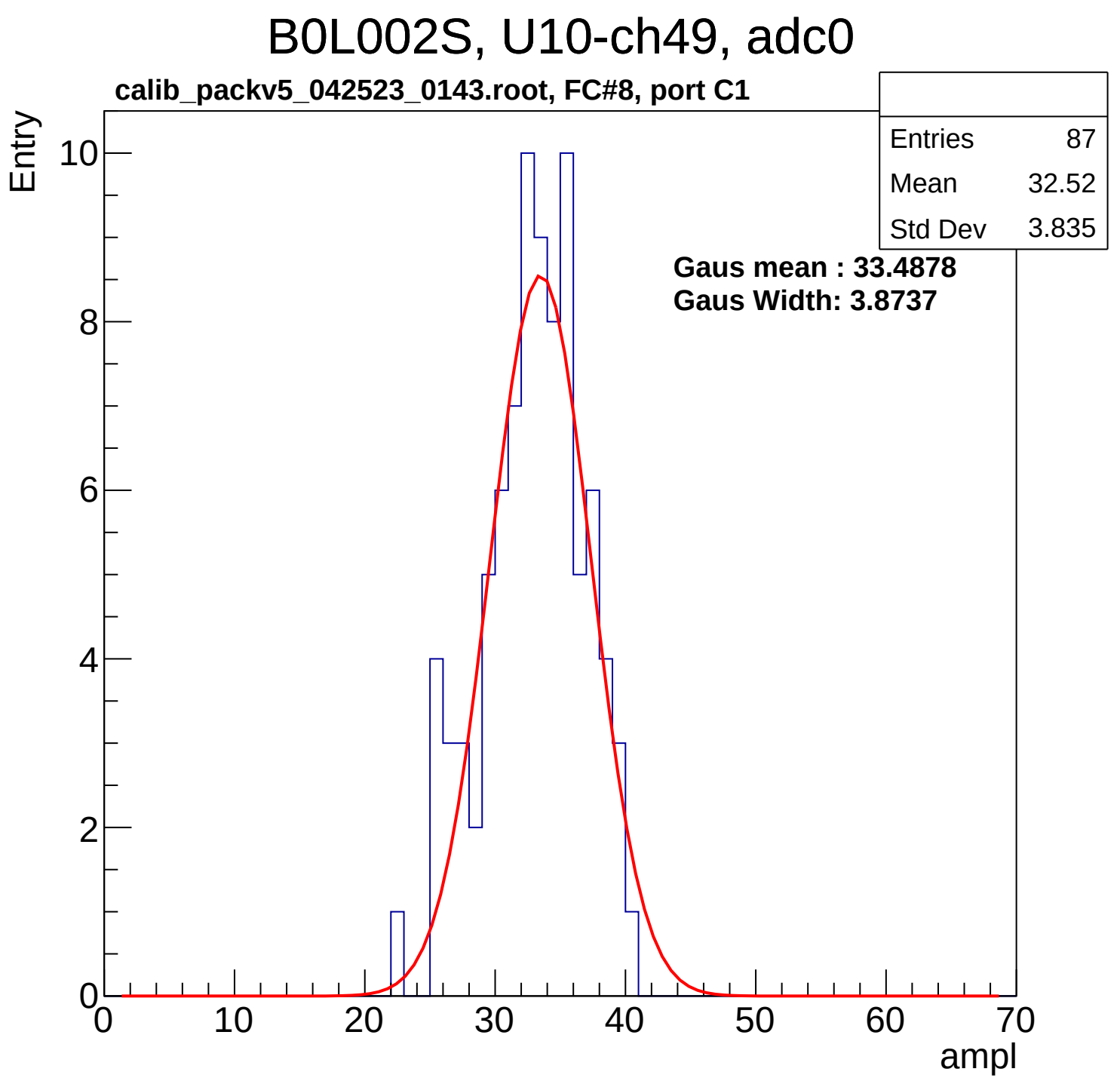

B0L002S, U10-ch48, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

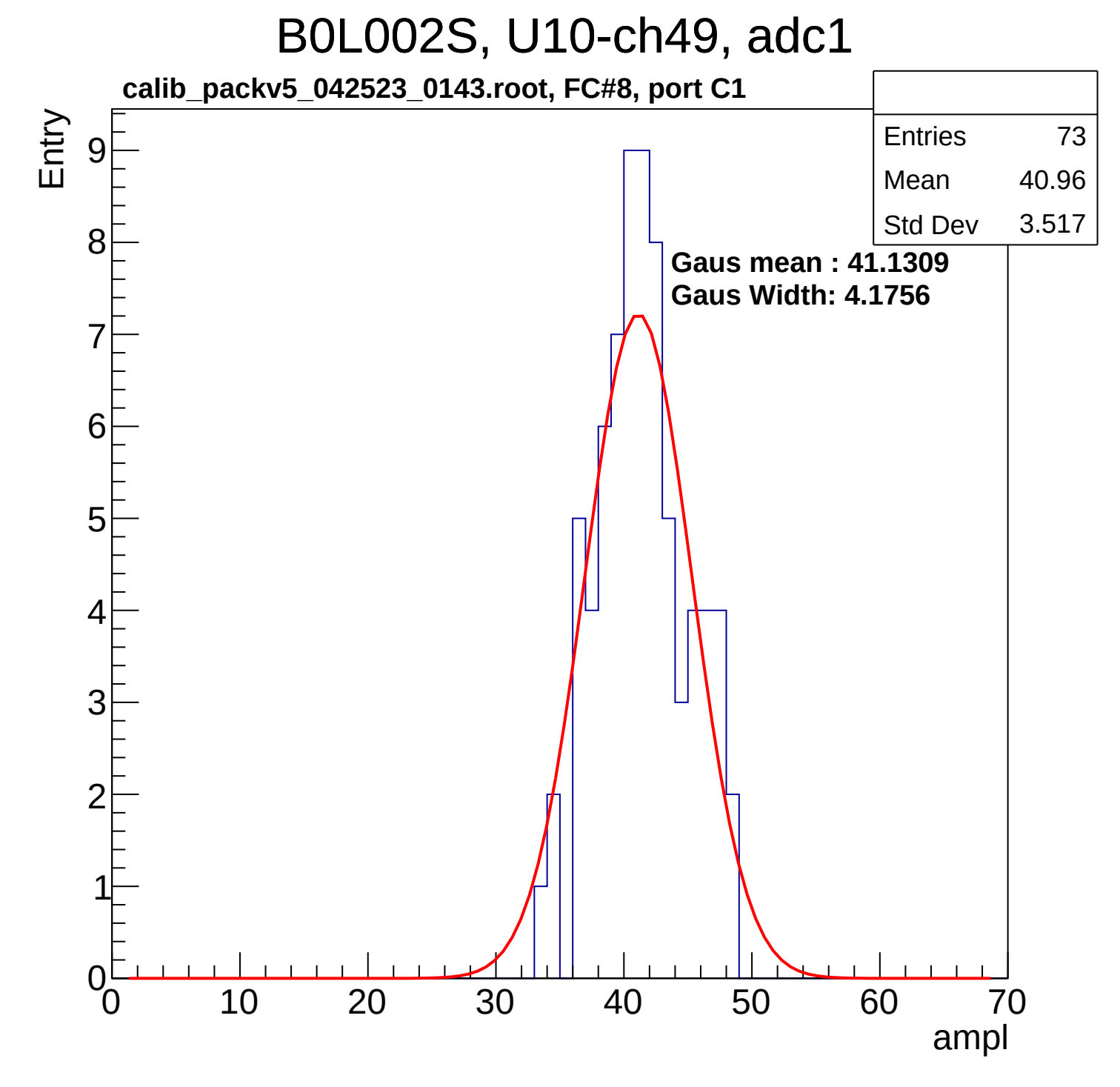

B0L002S, U10-ch48, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 57.53
Std Dev 2.833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

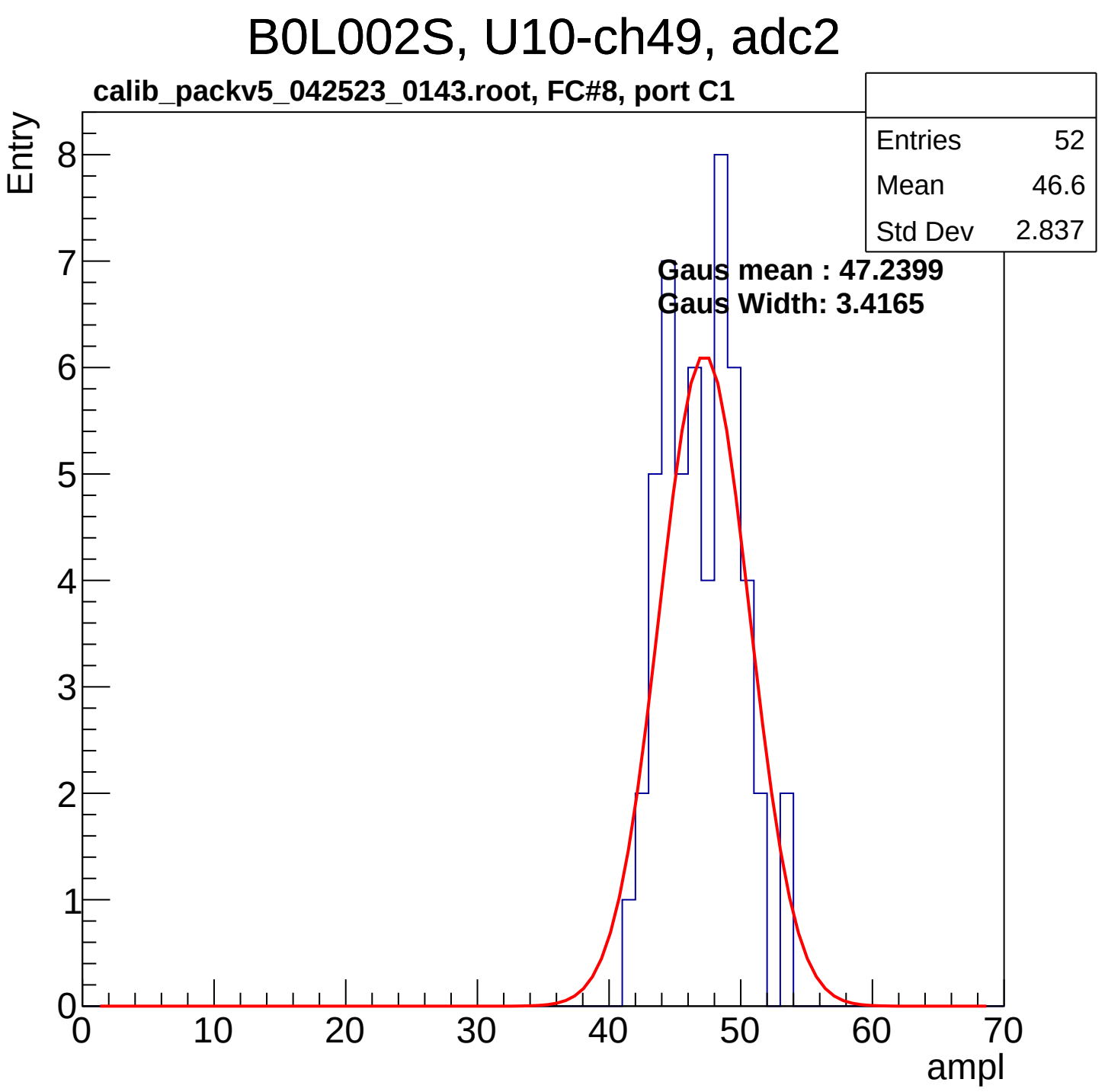

B0L002S, U10-ch48, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.73
Std Dev 11.12
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

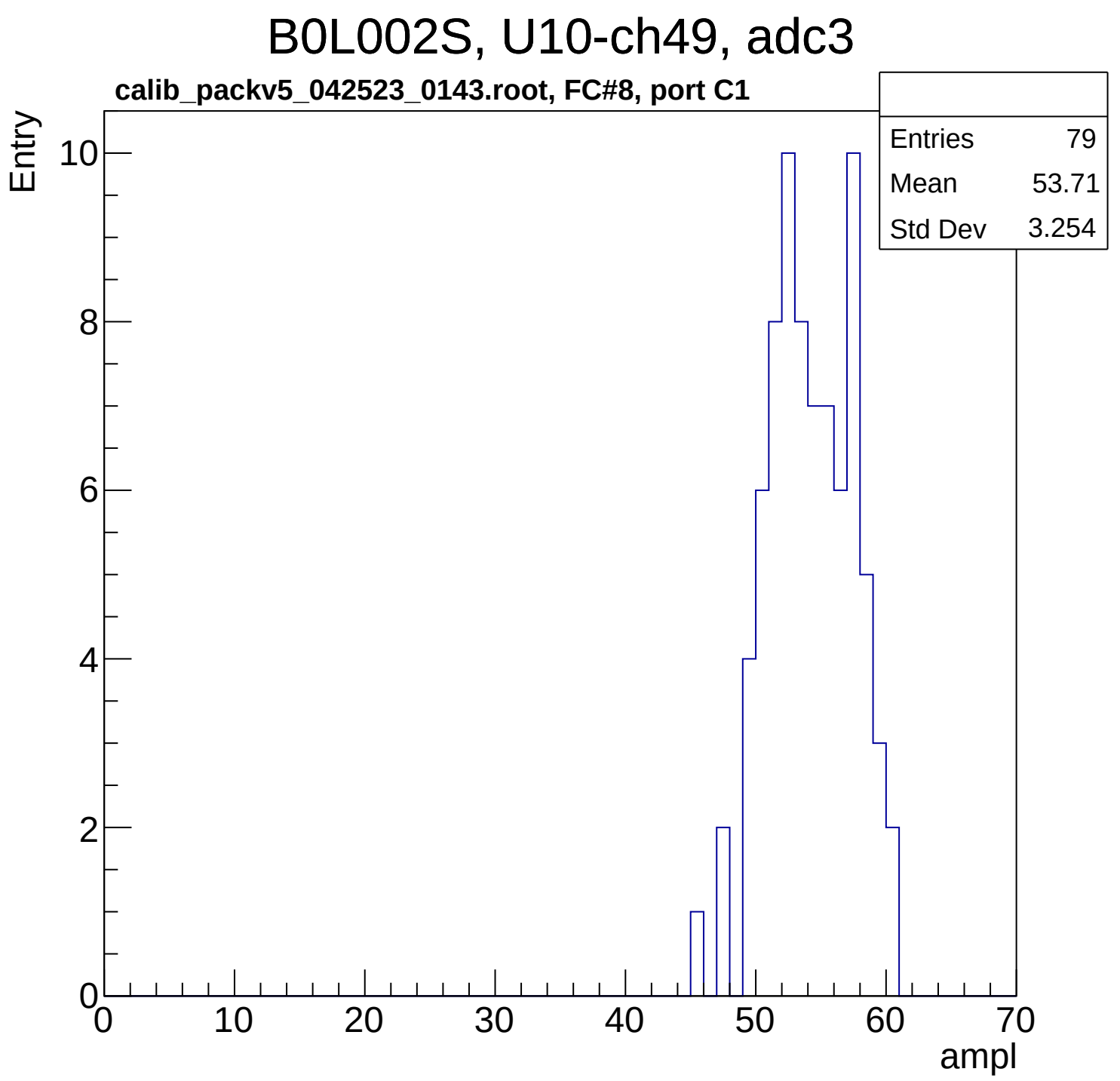

B0L002S, U10-ch48, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

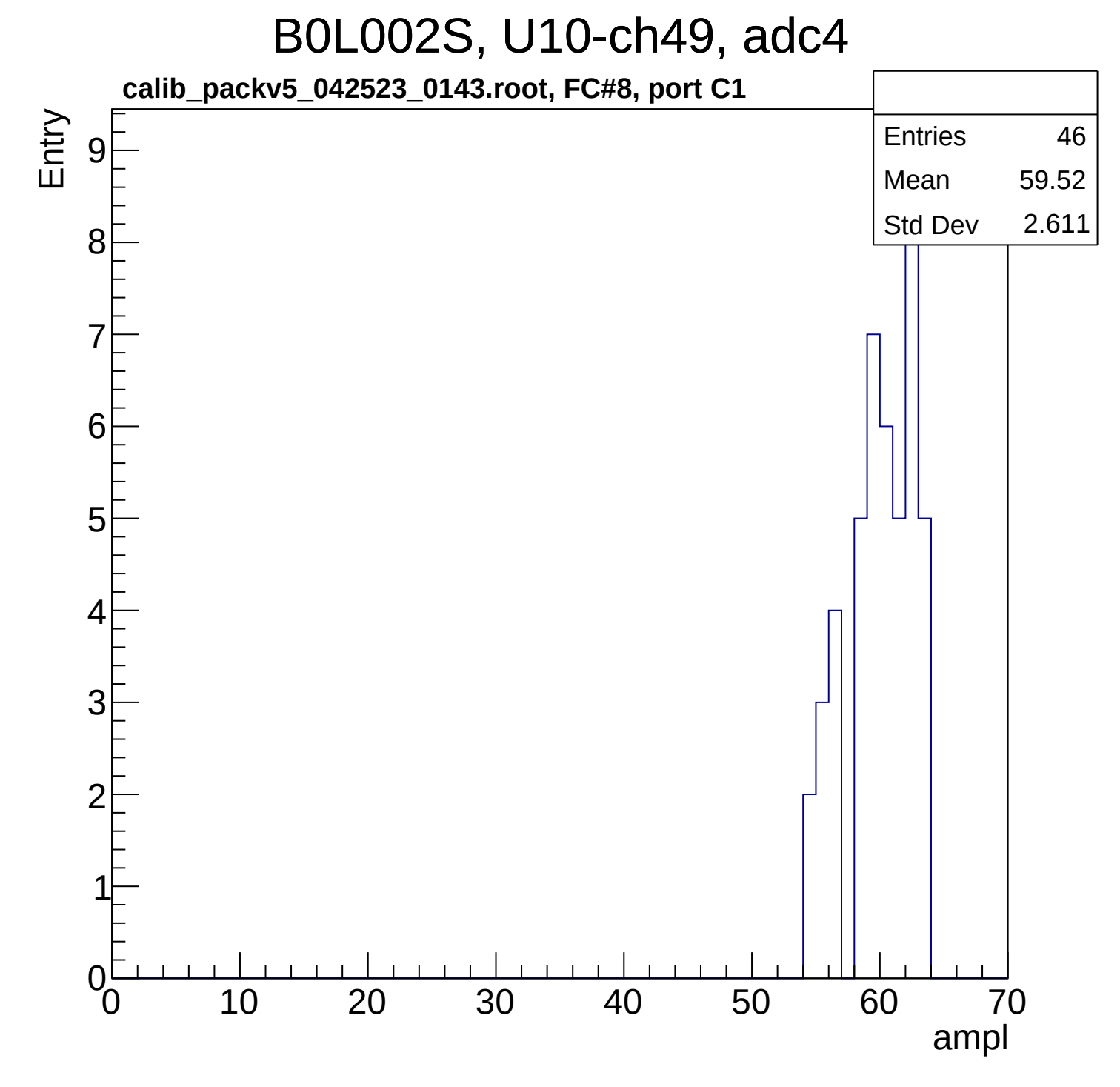

B0L002S, U10-ch48, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

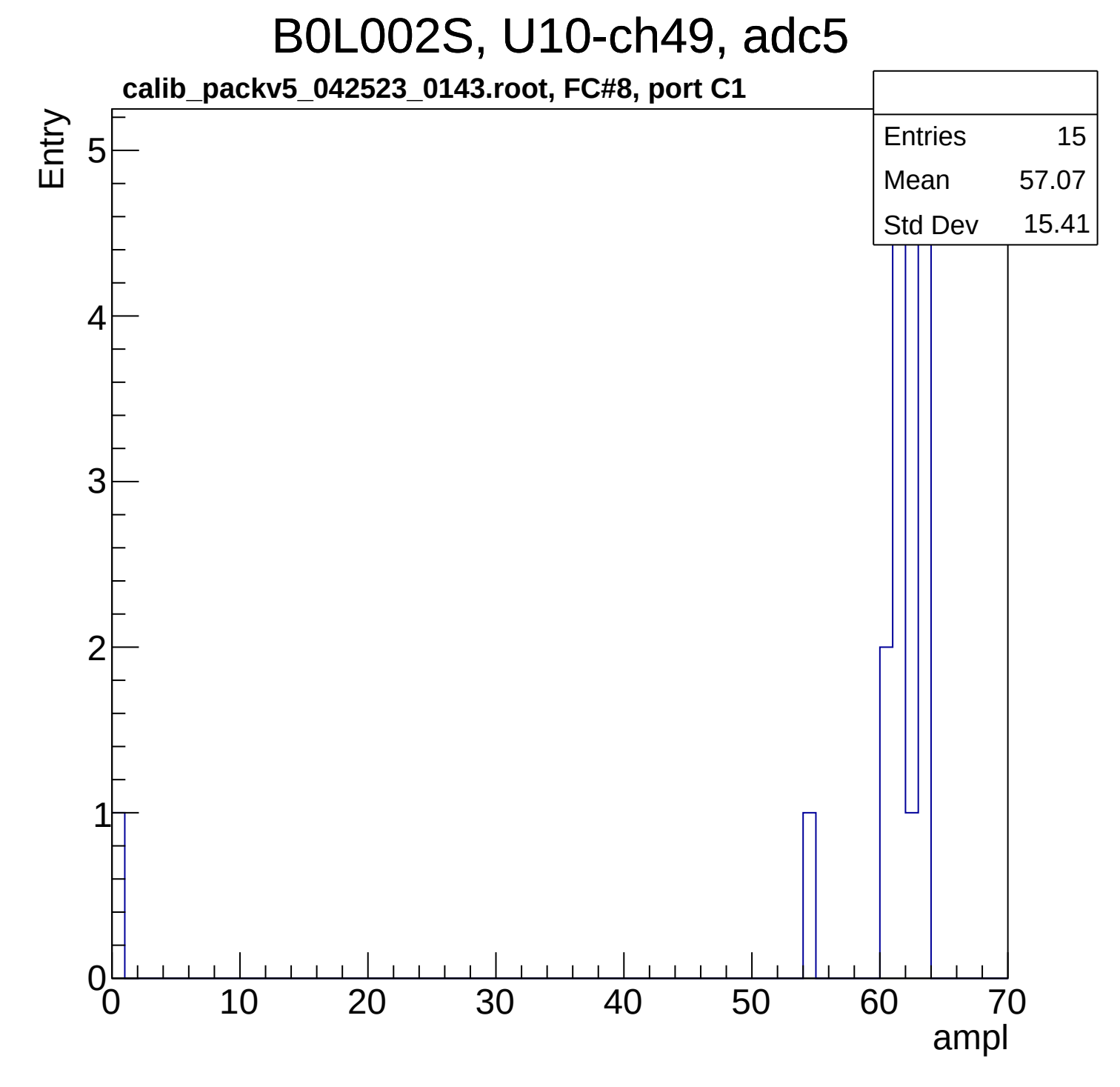

B0L002S, U10-ch49, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 32.52
Std Dev 3.835
Gaus mean : 33.4878
Gaus Width: 3.8737
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

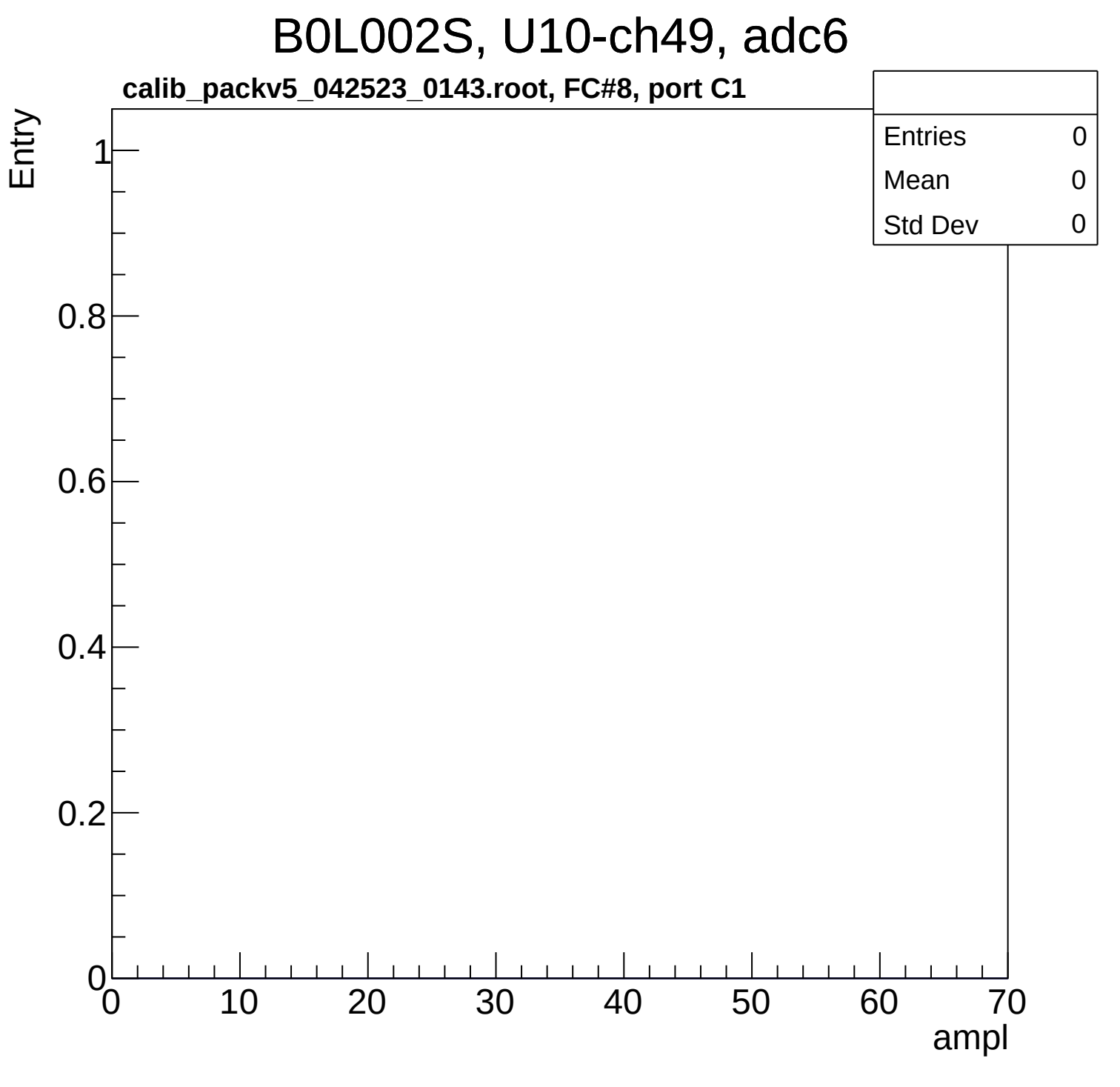

B0L002S, U10-ch49, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 40.96
Std Dev 3.517
Gaus mean : 41.1309
Gaus Width: 4.1756
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

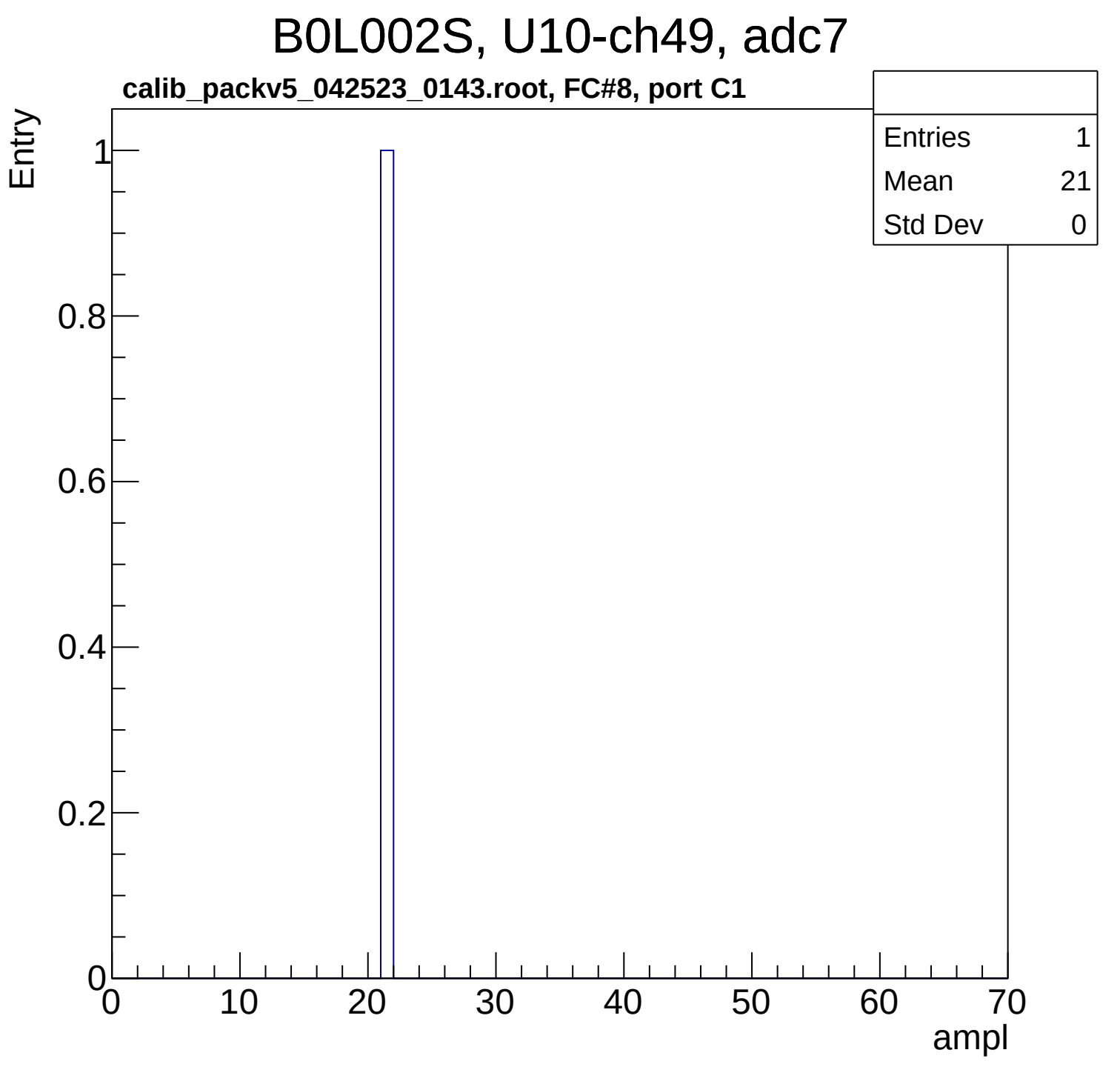

B0L002S, U10-ch49, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 46.6
Std Dev 2.837
Gaus mean : 47.2399
Gaus Width: 3.4165
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

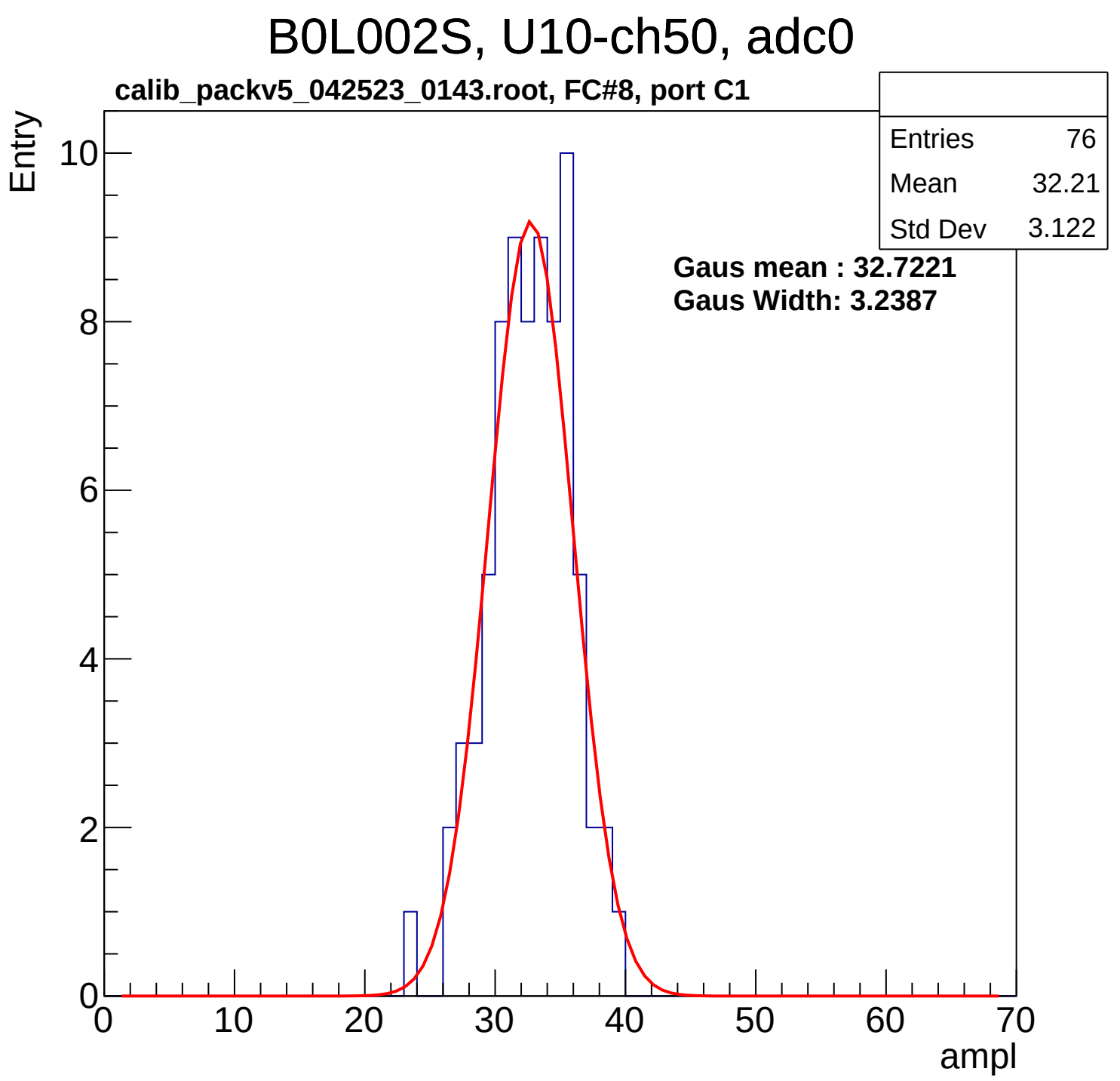

B0L002S, U10-ch49, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 53.71
Std Dev 3.254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

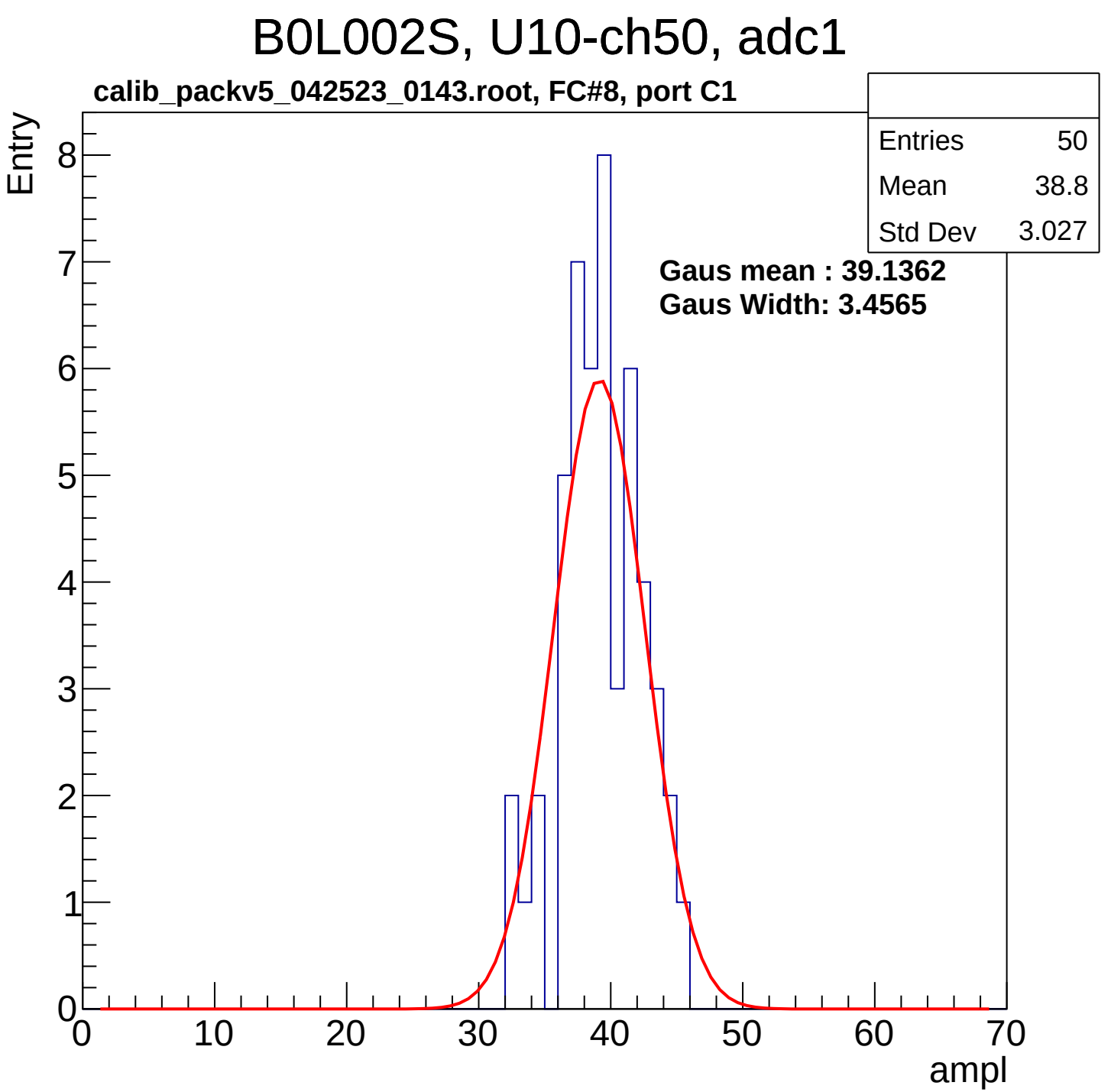

B0L002S, U10-ch49, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.52
Std Dev 2.611
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

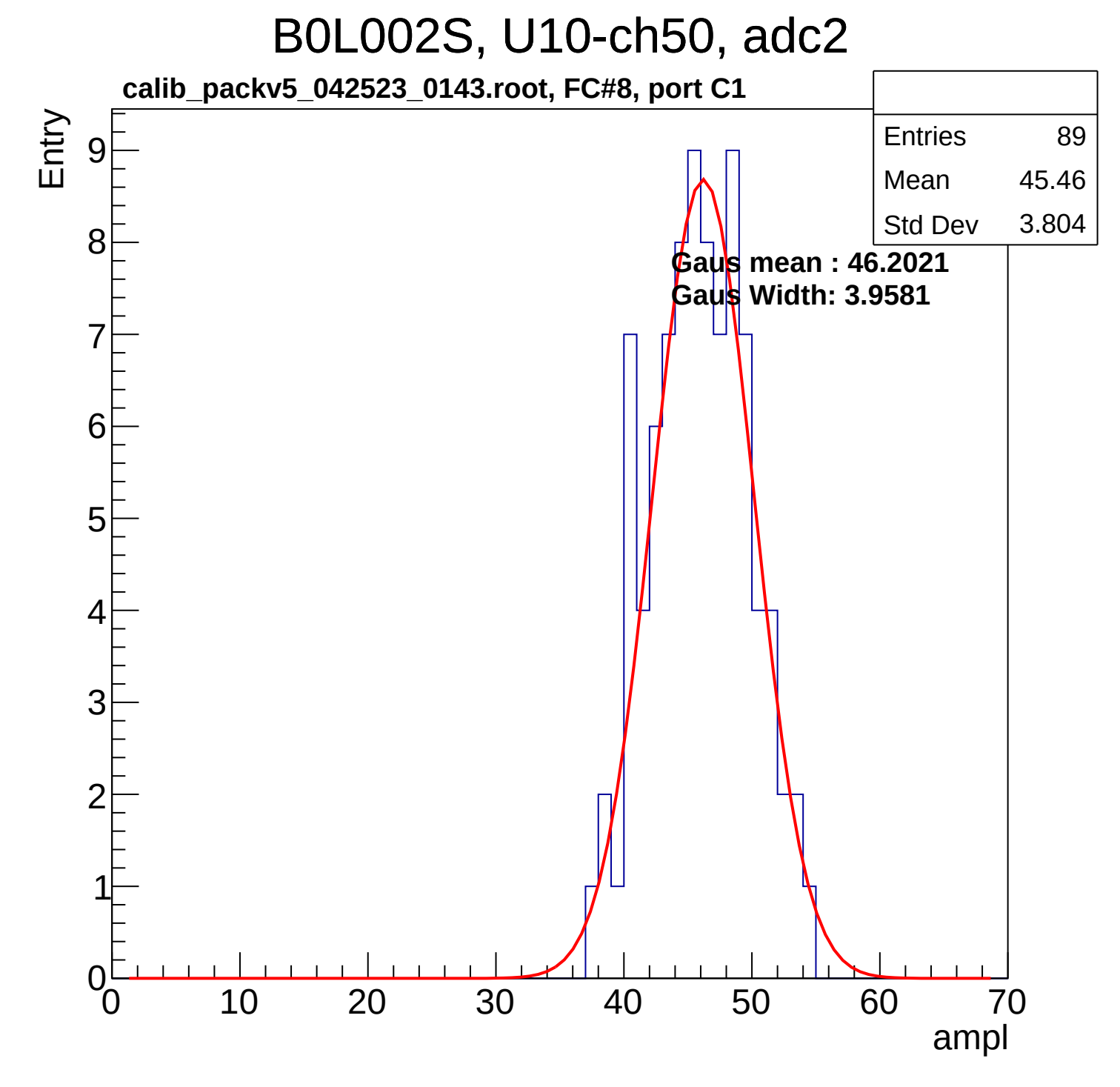

B0L002S, U10-ch49, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 57.07
Std Dev 15.41
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

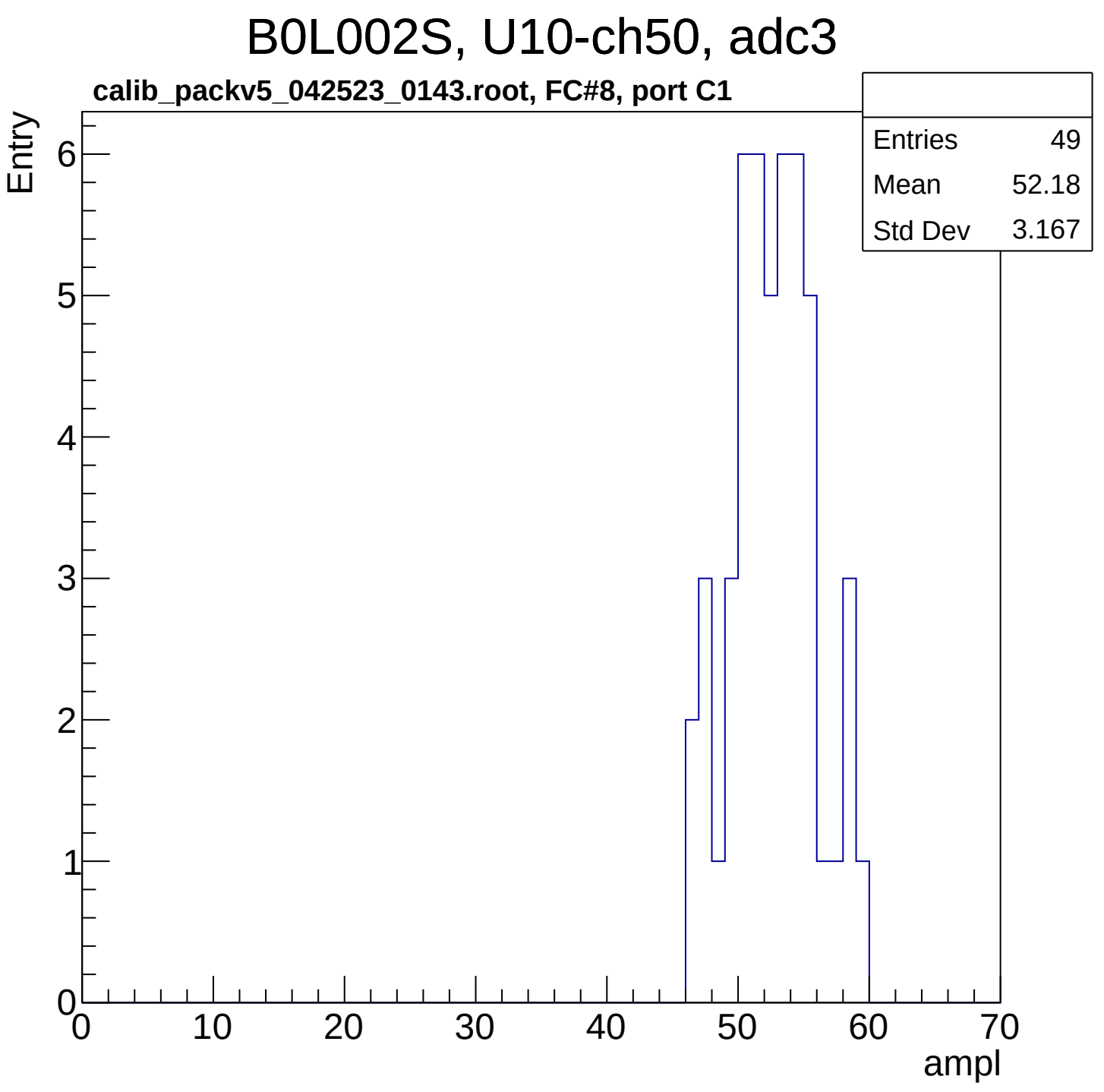

B0L002S, U10-ch49, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

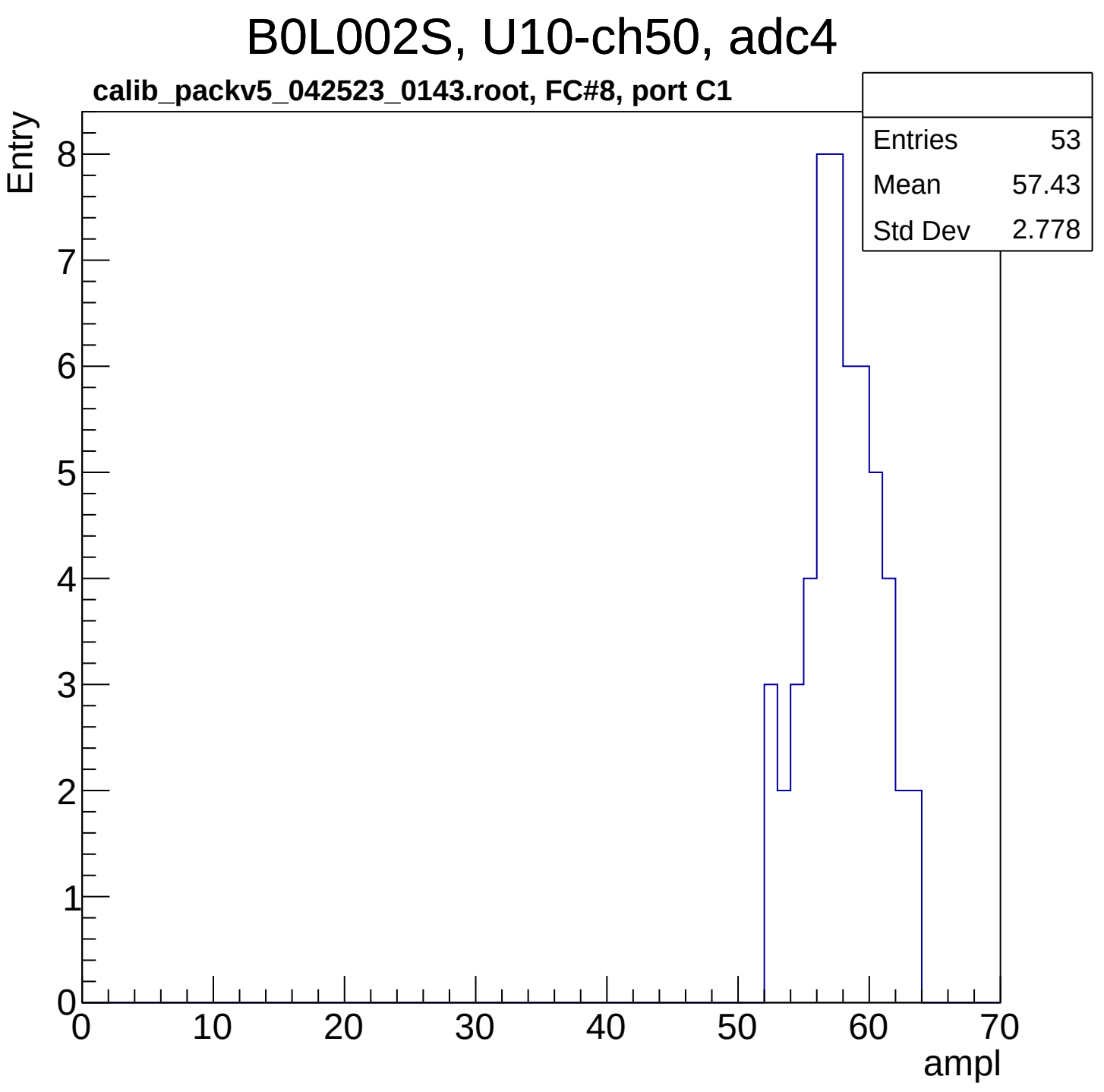

B0L002S, U10-ch49, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

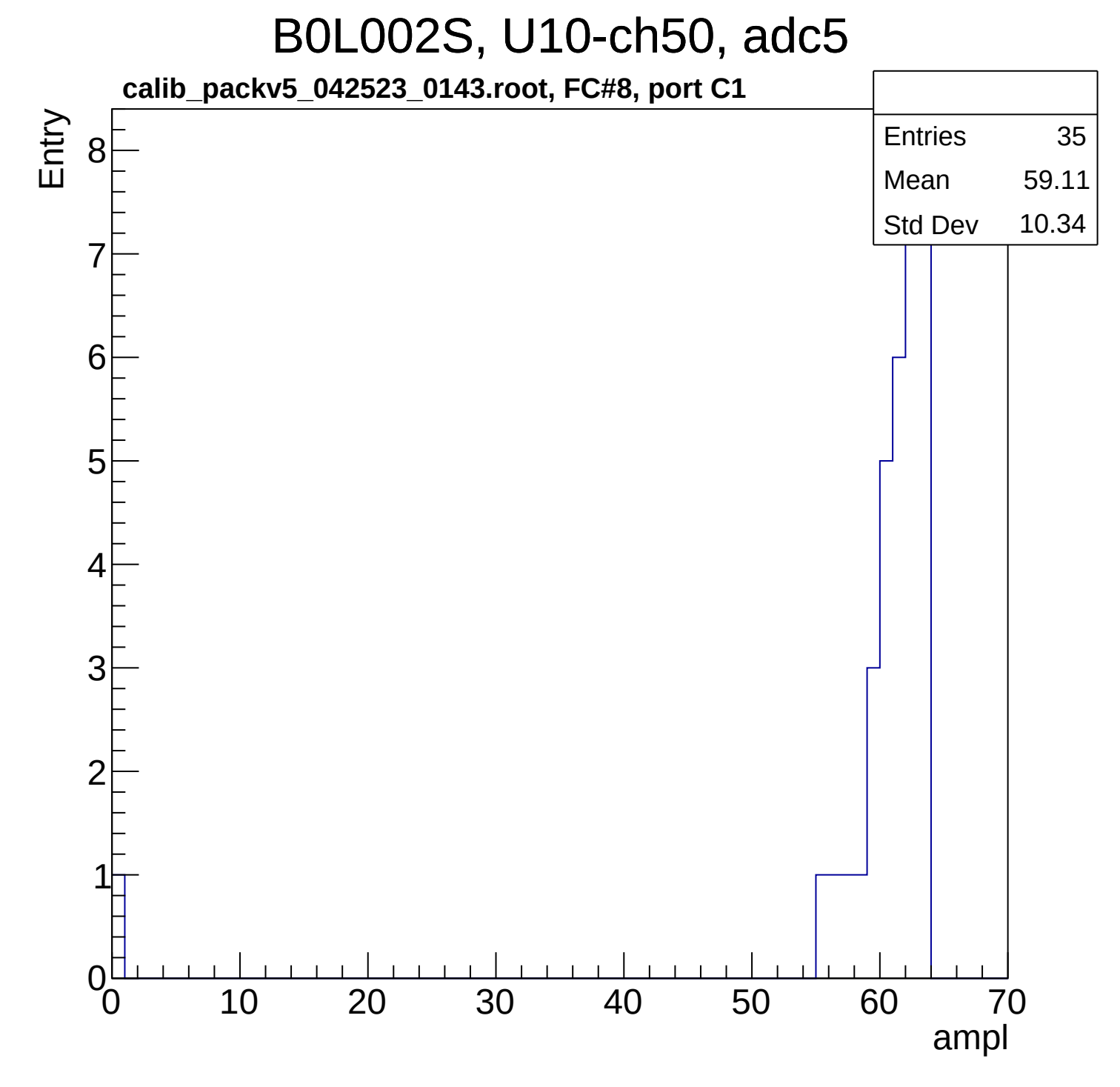

B0L002S, U10-ch50, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 32.21
Std Dev 3.122
Gaus mean : 32.7221
Gaus Width: 3.2387
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

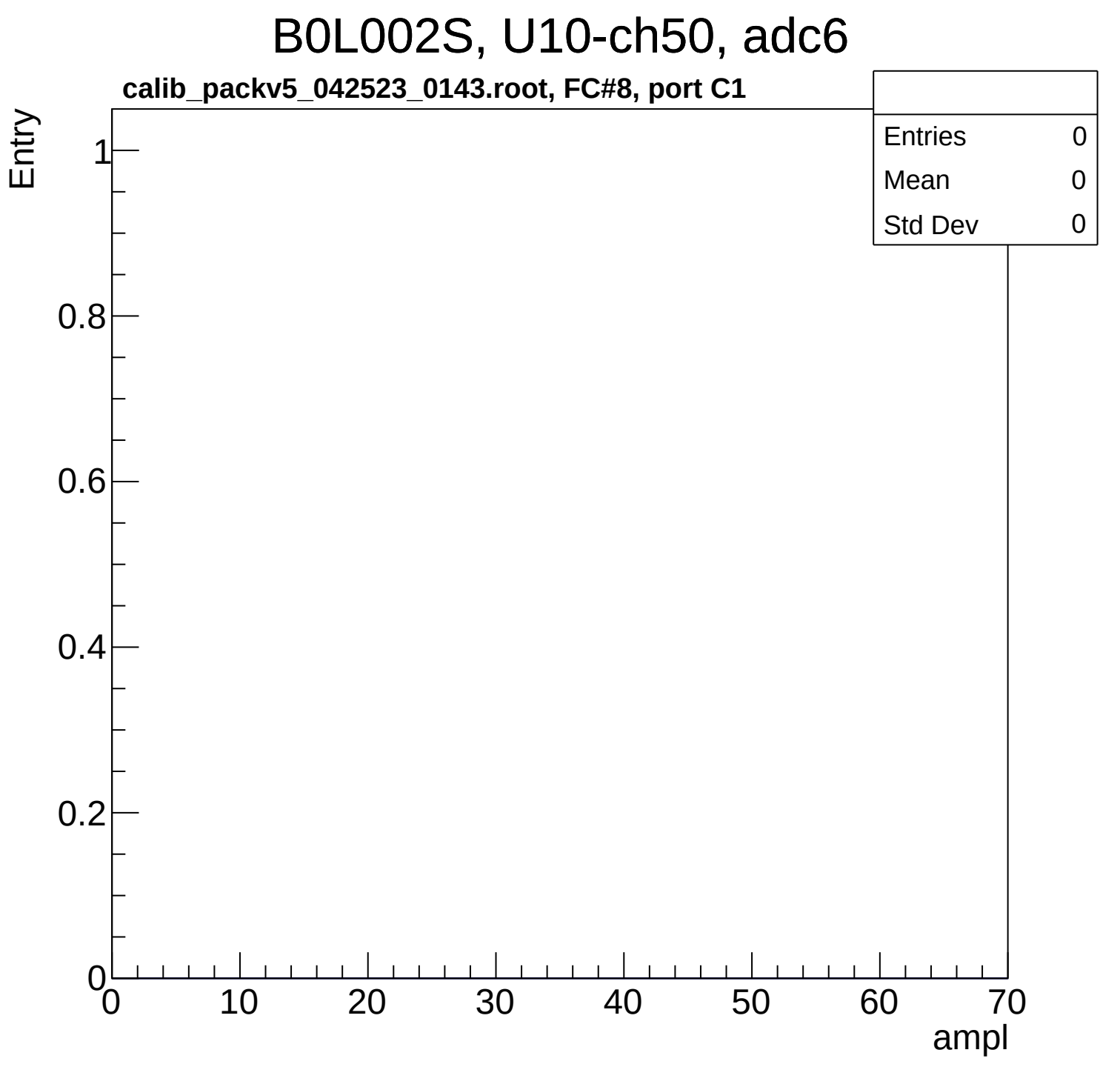

B0L002S, U10-ch50, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 38.8
Std Dev 3.027
Gaus mean : 39.1362
Gaus Width: 3.4565
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

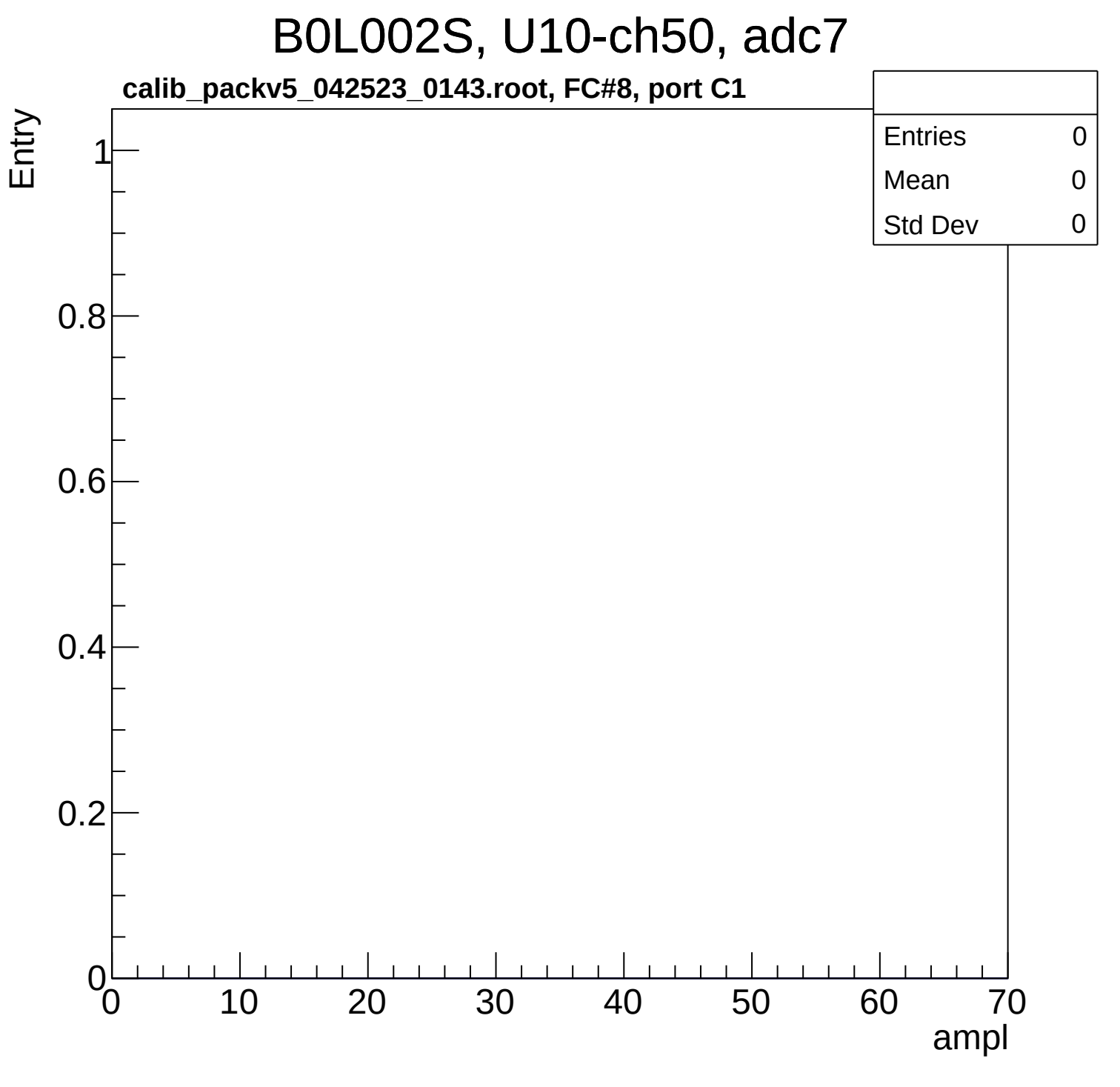

B0L002S, U10-ch50, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 89
Mean 45.46
Std Dev 3.804
Gaus mean : 46.2021
Gaus Width: 3.9581
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

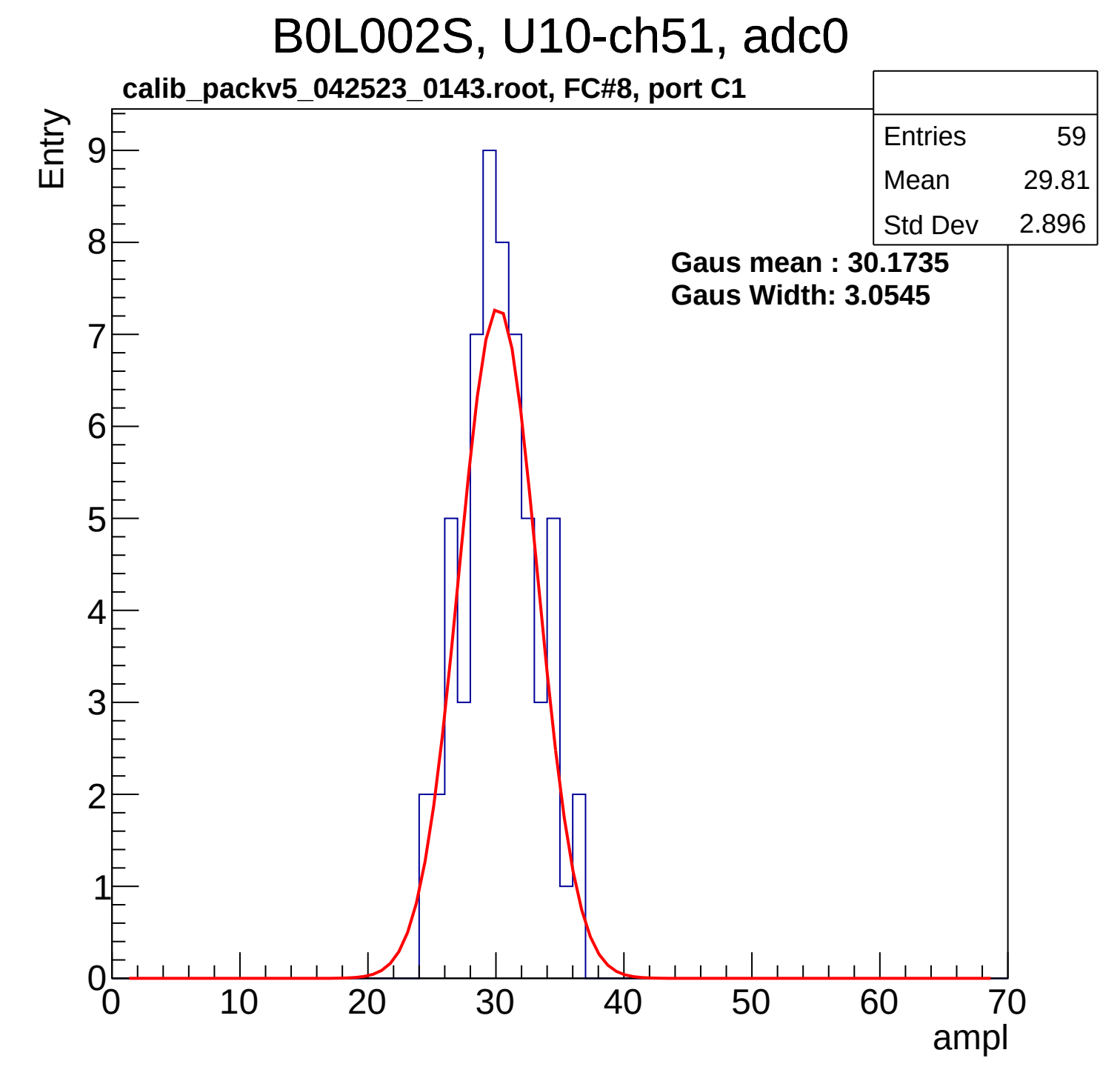

B0L002S, U10-ch50, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 49
Mean 52.18
Std Dev 3.167
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

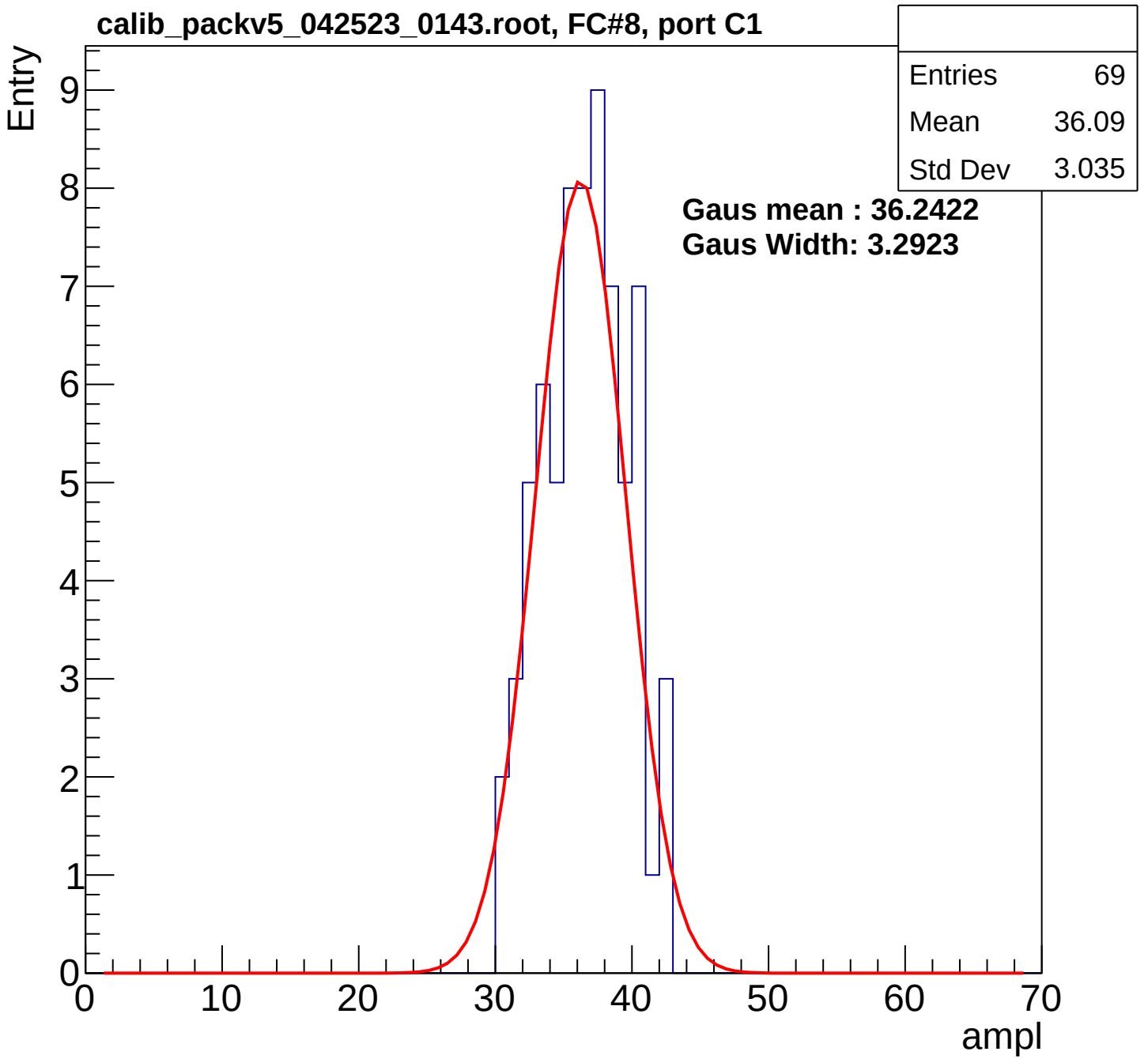

B0L002S, U10-ch50, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 57.43
Std Dev 2.778
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

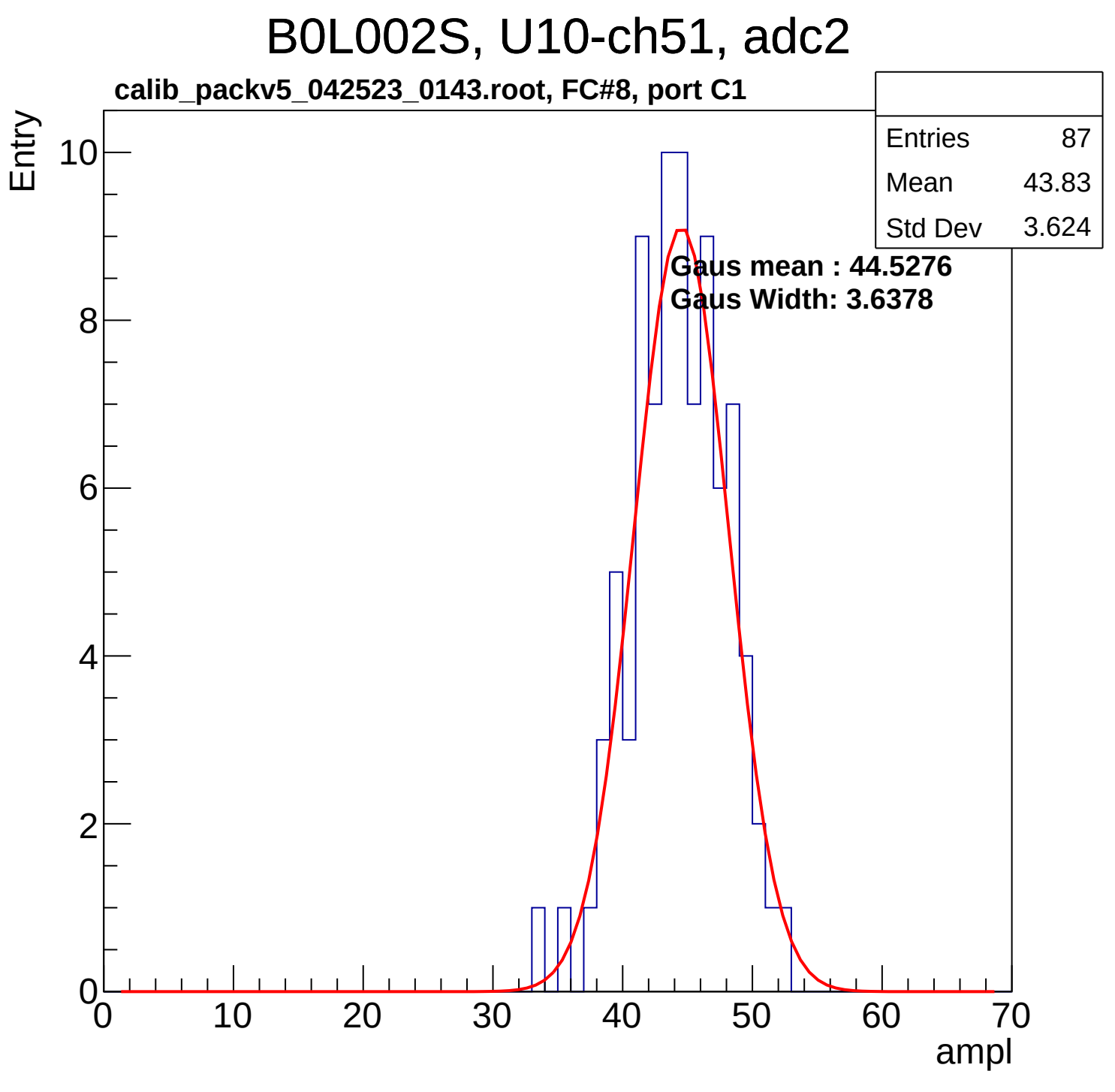

B0L002S, U10-ch50, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.11
Std Dev 10.34
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

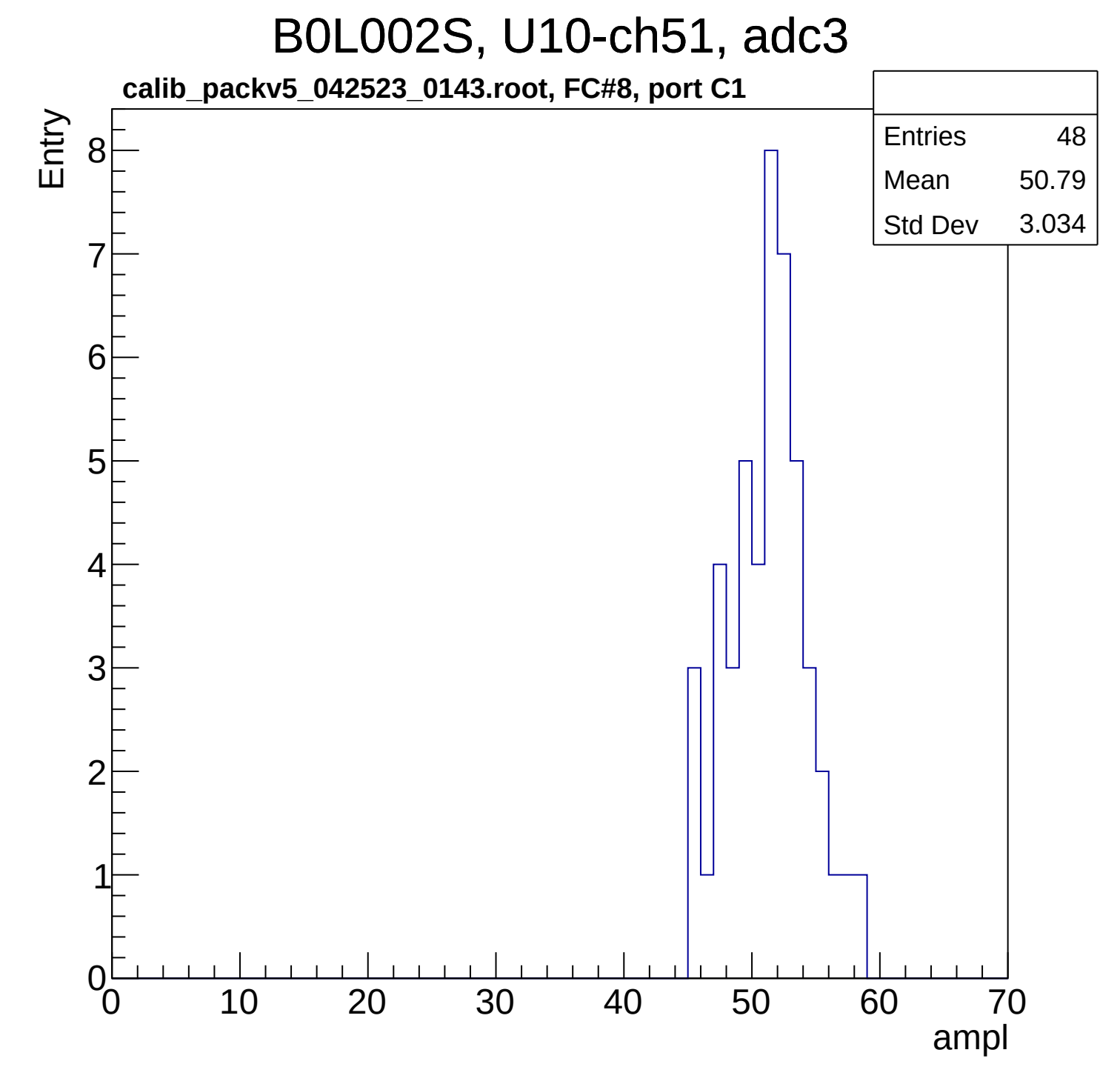

B0L002S, U10-ch50, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

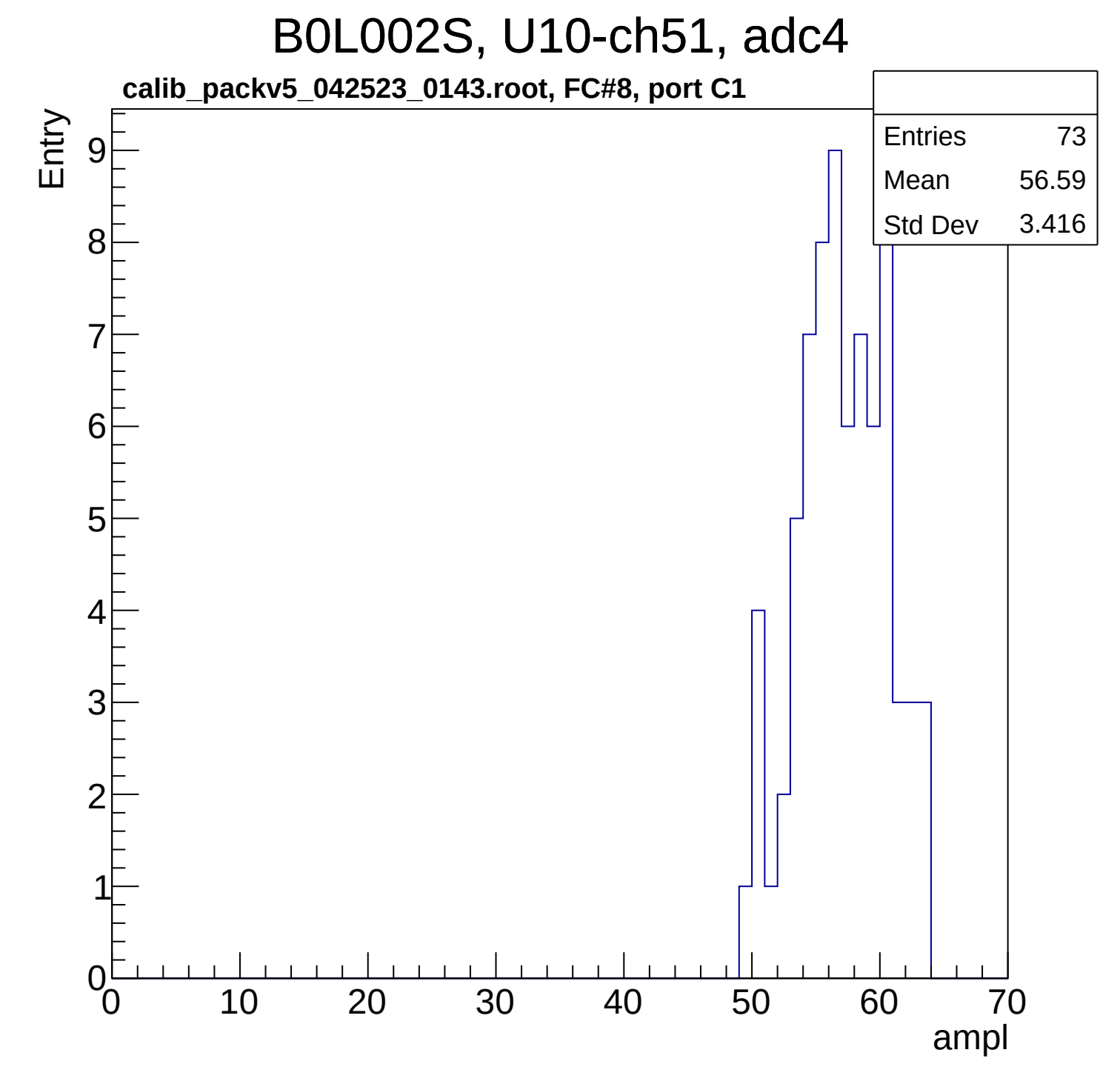

B0L002S, U10-ch50, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

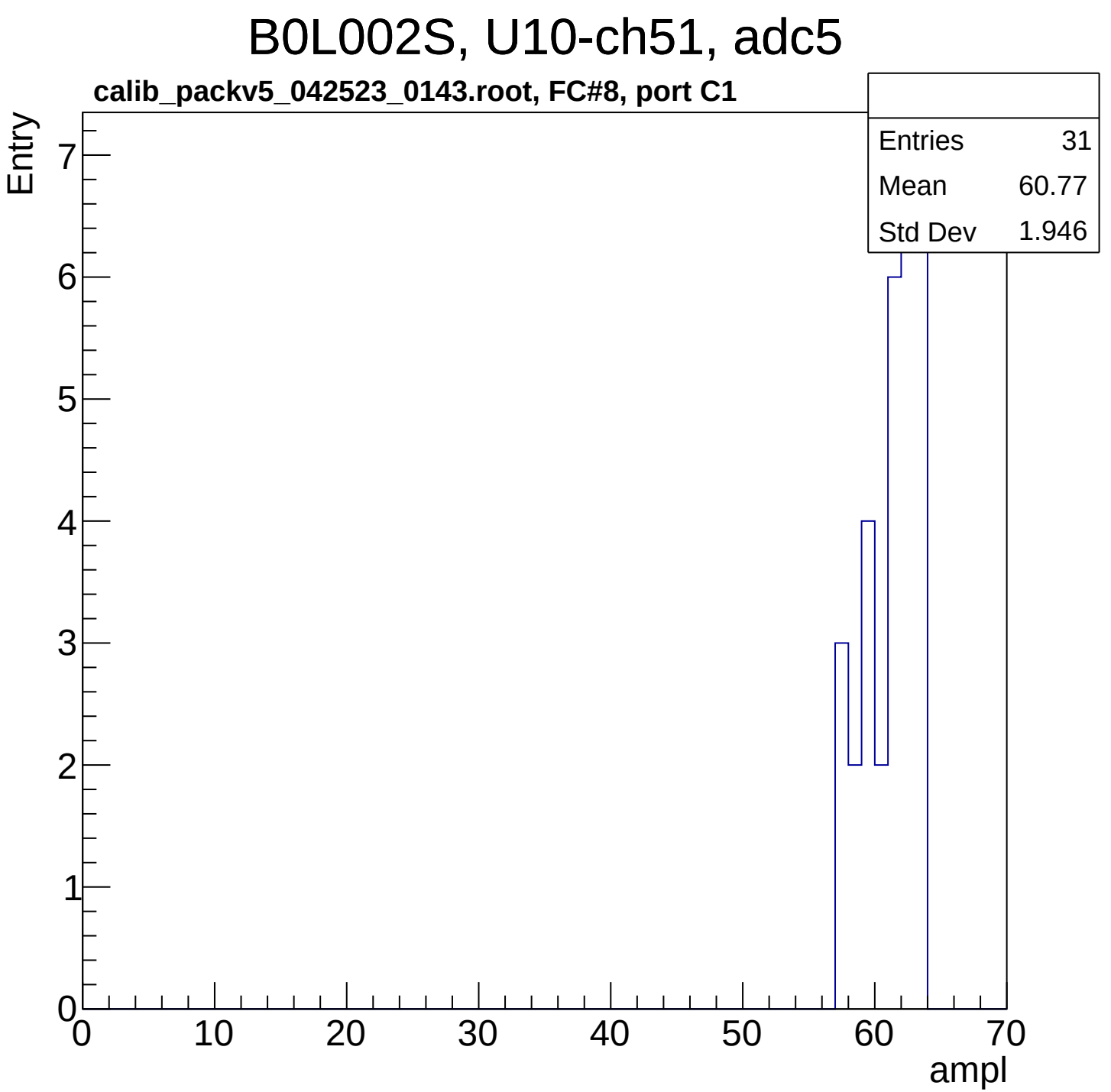

B0L002S, U10-ch51, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 29.81
Std Dev 2.896
Gaus mean : 30.1735
Gaus Width: 3.0545
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

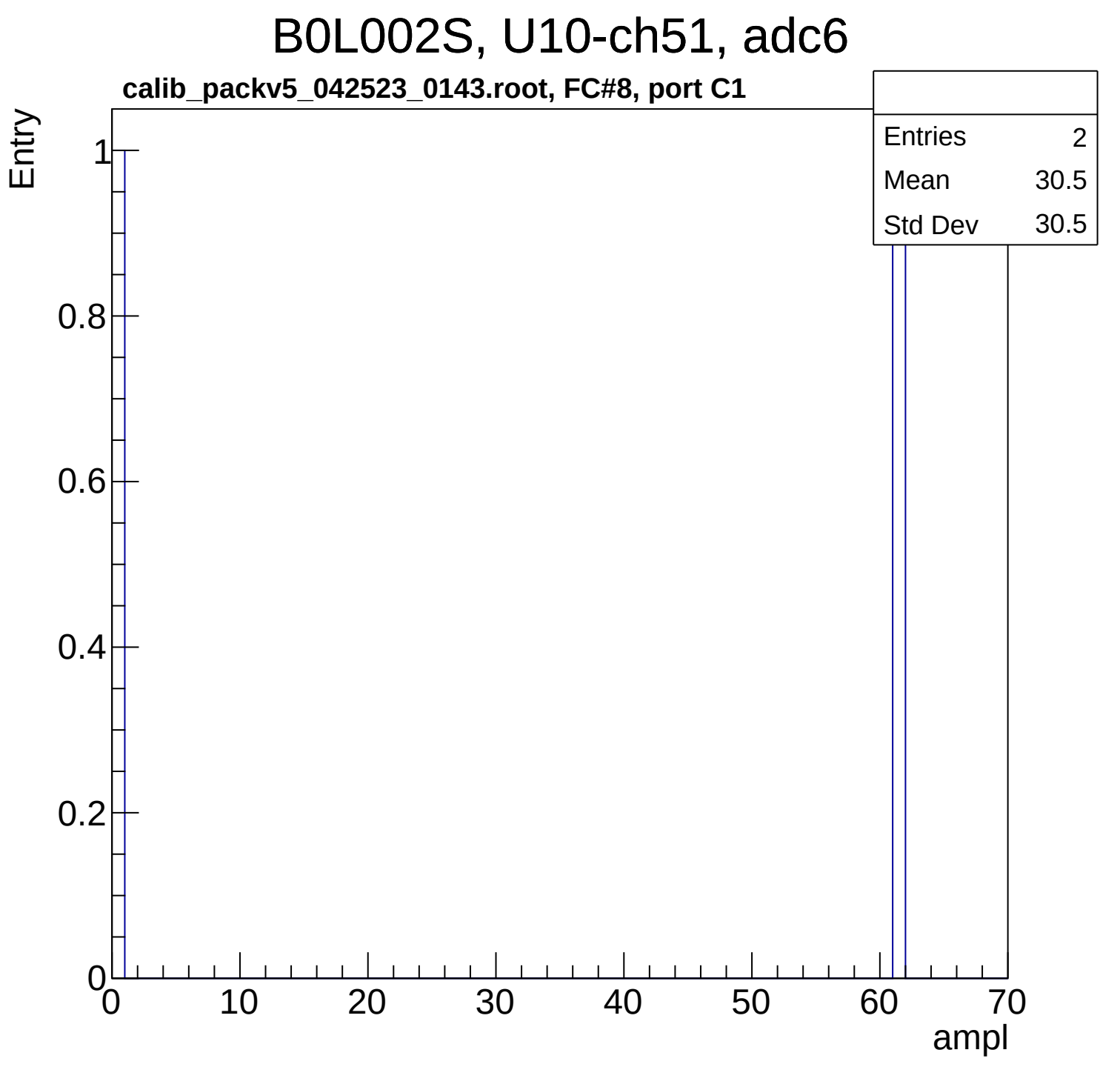

B0L002S, U10-ch51, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.09
Std Dev 3.035
Gaus mean : 36.2422
Gaus Width: 3.2923
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

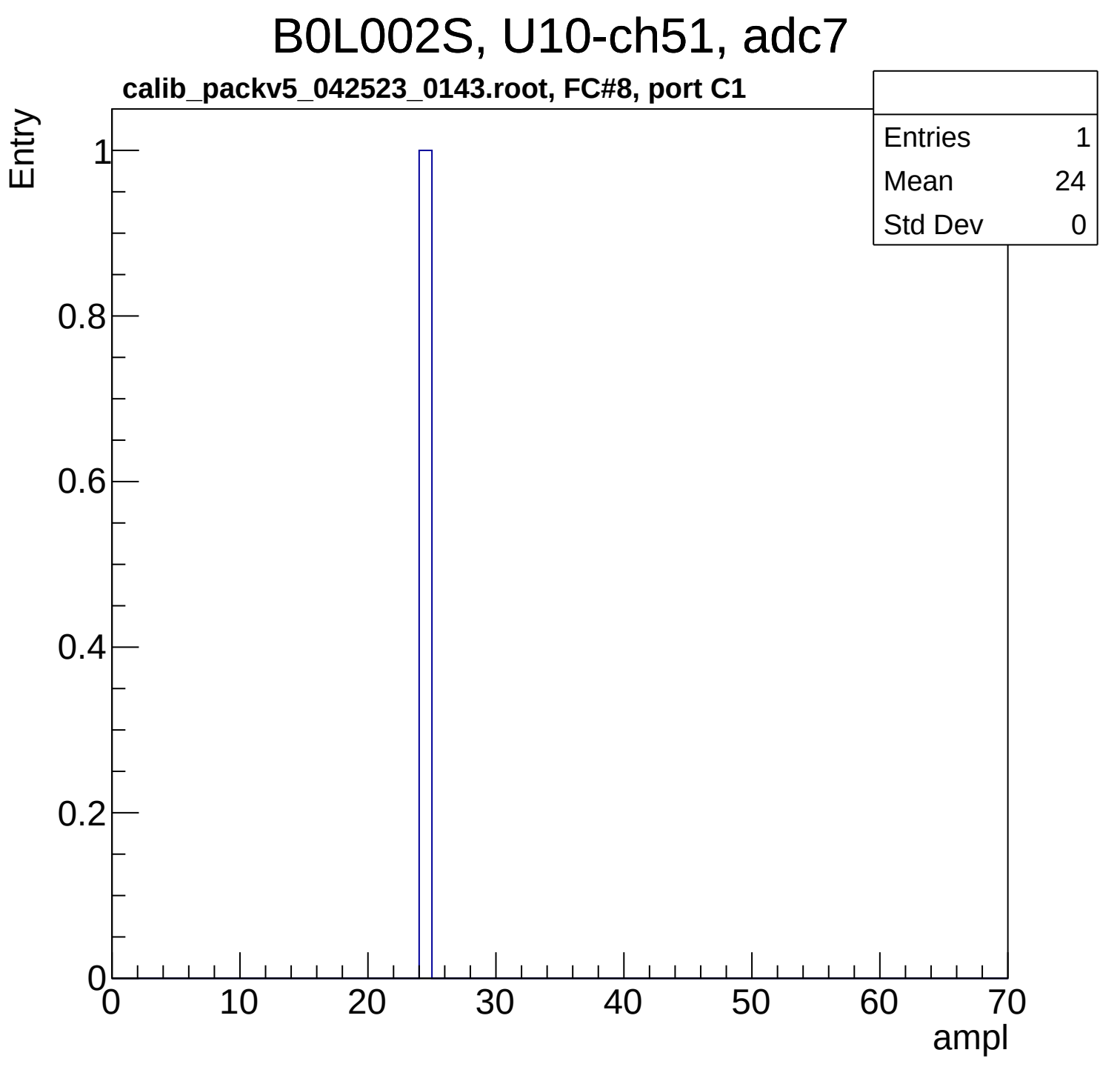

B0L002S, U10-ch51, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 43.83
Std Dev 3.624
Gaus mean : 44.5276
Gaus Width: 3.6378
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

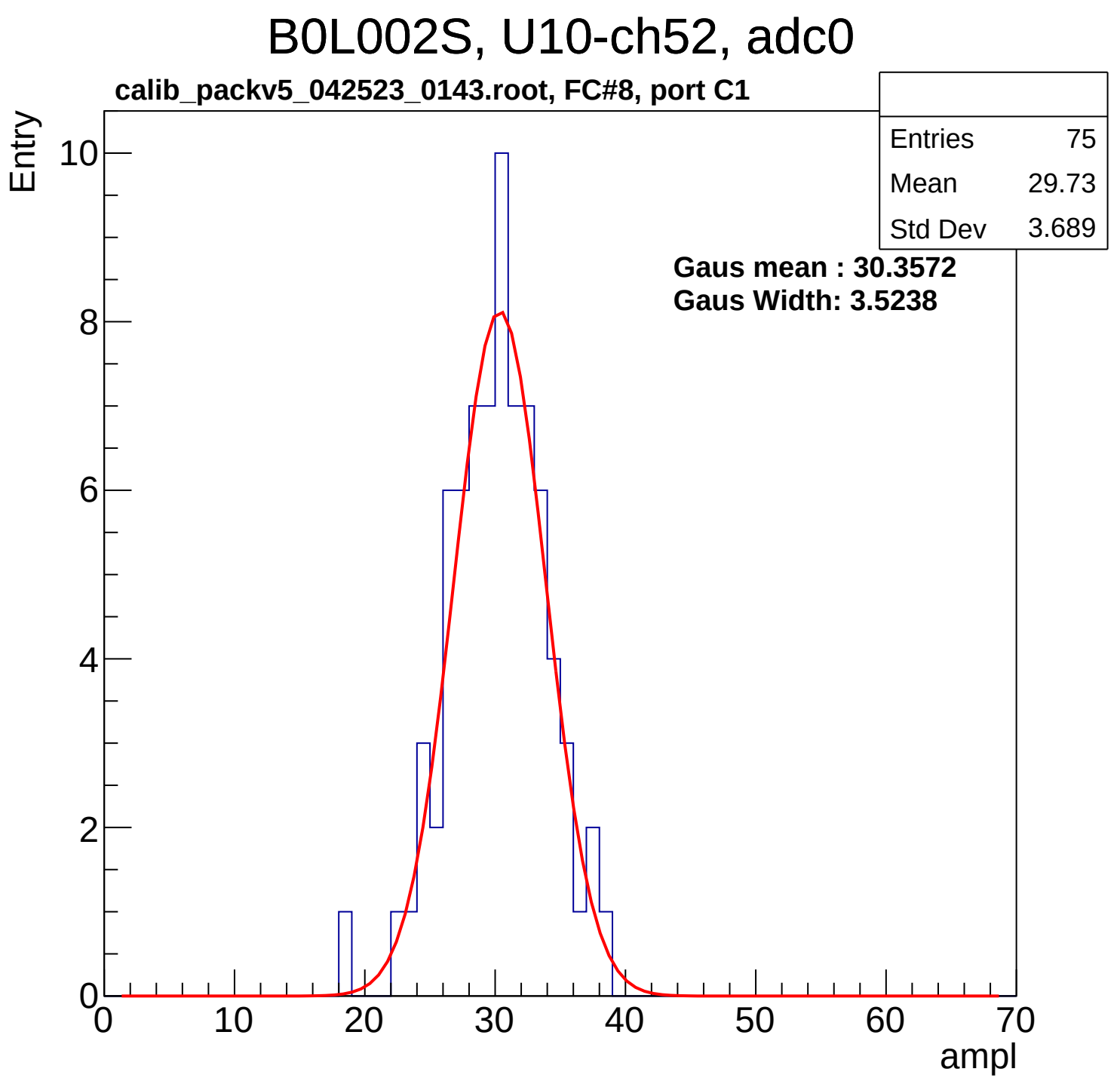

B0L002S, U10-ch51, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 50.79
Std Dev 3.034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

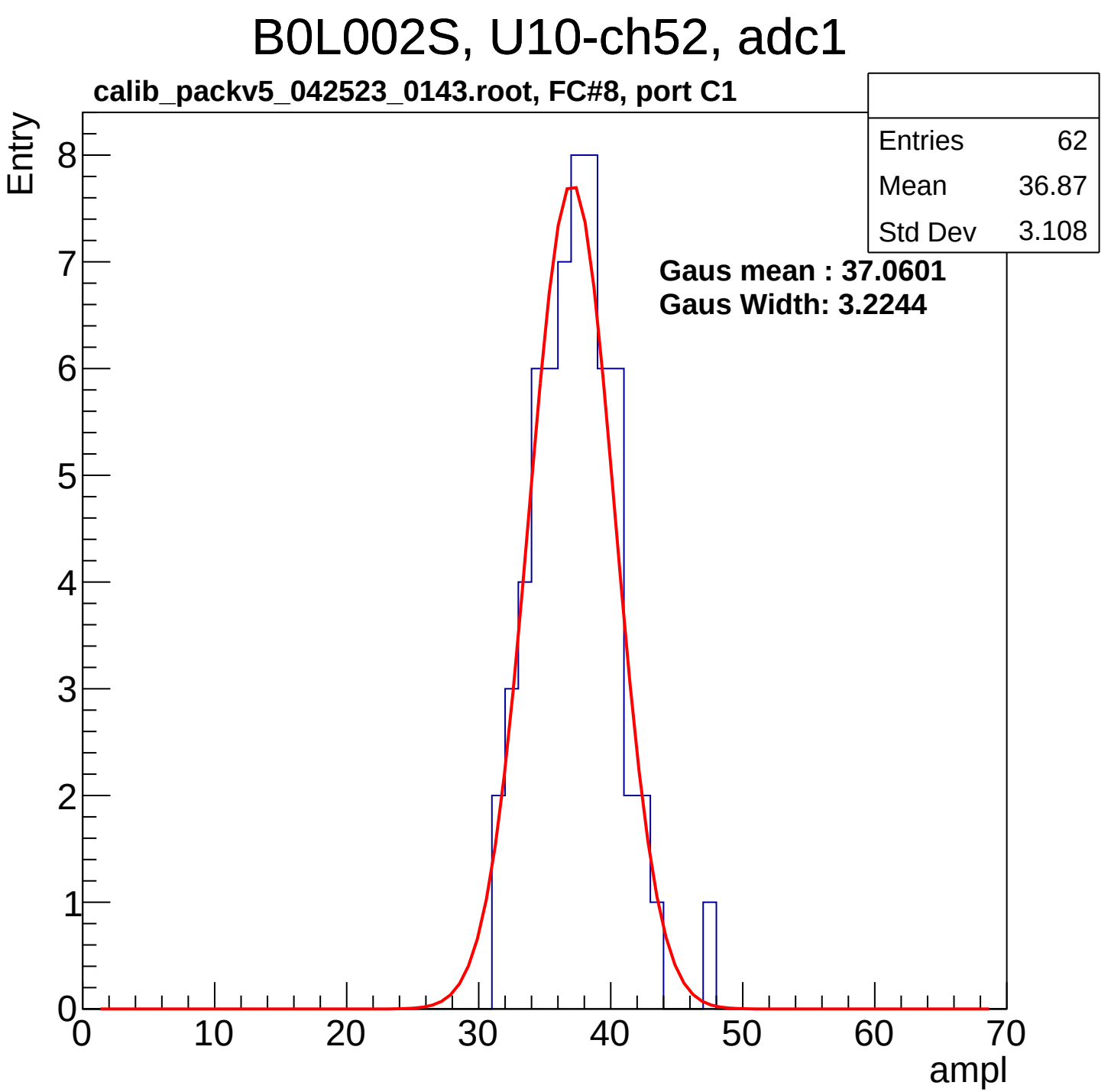

B0L002S, U10-ch51, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 56.59
Std Dev 3.416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

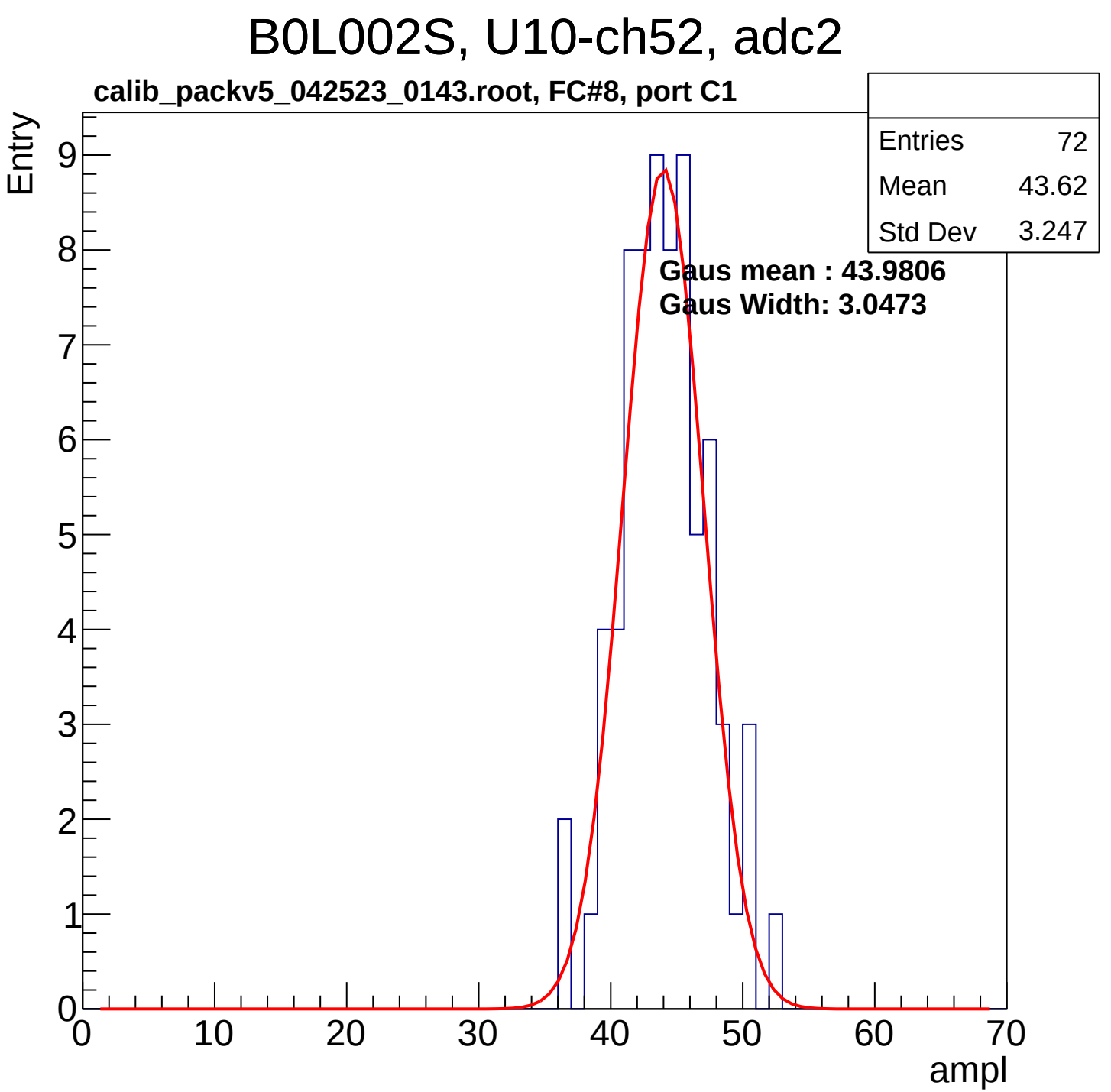

B0L002S, U10-ch51, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 60.77
Std Dev 1.946
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

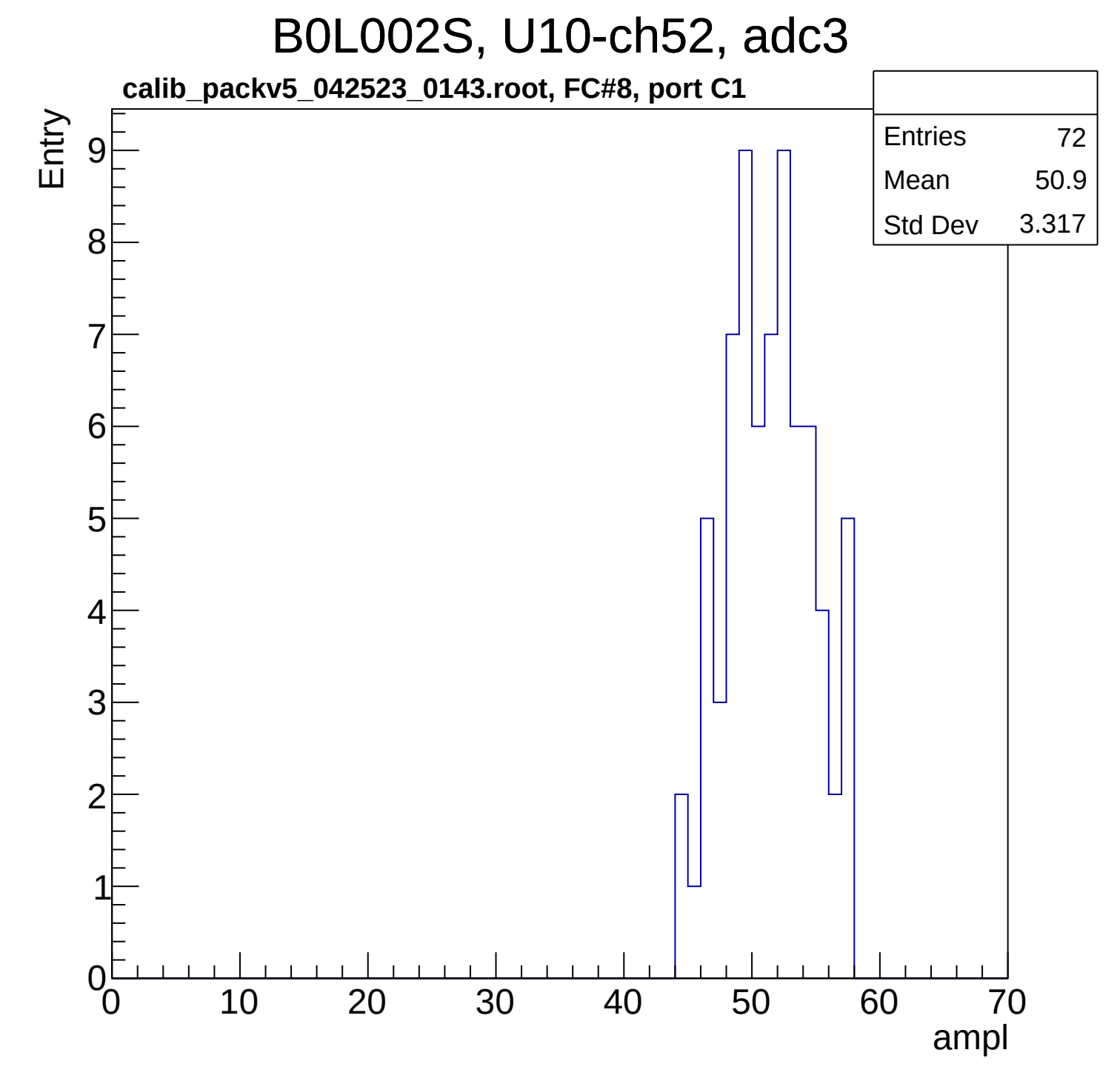

B0L002S, U10-ch51, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

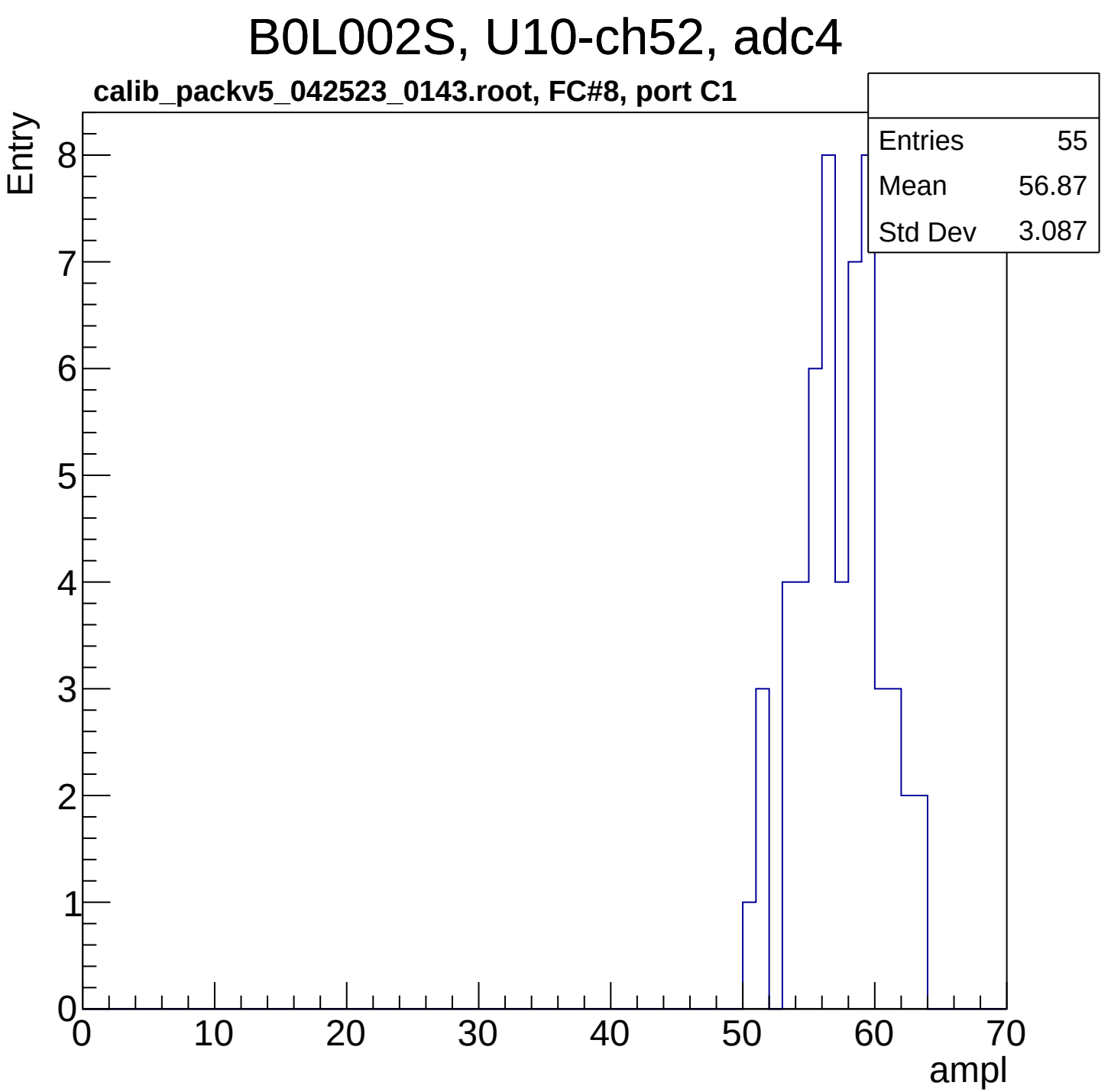

B0L002S, U10-ch51, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

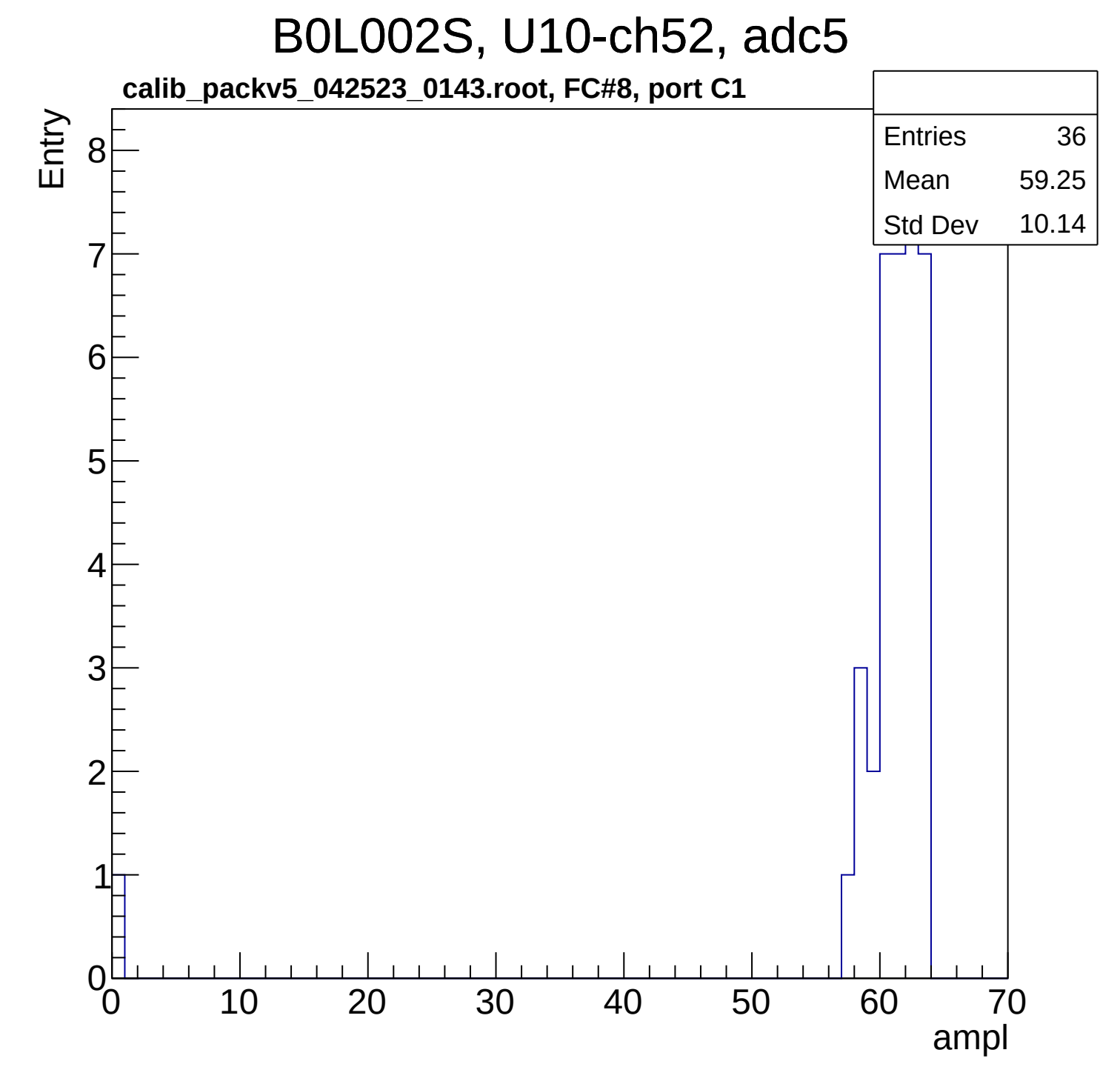

B0L002S, U10-ch52, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.73
Std Dev 3.689
Gaus mean : 30.3572
Gaus Width: 3.5238
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

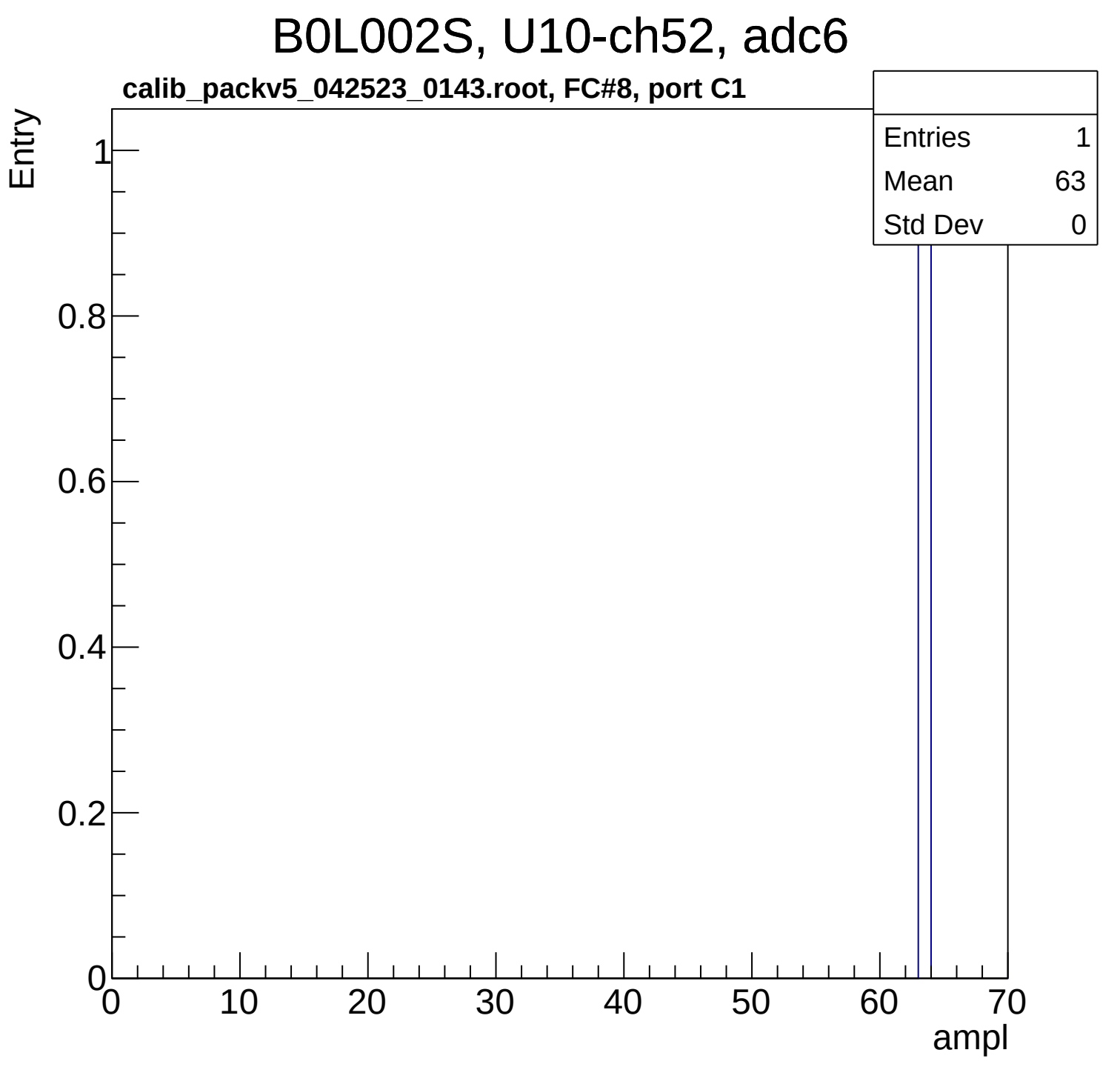

B0L002S, U10-ch52, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 36.87
Std Dev 3.108
Gaus mean : 37.0601
Gaus Width: 3.2244
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

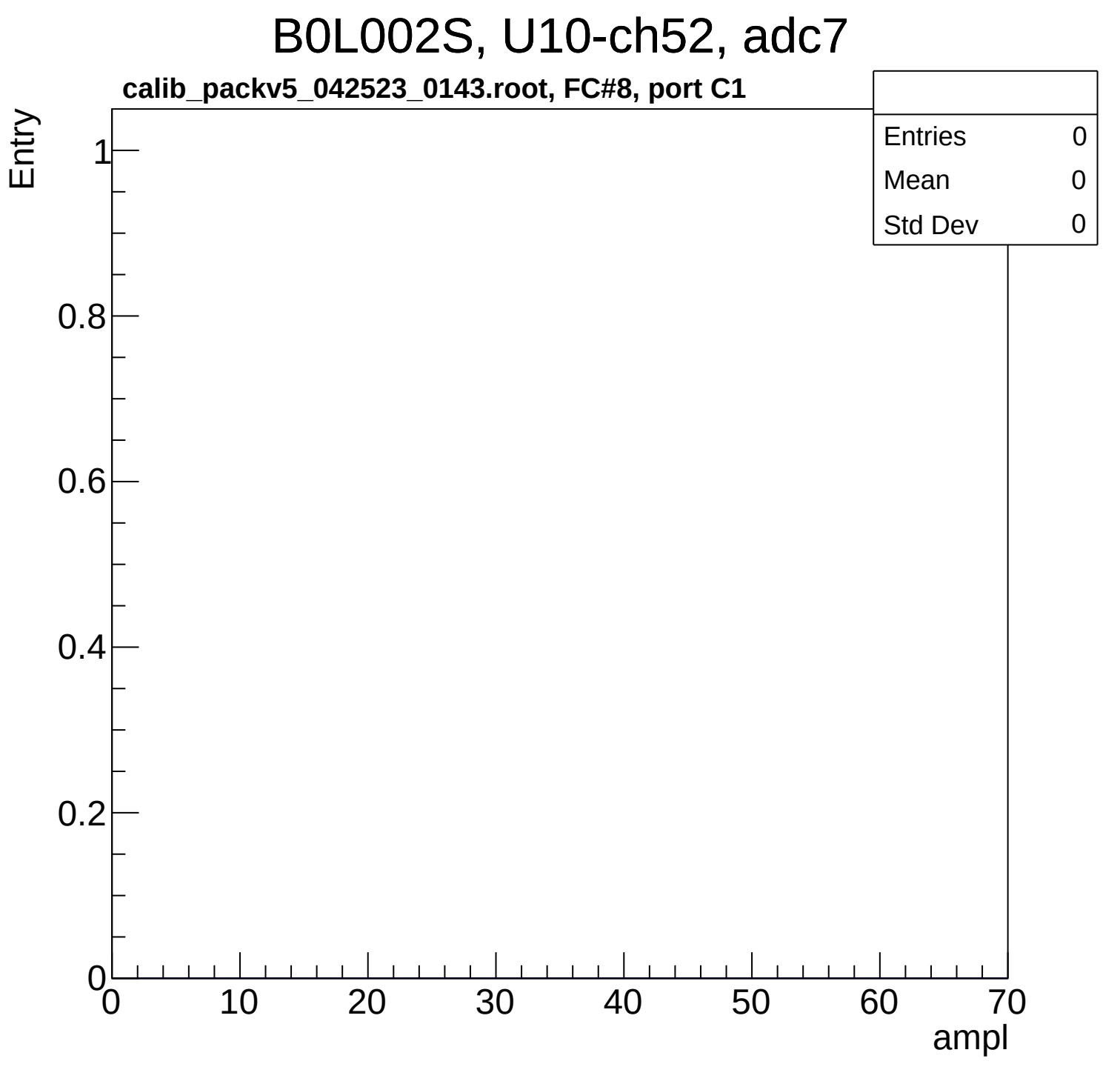

B0L002S, U10-ch52, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.62
Std Dev 3.247
Gaus mean : 43.9806
Gaus Width: 3.0473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

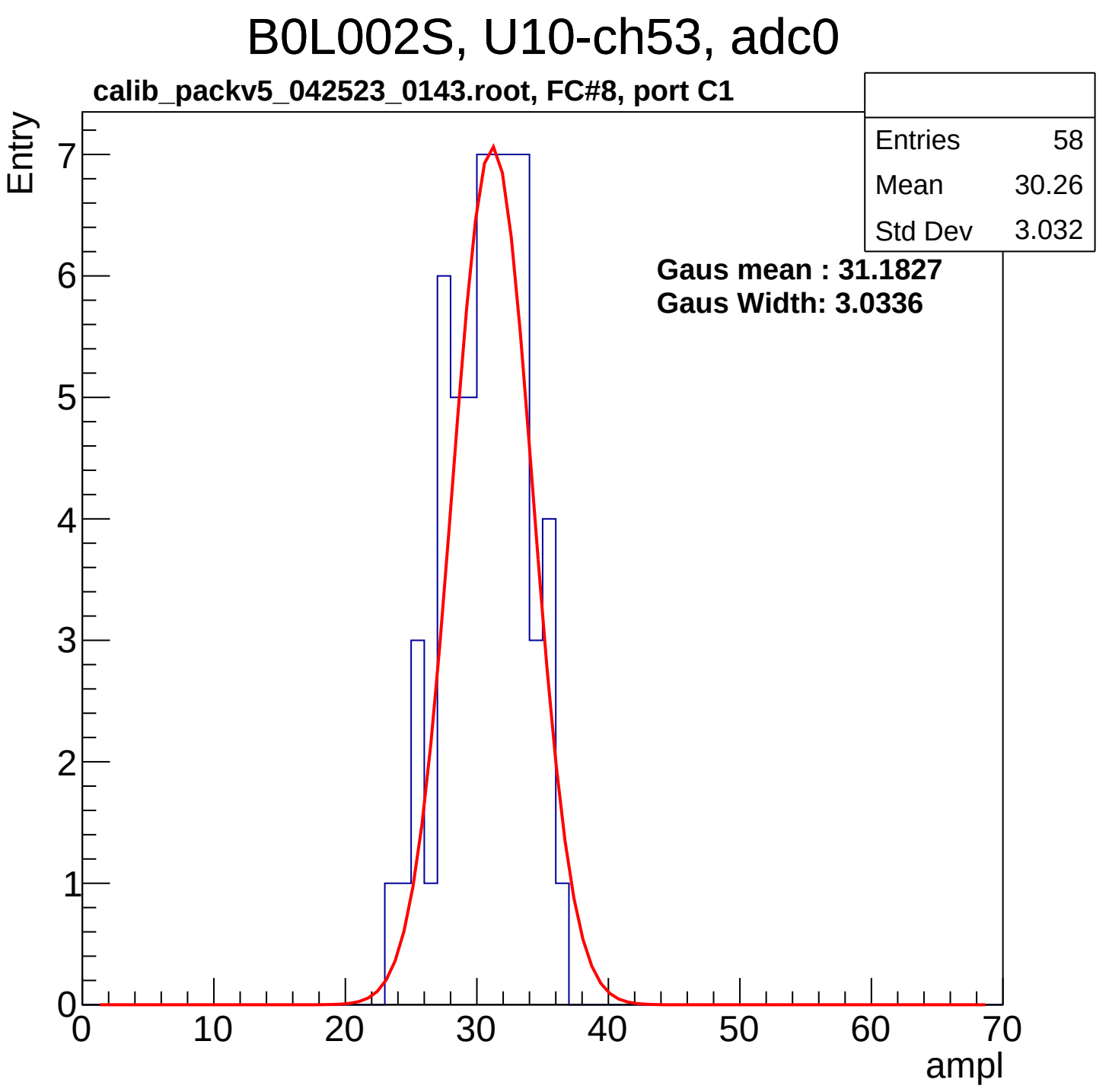

B0L002S, U10-ch52, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 50.9
Std Dev 3.317
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

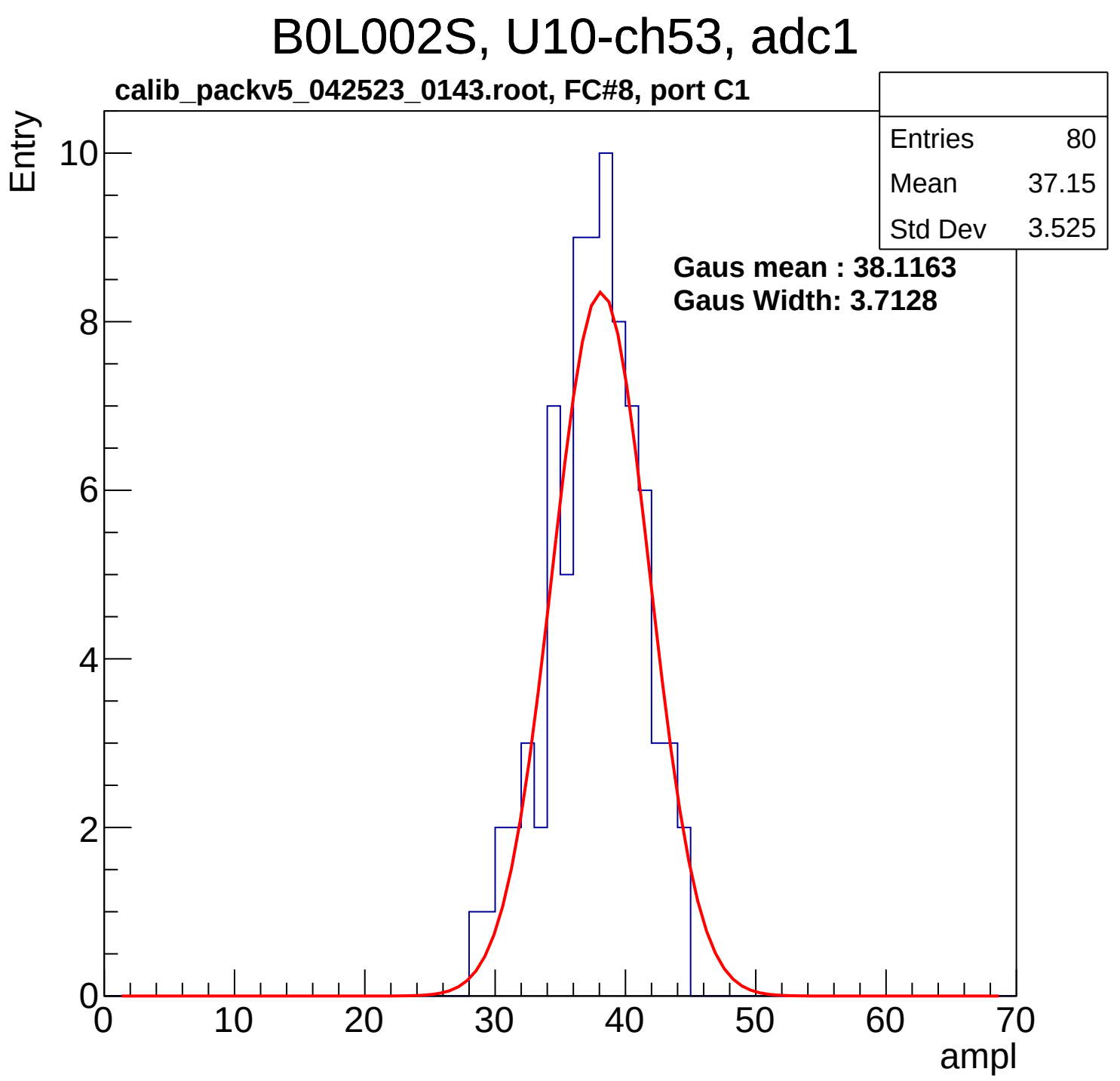

B0L002S, U10-ch52, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 56.87
Std Dev 3.087
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

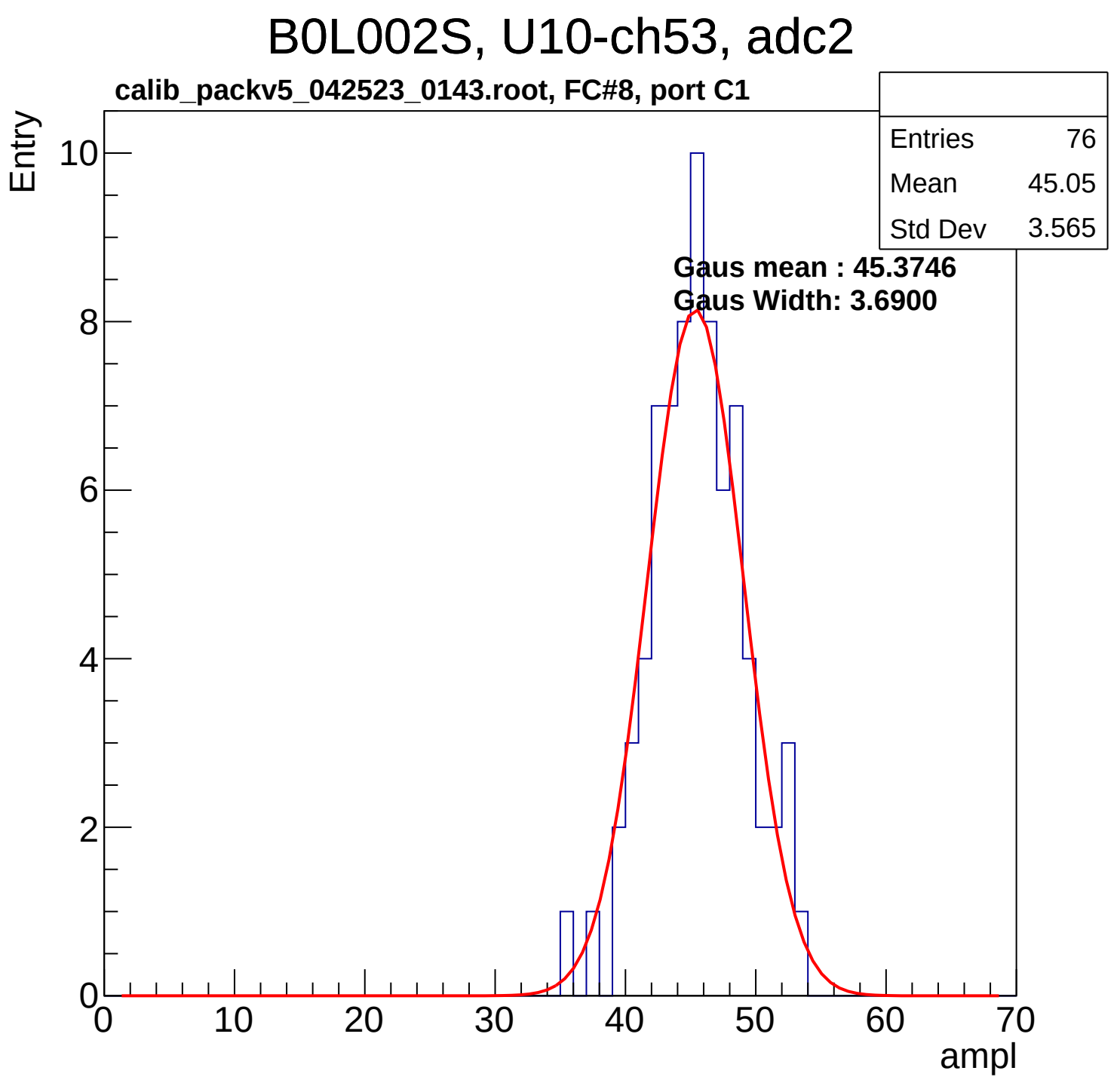

B0L002S, U10-ch52, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.25
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

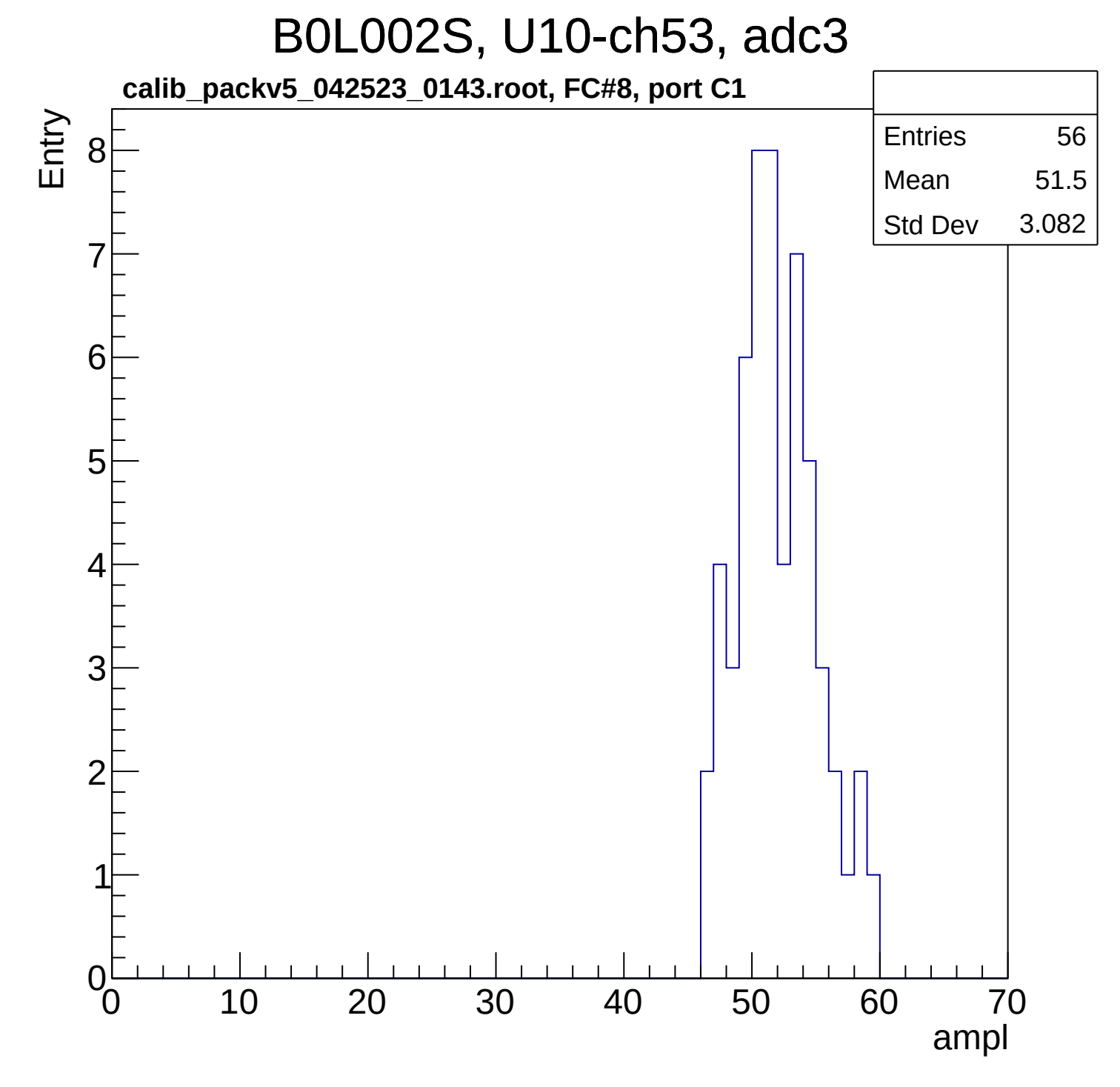

B0L002S, U10-ch52, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

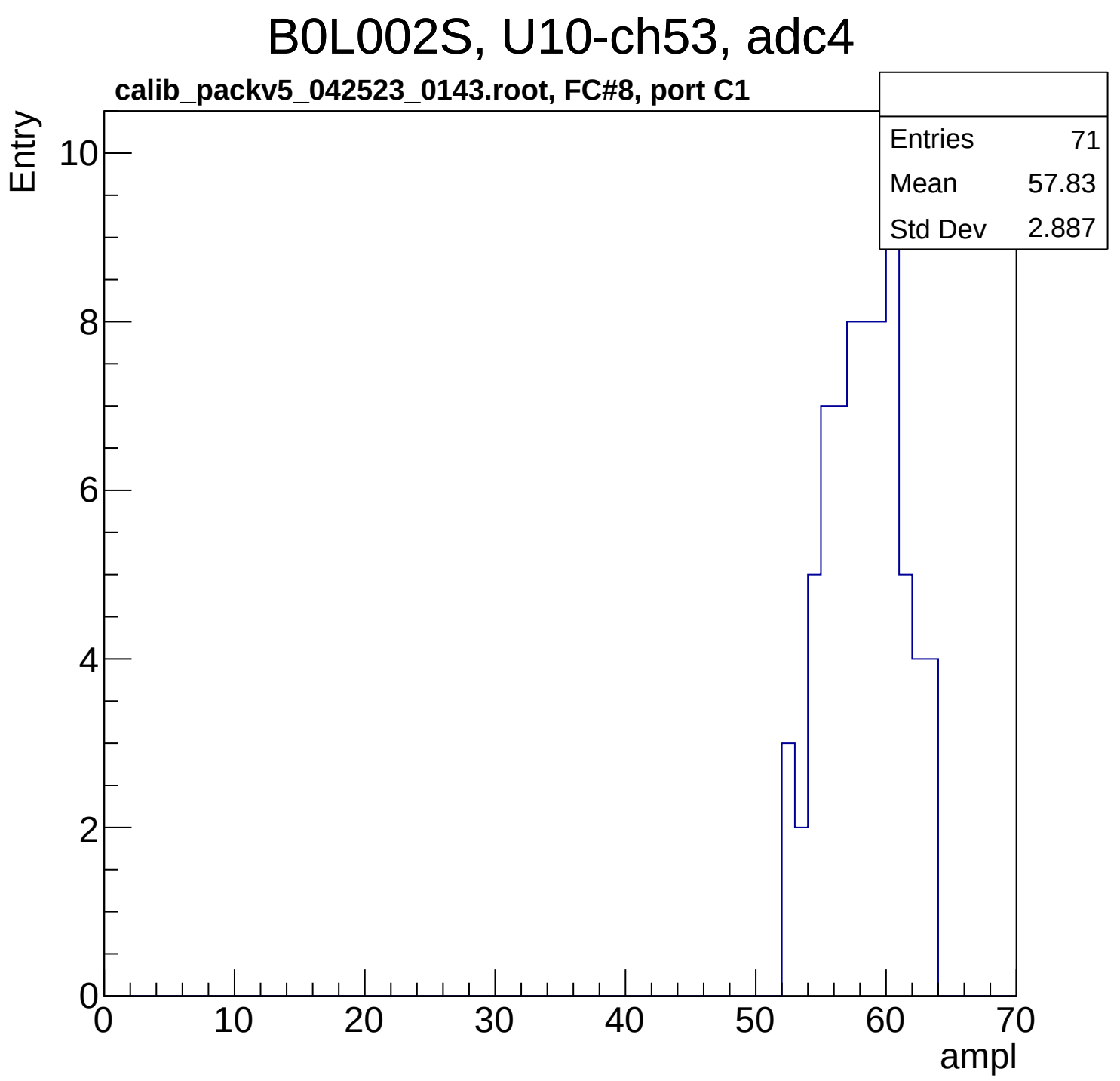

B0L002S, U10-ch52, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

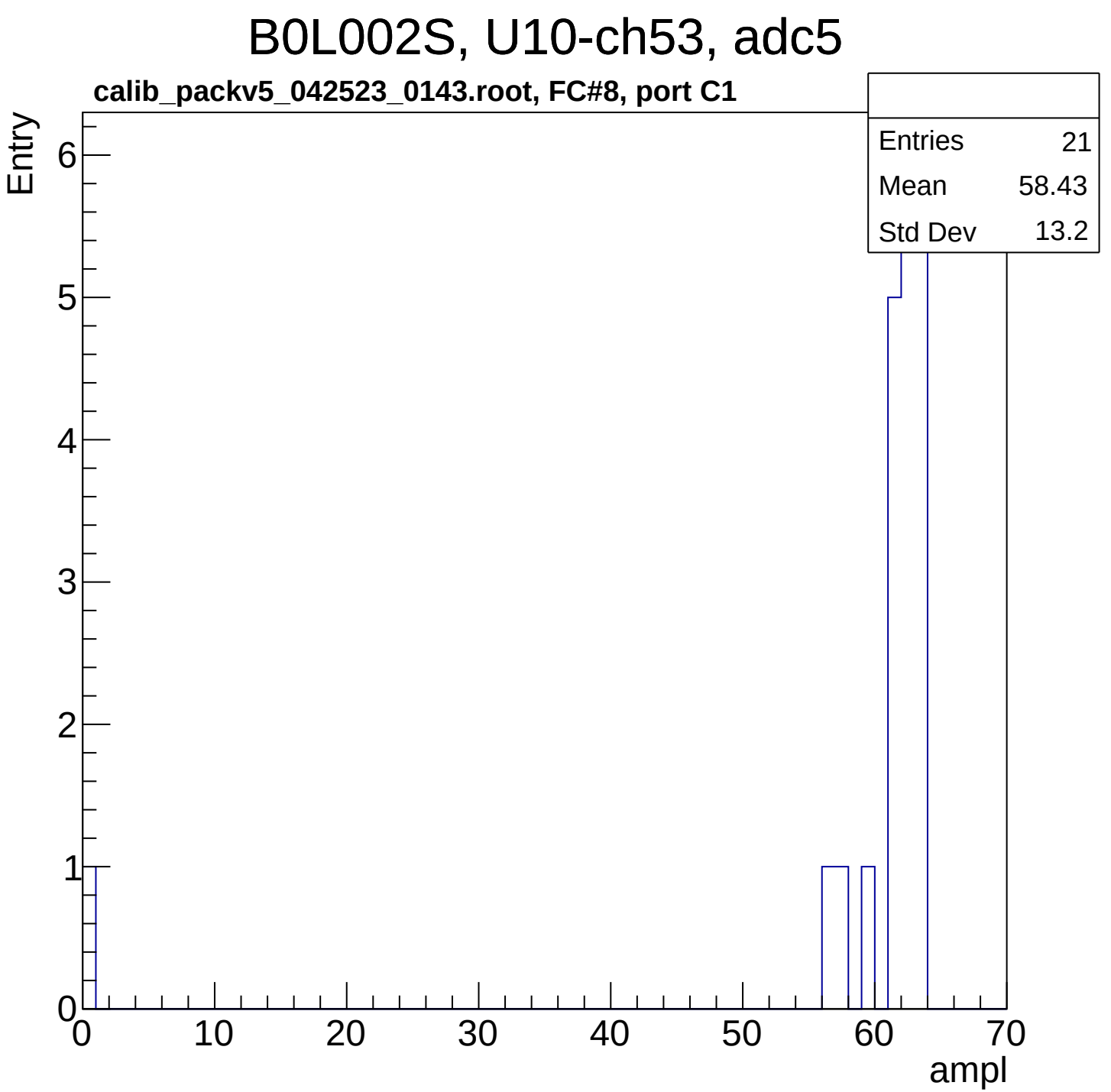

B0L002S, U10-ch53, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 30.26
Std Dev 3.032
Gaus mean : 31.1827
Gaus Width: 3.0336
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

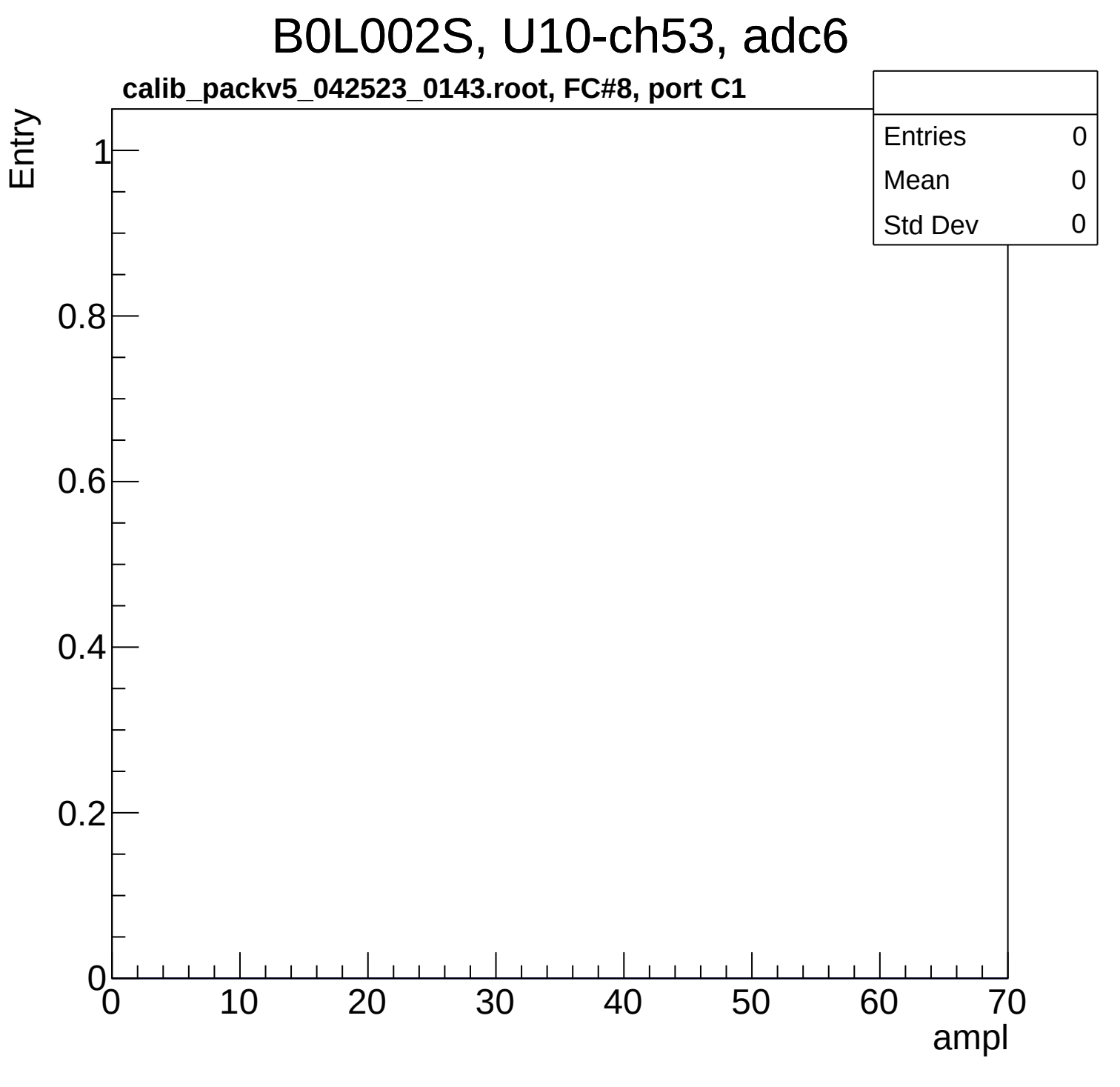

B0L002S, U10-ch53, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 37.15
Std Dev 3.525
Gaus mean : 38.1163
Gaus Width: 3.7128
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

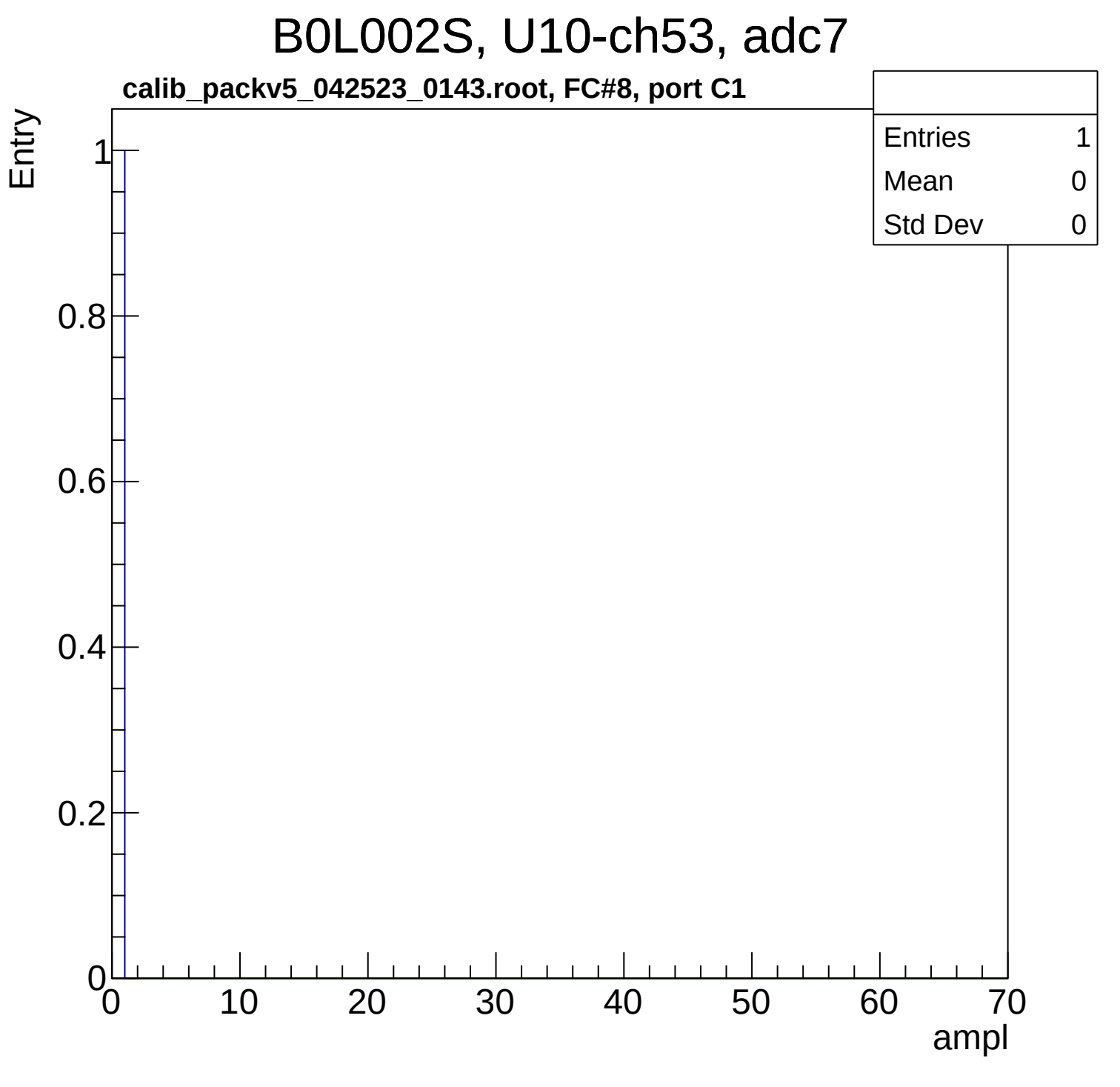

B0L002S, U10-ch53, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 45.05
Std Dev 3.565
Gaus mean : 45.3746
Gaus Width: 3.6900
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

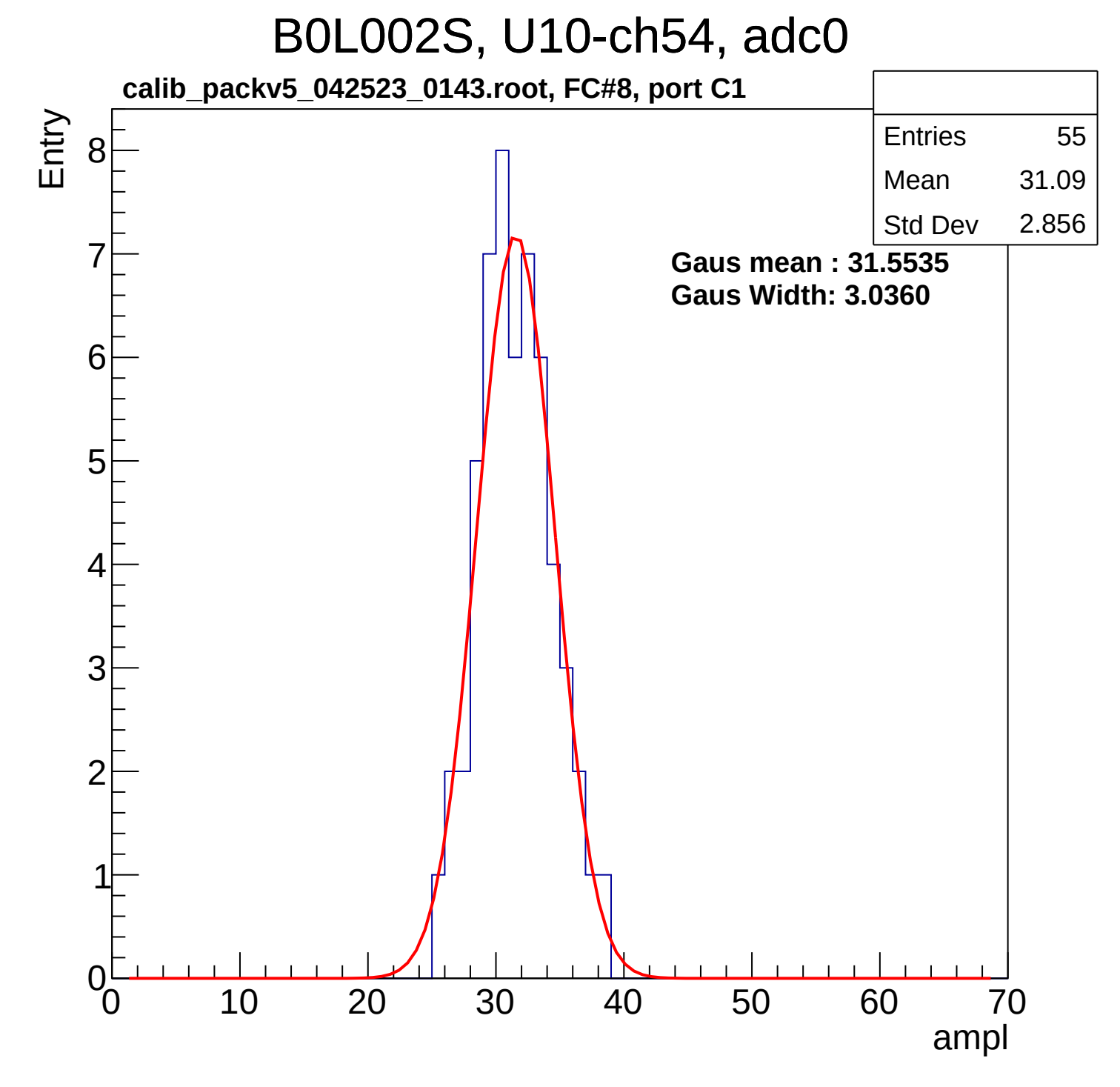

B0L002S, U10-ch53, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 51.5
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

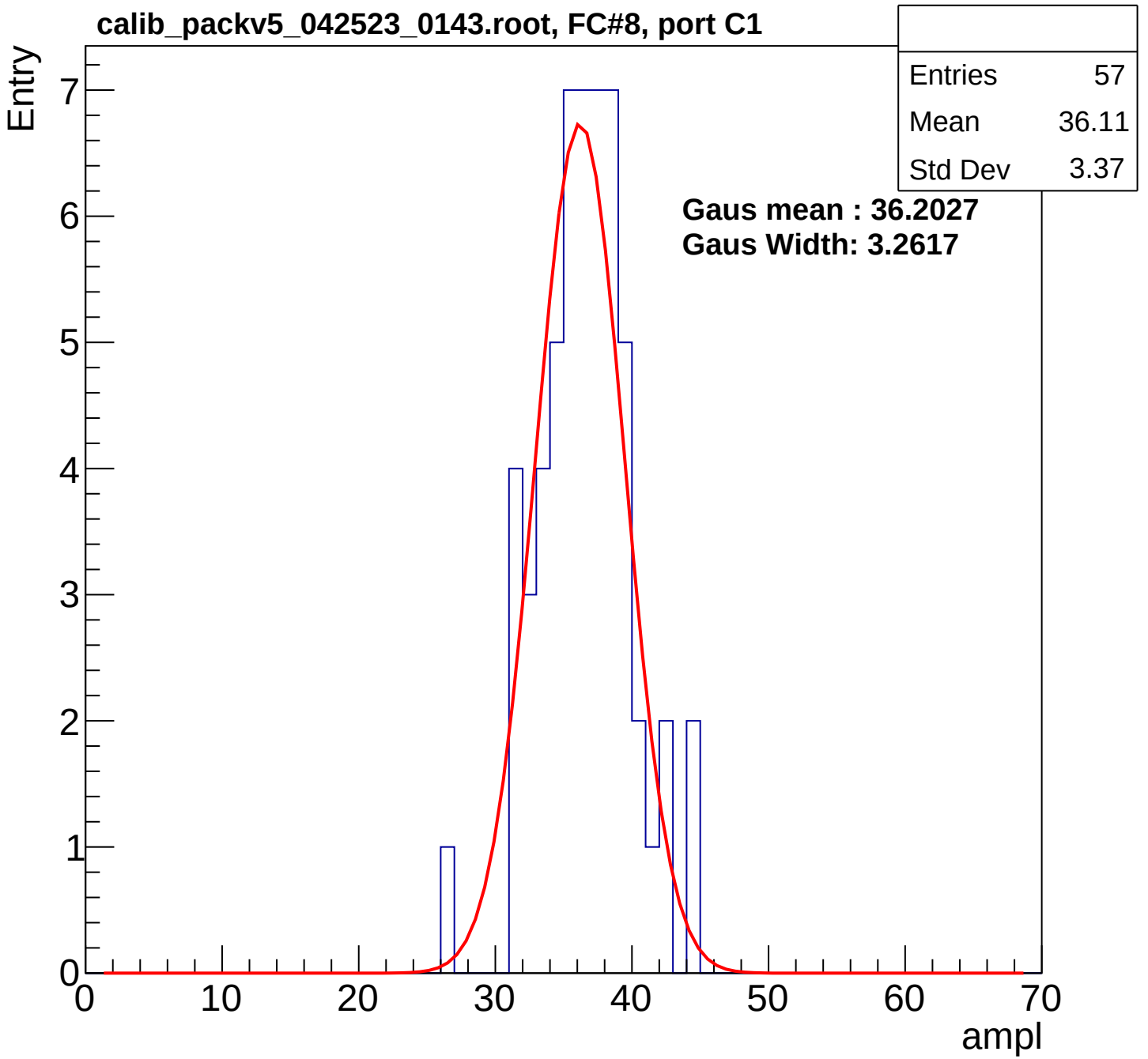

B0L002S, U10-ch53, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 57.83
Std Dev 2.887
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

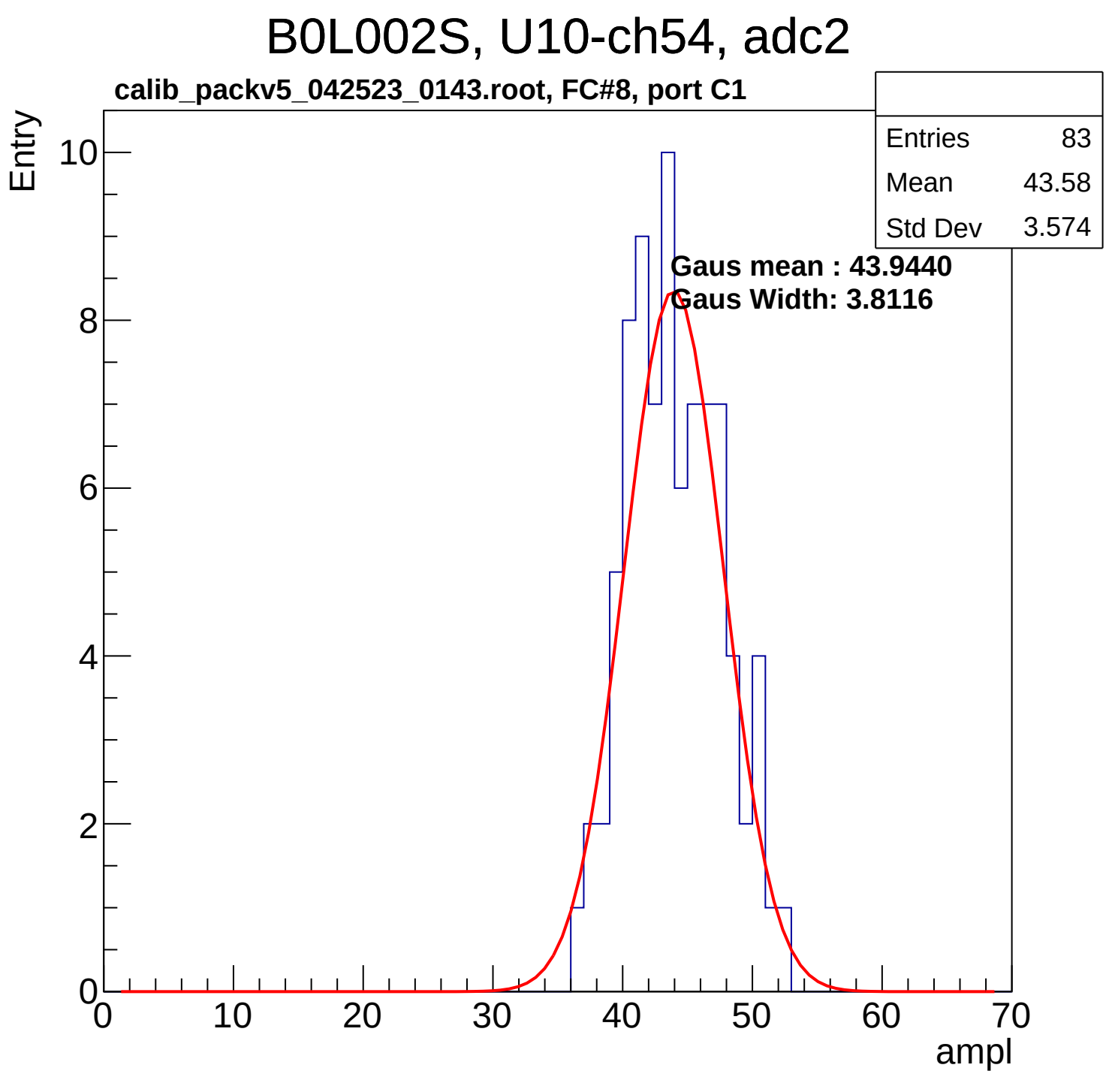

B0L002S, U10-ch53, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.43
Std Dev 13.2
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

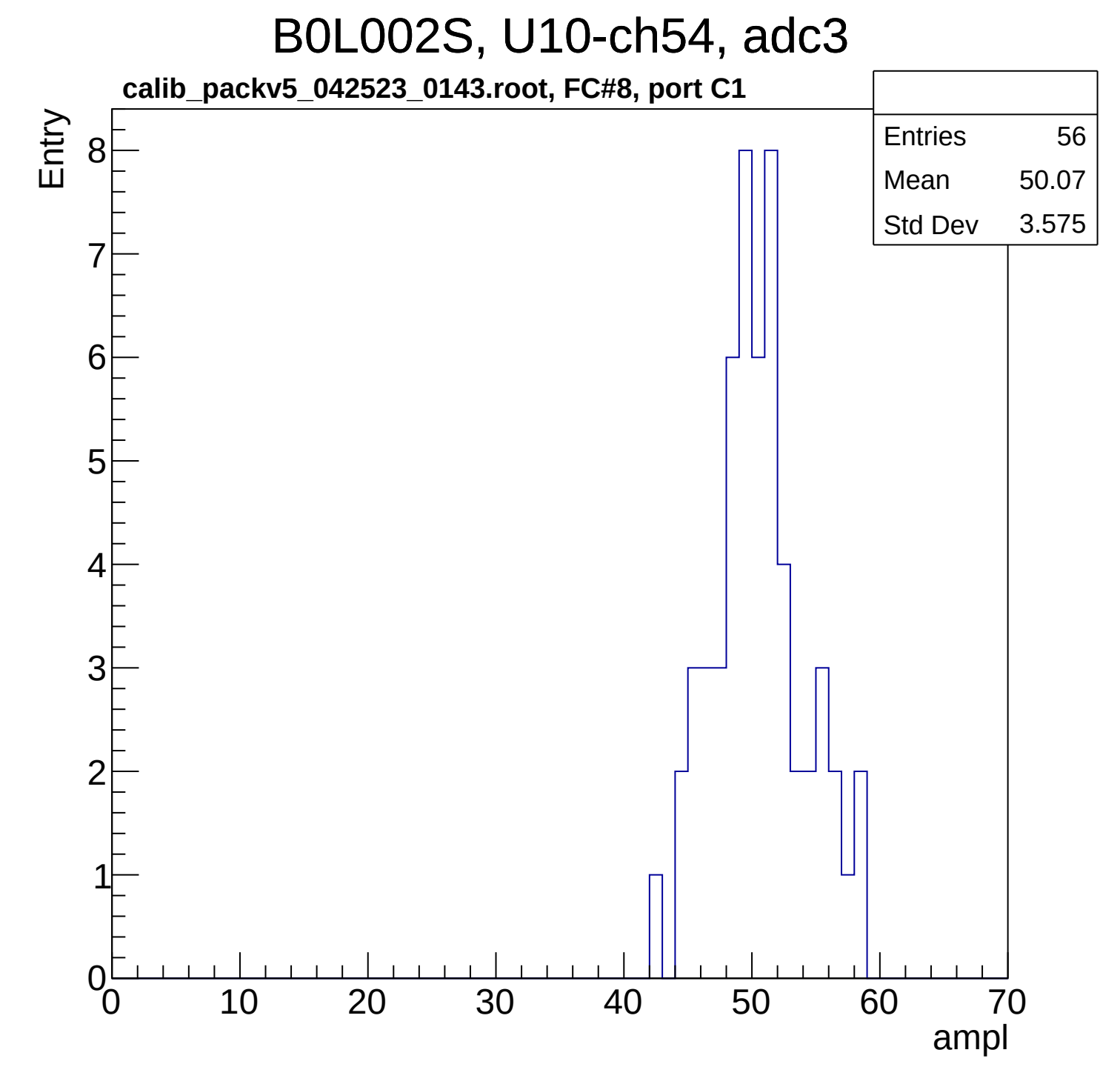

B0L002S, U10-ch53, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

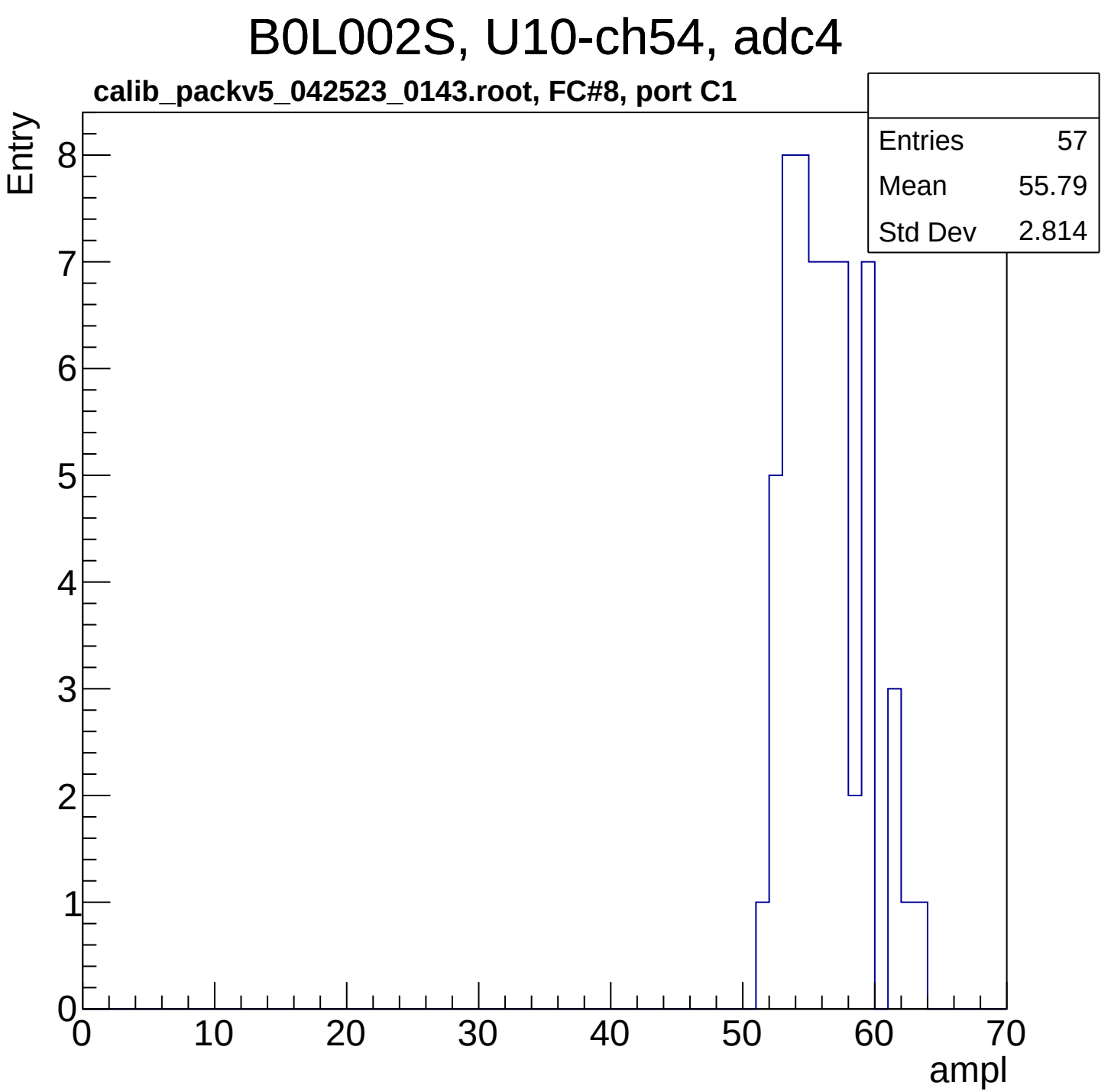

B0L002S, U10-ch53, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

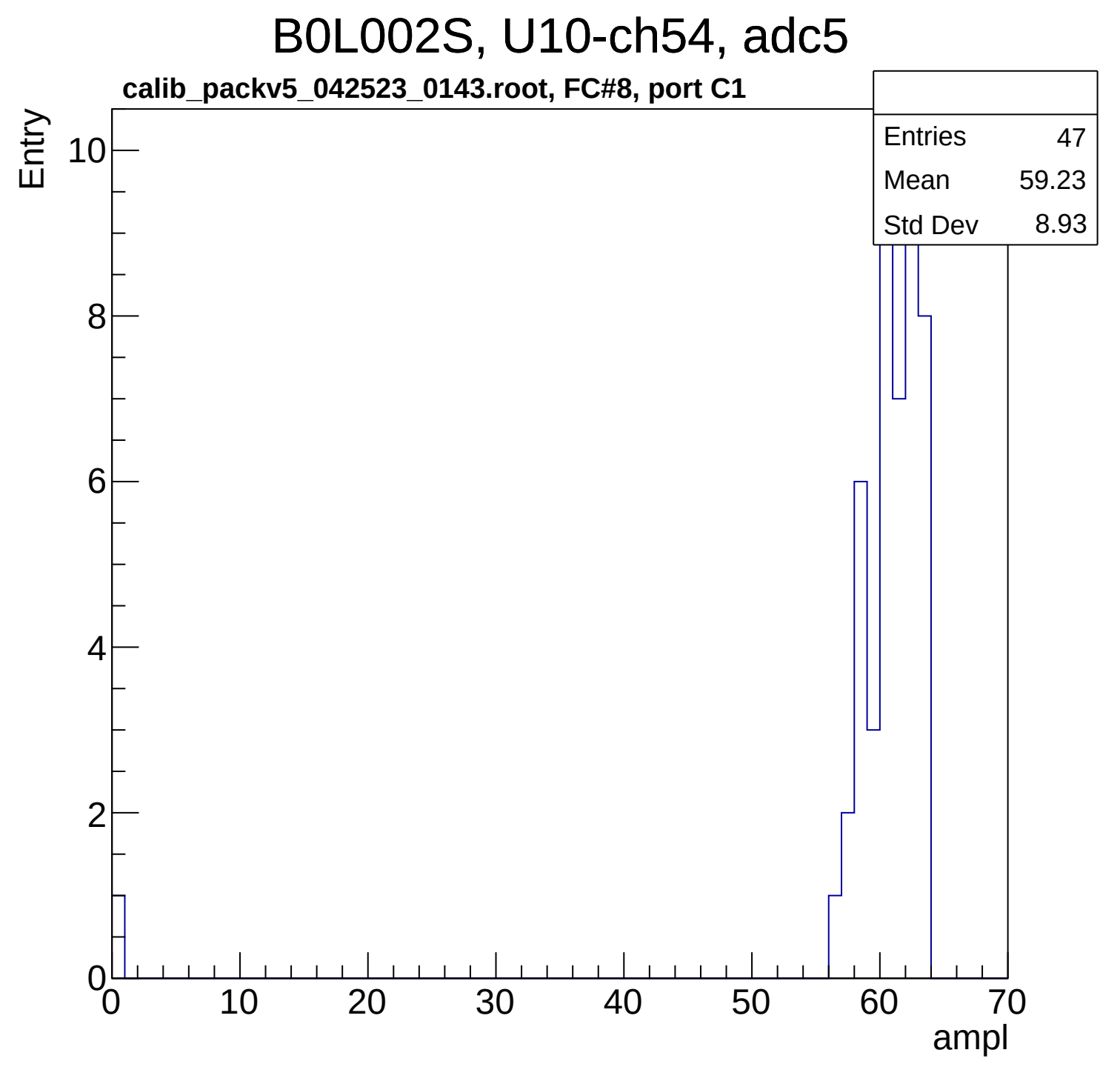

B0L002S, U10-ch54, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 31.09
Std Dev 2.856
Gaus mean : 31.5535
Gaus Width: 3.0360
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

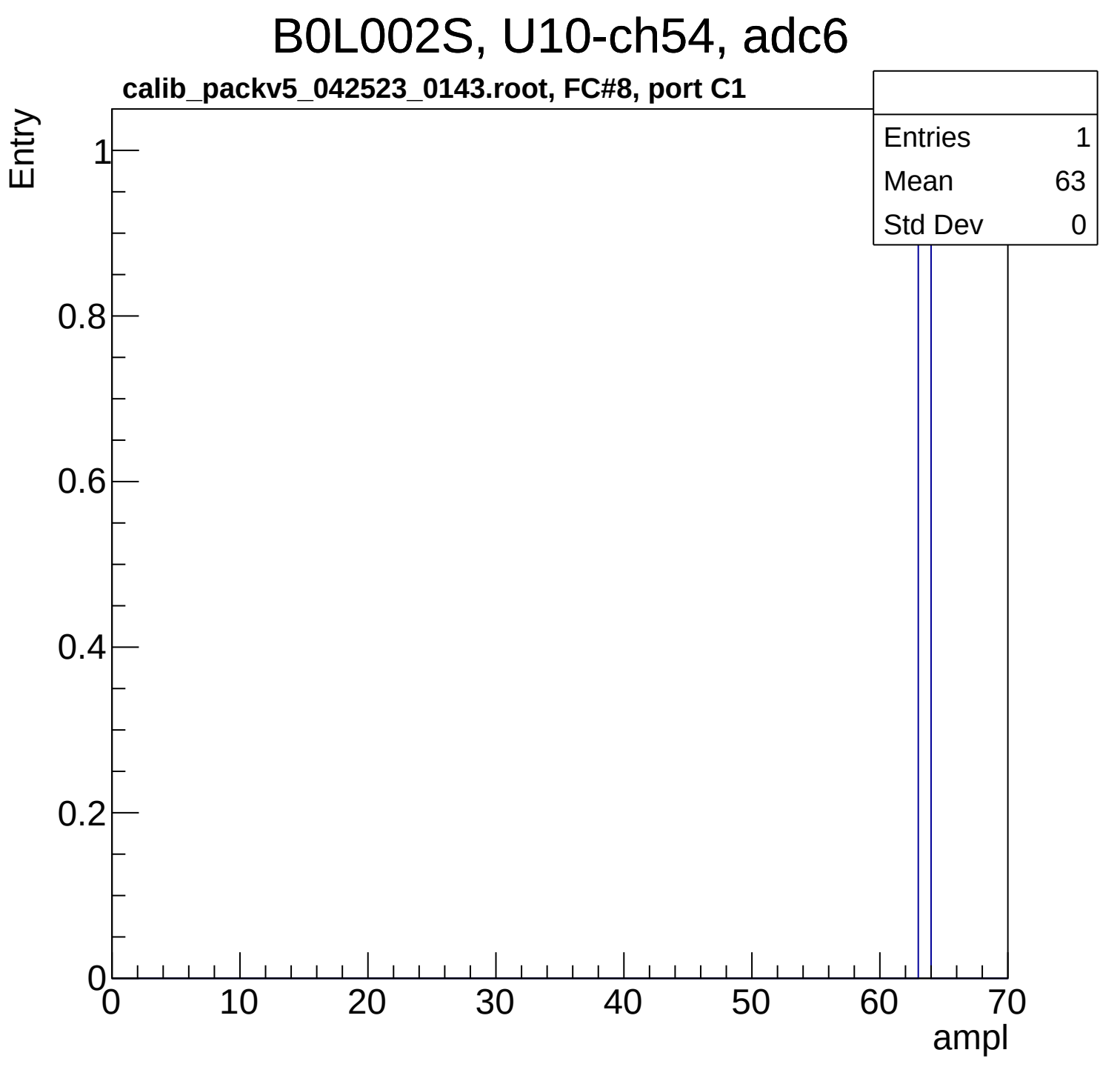

B0L002S, U10-ch54, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 36.11
Std Dev 3.37
Gaus mean : 36.2027
Gaus Width: 3.2617
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

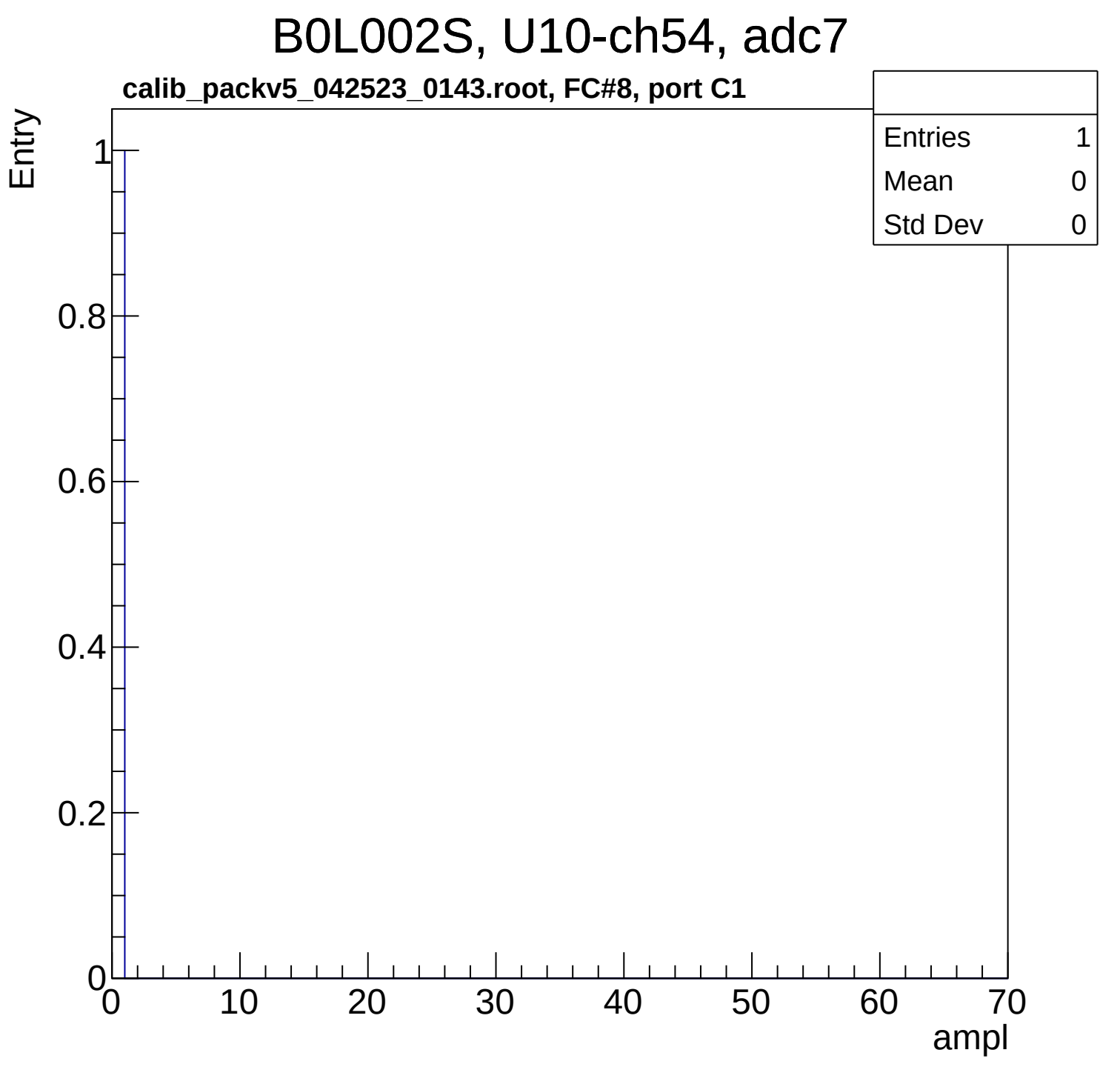

B0L002S, U10-ch54, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 43.58
Std Dev 3.574
Gaus mean : 43.9440
Gaus Width: 3.8116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

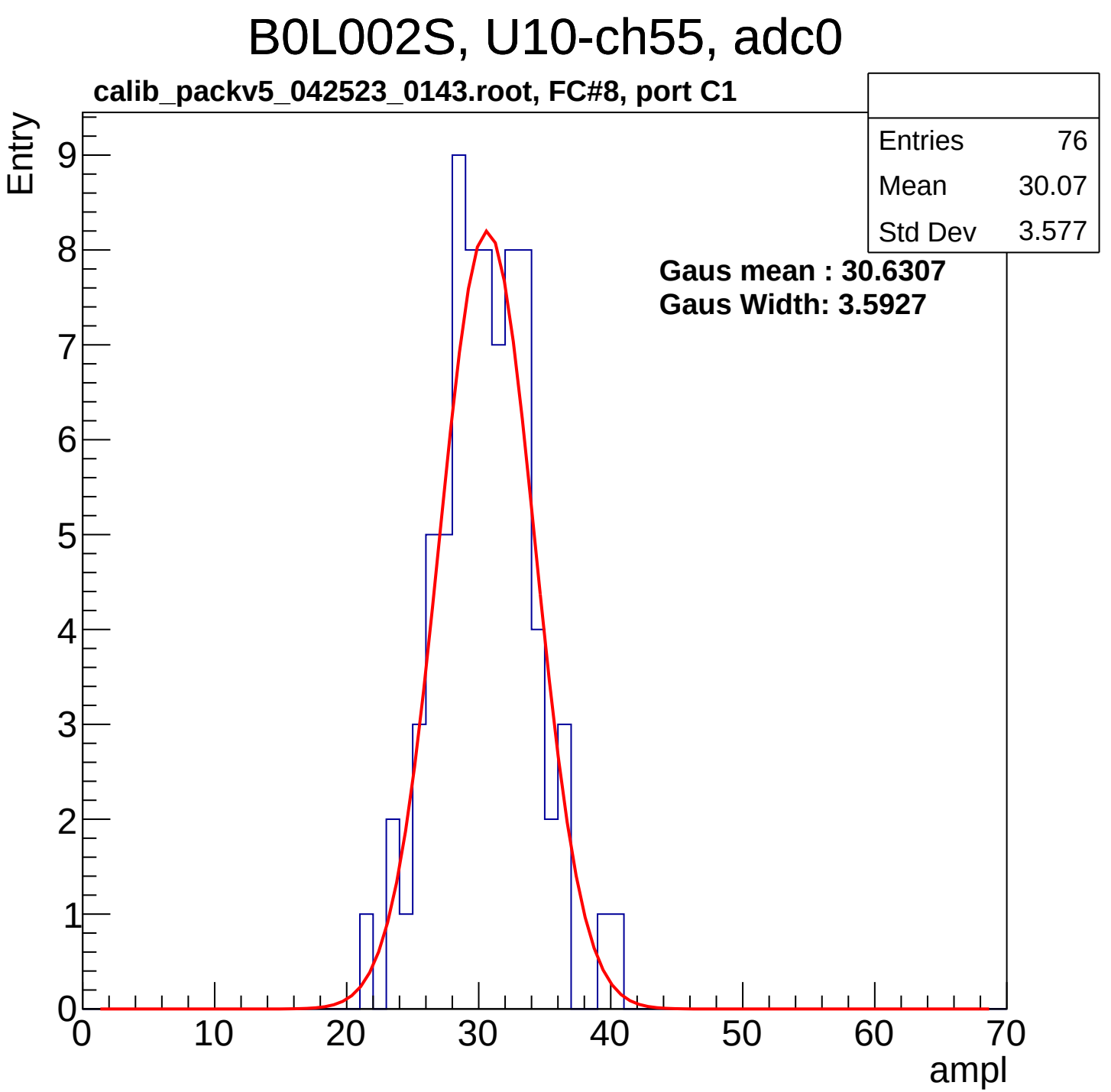

B0L002S, U10-ch54, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 50.07
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

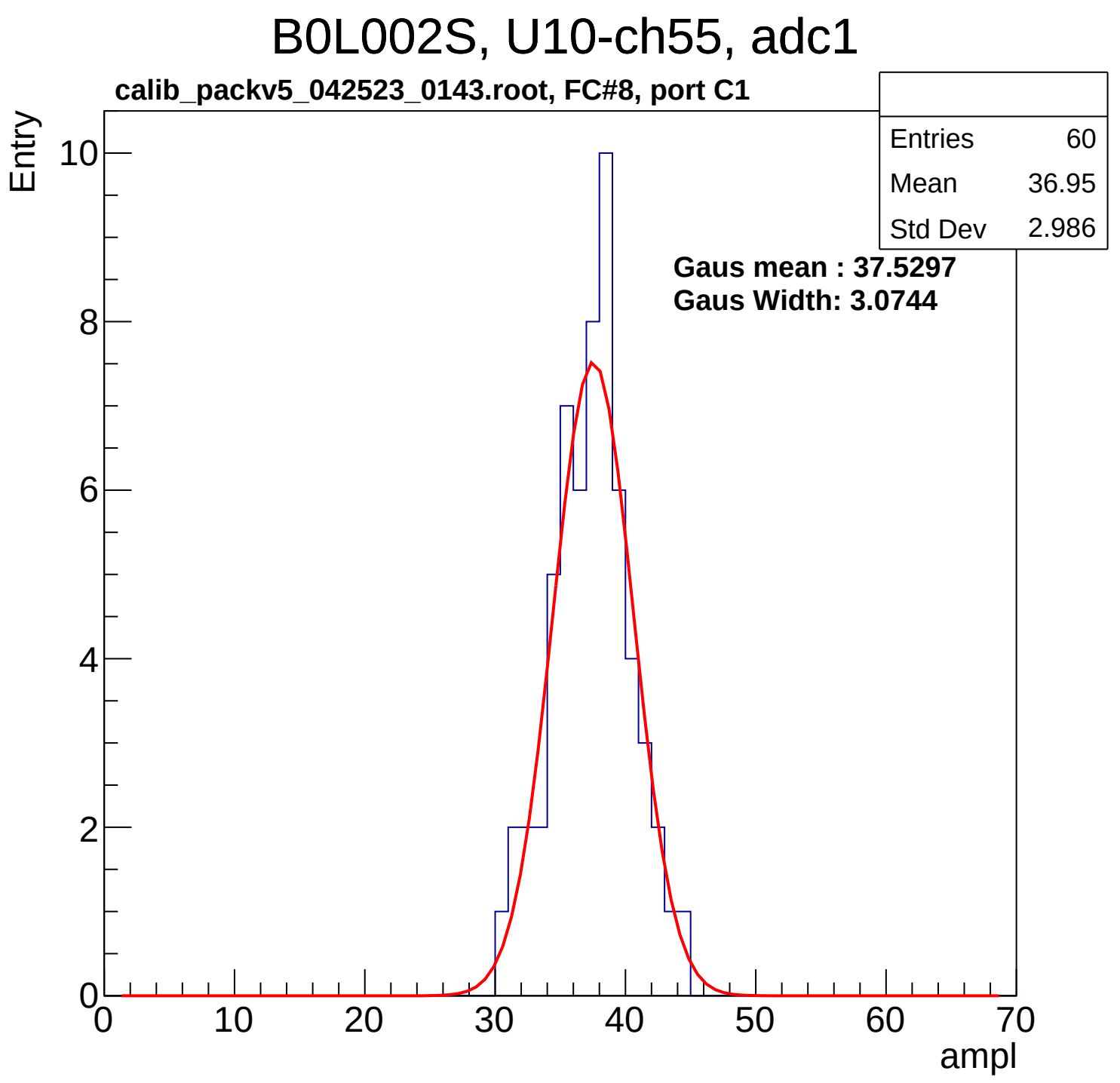

B0L002S, U10-ch54, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 55.79
Std Dev 2.814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

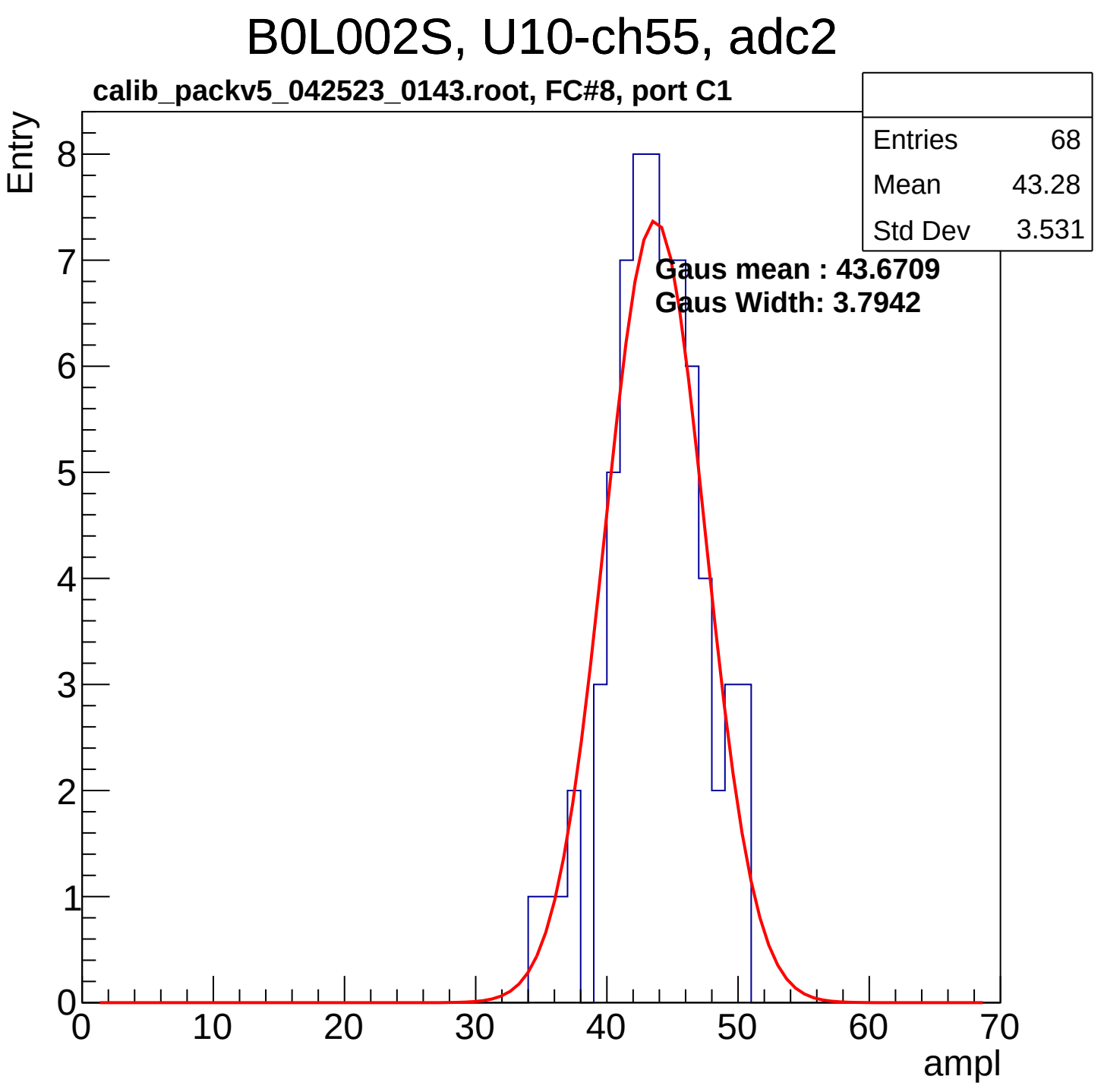

B0L002S, U10-ch54, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 59.23
Std Dev 8.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

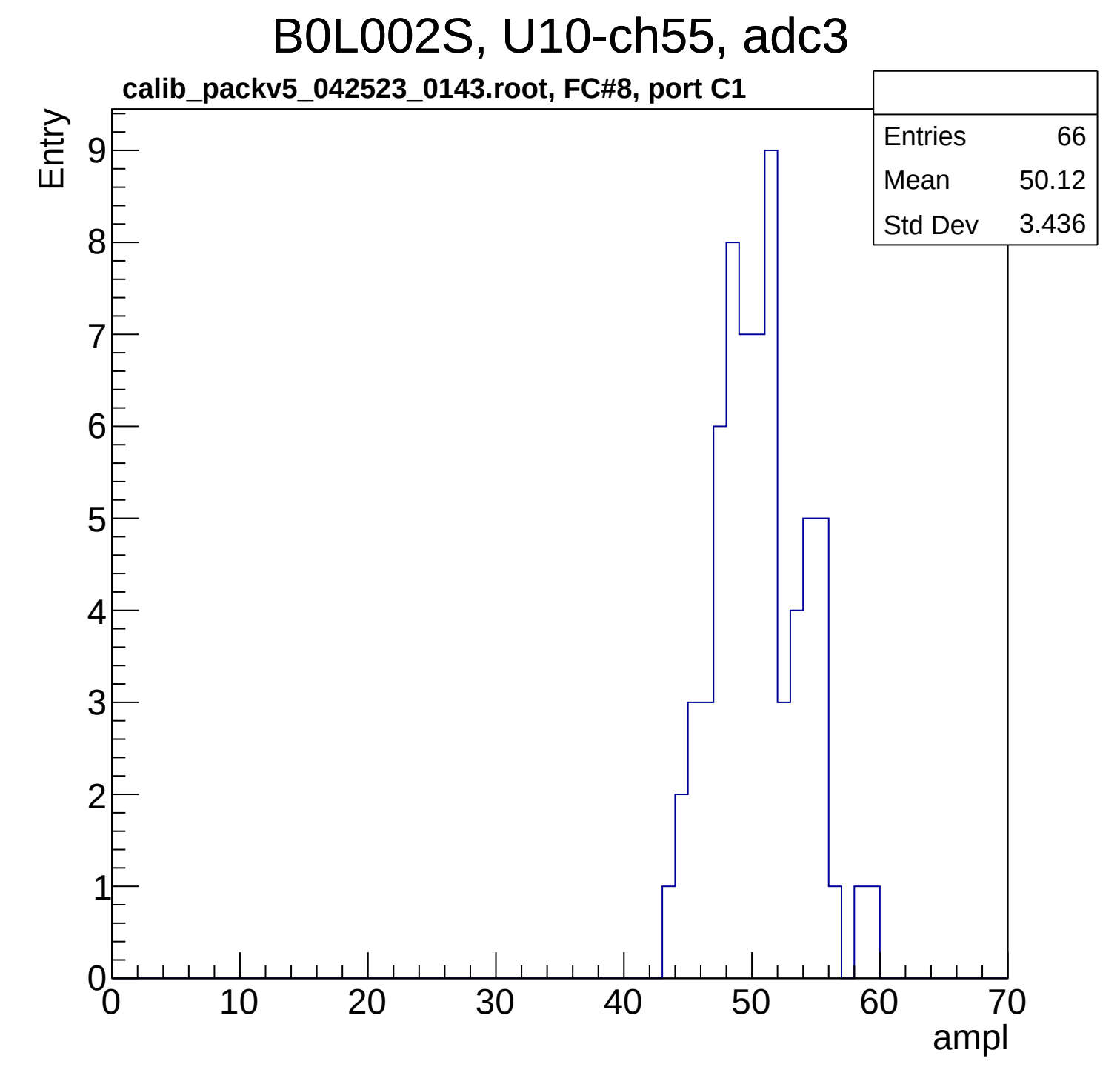

B0L002S, U10-ch54, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

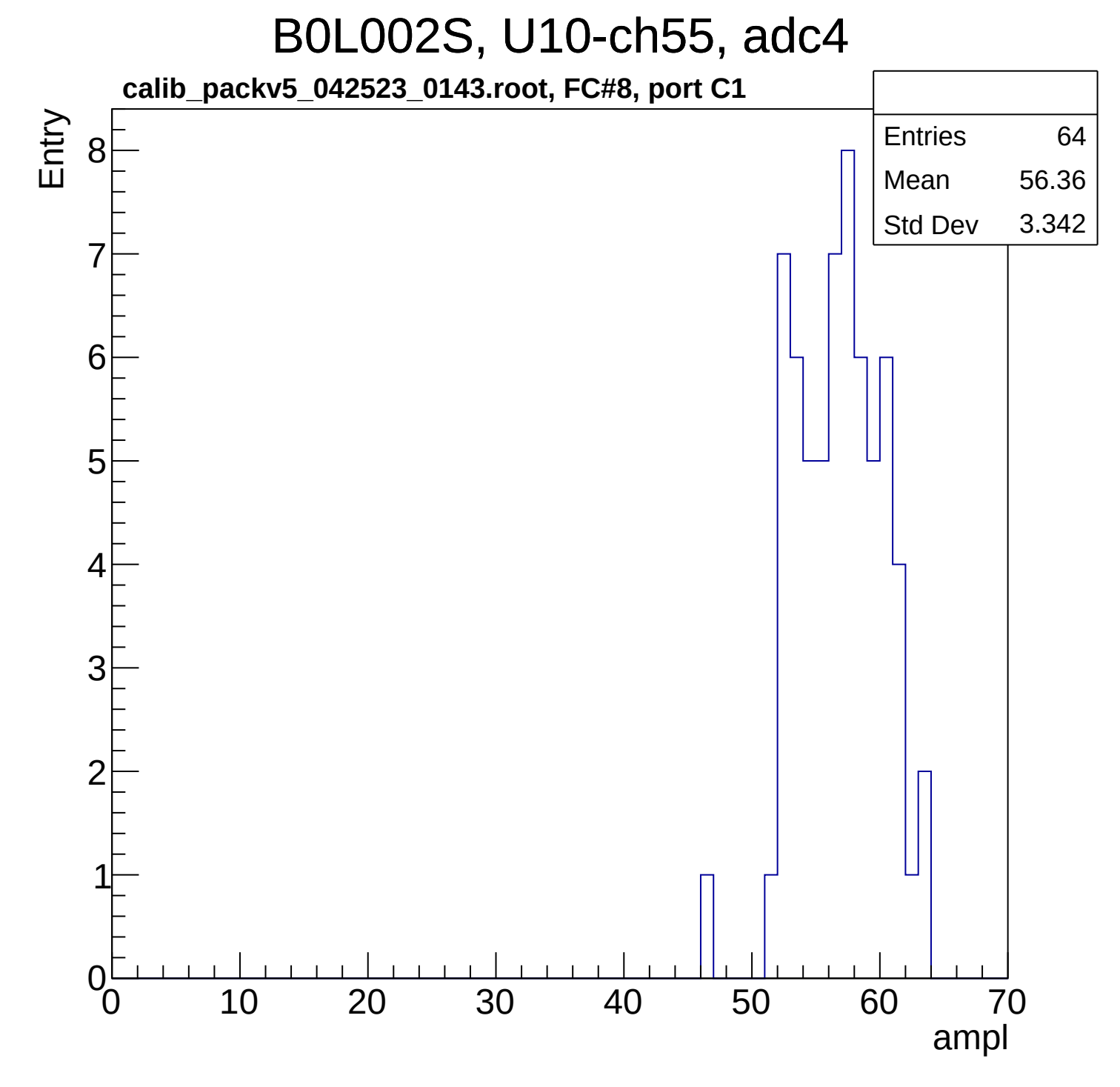

B0L002S, U10-ch54, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

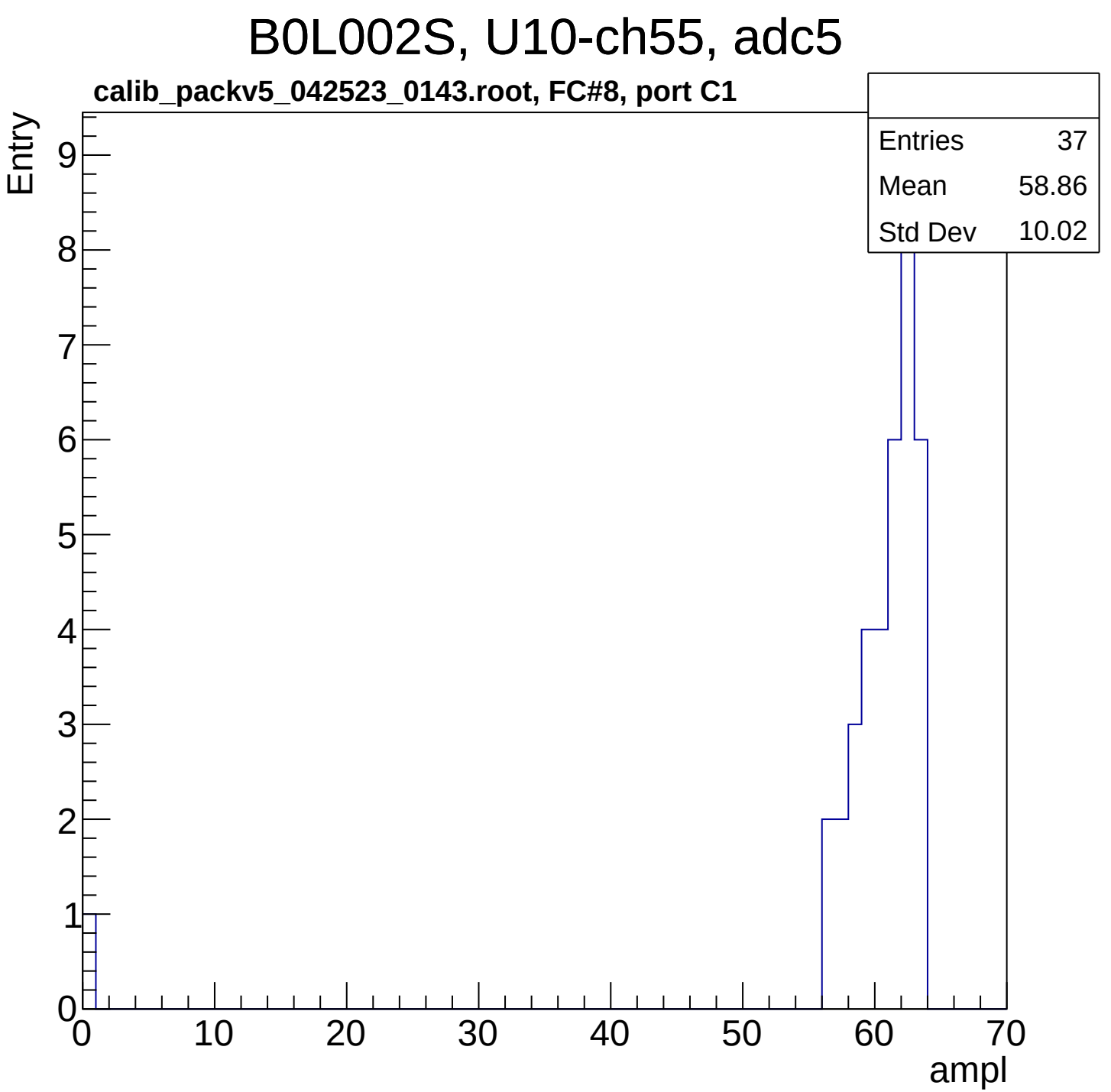

B0L002S, U10-ch55, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 30.07
Std Dev 3.577
Gaus mean : 30.6307
Gaus Width: 3.5927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

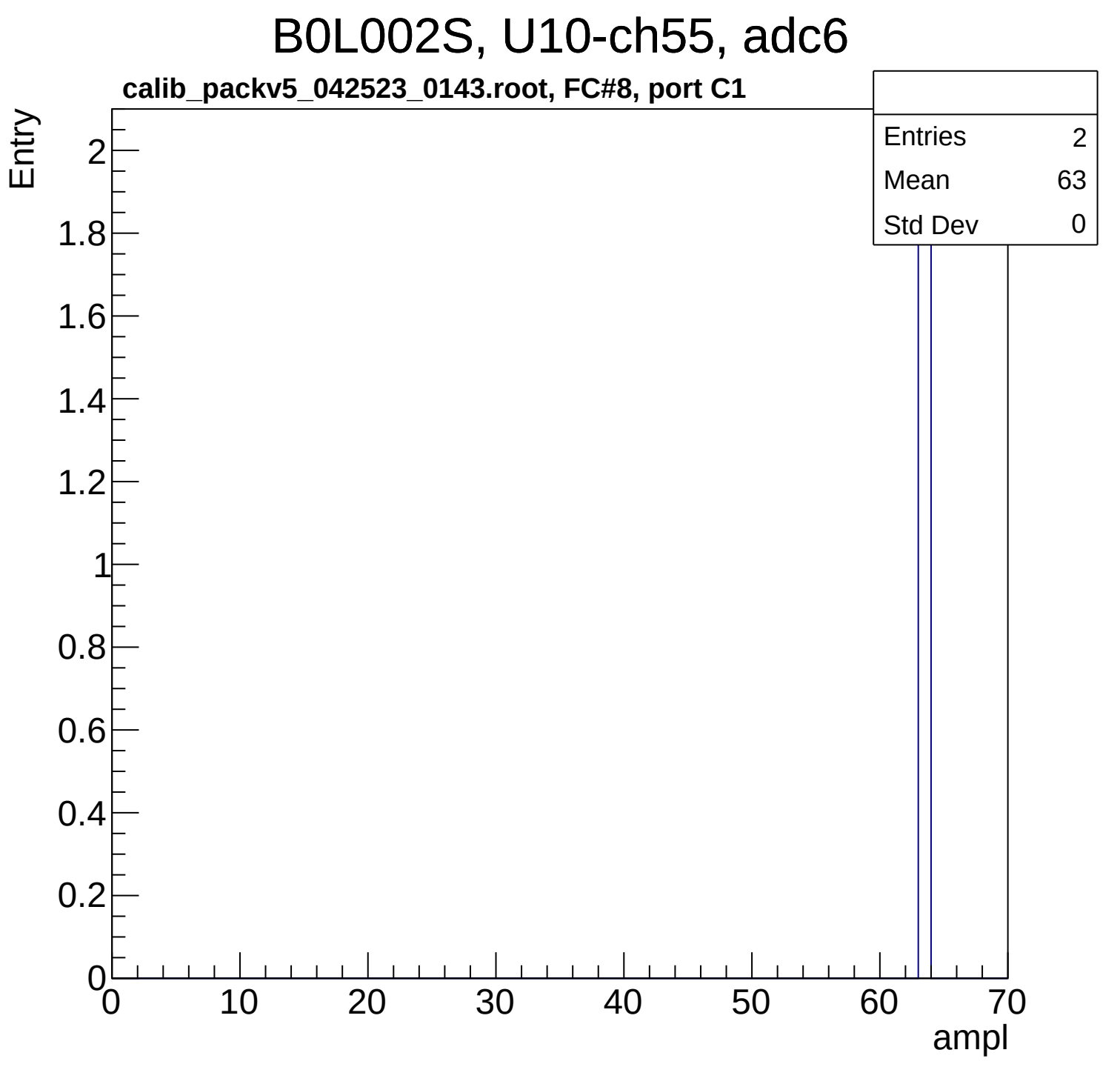

B0L002S, U10-ch55, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 36.95
Std Dev 2.986
Gaus mean : 37.5297
Gaus Width: 3.0744
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

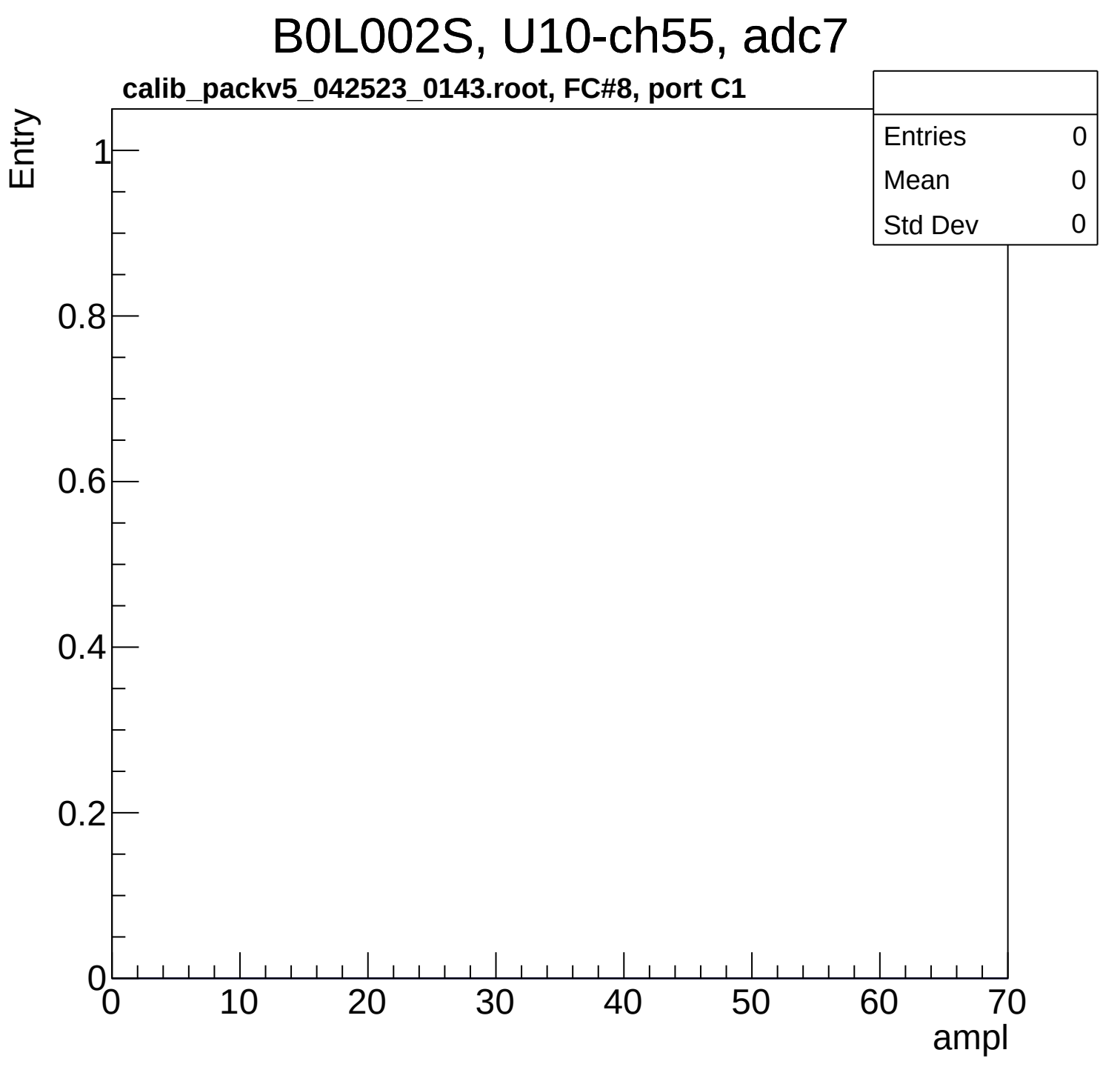

B0L002S, U10-ch55, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.28
Std Dev 3.531
Gaus mean : 43.6709
Gaus Width: 3.7942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

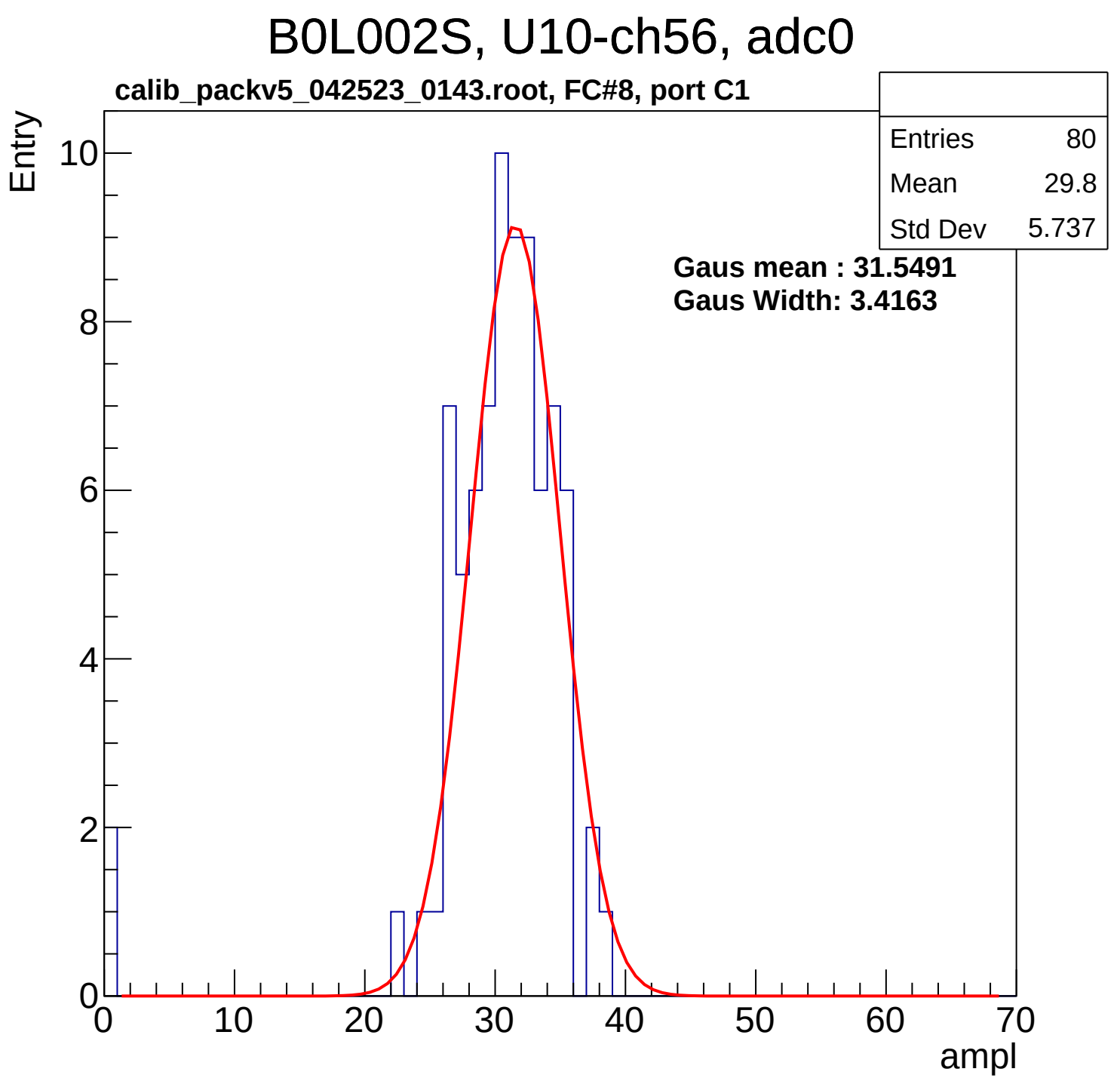

B0L002S, U10-ch55, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 50.12
Std Dev 3.436
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

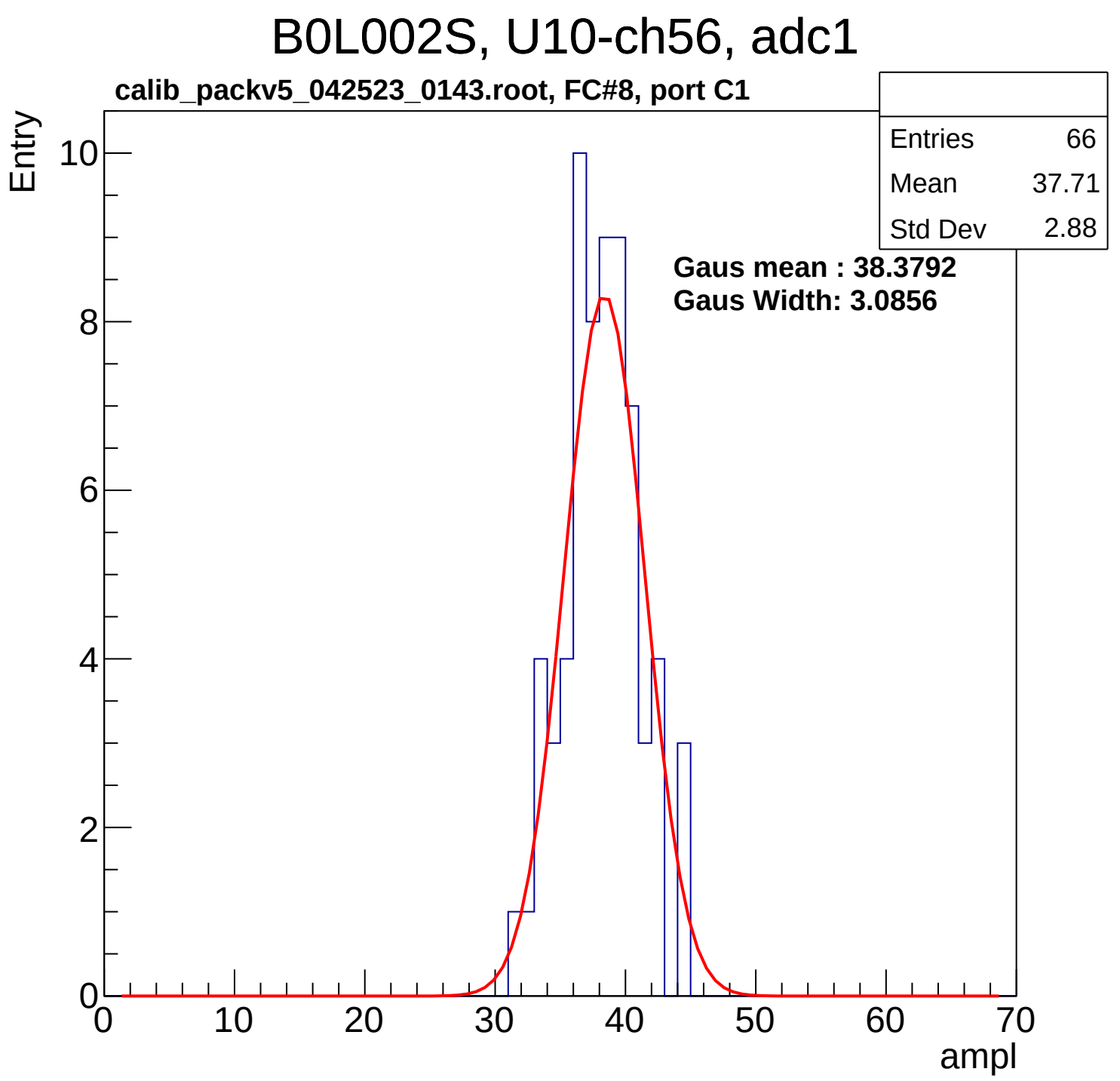

B0L002S, U10-ch55, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 56.36
Std Dev 3.342
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

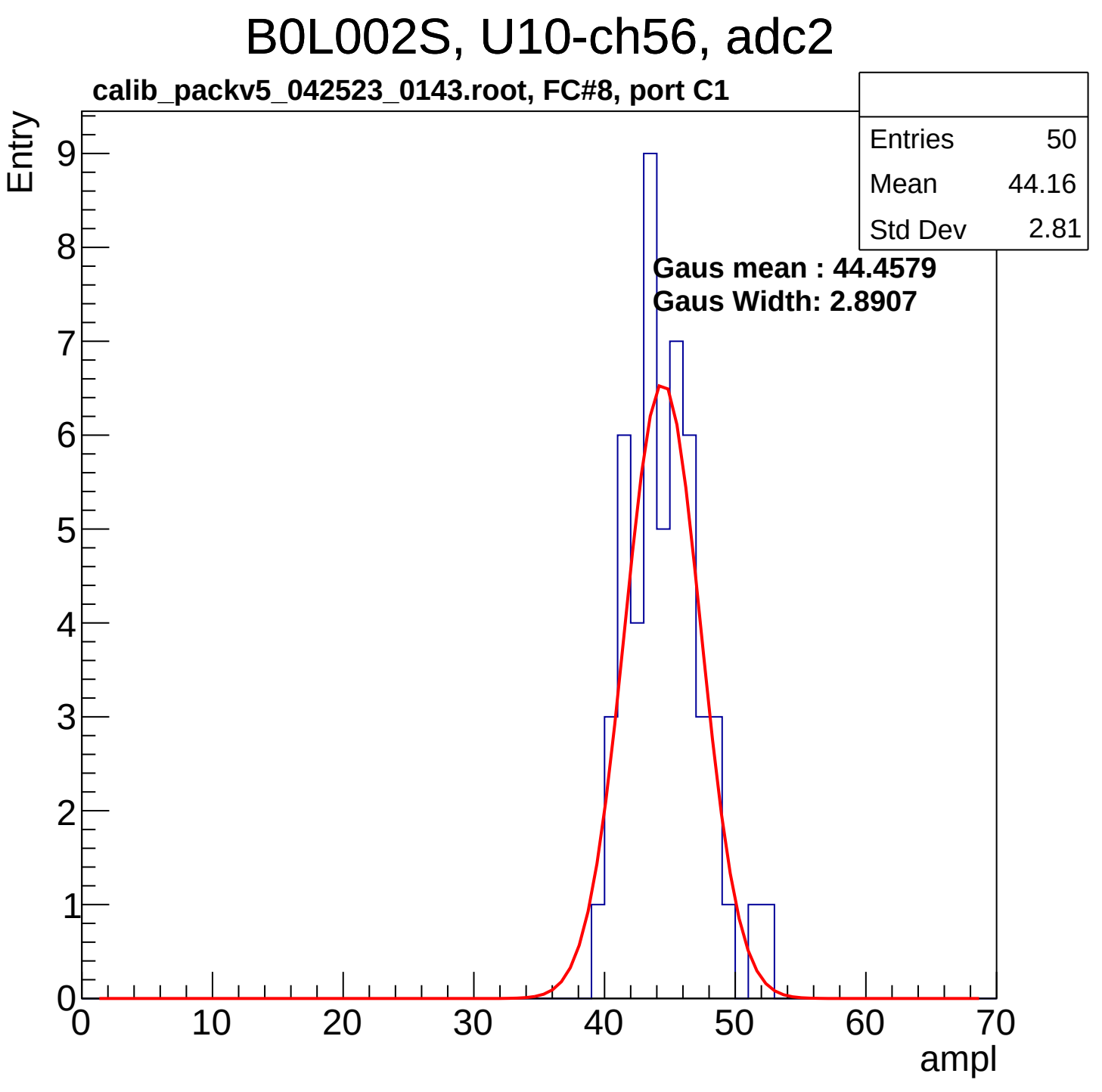

B0L002S, U10-ch55, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.86
Std Dev 10.02
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

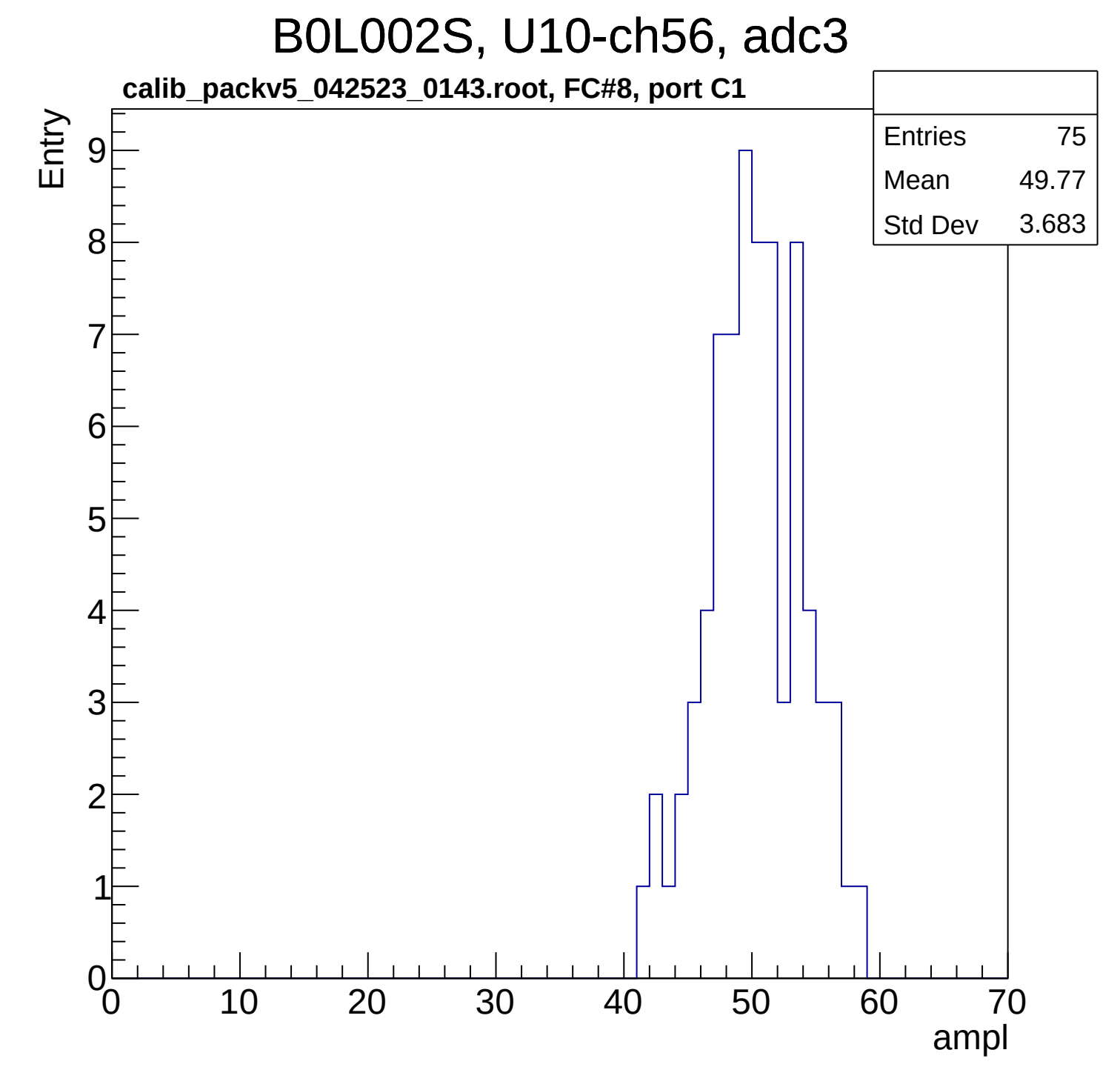

B0L002S, U10-ch55, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

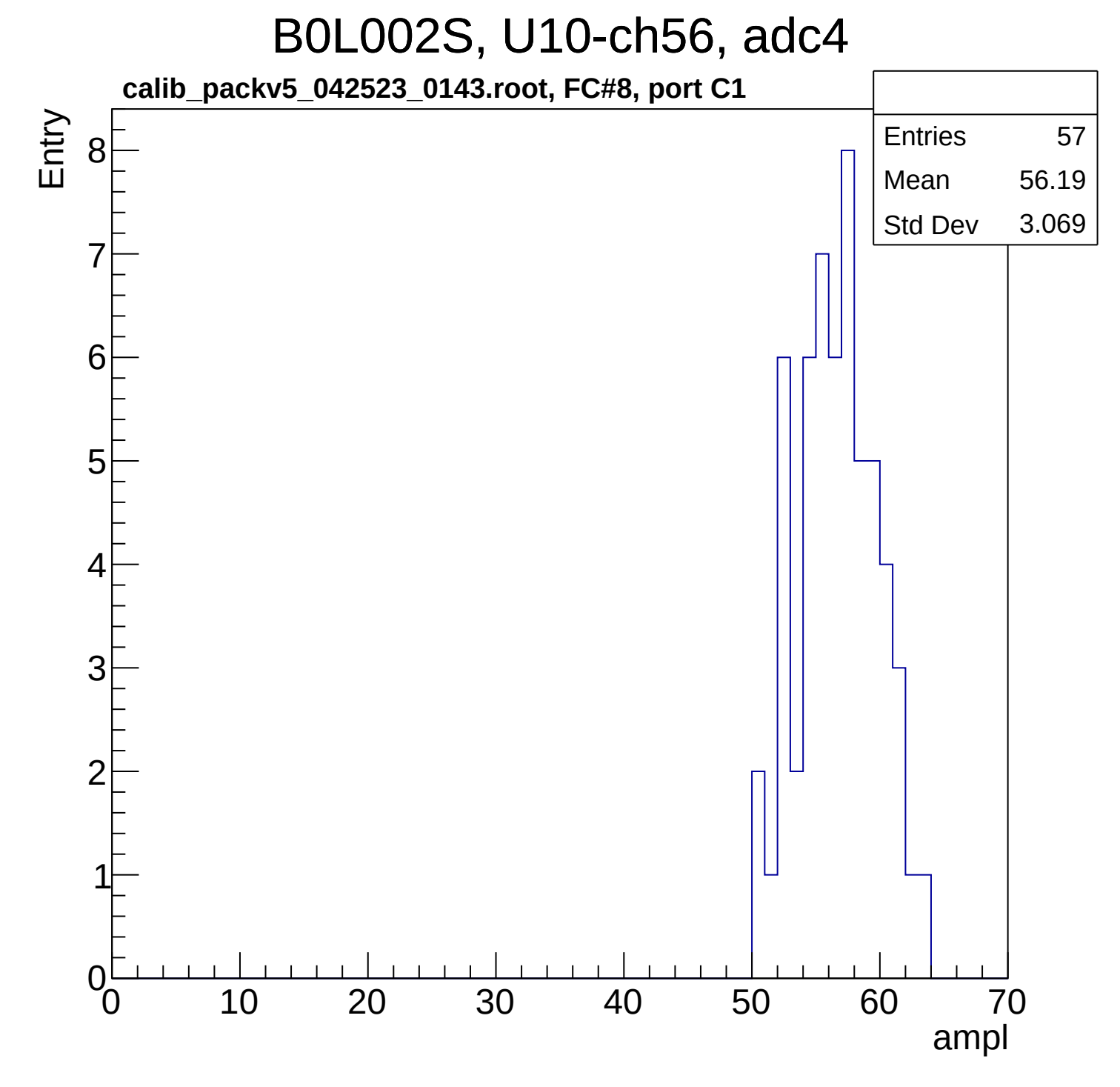

B0L002S, U10-ch55, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

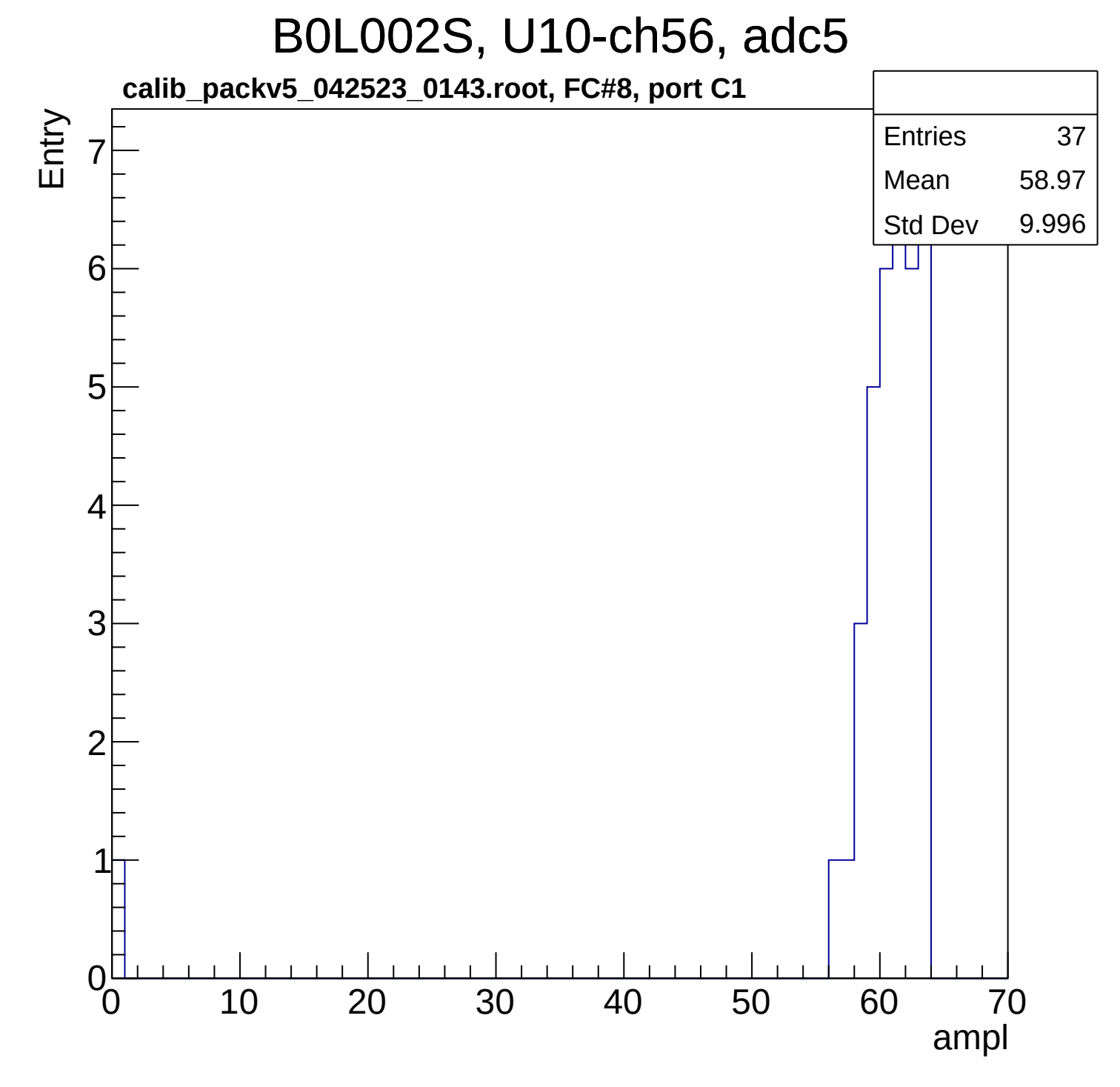

B0L002S, U10-ch56, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 29.8
Std Dev 5.737
Gaus mean : 31.5491
Gaus Width: 3.4163
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

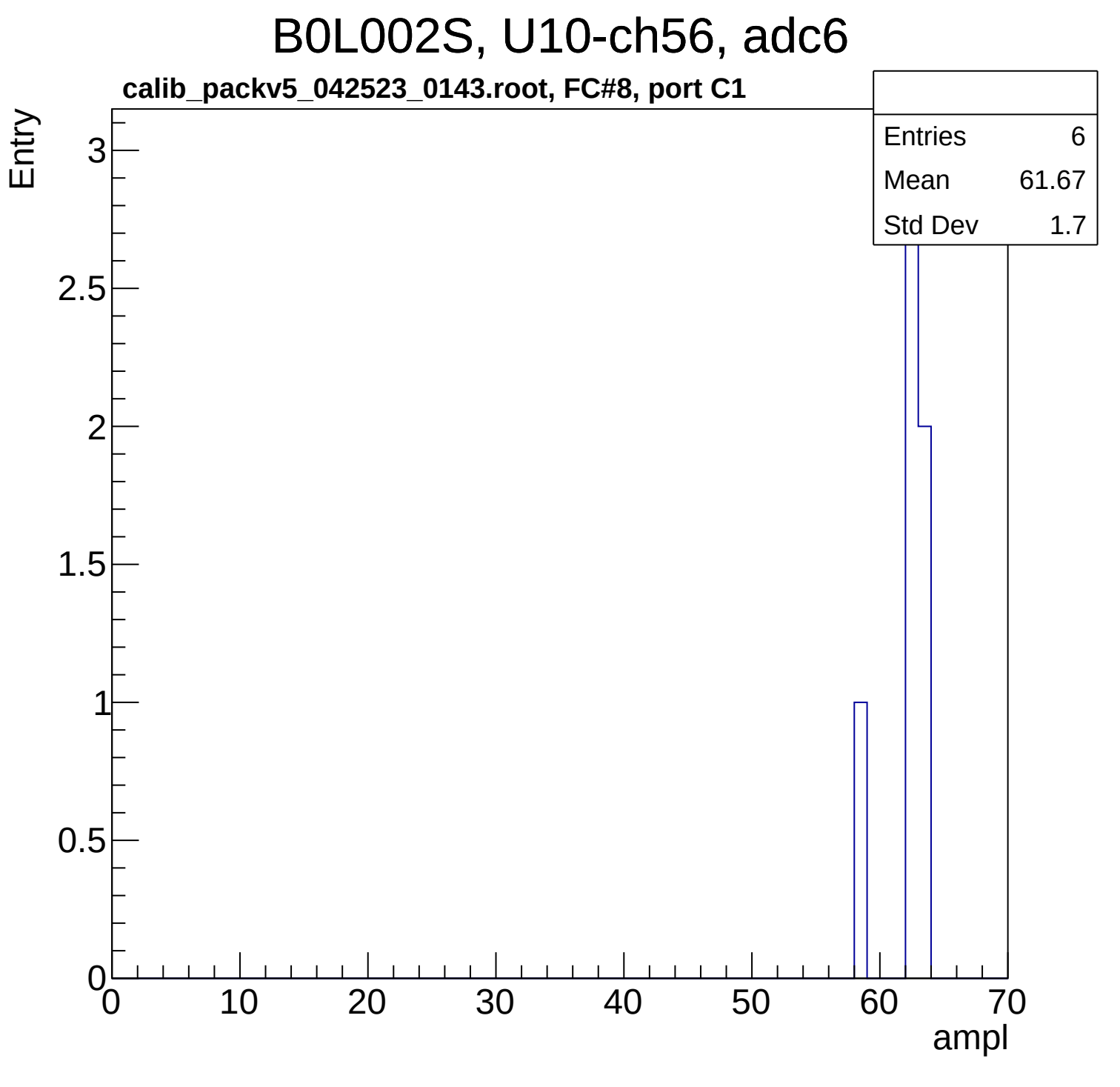

B0L002S, U10-ch56, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.71
Std Dev 2.88
Gaus mean : 38.3792
Gaus Width: 3.0856
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

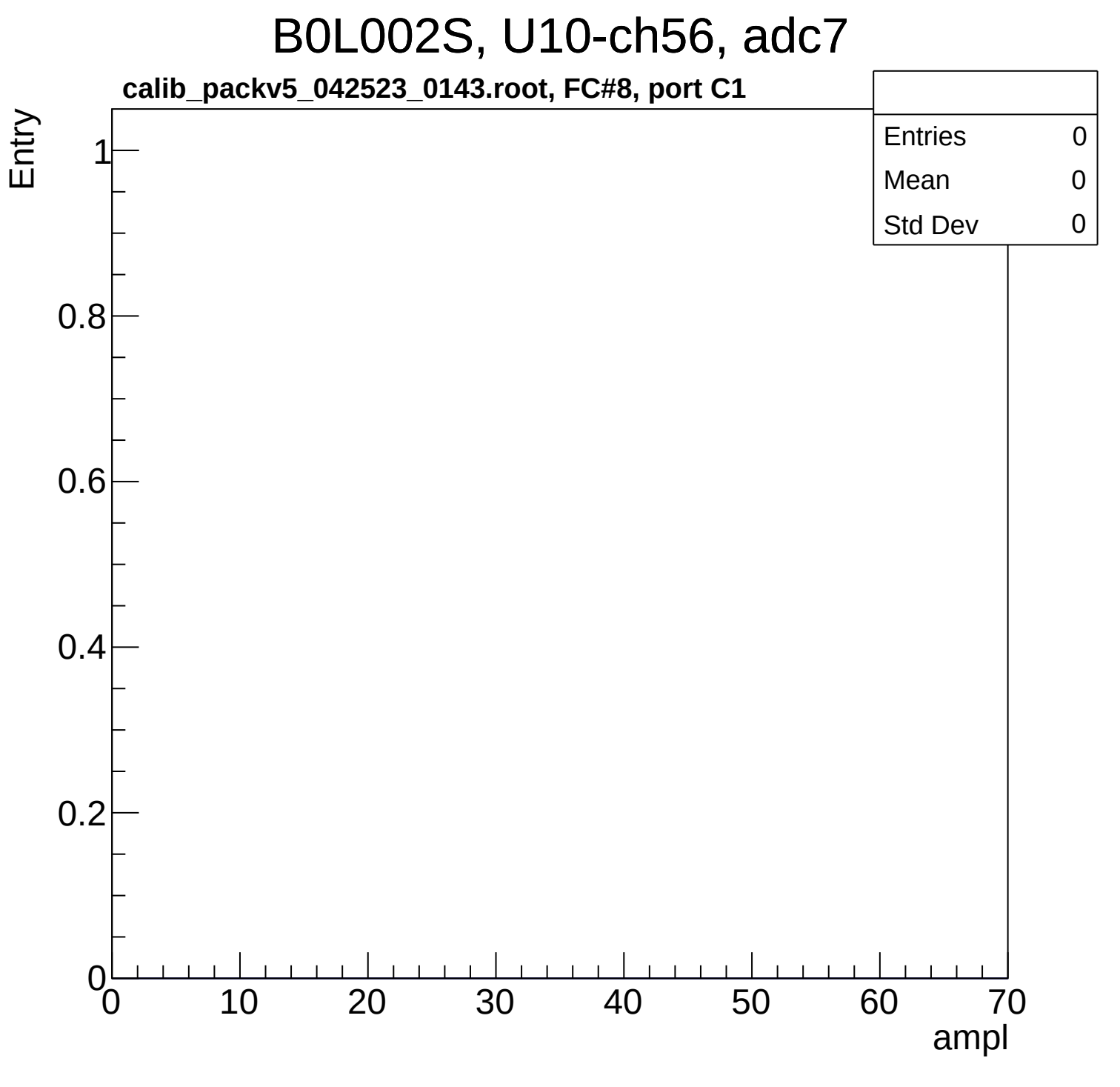

B0L002S, U10-ch56, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 44.16
Std Dev 2.81
Gaus mean : 44.4579
Gaus Width: 2.8907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

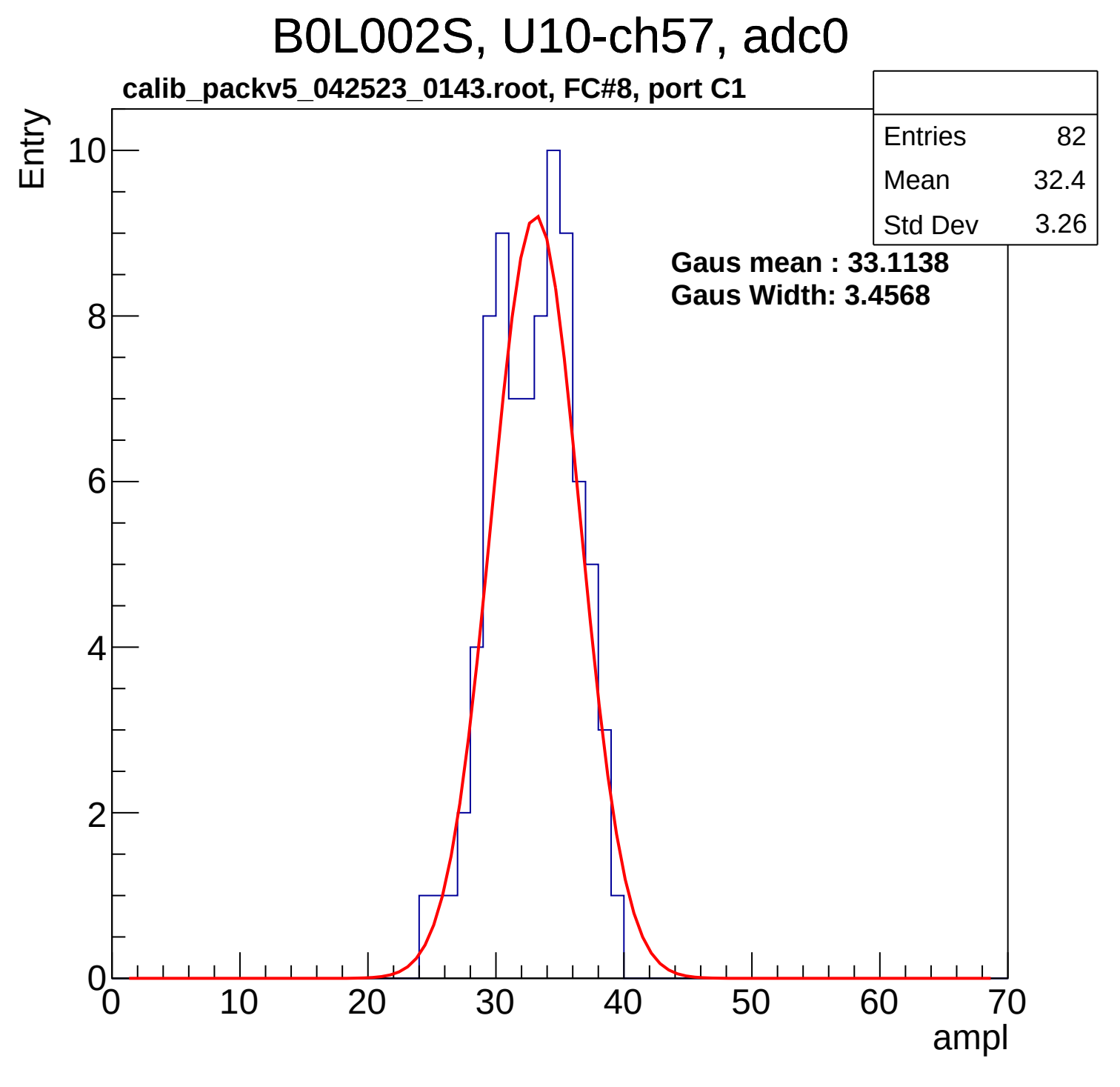

B0L002S, U10-ch56, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 49.77
Std Dev 3.683
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

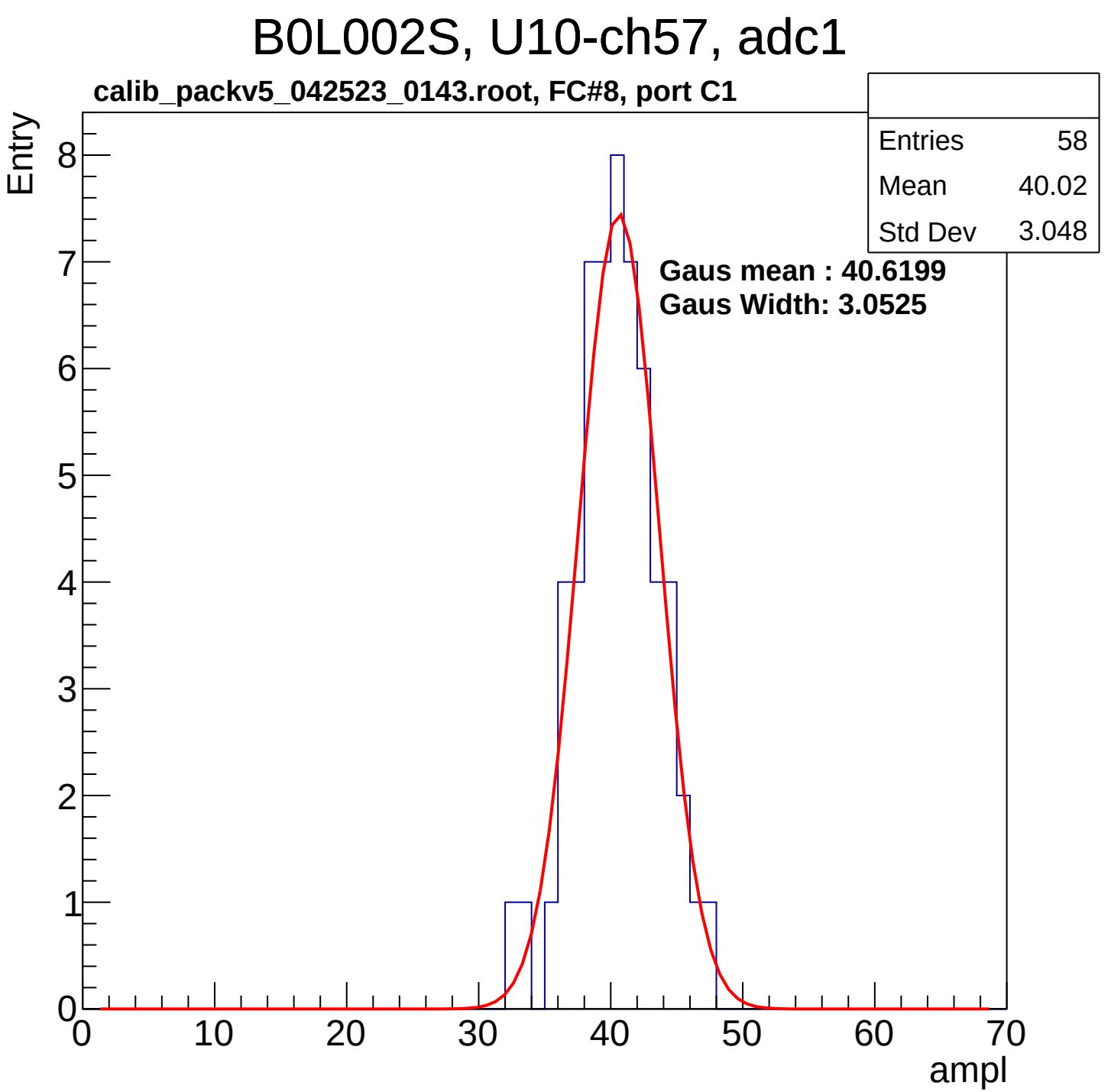

B0L002S, U10-ch56, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.19
Std Dev 3.069
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

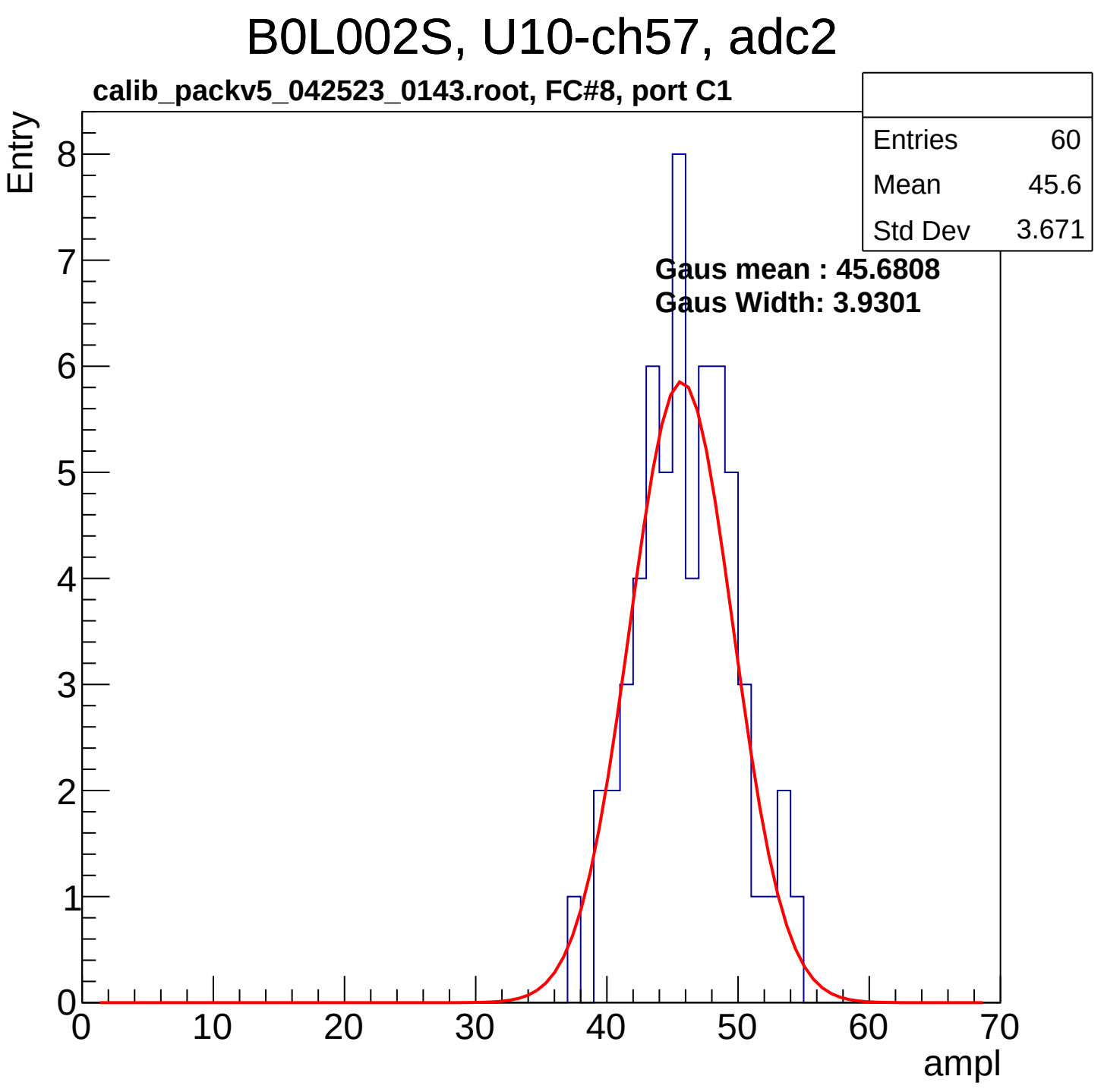

B0L002S, U10-ch56, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 58.97
Std Dev 9.996
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

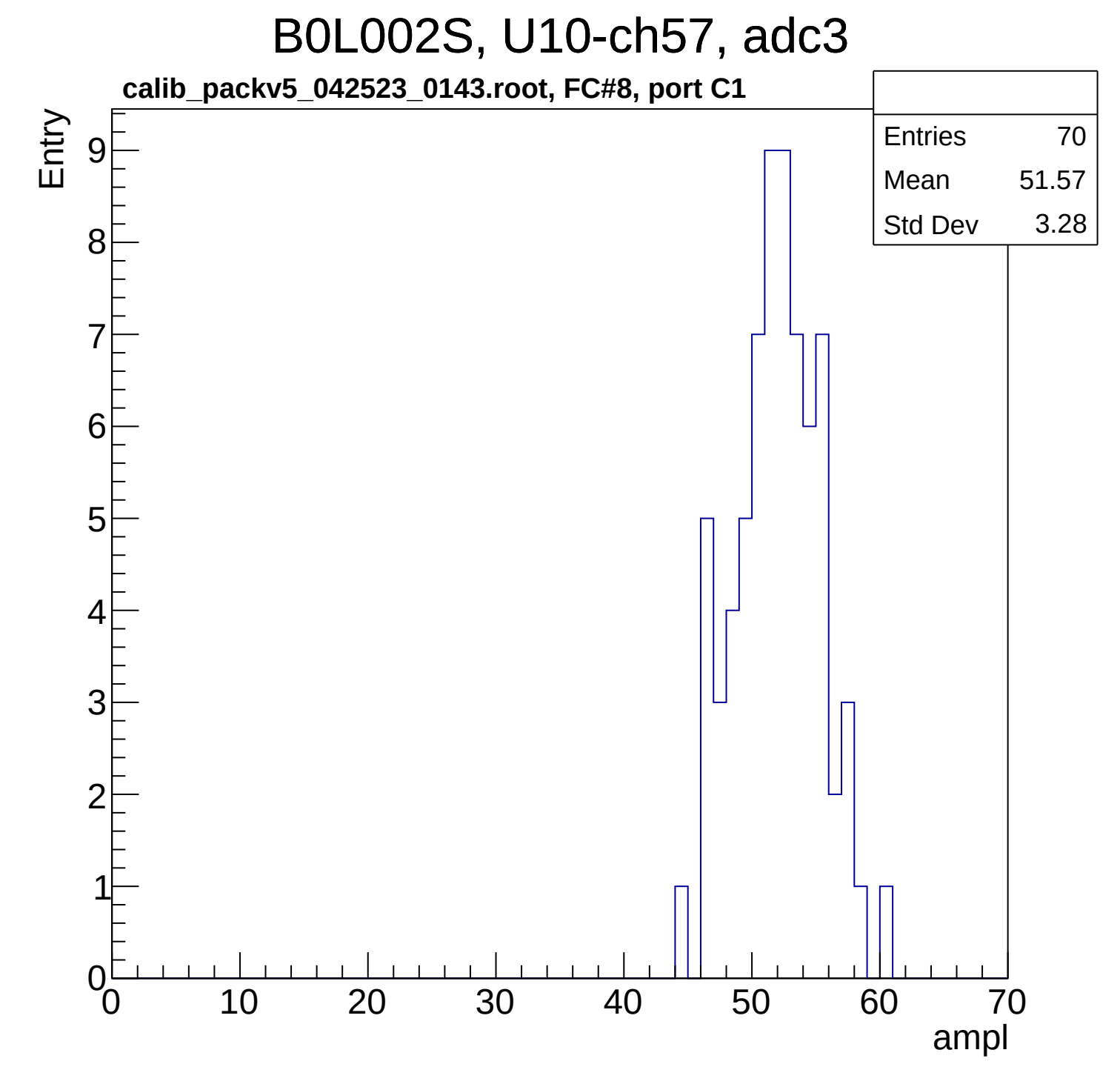

B0L002S, U10-ch56, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 6
Mean 61.67
Std Dev 1.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

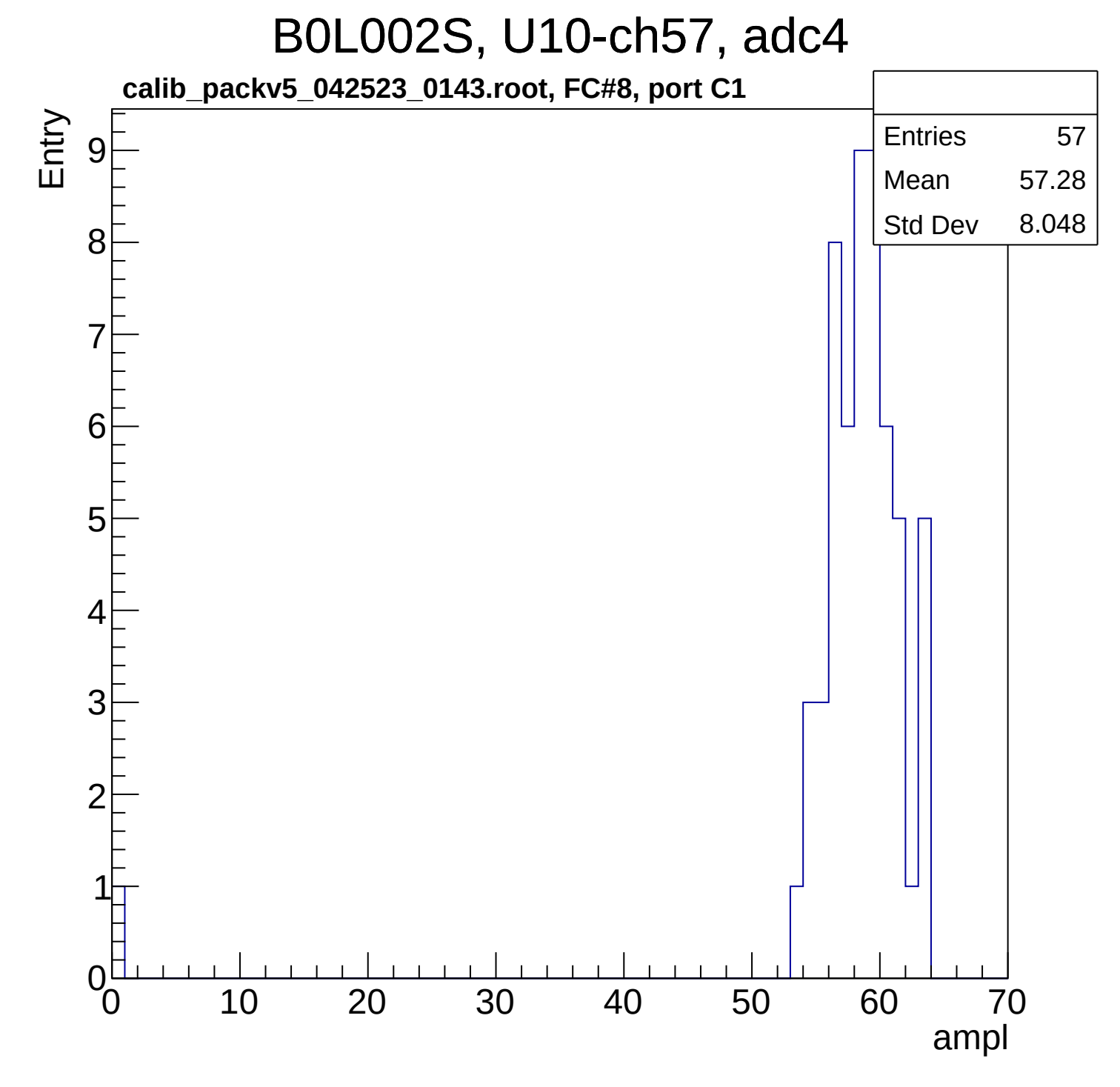

B0L002S, U10-ch56, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

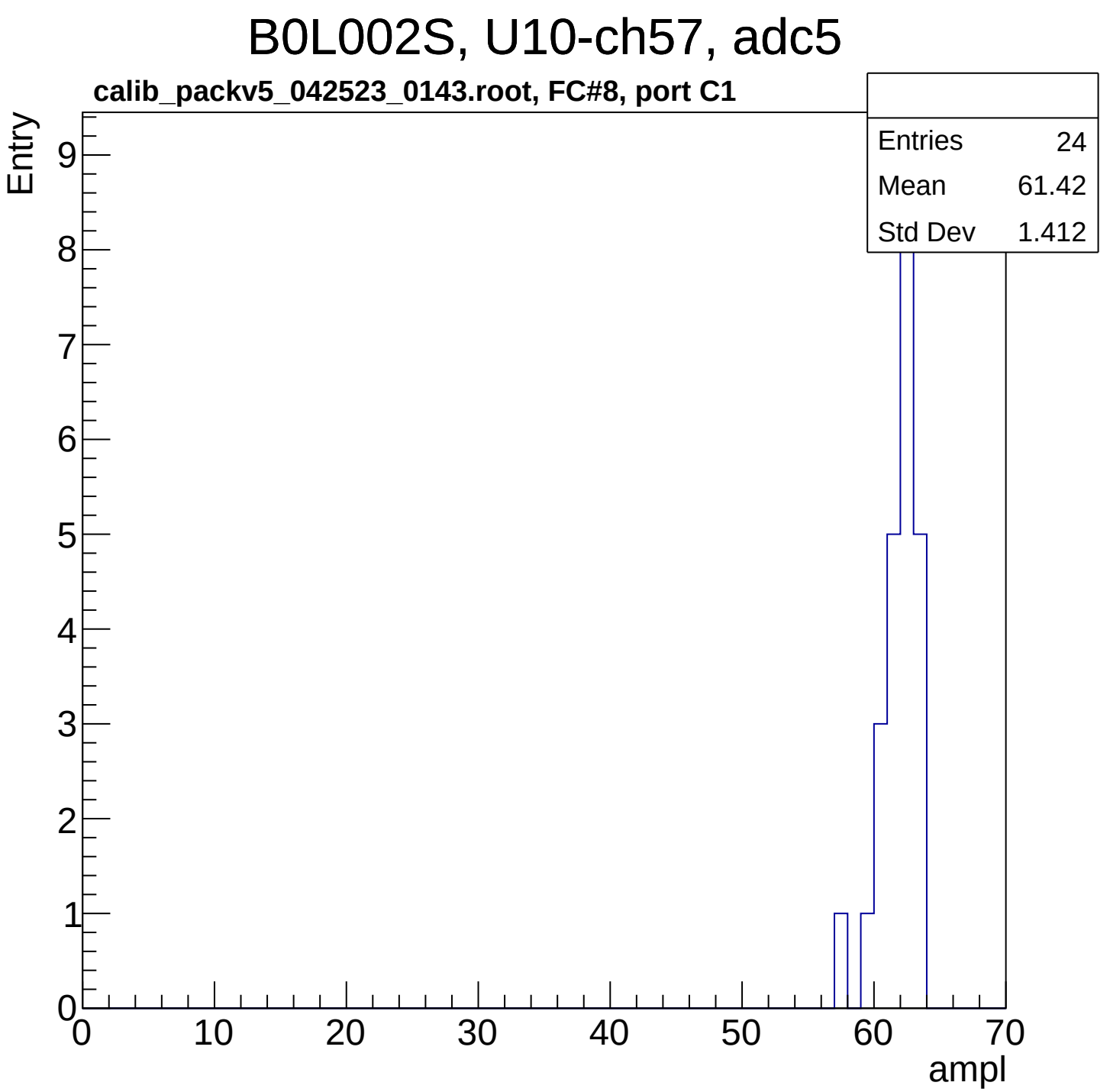

B0L002S, U10-ch57, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 32.4
Std Dev 3.26
Gaus mean : 33.1138
Gaus Width: 3.4568
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

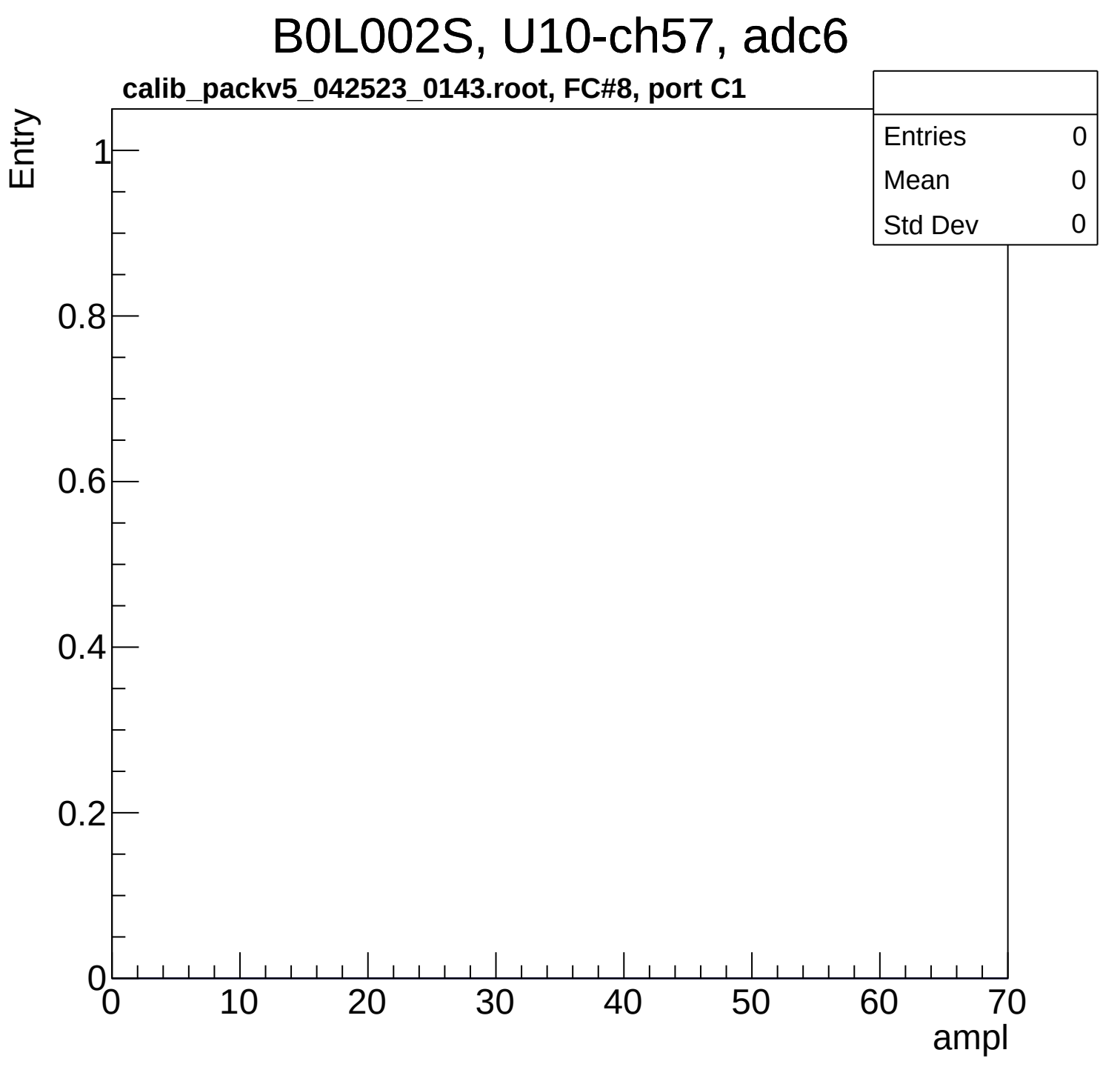

B0L002S, U10-ch57, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 40.02
Std Dev 3.048
Gaus mean : 40.6199
Gaus Width: 3.0525
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

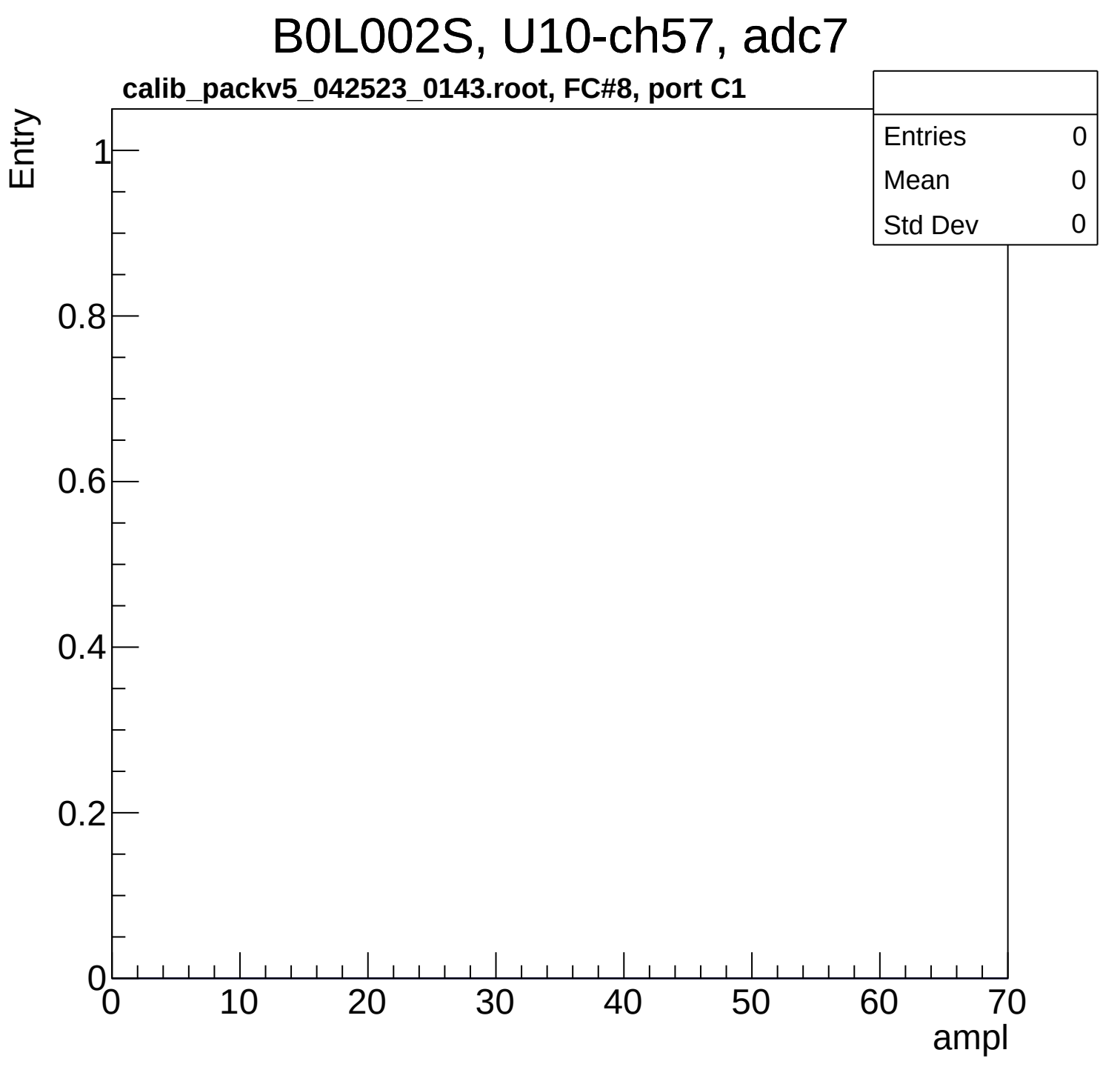

B0L002S, U10-ch57, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.6
Std Dev 3.671
Gaus mean : 45.6808
Gaus Width: 3.9301
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

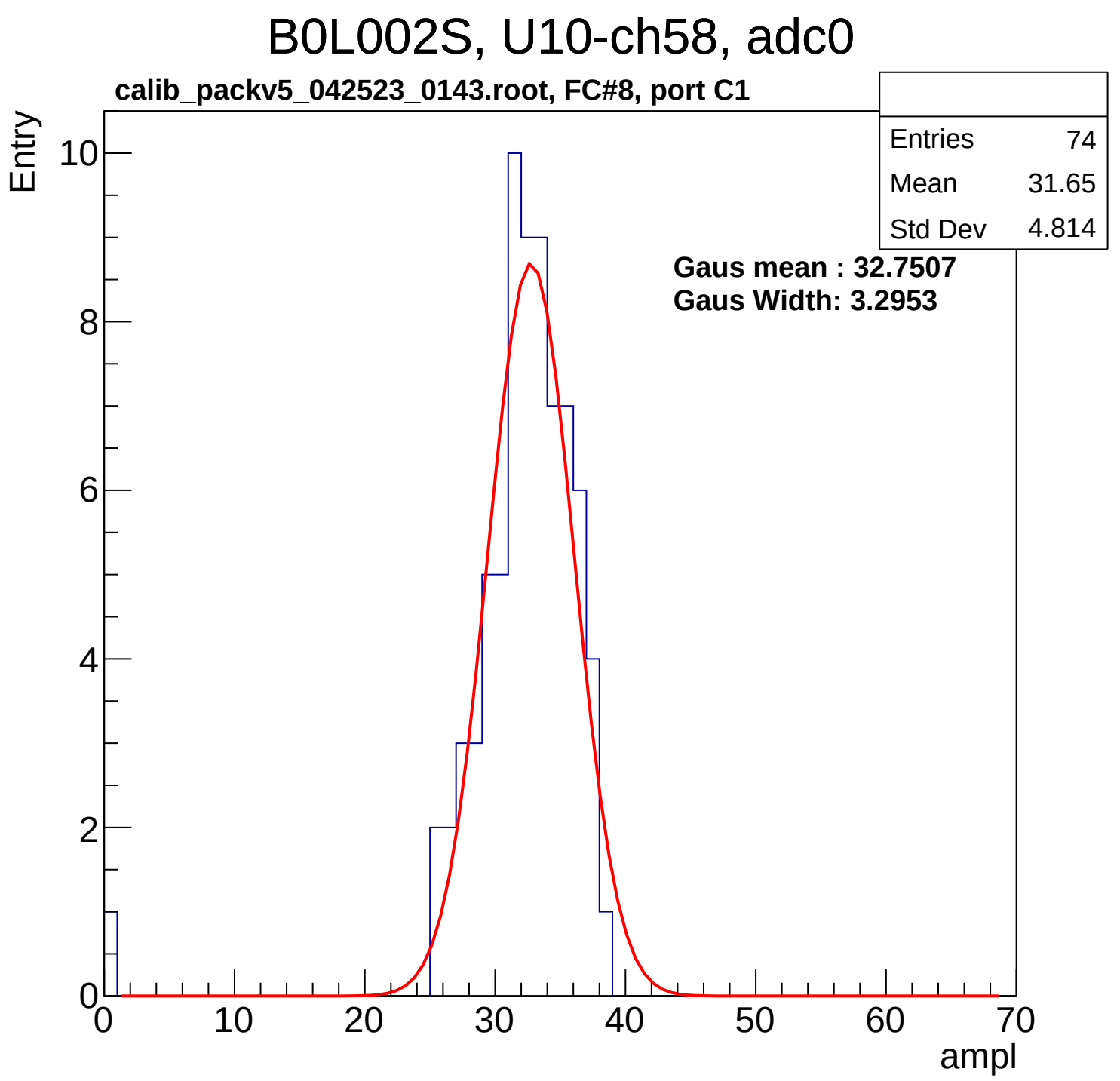

B0L002S, U10-ch57, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 51.57
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

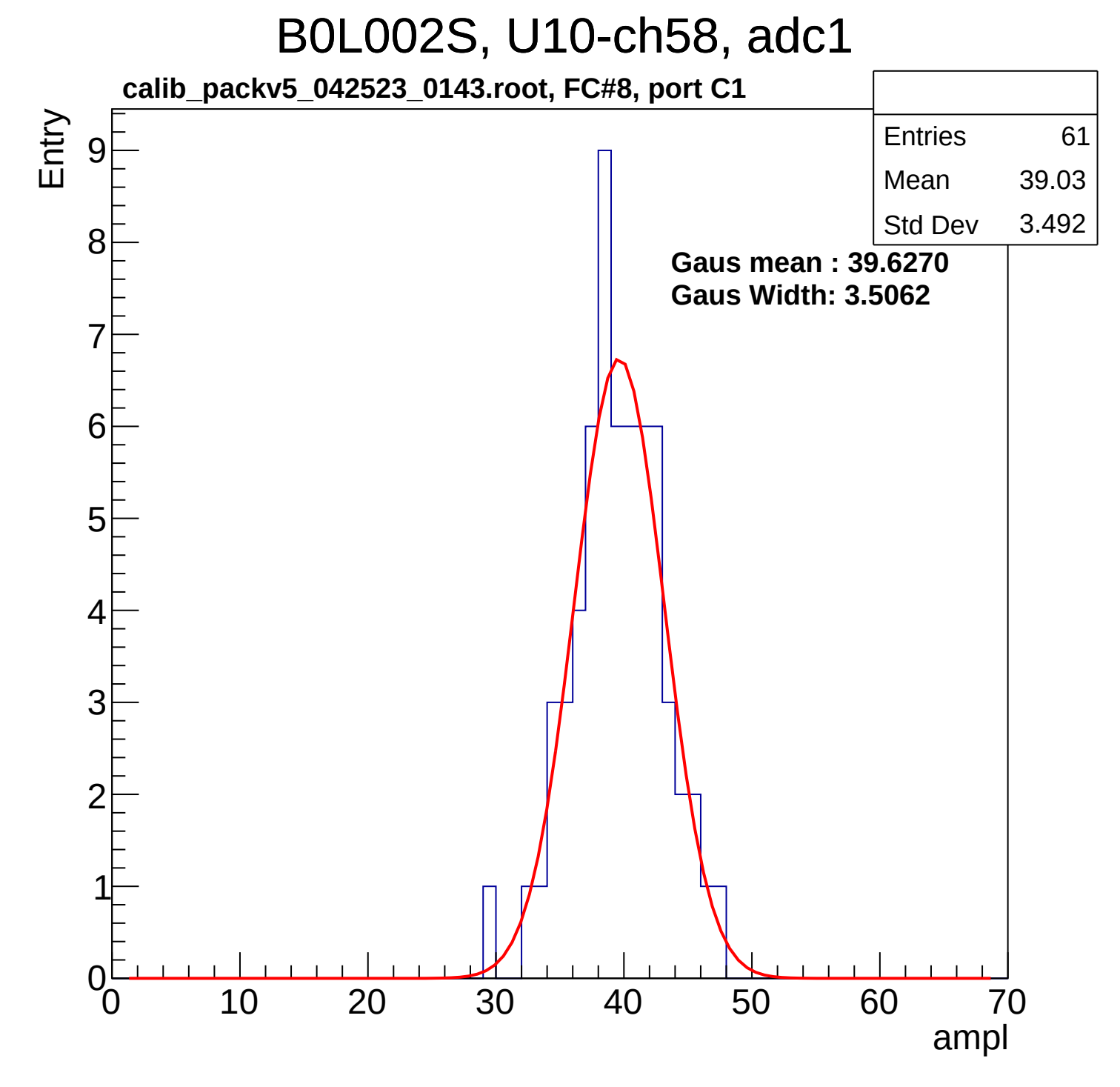

B0L002S, U10-ch57, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.28
Std Dev 8.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

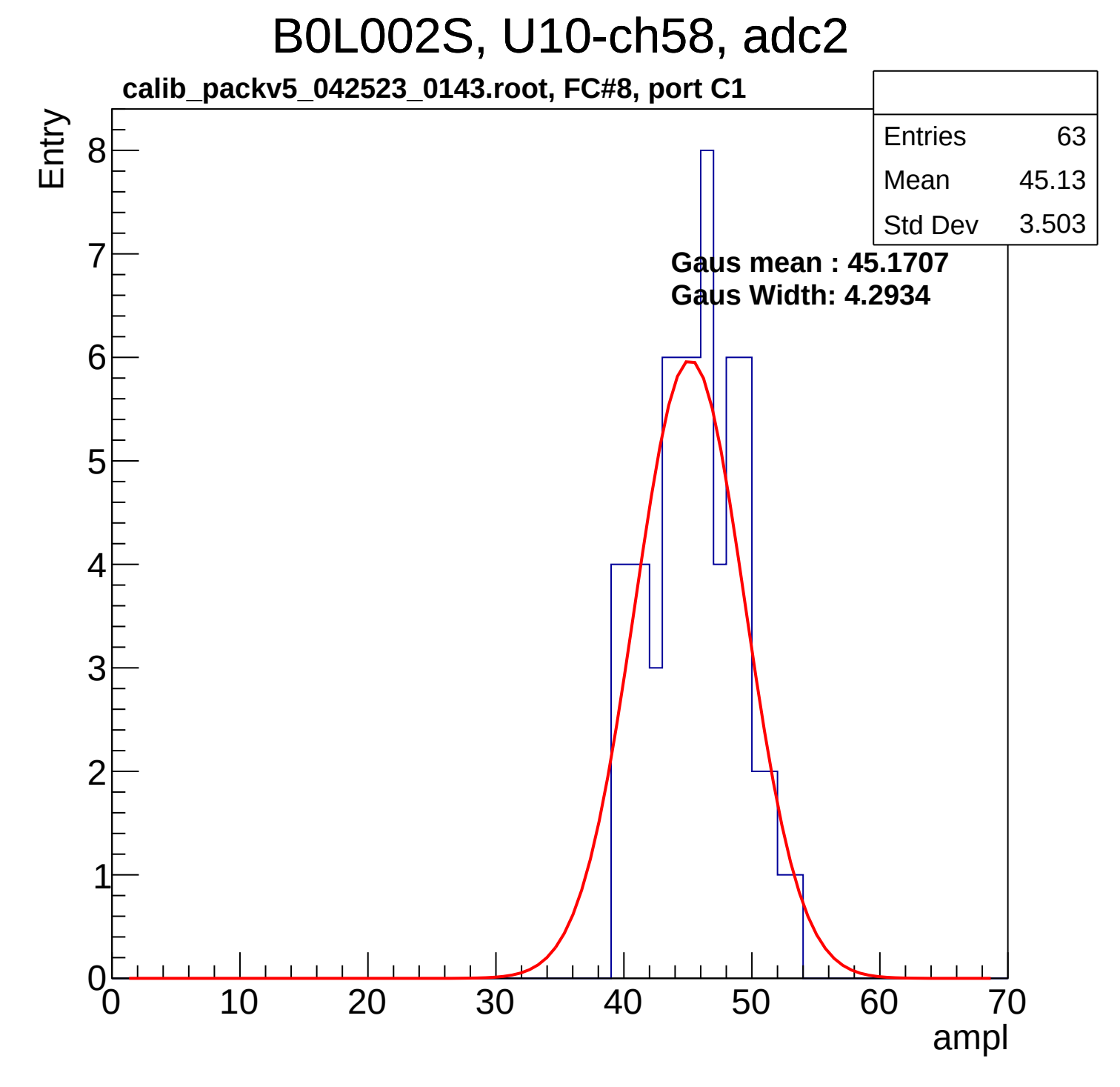

B0L002S, U10-ch57, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.42
Std Dev 1.412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

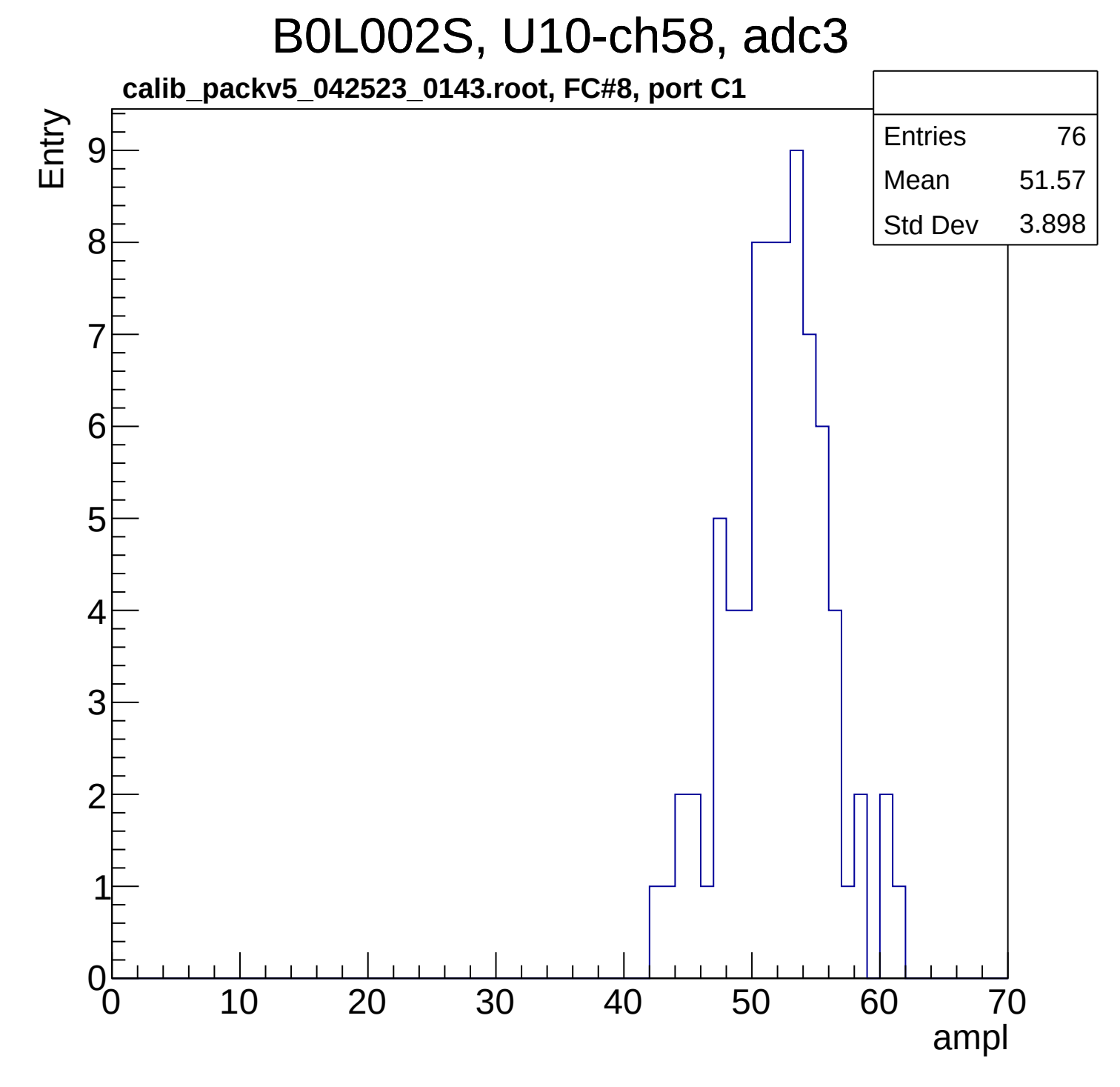

B0L002S, U10-ch57, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

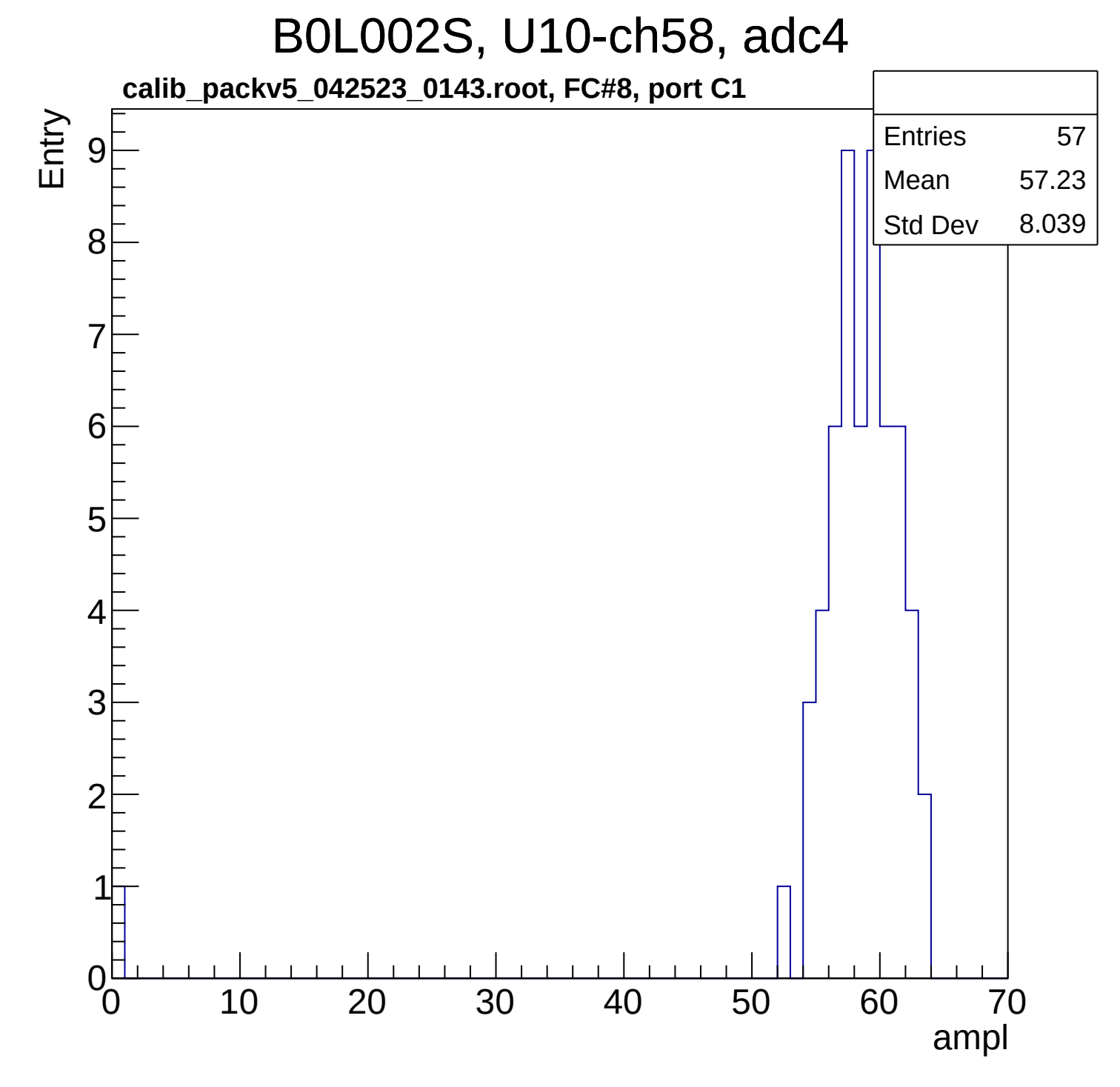

B0L002S, U10-ch57, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

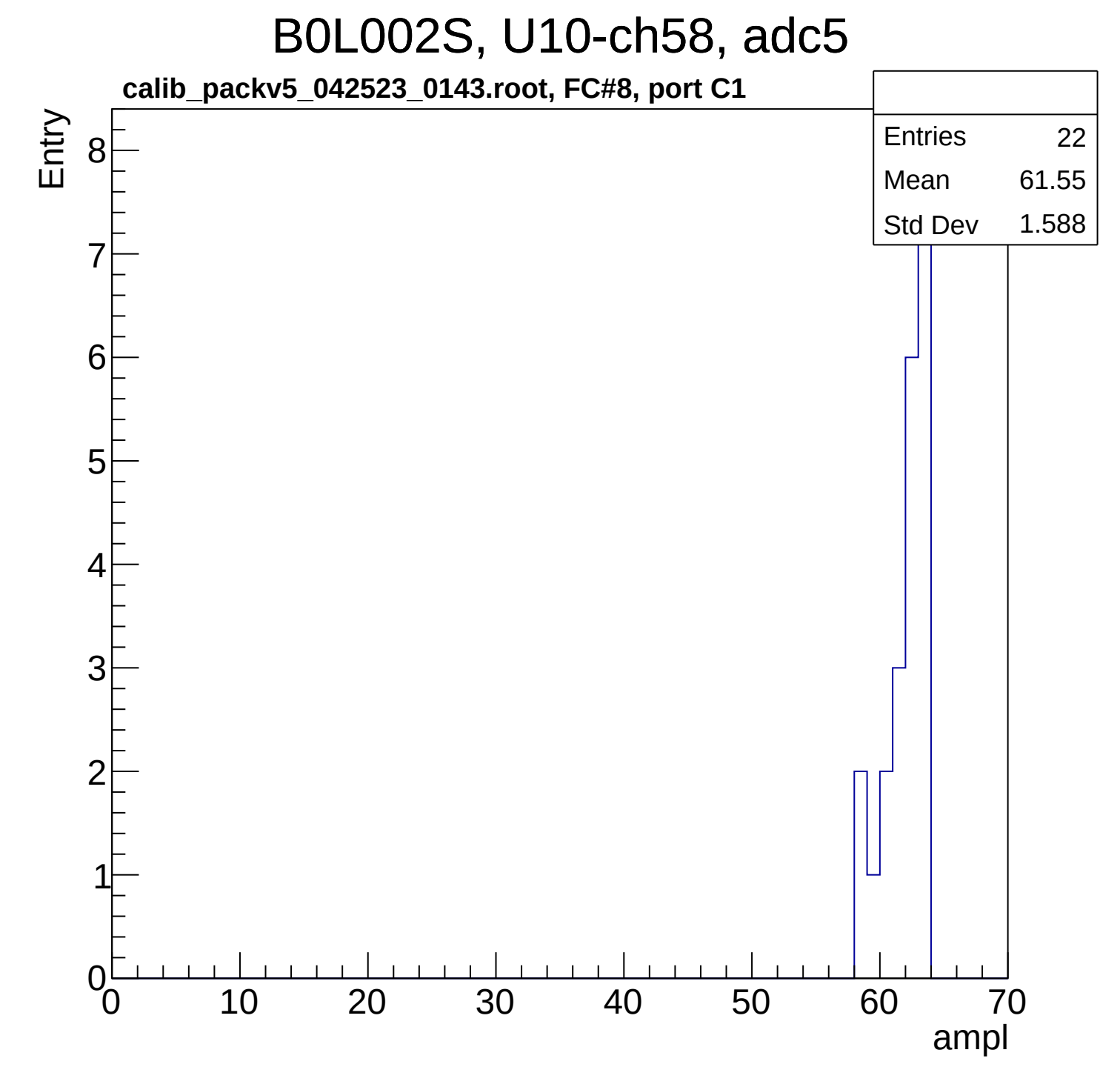

B0L002S, U10-ch58, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.65
Std Dev 4.814
Gaus mean : 32.7507
Gaus Width: 3.2953
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

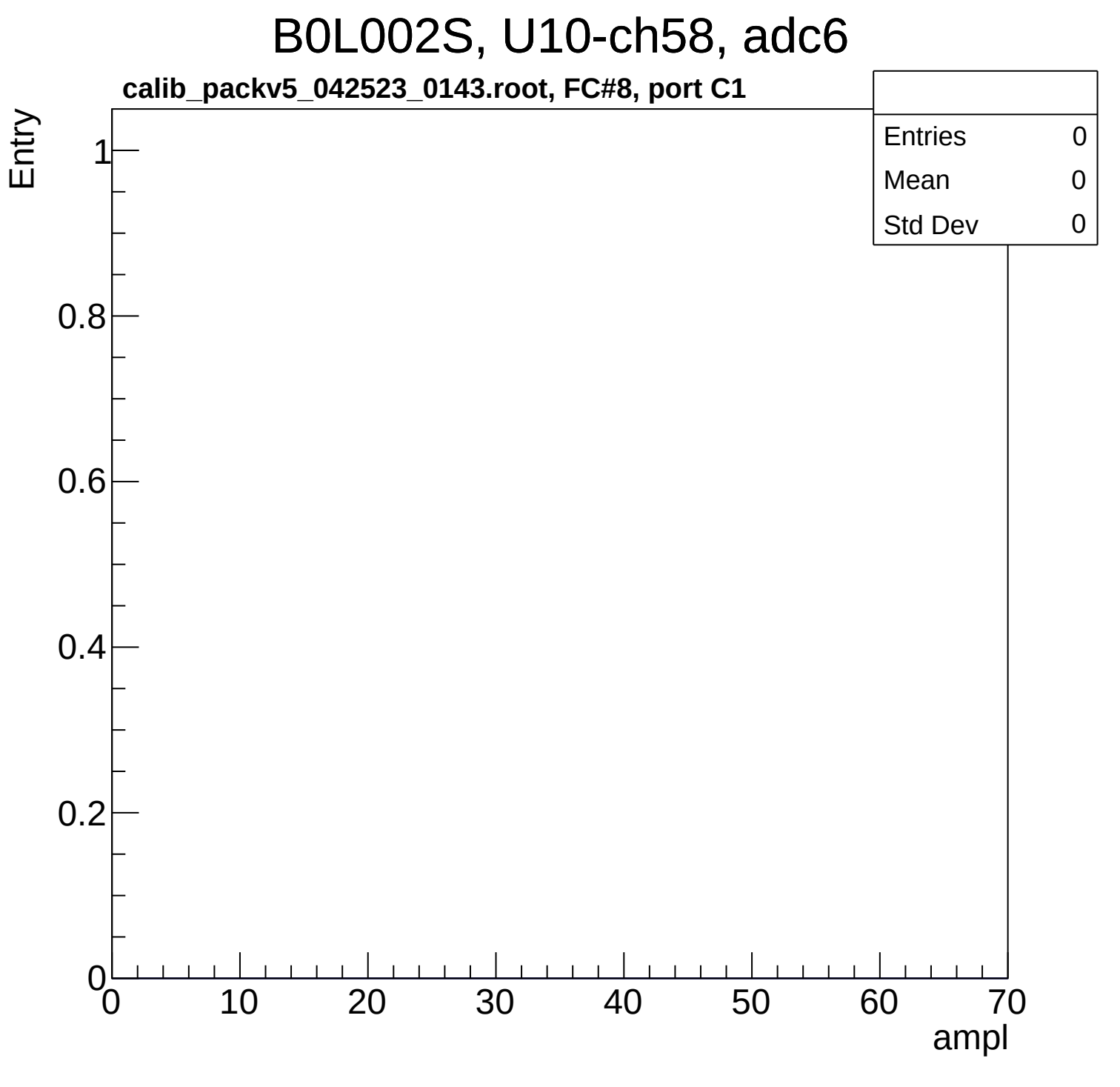

B0L002S, U10-ch58, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 39.03
Std Dev 3.492
Gaus mean : 39.6270
Gaus Width: 3.5062
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

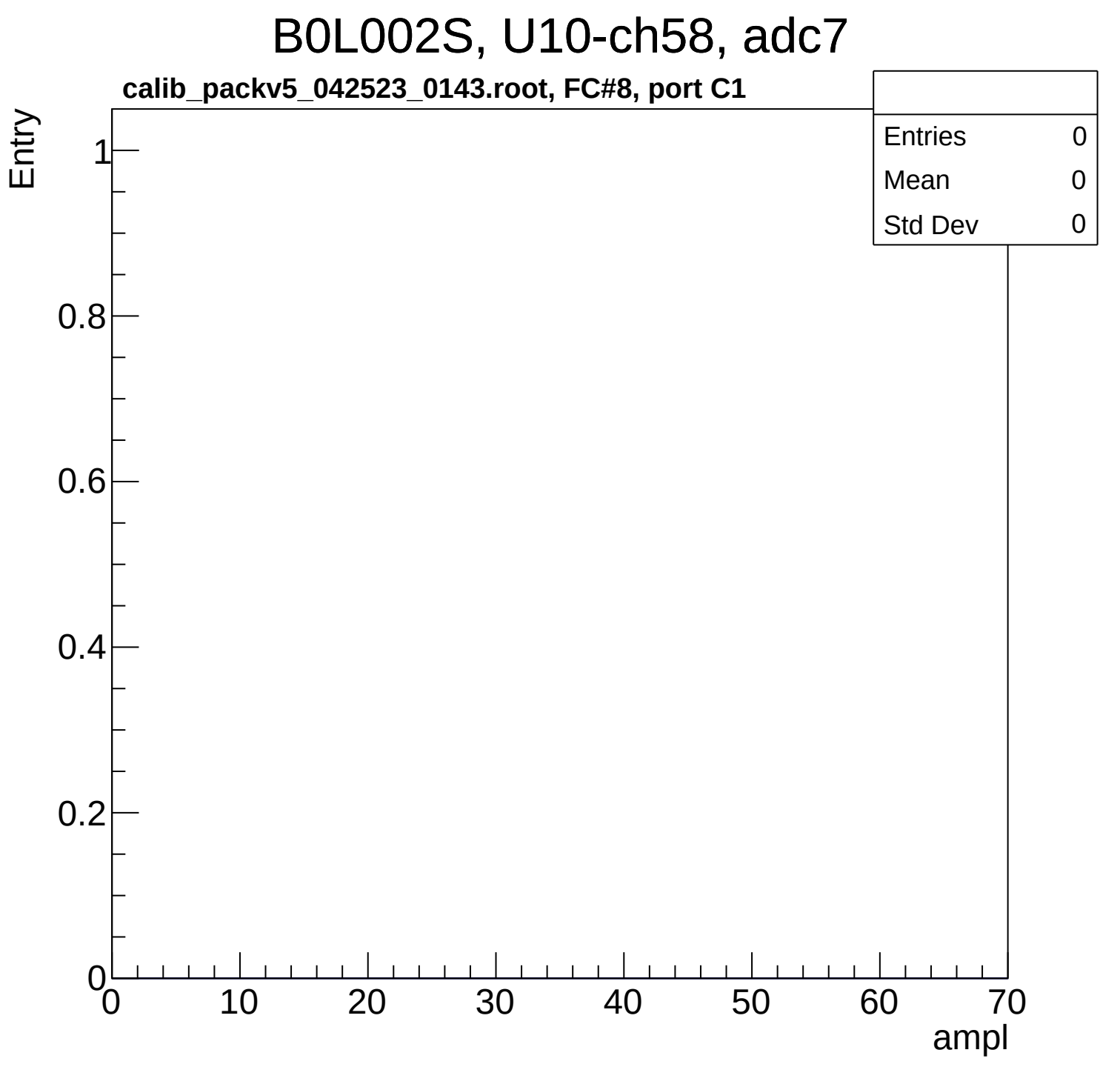

B0L002S, U10-ch58, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 45.13
Std Dev 3.503
Gaus mean : 45.1707
Gaus Width: 4.2934
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

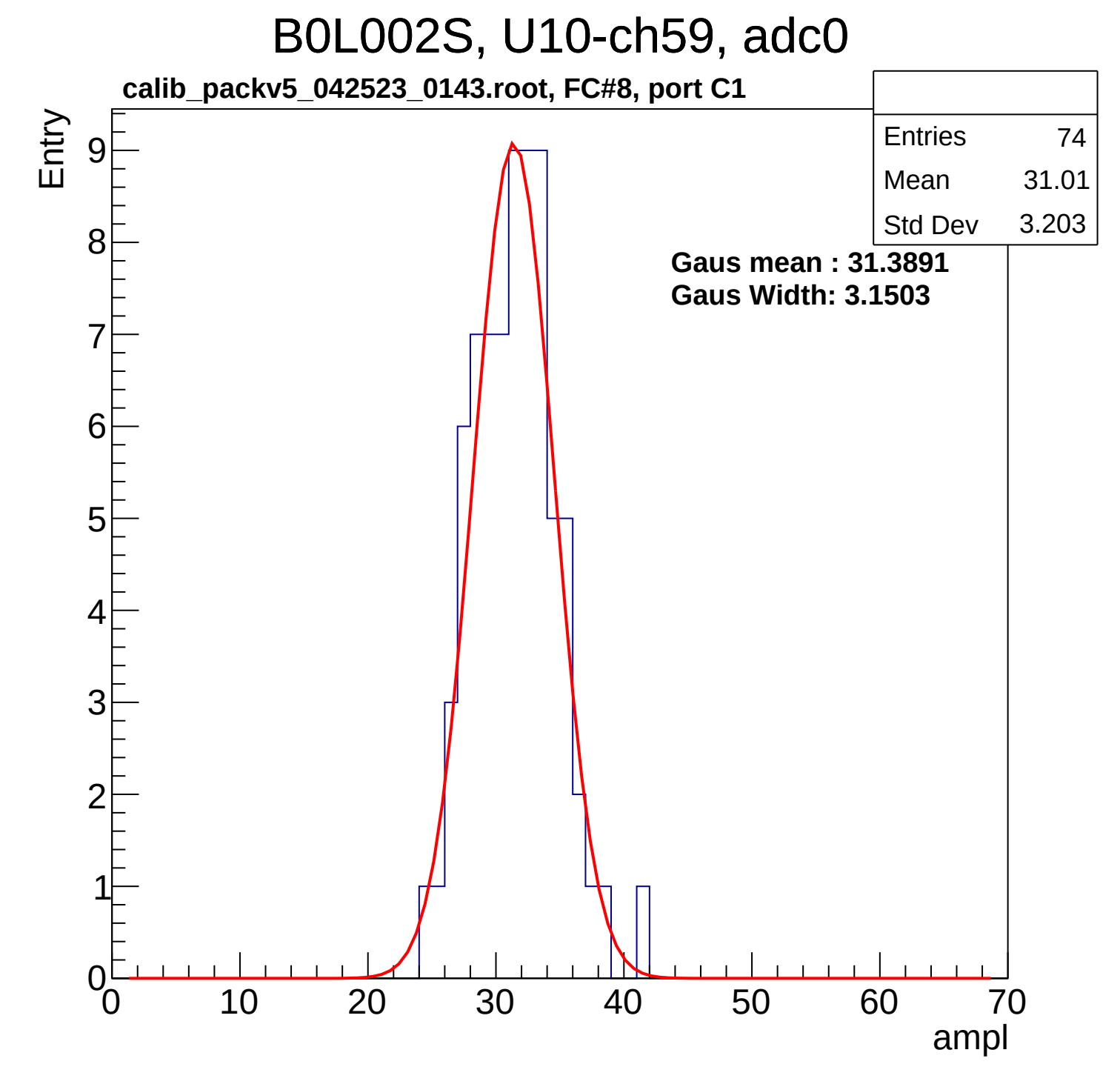

B0L002S, U10-ch58, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.57
Std Dev 3.898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

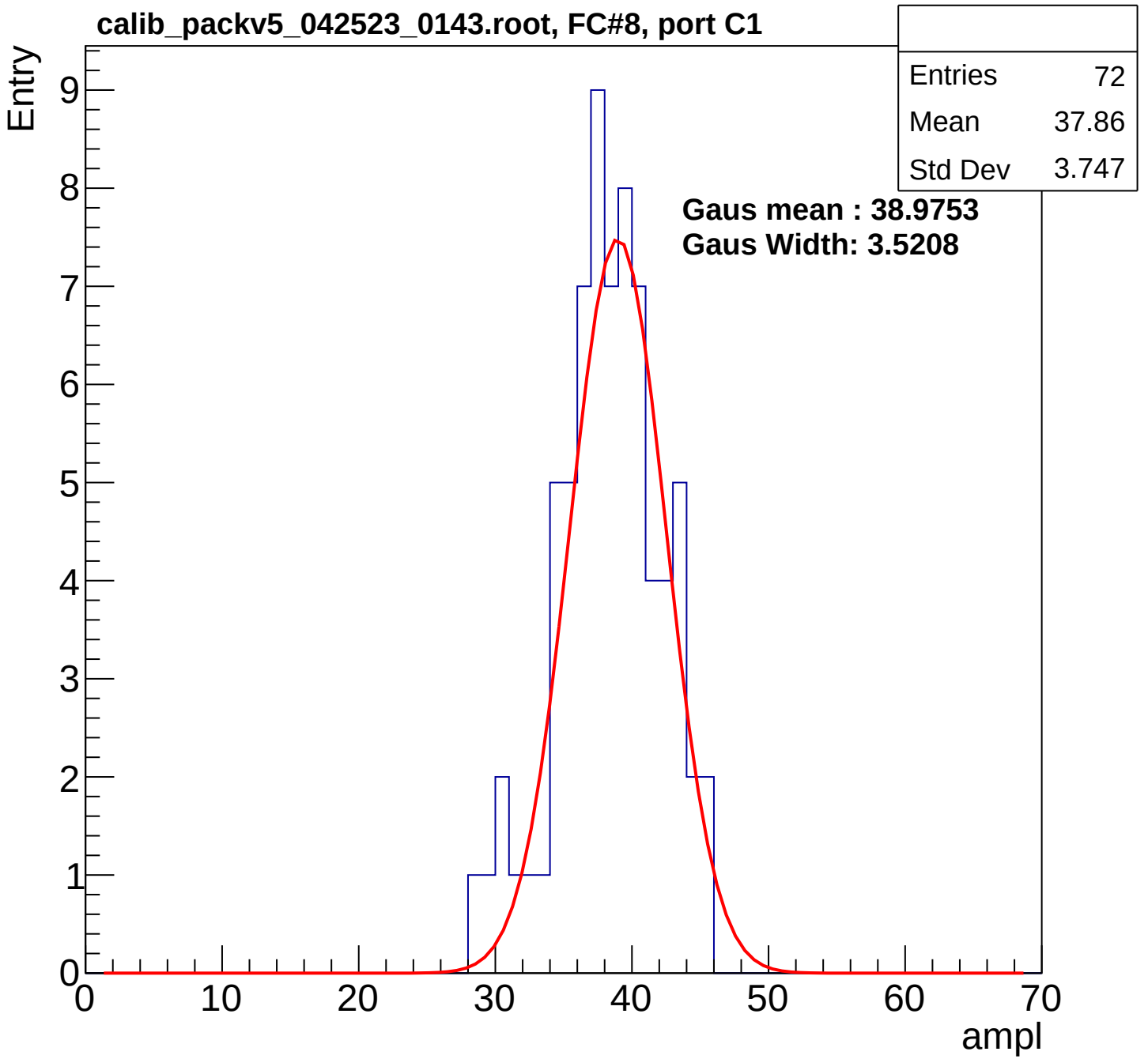

B0L002S, U10-ch58, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.23
Std Dev 8.039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

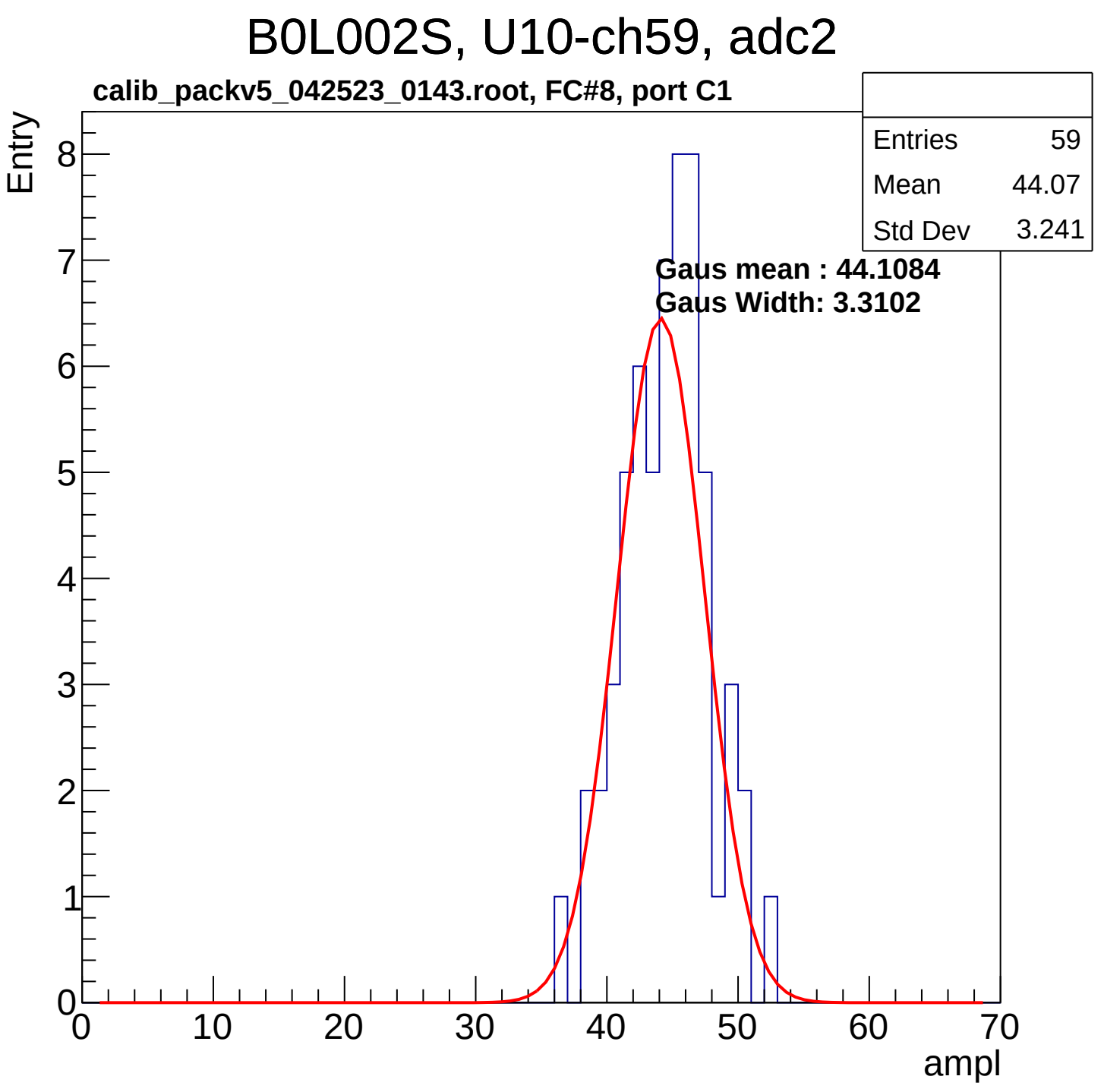

B0L002S, U10-ch58, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 22
Mean 61.55
Std Dev 1.588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

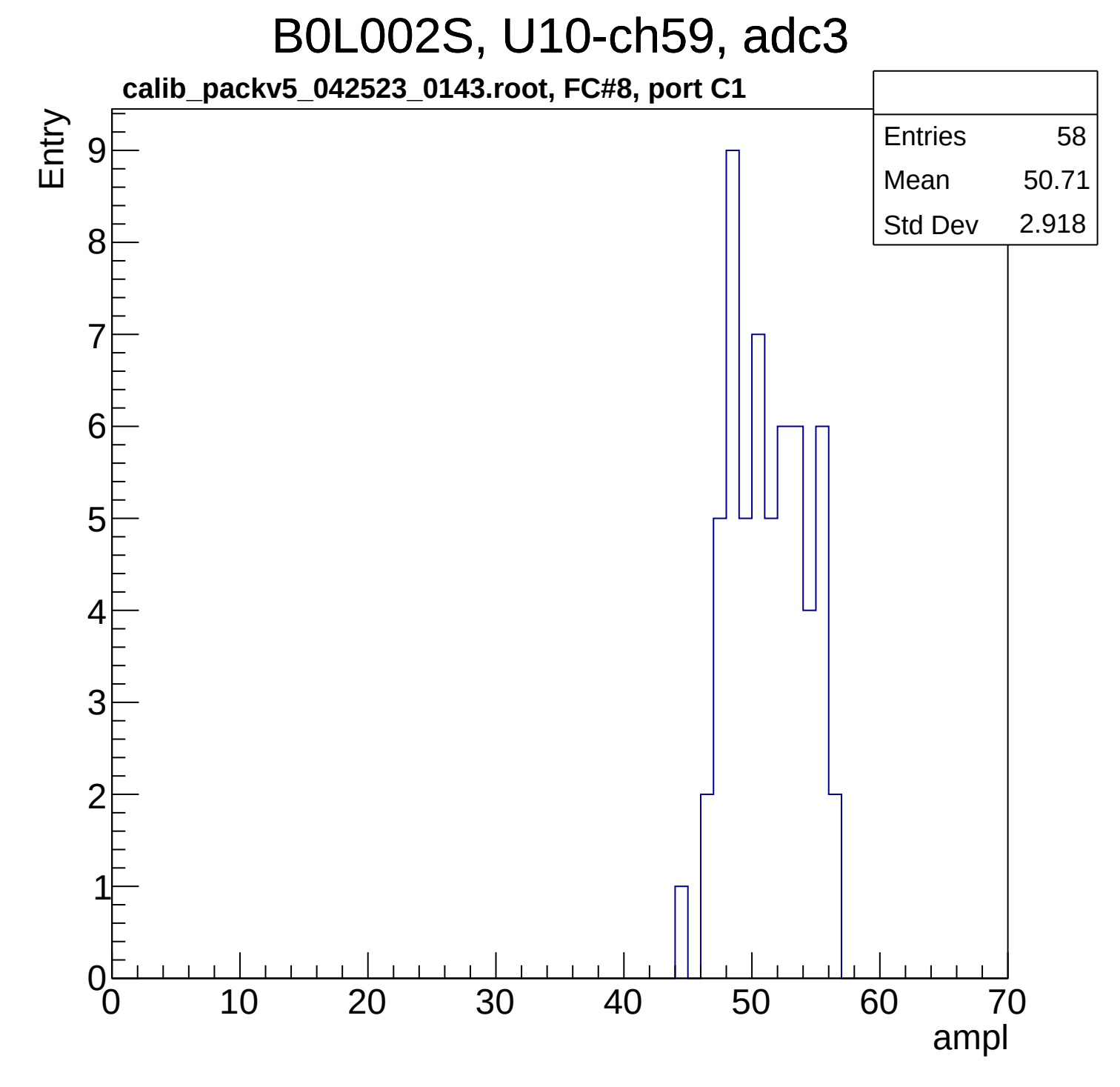

B0L002S, U10-ch58, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

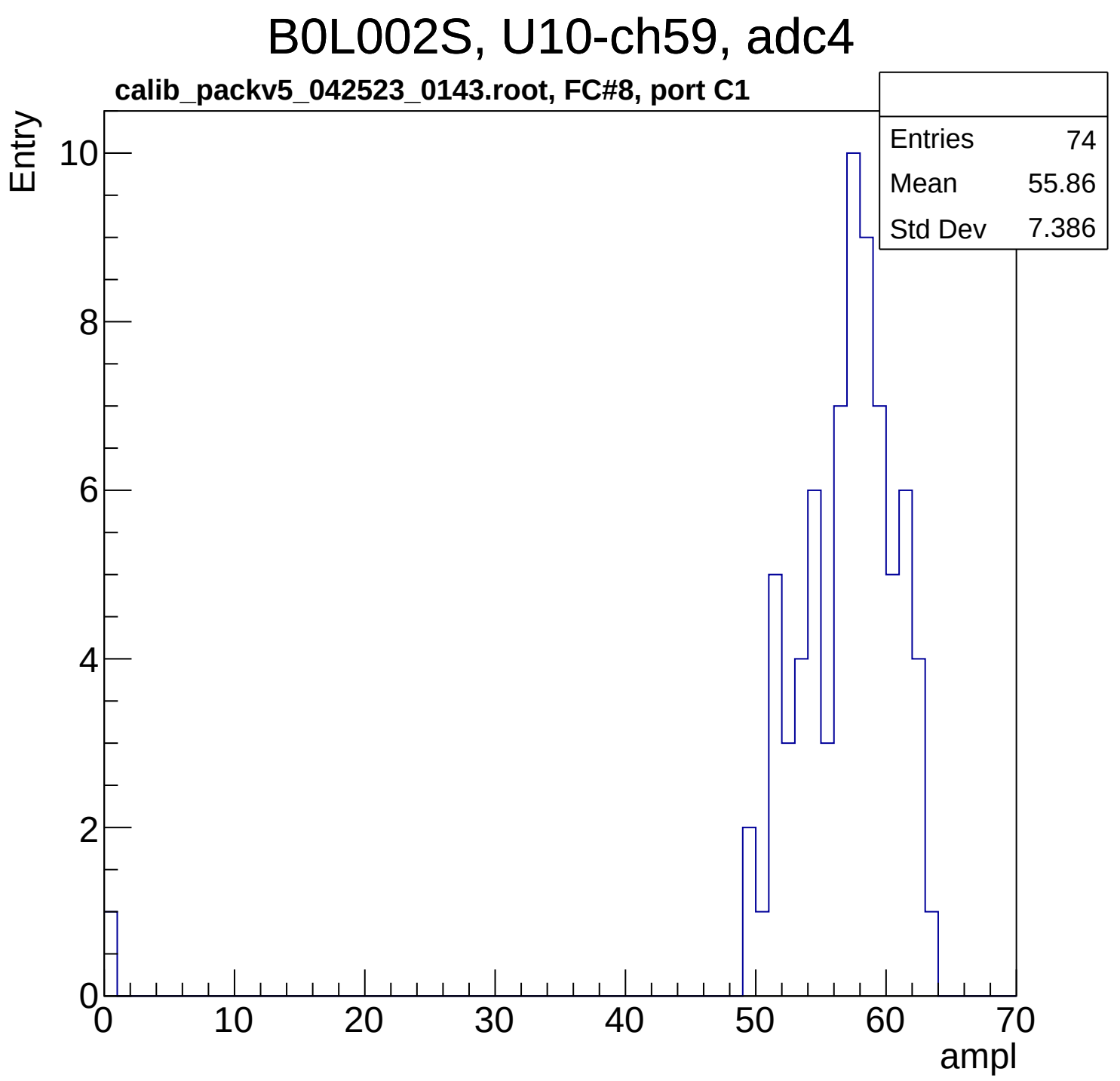

B0L002S, U10-ch58, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

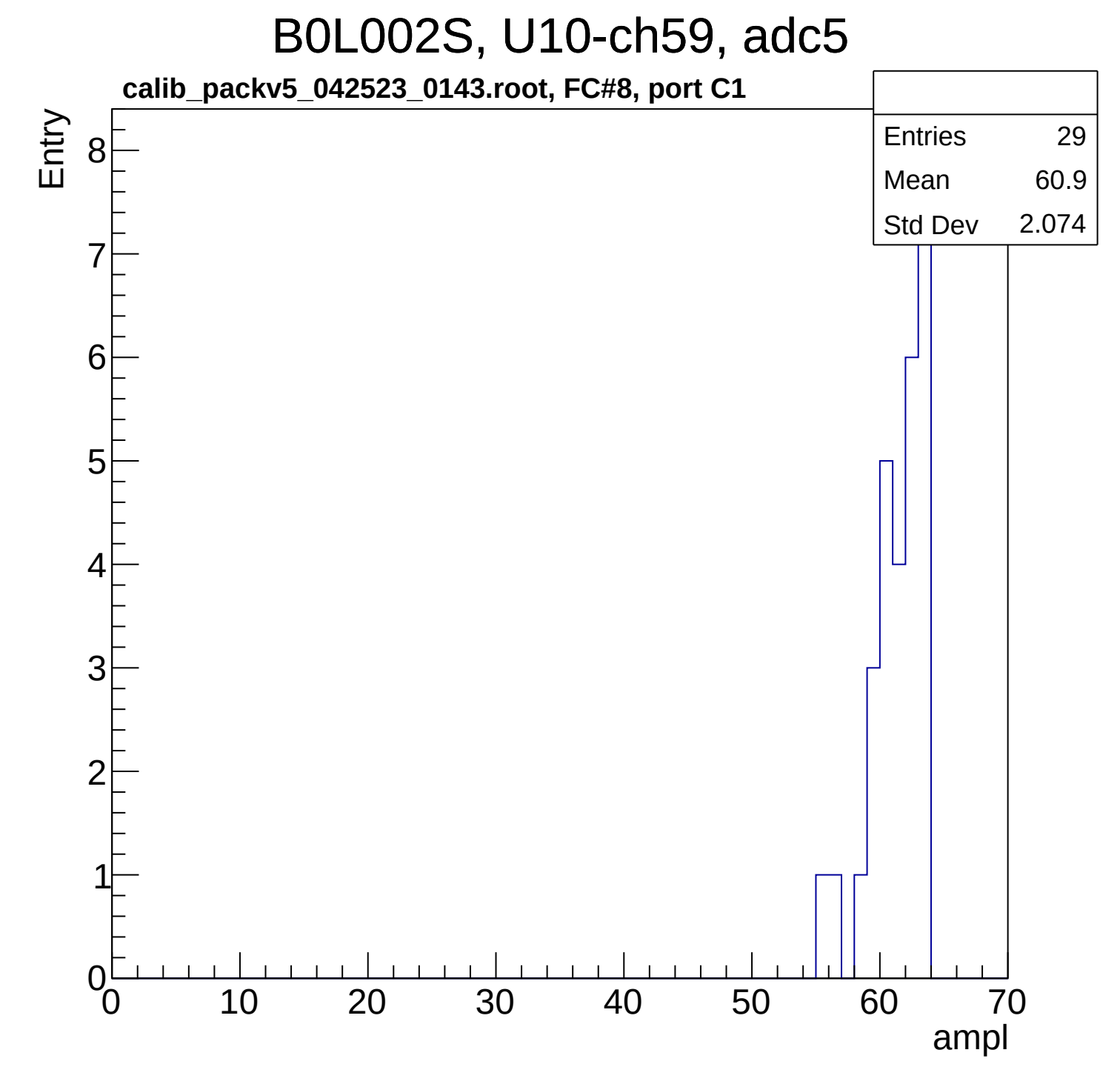

B0L002S, U10-ch59, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.01
Std Dev 3.203
Gaus mean : 31.3891
Gaus Width: 3.1503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

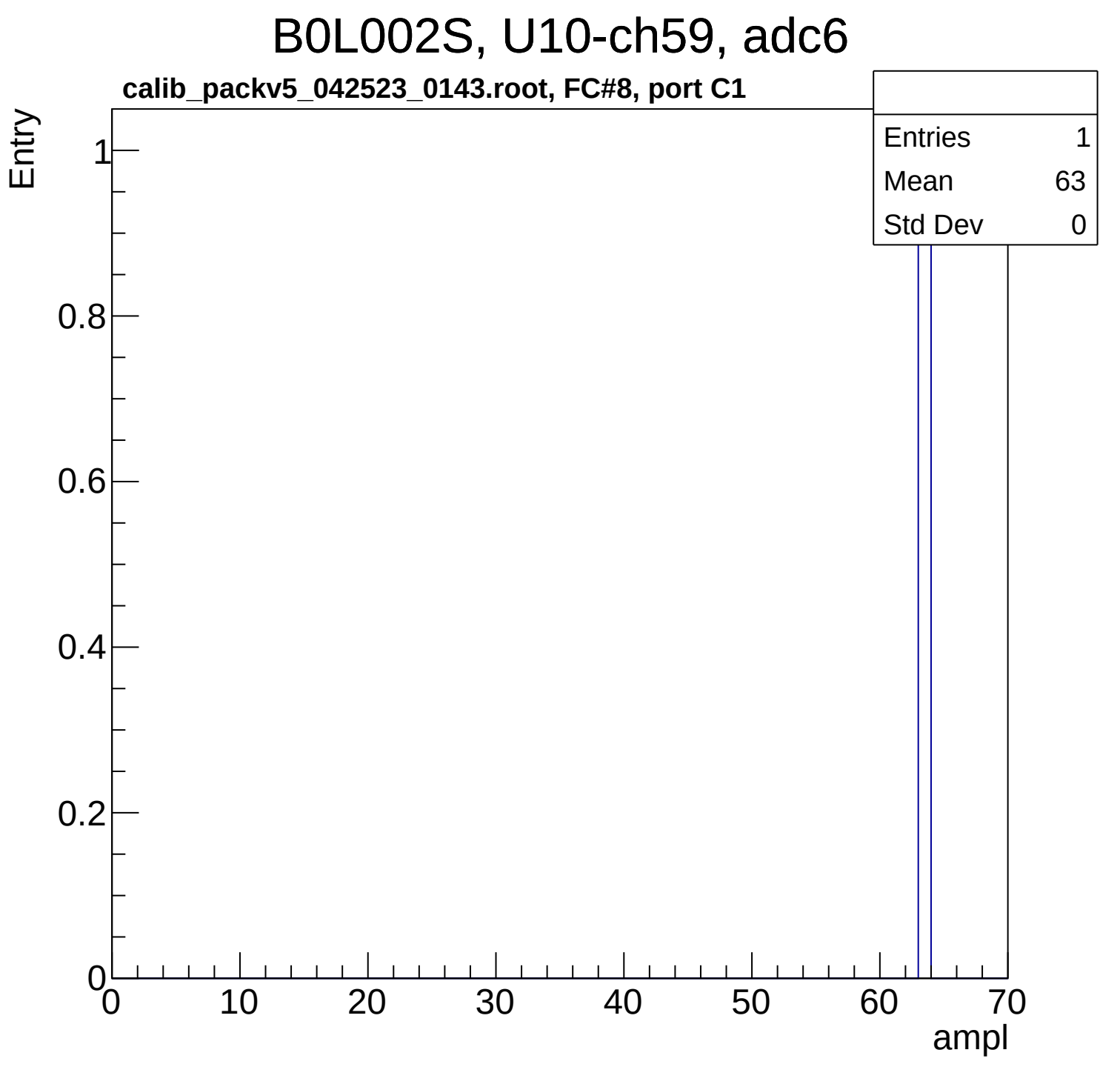

B0L002S, U10-ch59, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 37.86
Std Dev 3.747
Gaus mean : 38.9753
Gaus Width: 3.5208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

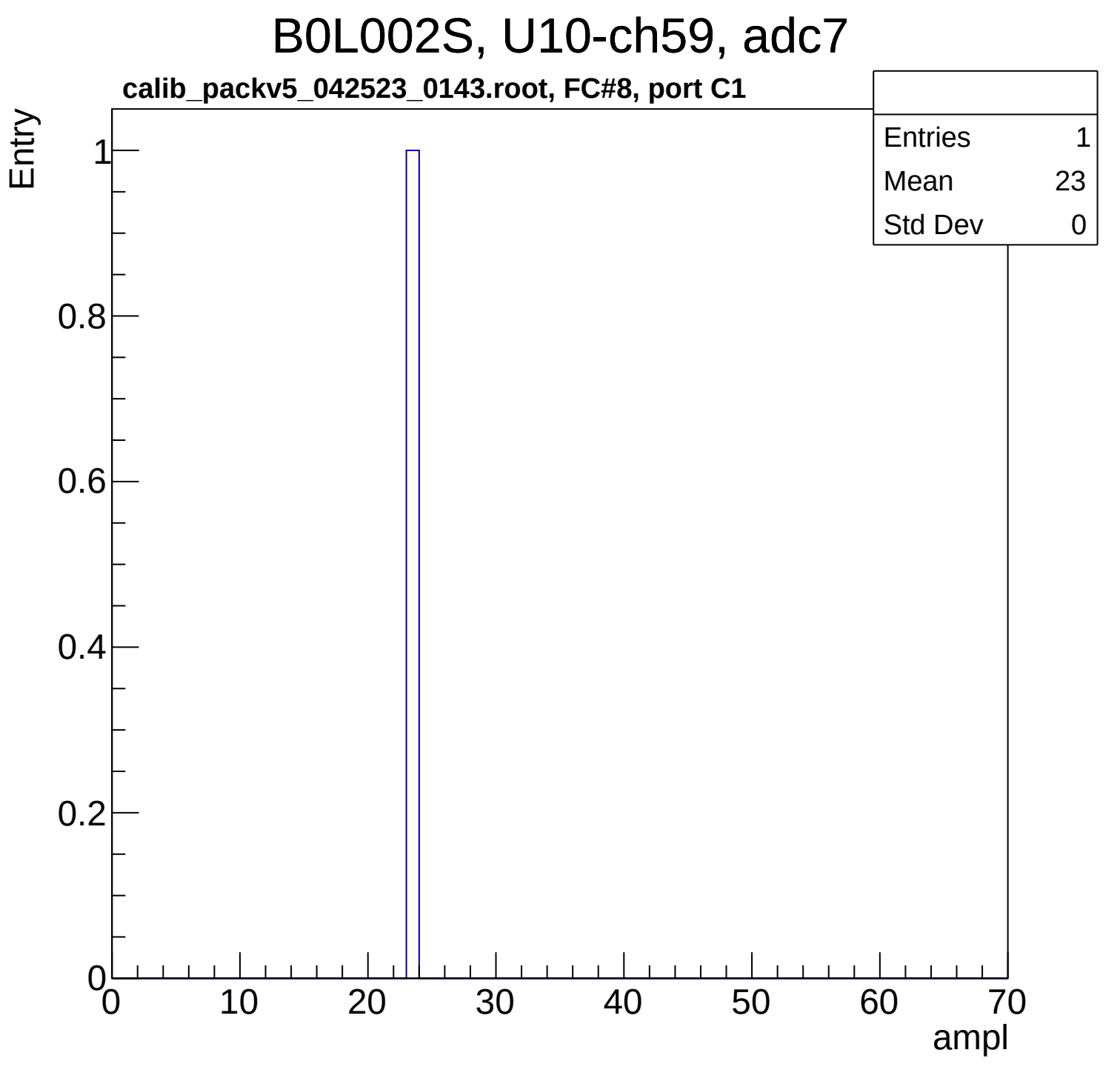

B0L002S, U10-ch59, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 44.07
Std Dev 3.241
Gaus mean : 44.1084
Gaus Width: 3.3102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

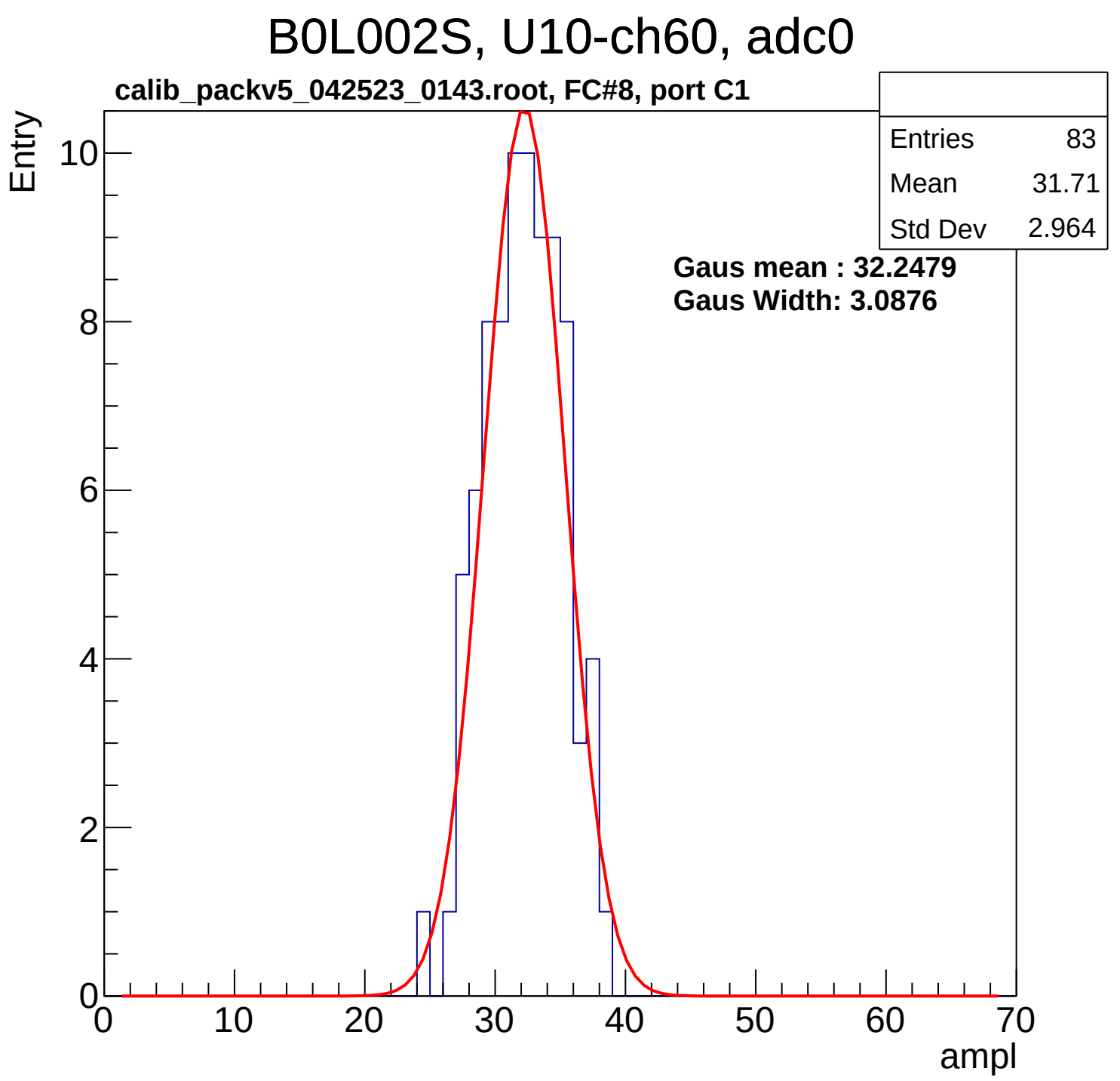

B0L002S, U10-ch59, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 50.71
Std Dev 2.918
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

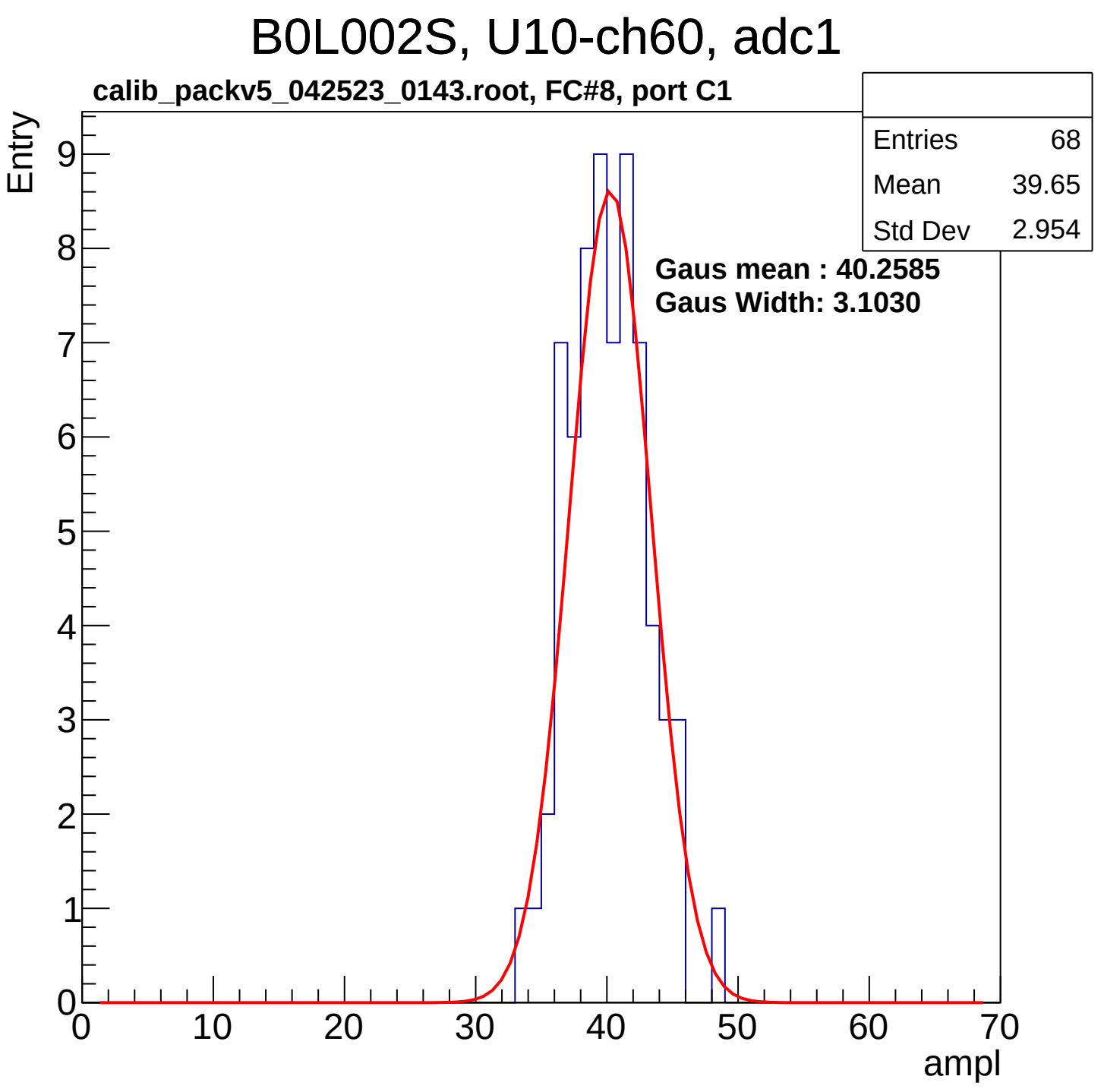

B0L002S, U10-ch59, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 55.86
Std Dev 7.386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

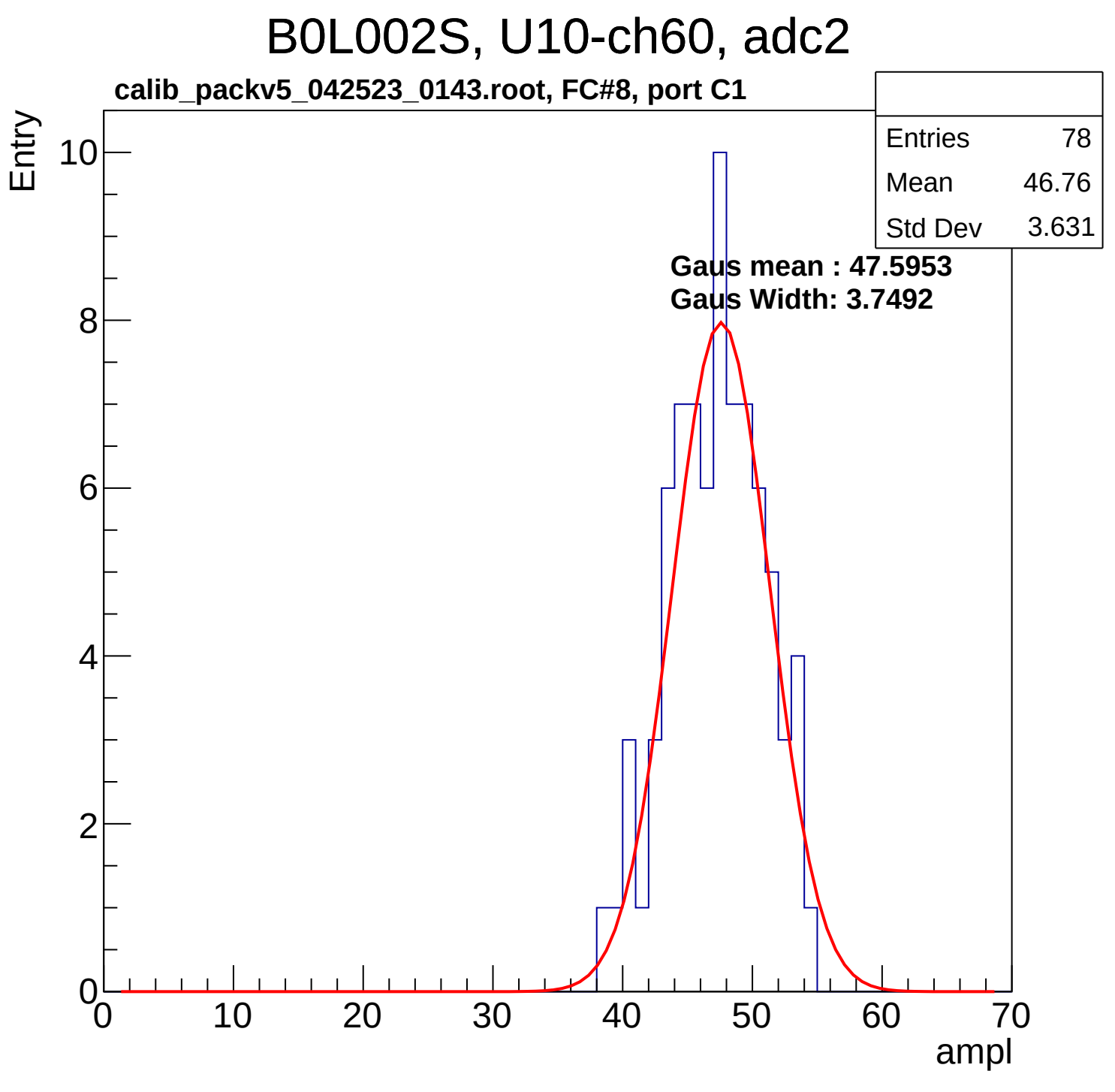

B0L002S, U10-ch59, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 60.9
Std Dev 2.074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

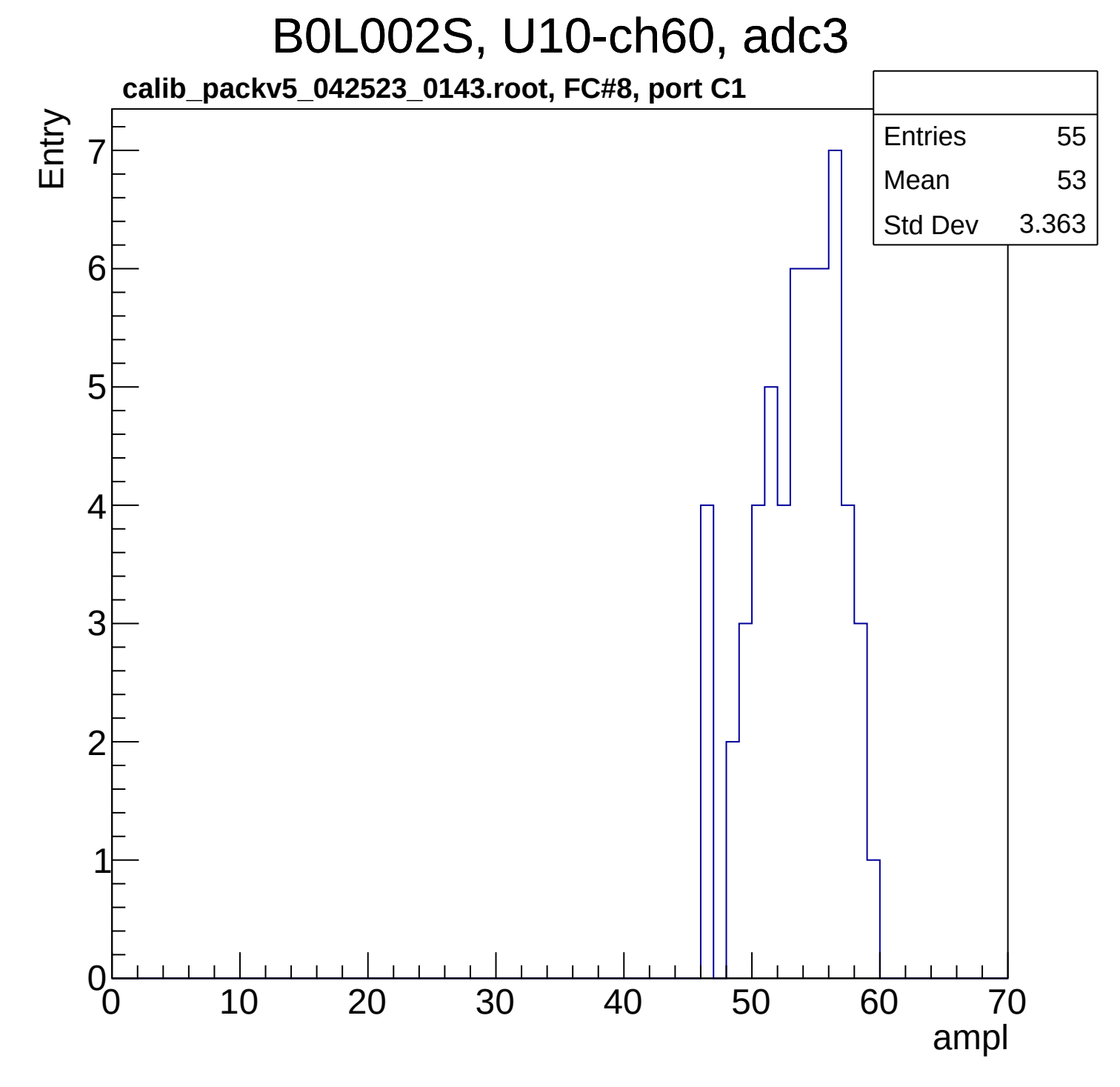

B0L002S, U10-ch59, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

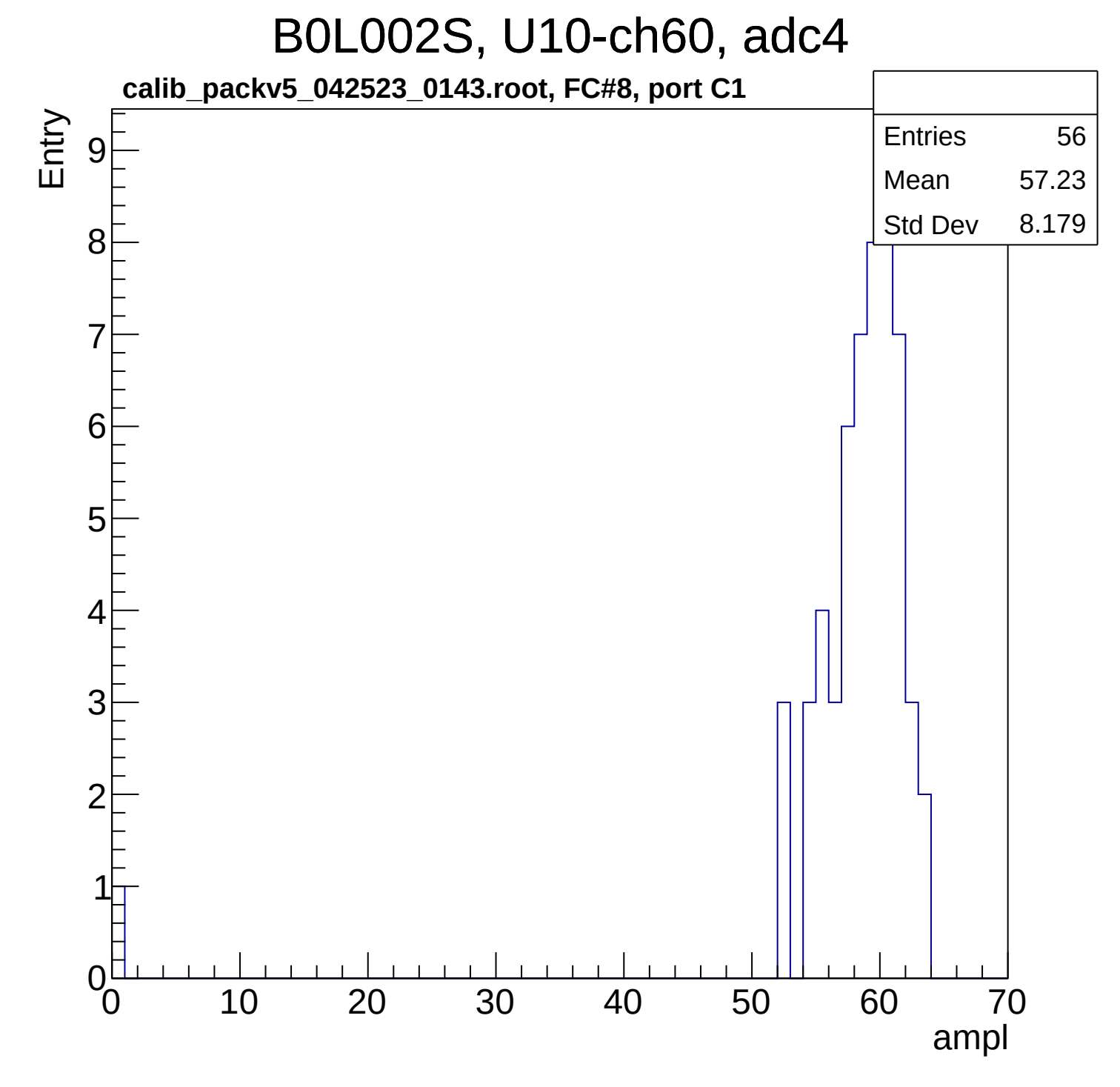

B0L002S, U10-ch59, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

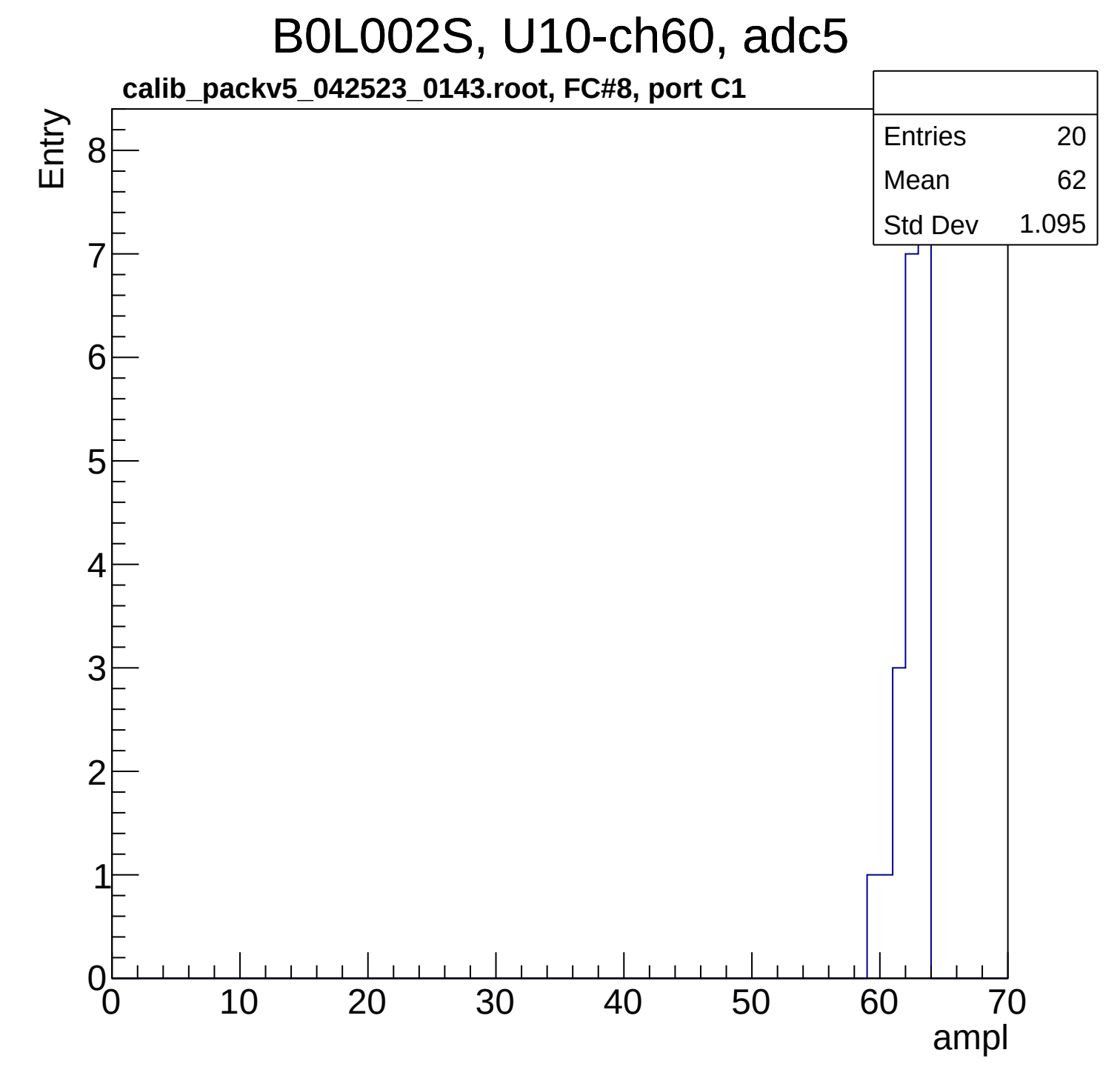

B0L002S, U10-ch60, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 31.71
Std Dev 2.964
Gaus mean : 32.2479
Gaus Width: 3.0876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

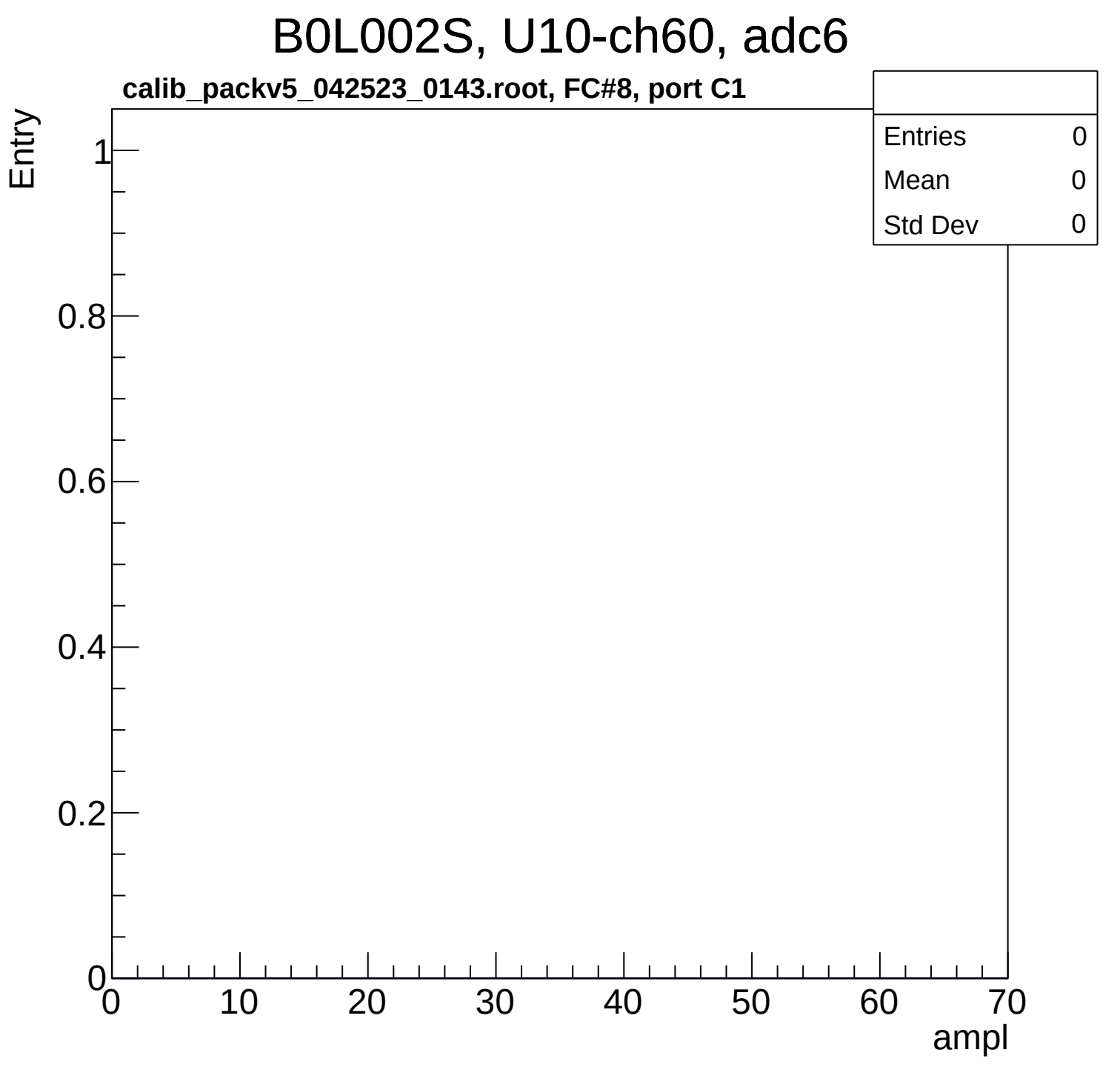

B0L002S, U10-ch60, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 39.65
Std Dev 2.954
Gaus mean : 40.2585
Gaus Width: 3.1030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

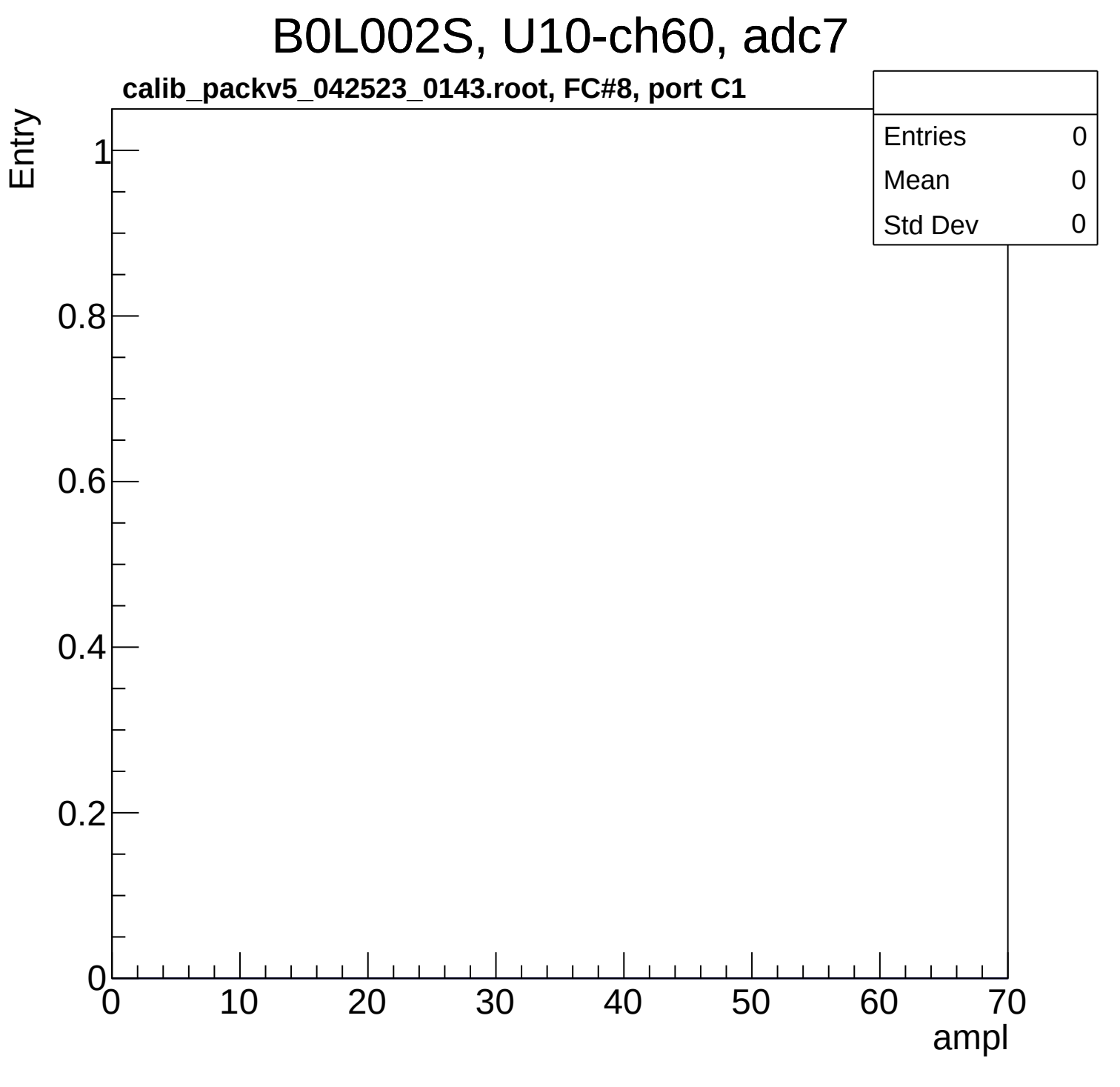

B0L002S, U10-ch60, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 46.76
Std Dev 3.631
Gaus mean : 47.5953
Gaus Width: 3.7492
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

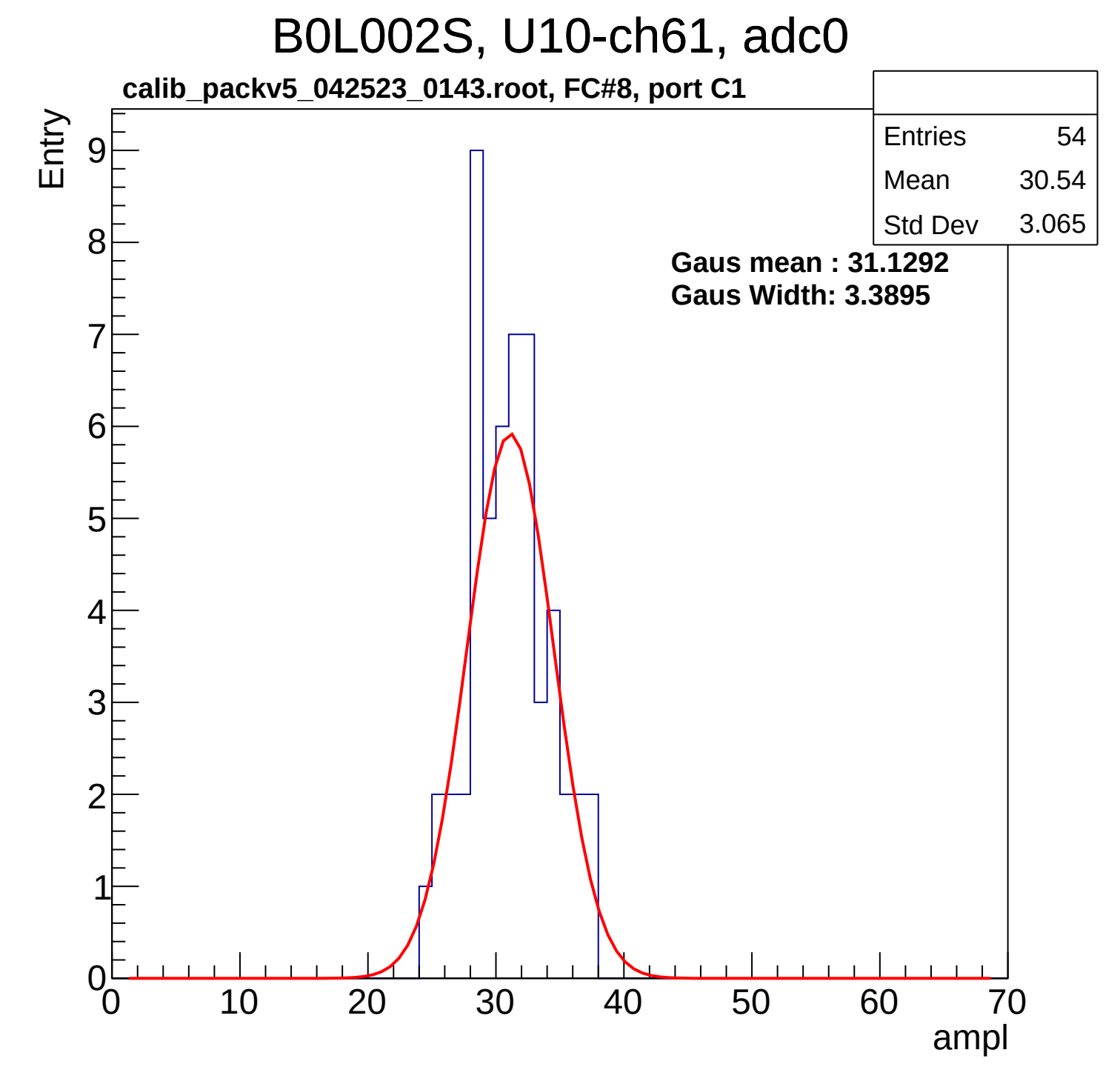

B0L002S, U10-ch60, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 53
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

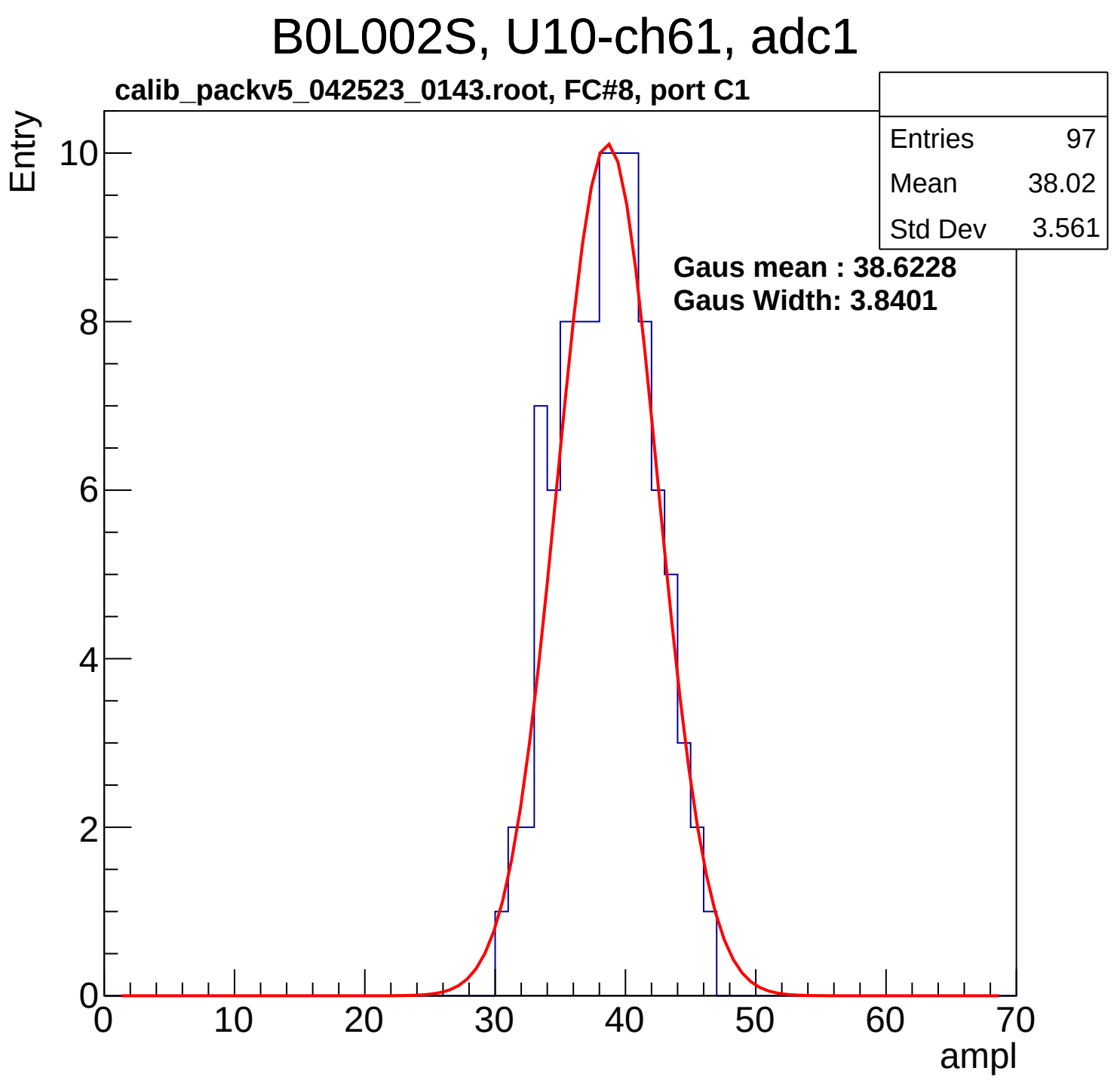

B0L002S, U10-ch60, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.23
Std Dev 8.179
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

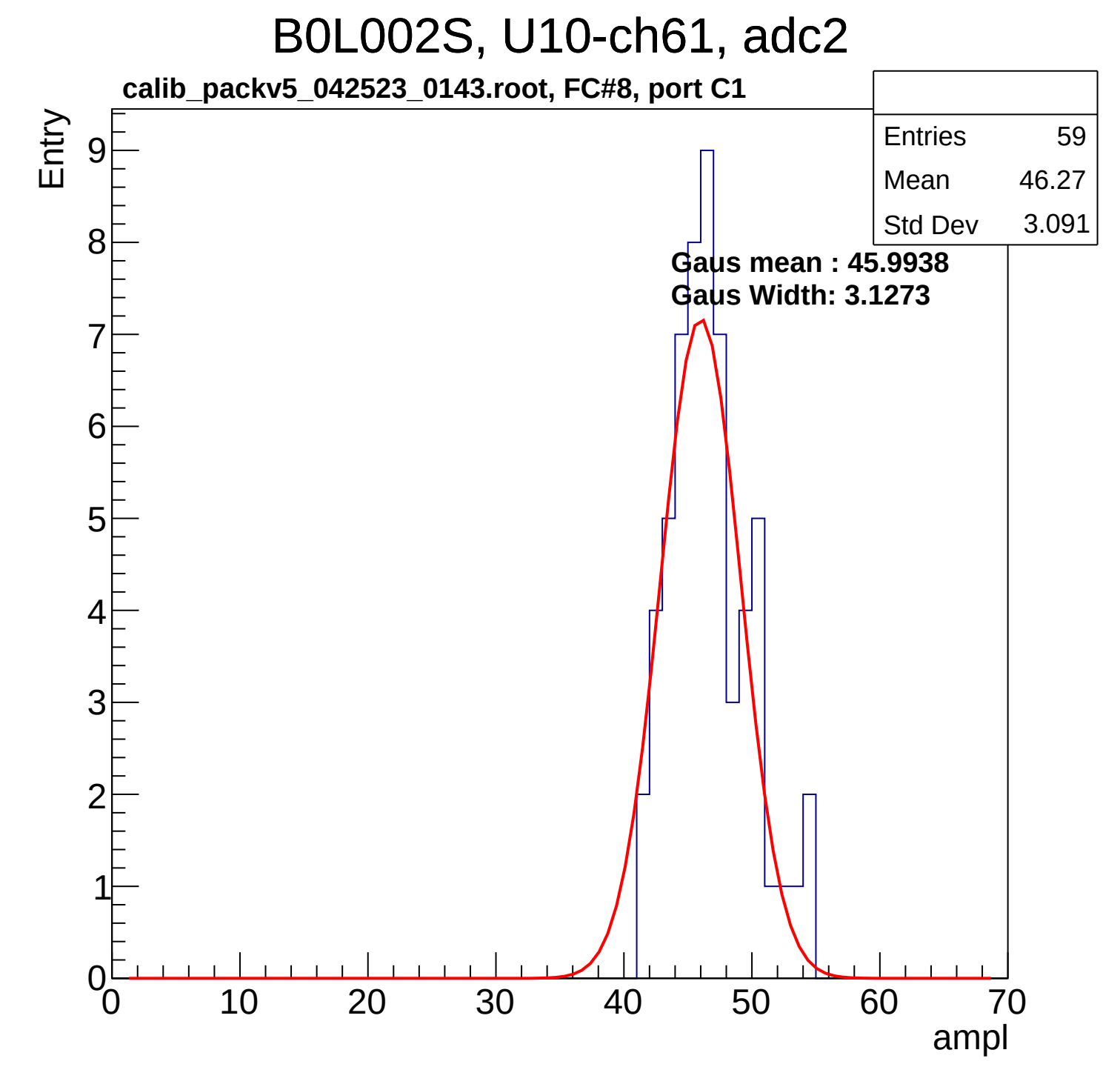

B0L002S, U10-ch60, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 62
Std Dev 1.095
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

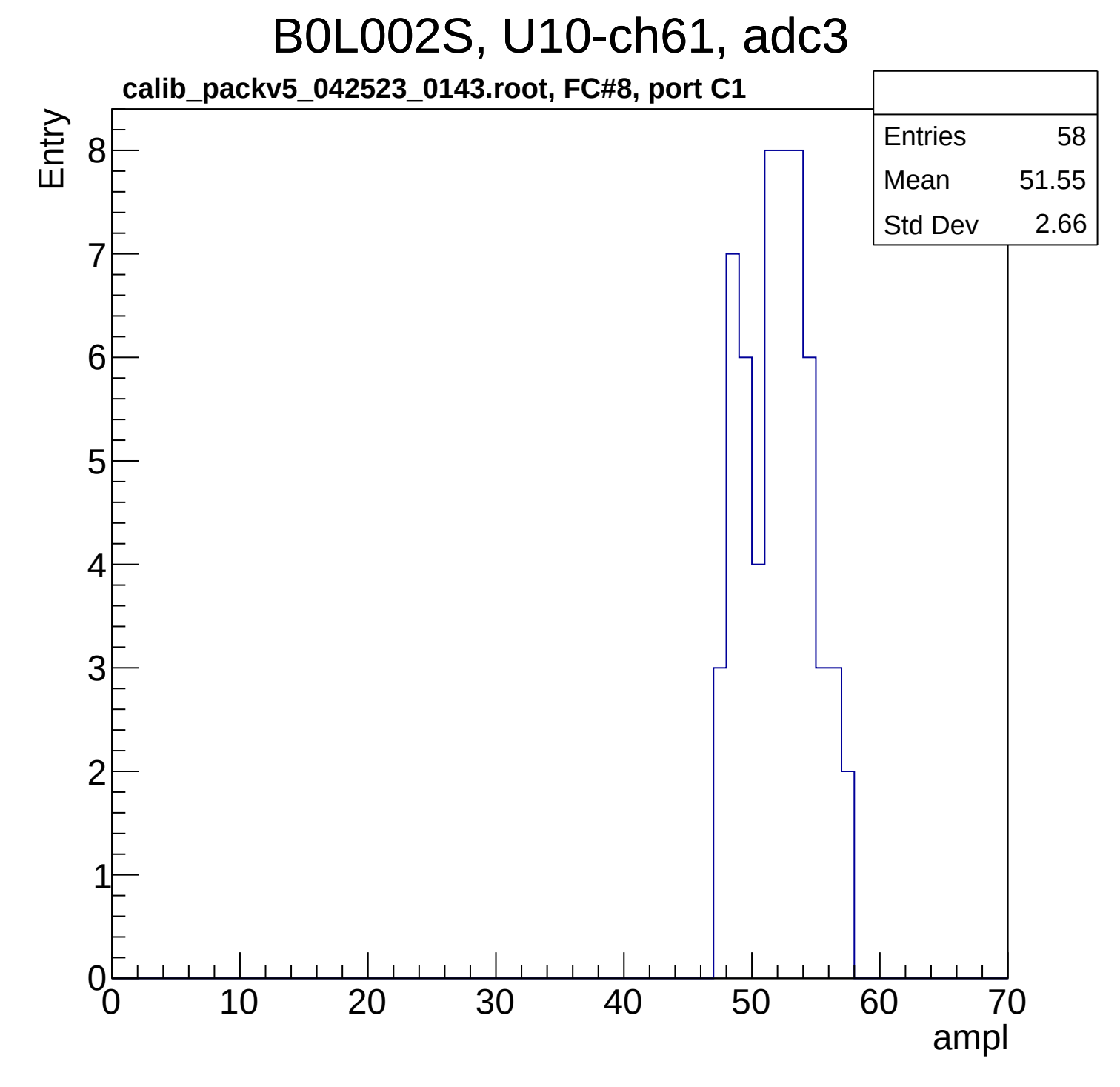

B0L002S, U10-ch60, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

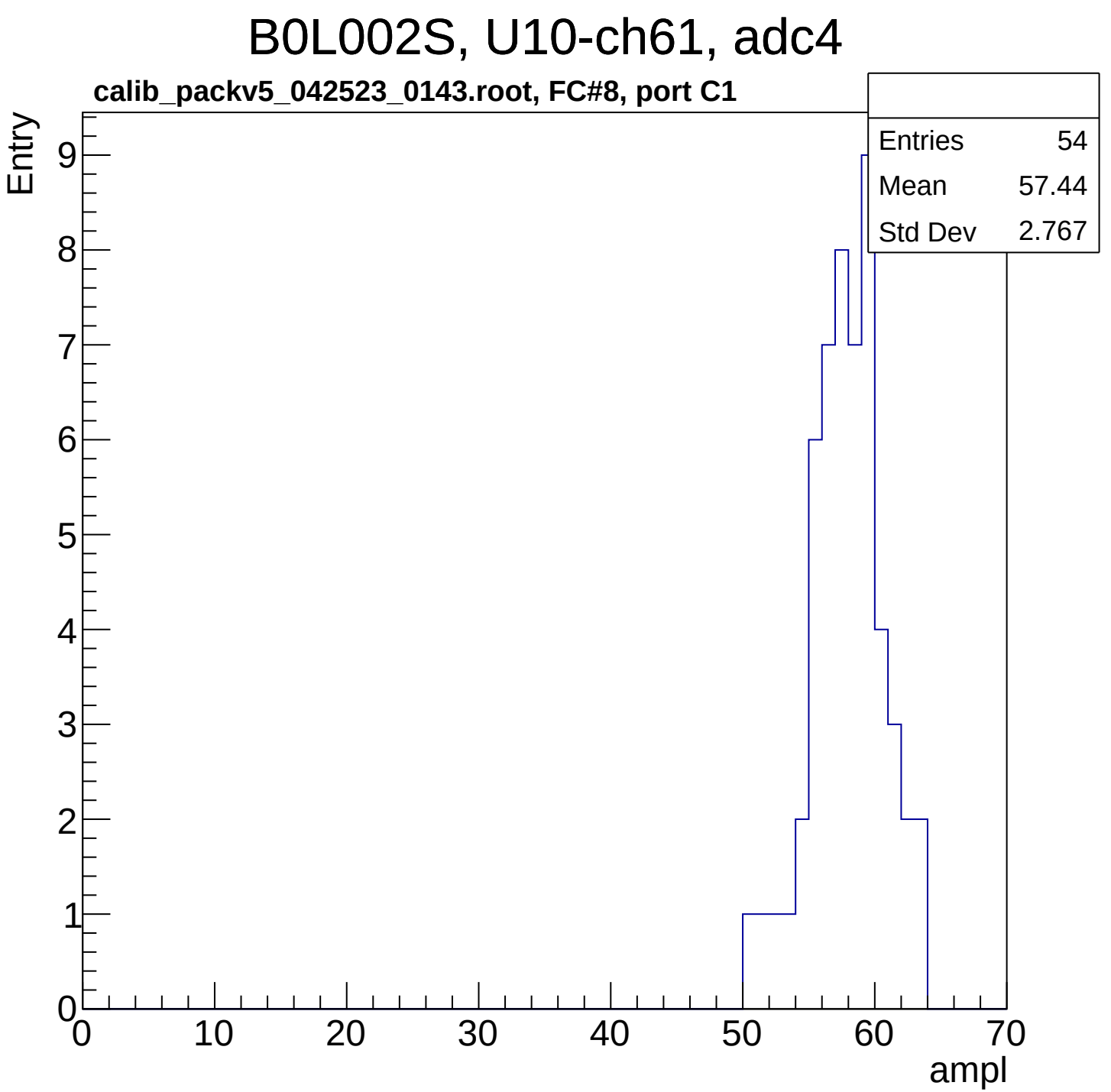

B0L002S, U10-ch60, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

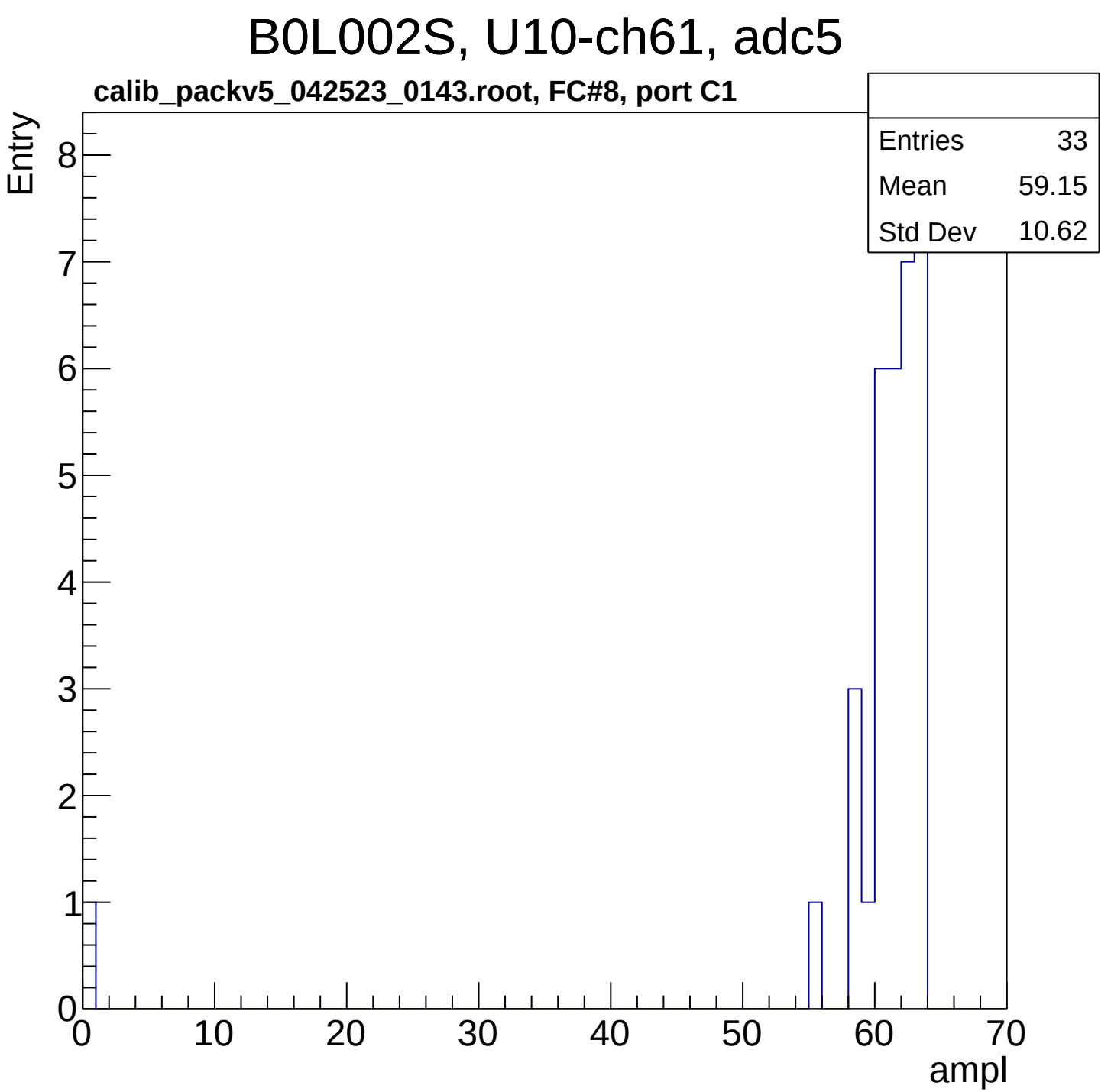

B0L002S, U10-ch61, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 30.54
Std Dev 3.065
Gaus mean : 31.1292
Gaus Width: 3.3895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

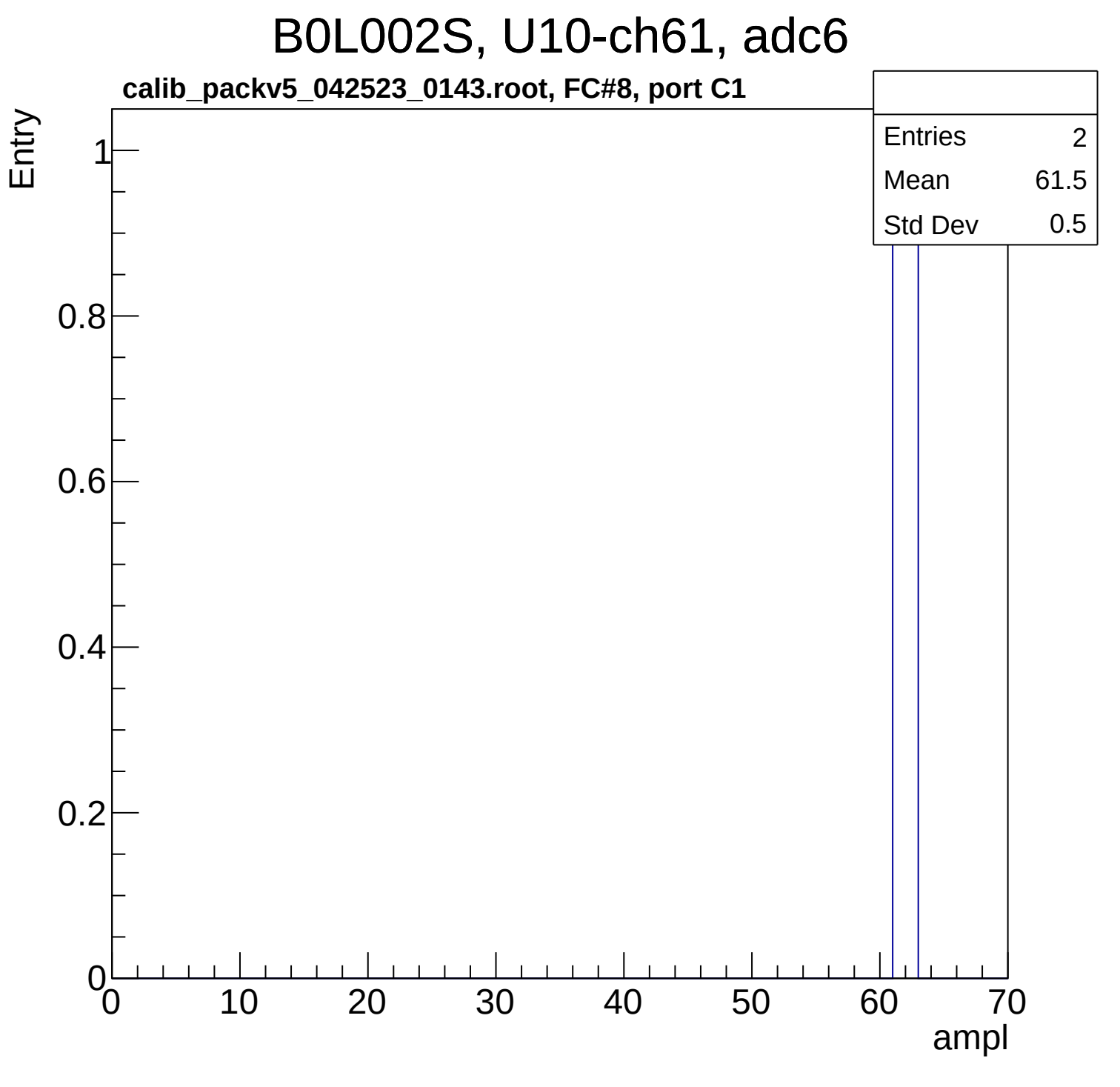

B0L002S, U10-ch61, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 97
Mean 38.02
Std Dev 3.561
Gaus mean : 38.6228
Gaus Width: 3.8401
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

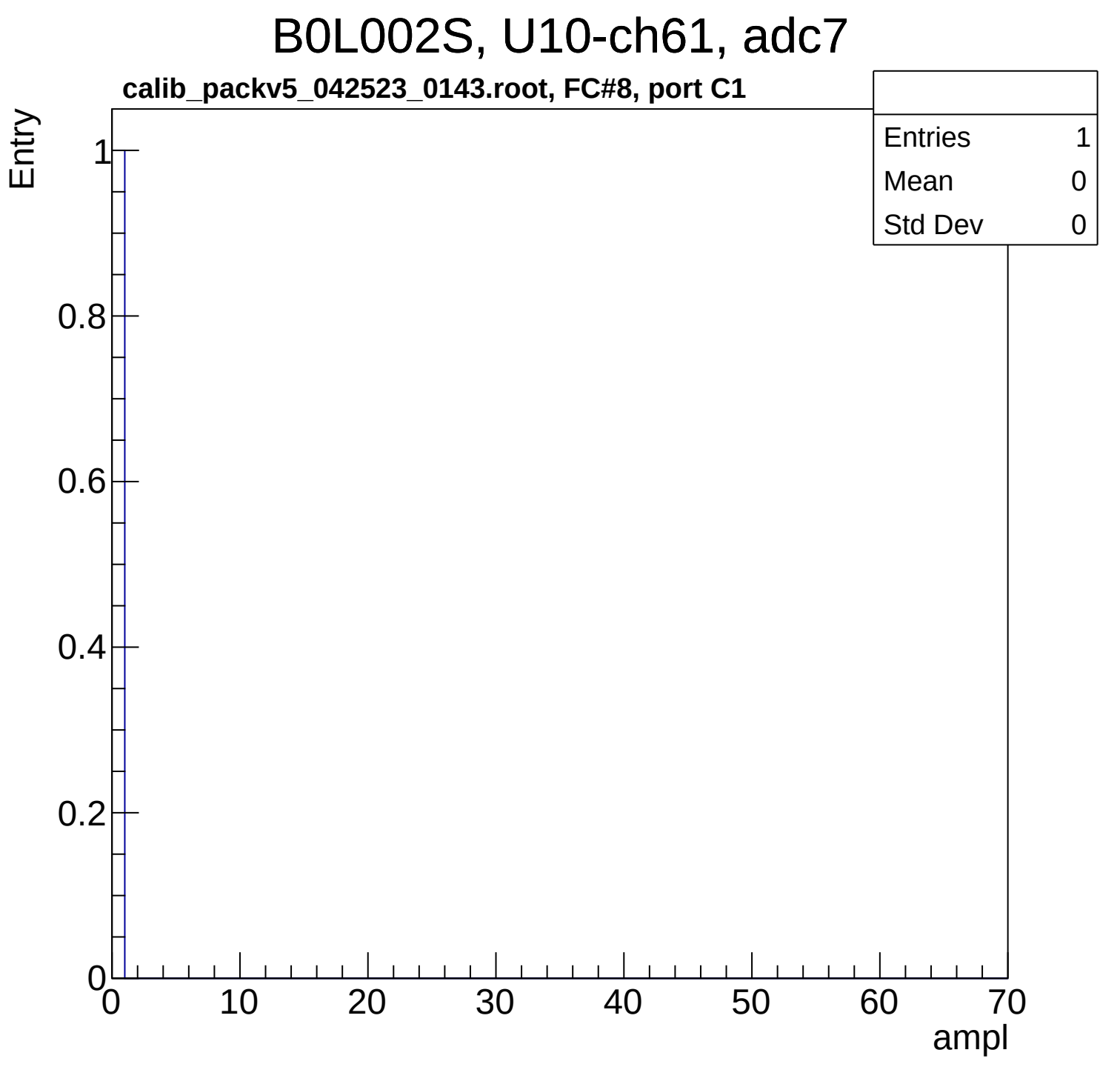

B0L002S, U10-ch61, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 46.27
Std Dev 3.091
Gaus mean : 45.9938
Gaus Width: 3.1273
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

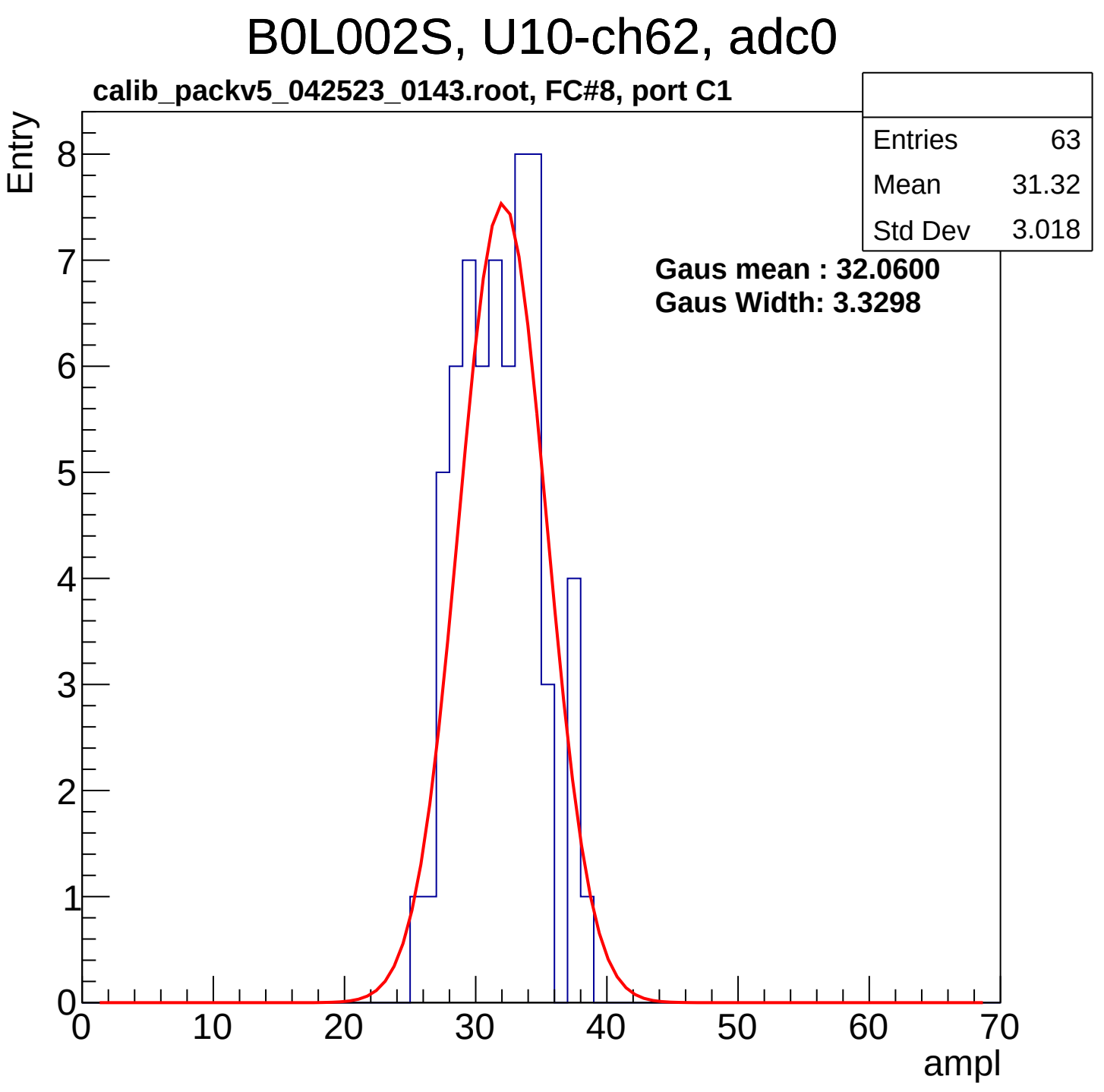

B0L002S, U10-ch61, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 51.55
Std Dev 2.66
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

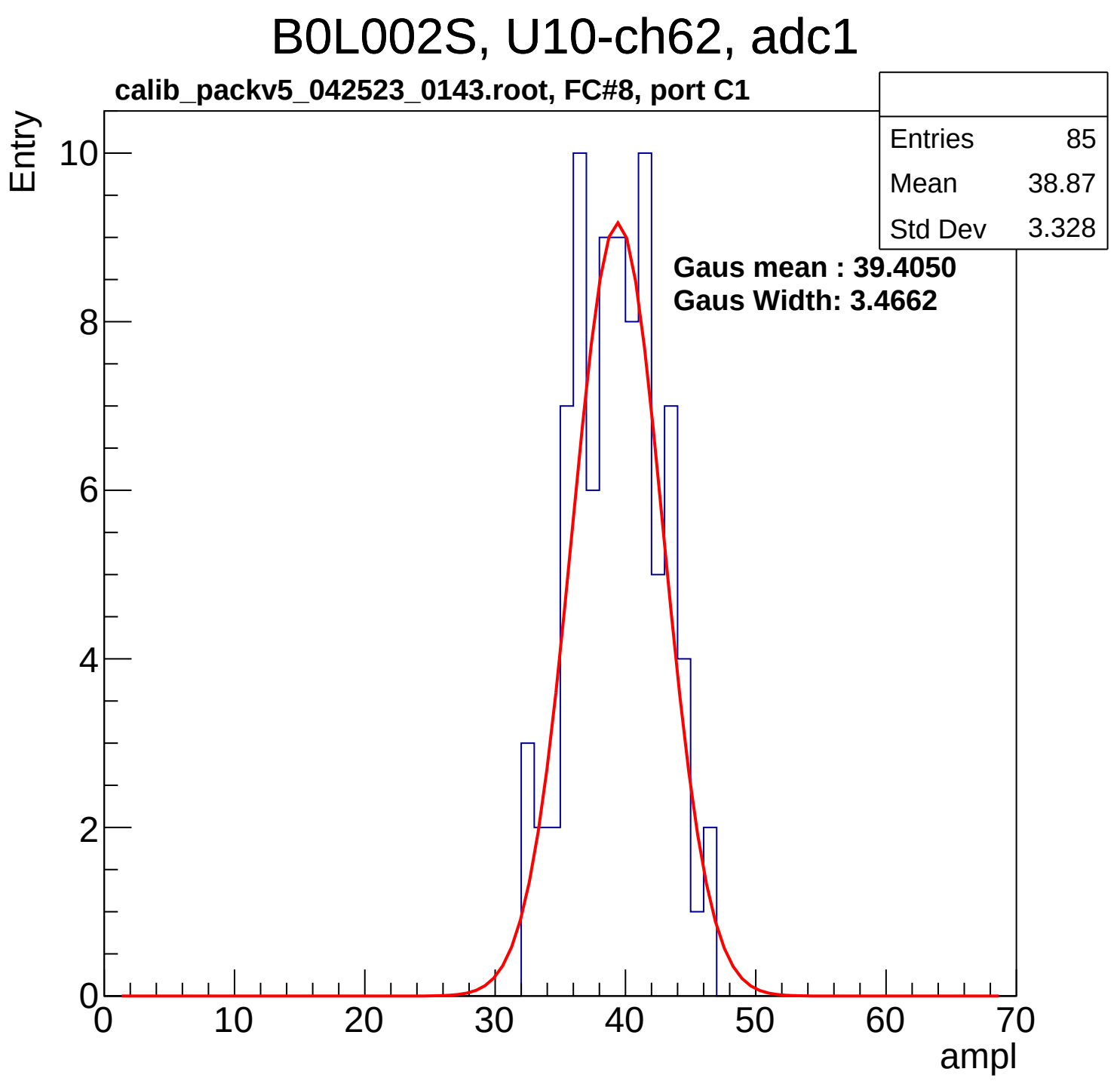

B0L002S, U10-ch61, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 57.44
Std Dev 2.767
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

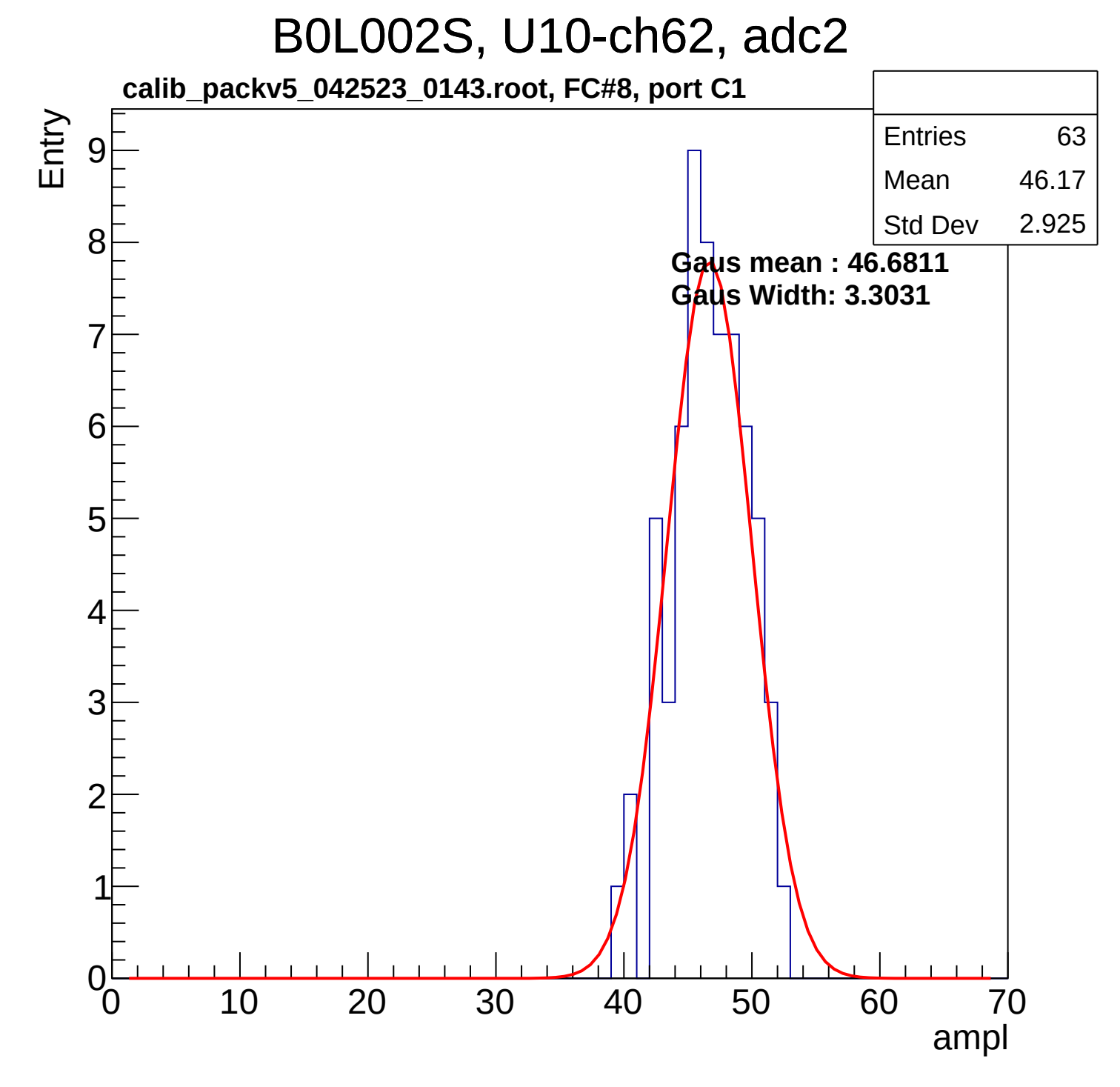

B0L002S, U10-ch61, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 59.15
Std Dev 10.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

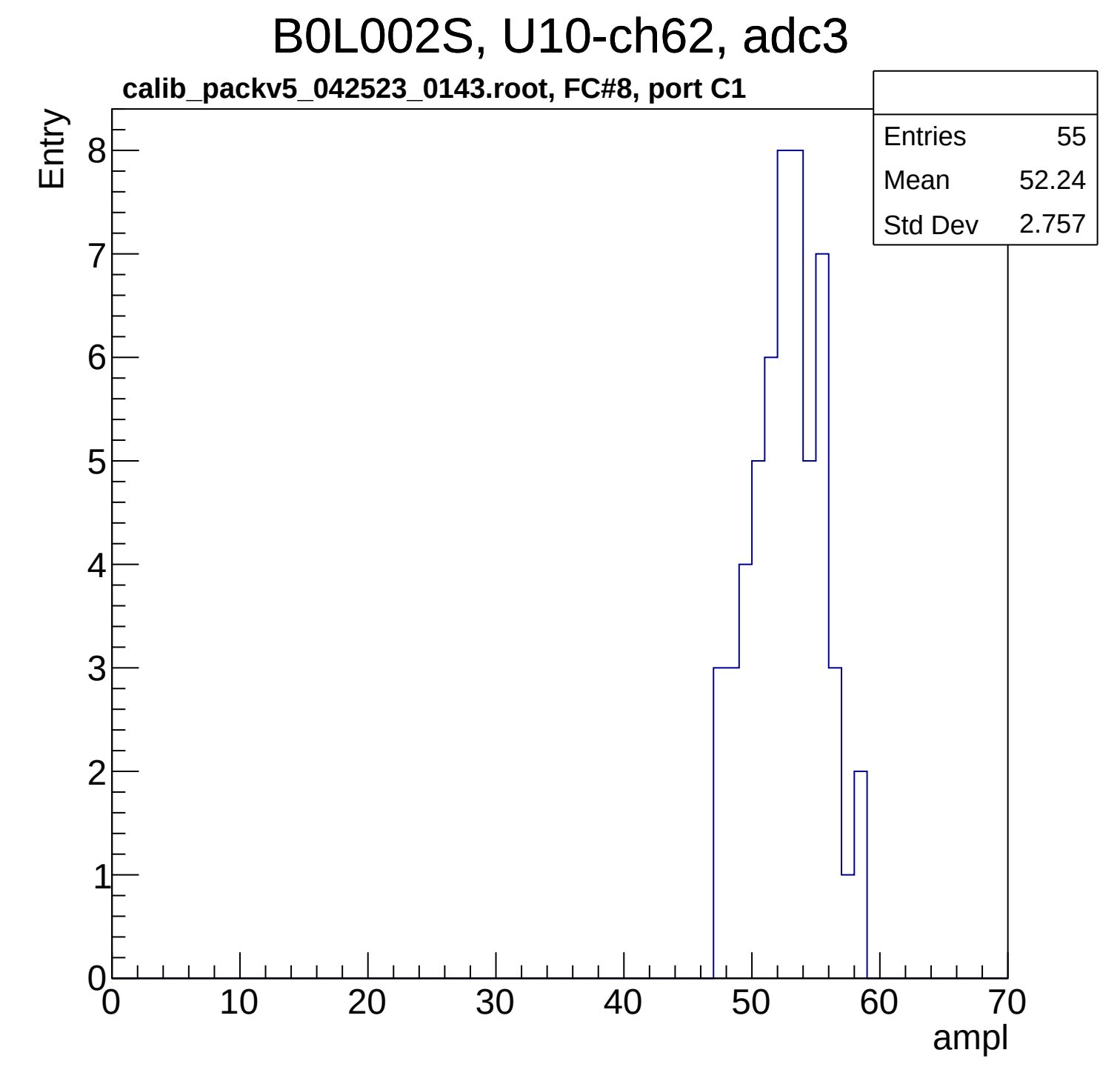

B0L002S, U10-ch61, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

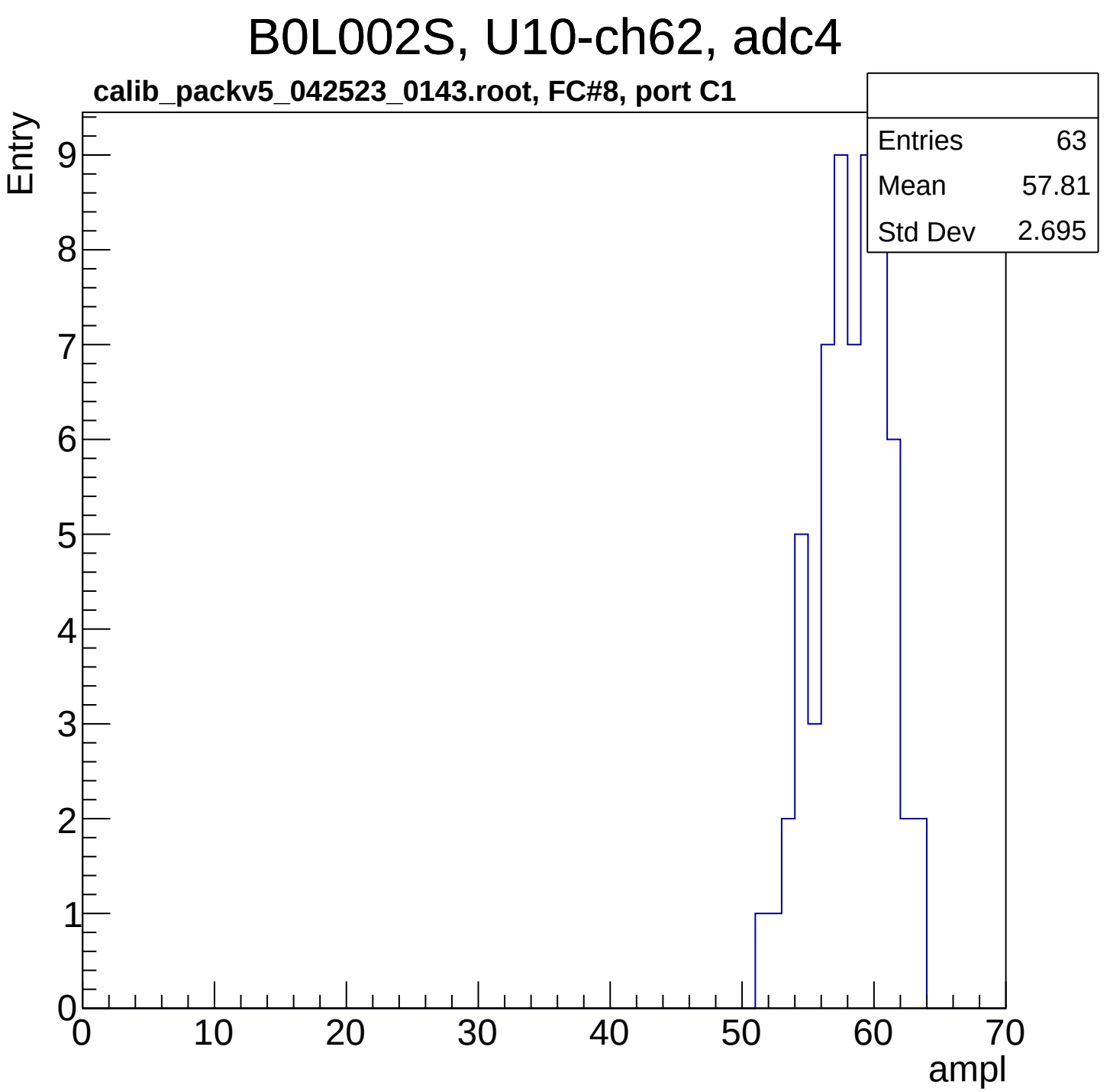

B0L002S, U10-ch61, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

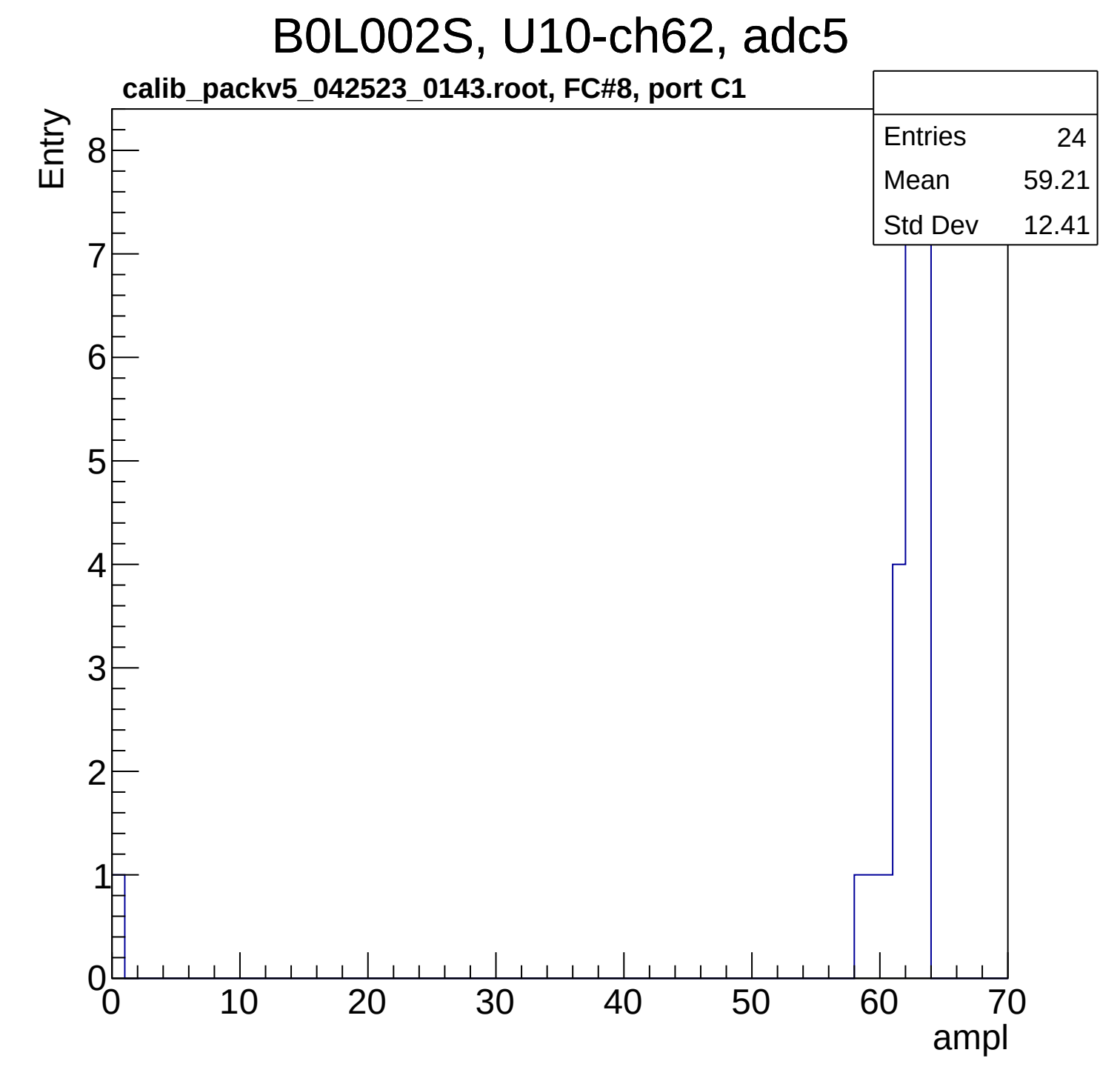

B0L002S, U10-ch62, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.32
Std Dev 3.018
Gaus mean : 32.0600
Gaus Width: 3.3298
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

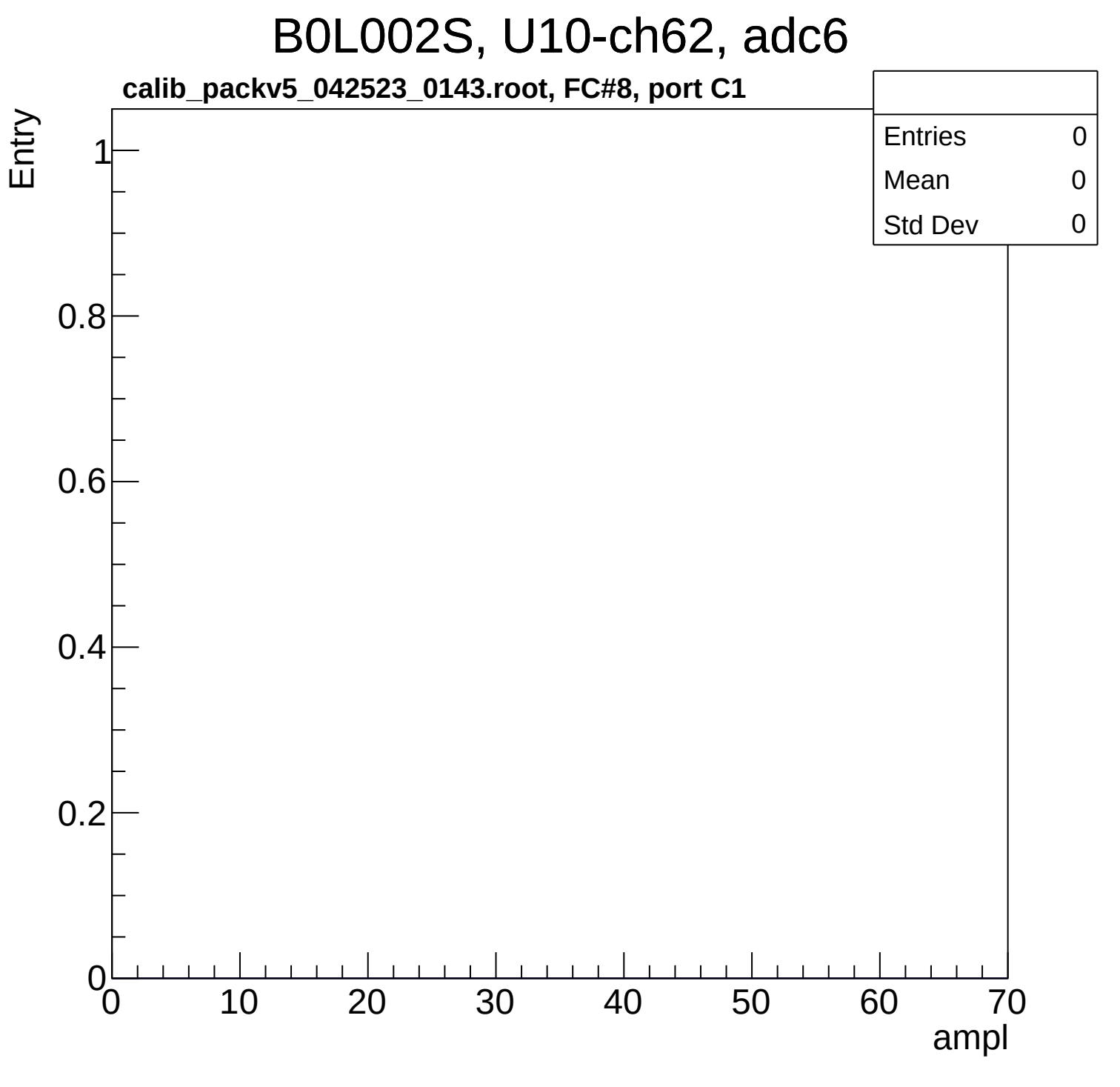

B0L002S, U10-ch62, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 38.87
Std Dev 3.328
Gaus mean : 39.4050
Gaus Width: 3.4662
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

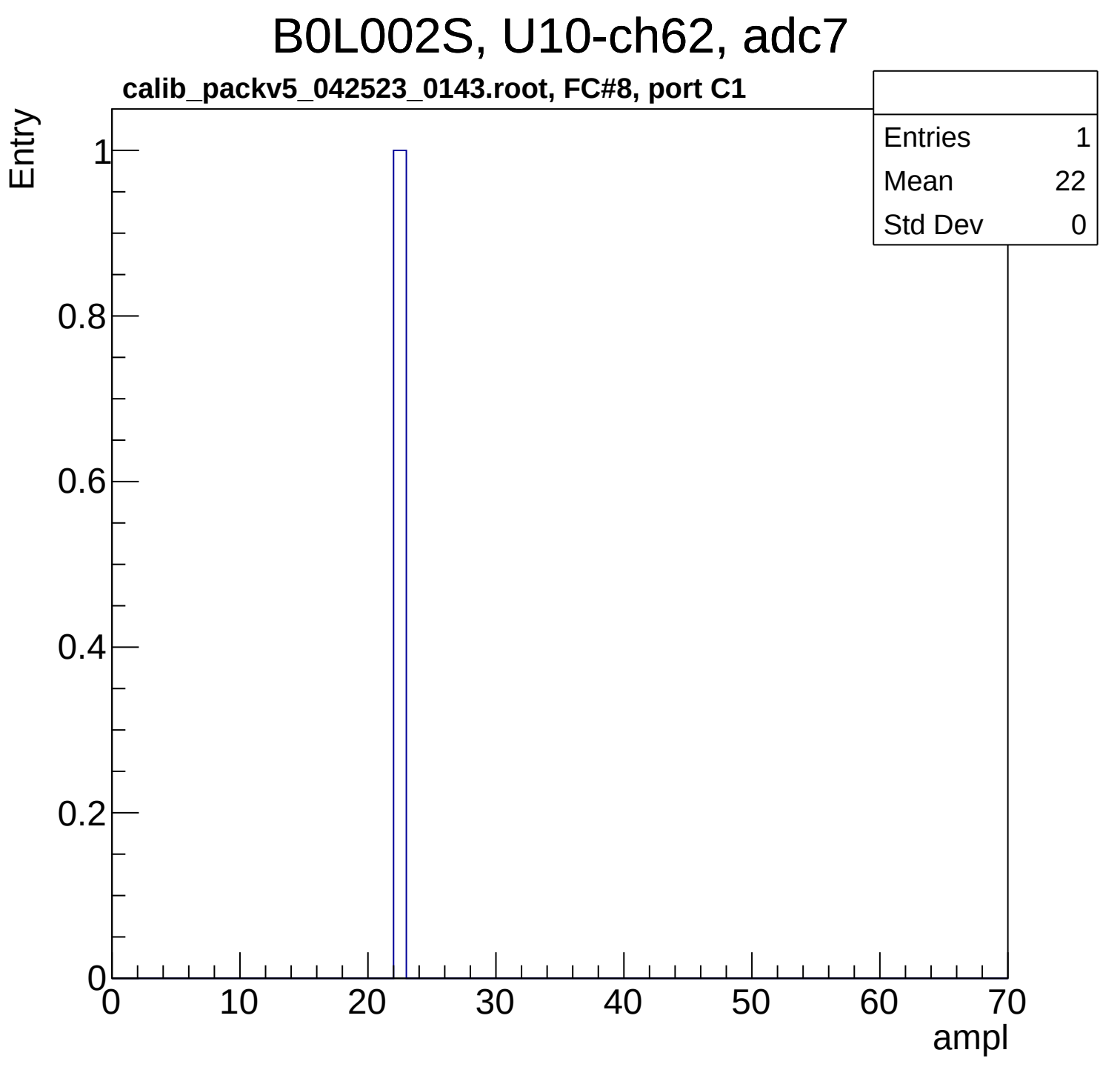

B0L002S, U10-ch62, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 46.17
Std Dev 2.925
Gaus mean : 46.6811
Gaus Width: 3.3031
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

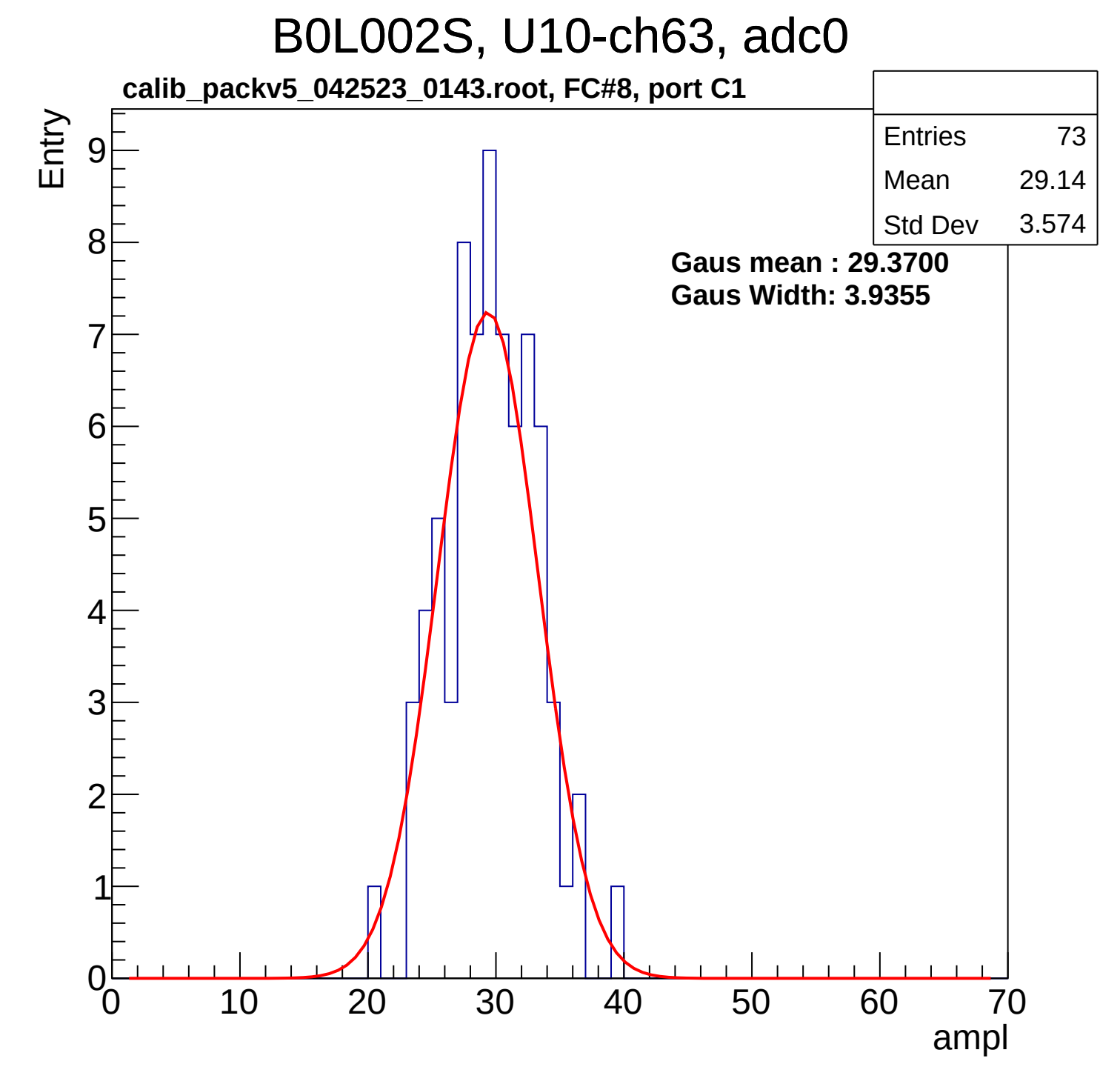

B0L002S, U10-ch62, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 52.24
Std Dev 2.757
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

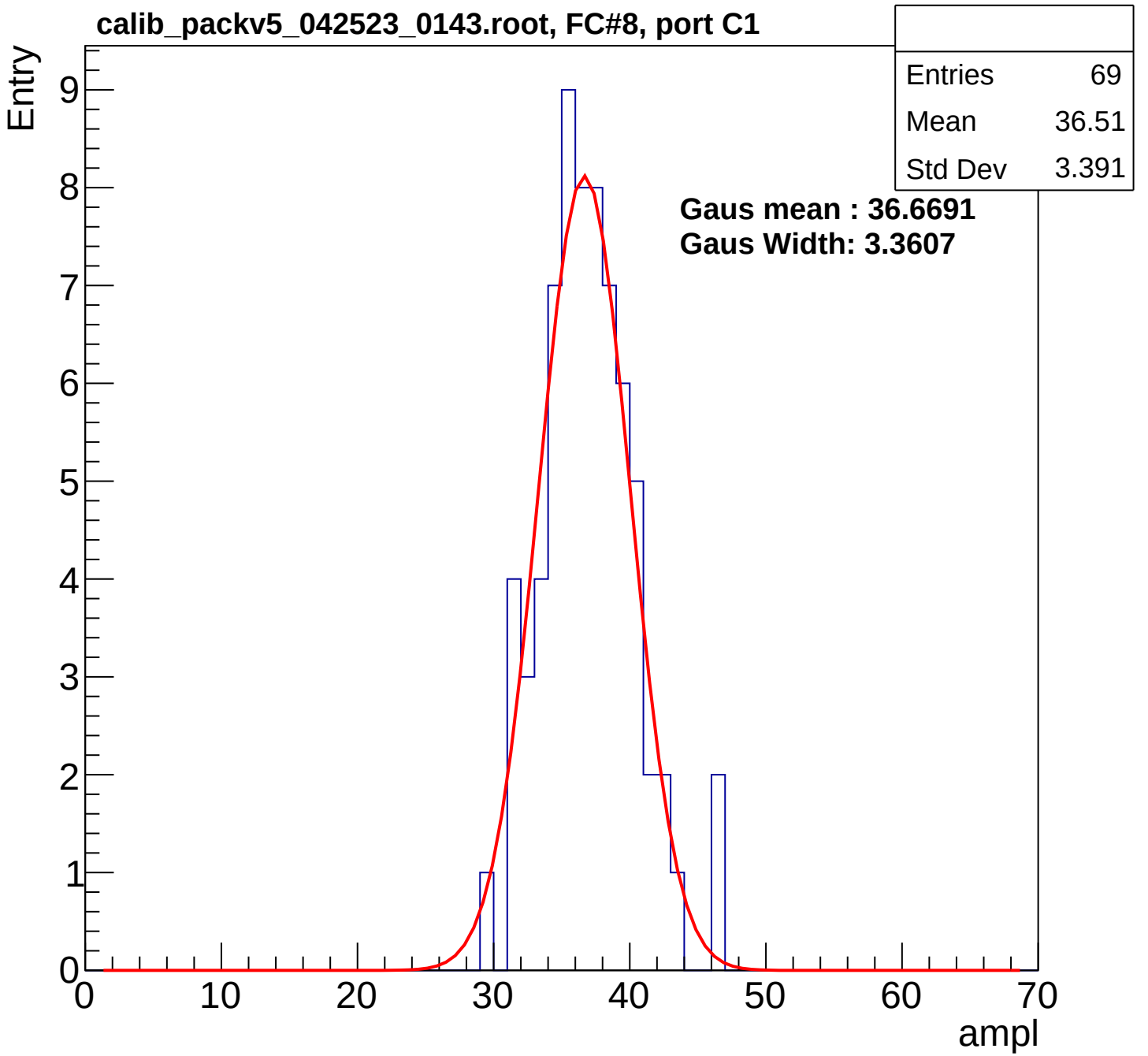

B0L002S, U10-ch62, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.81
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

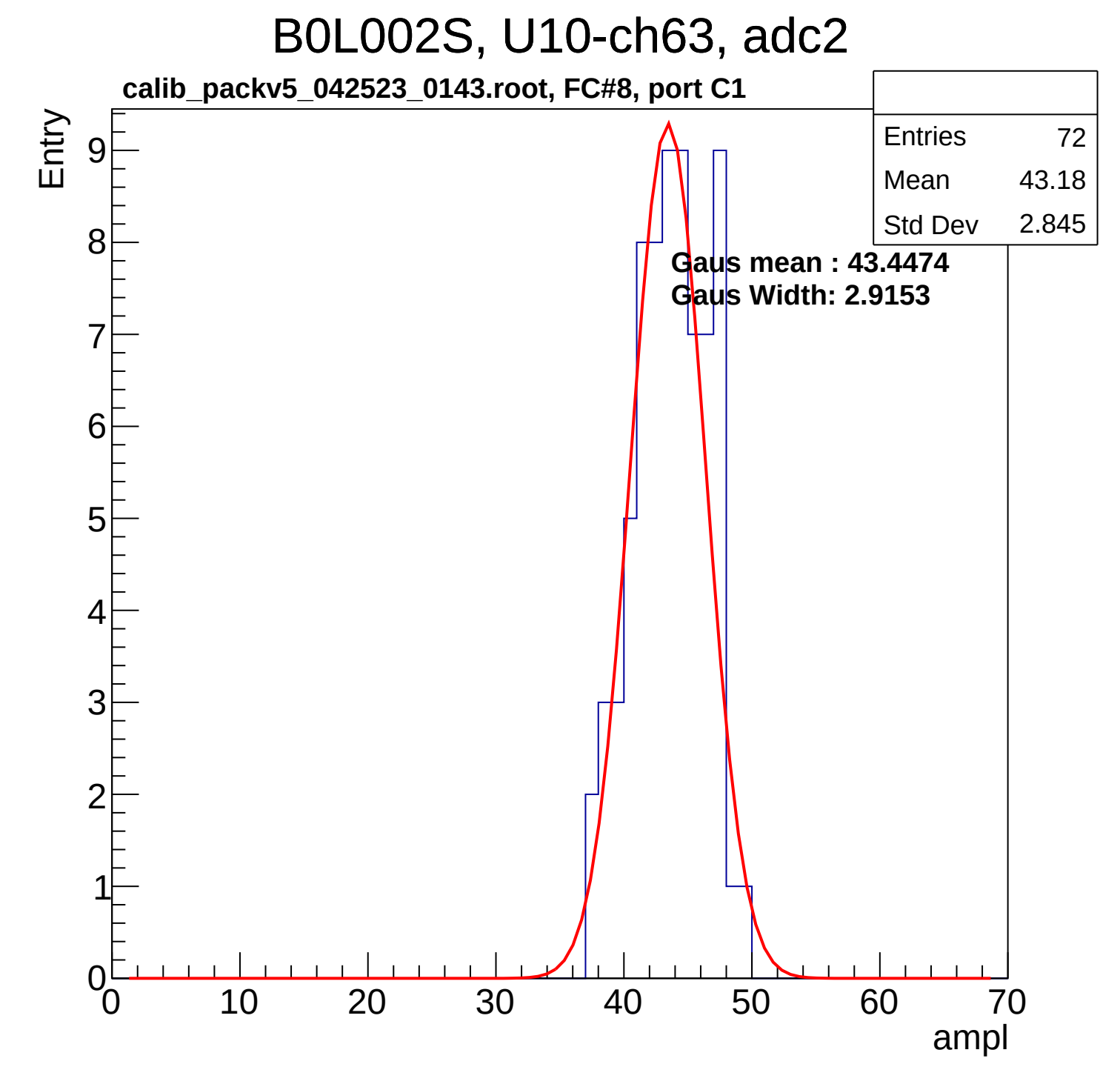

B0L002S, U10-ch62, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 59.21
Std Dev 12.41
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

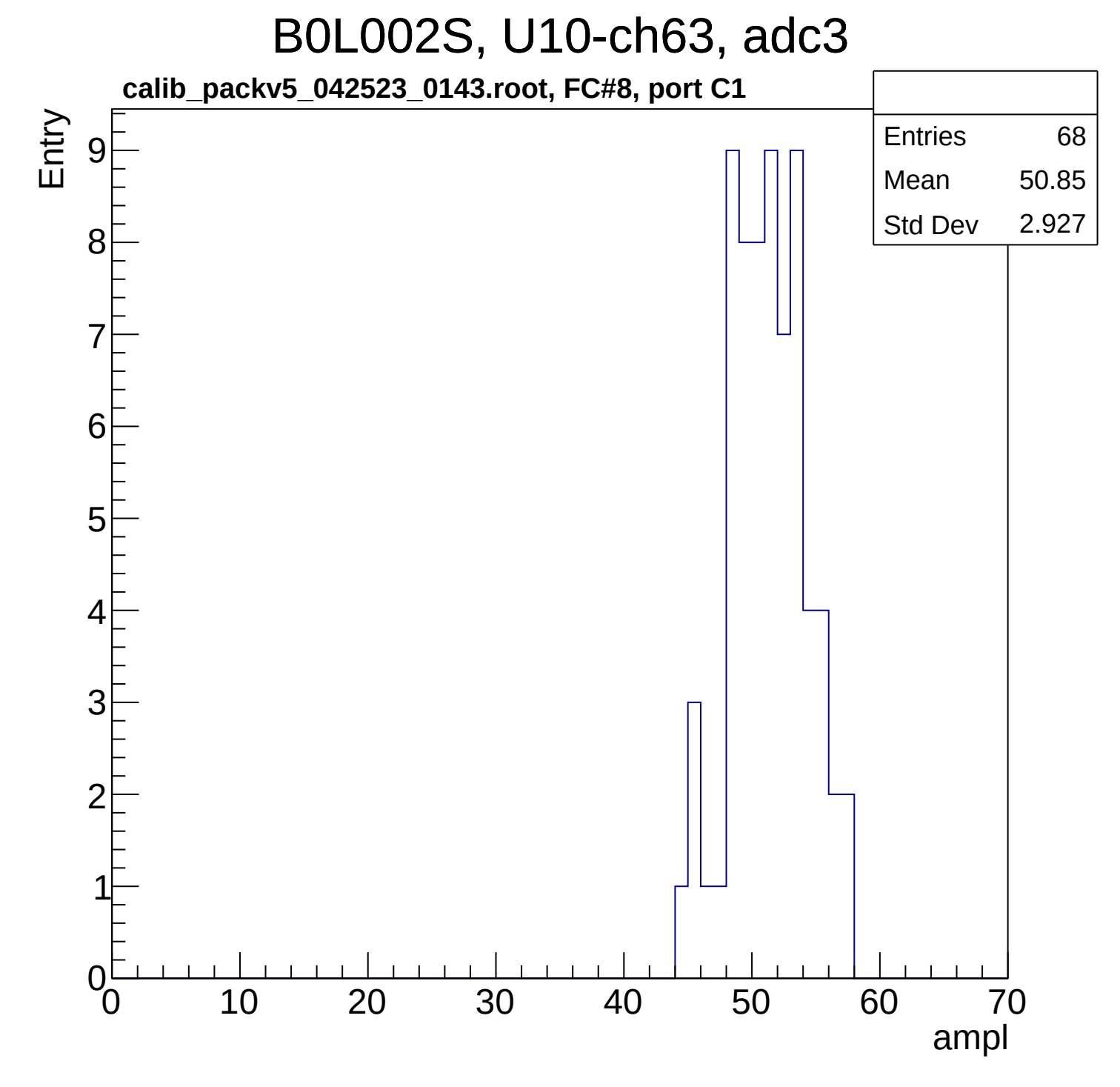

B0L002S, U10-ch62, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

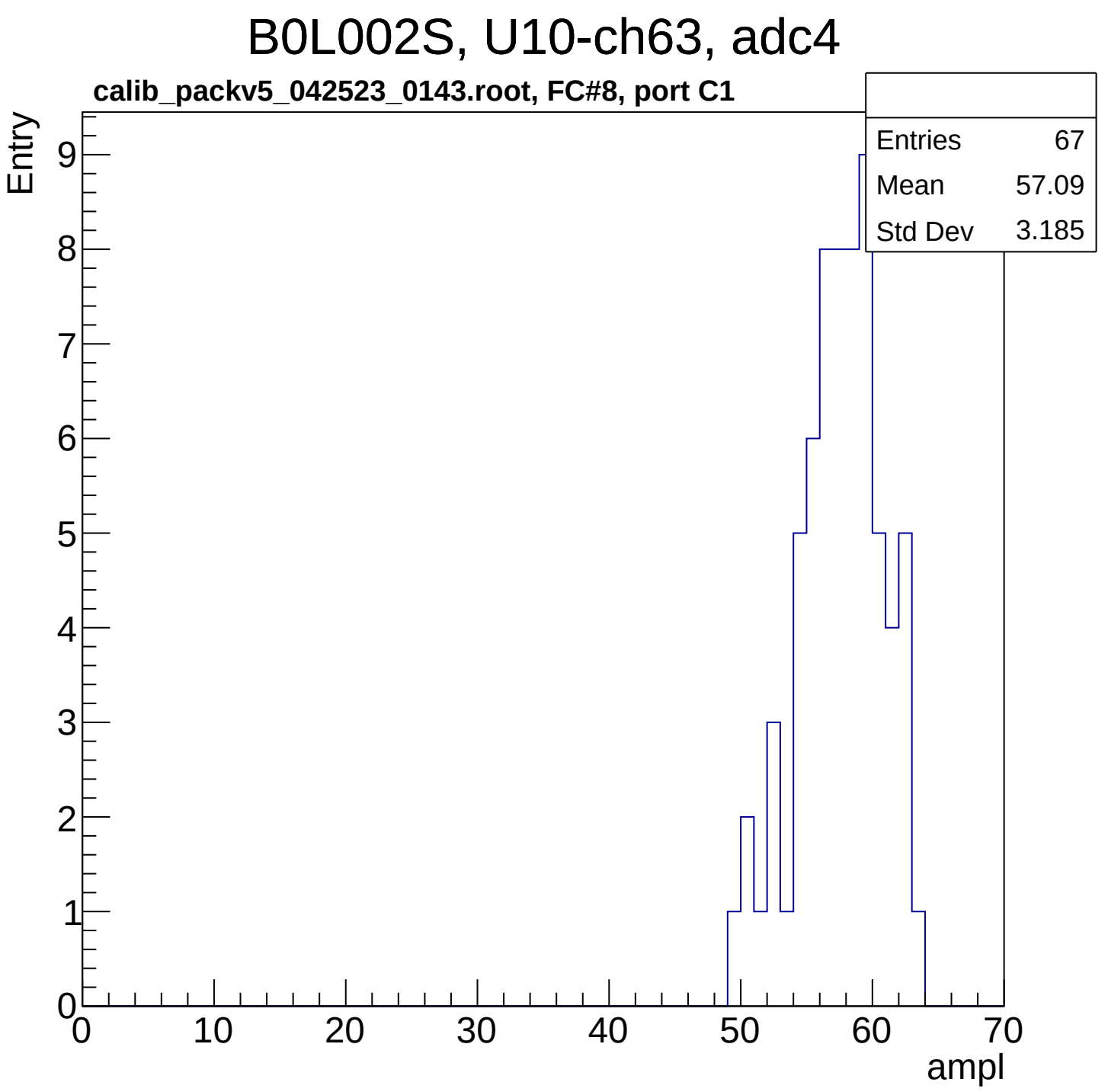

B0L002S, U10-ch62, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

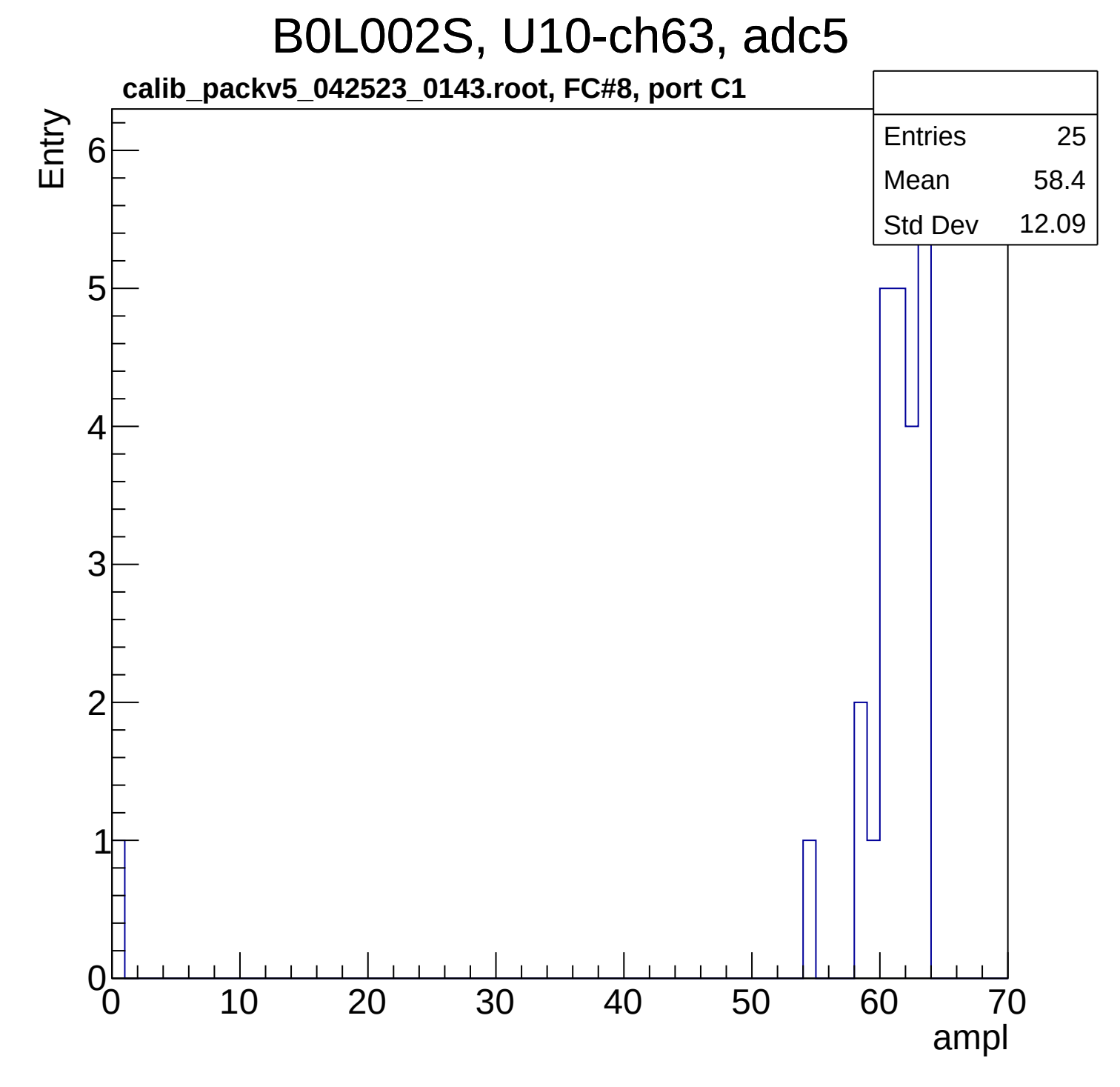

B0L002S, U10-ch63, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 29.14
Std Dev 3.574
Gaus mean : 29.3700
Gaus Width: 3.9355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

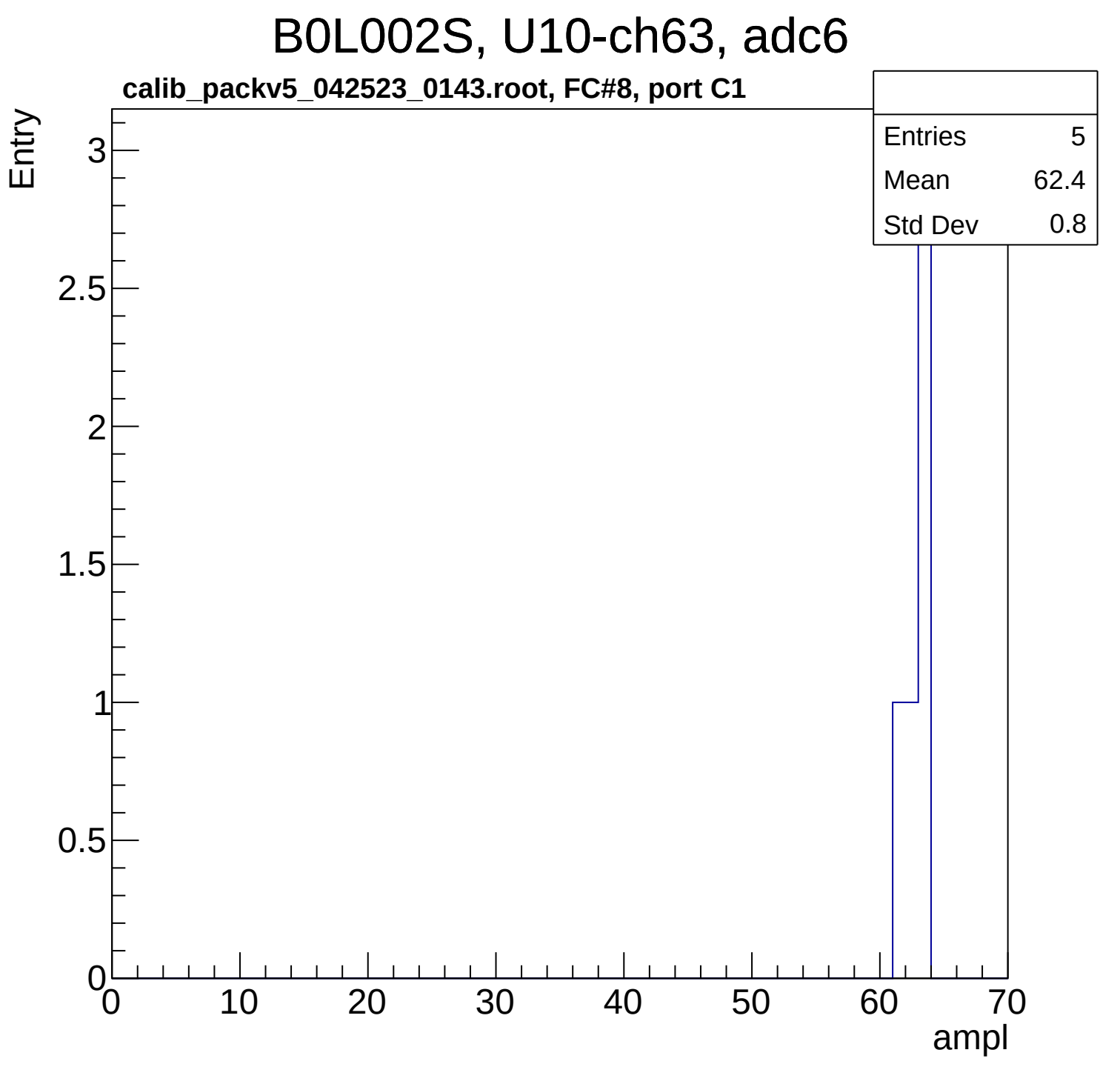

B0L002S, U10-ch63, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.51
Std Dev 3.391
Gaus mean : 36.6691
Gaus Width: 3.3607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

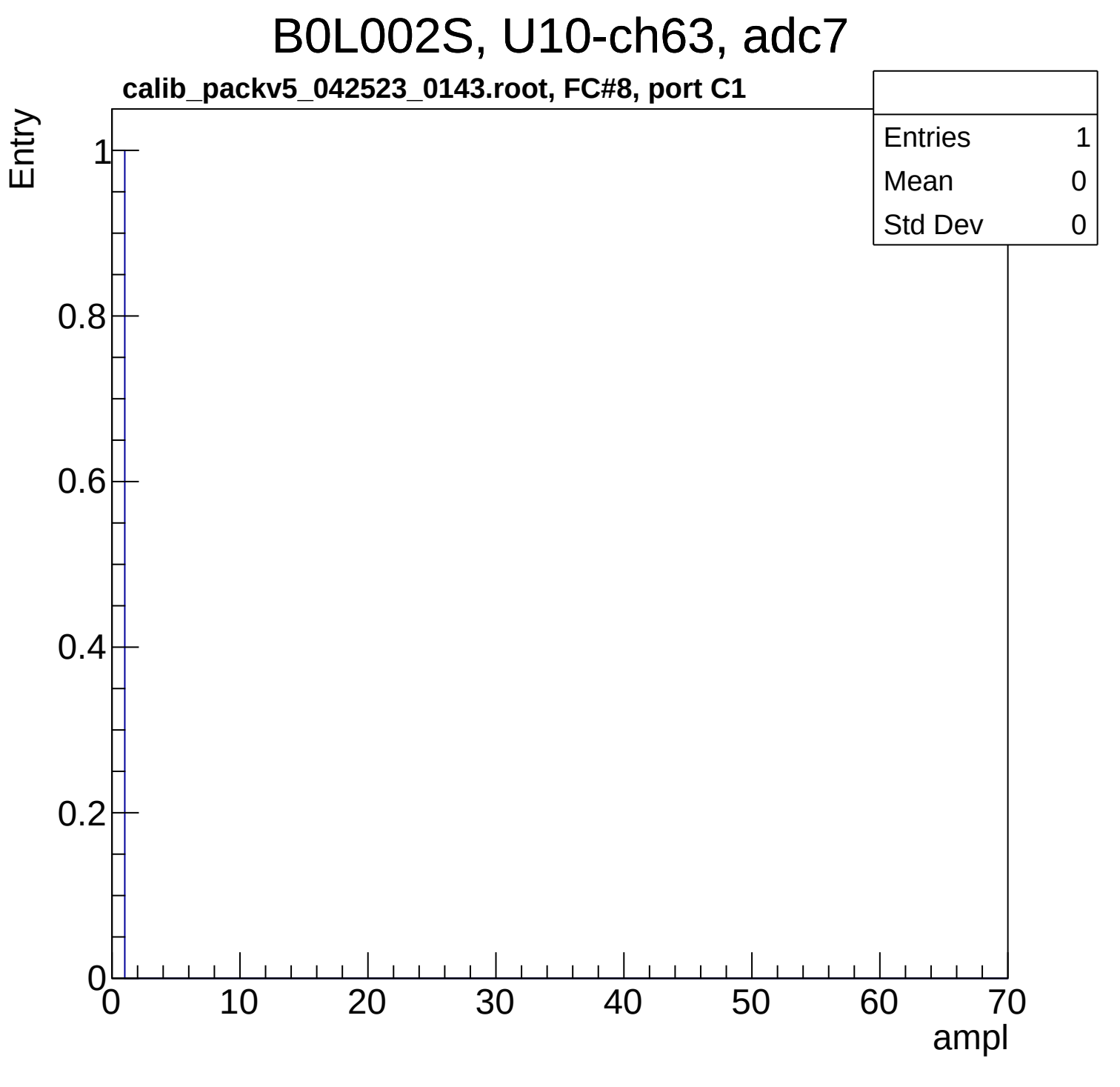

B0L002S, U10-ch63, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 43.18
Std Dev 2.845
Gaus mean : 43.4474
Gaus Width: 2.9153
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

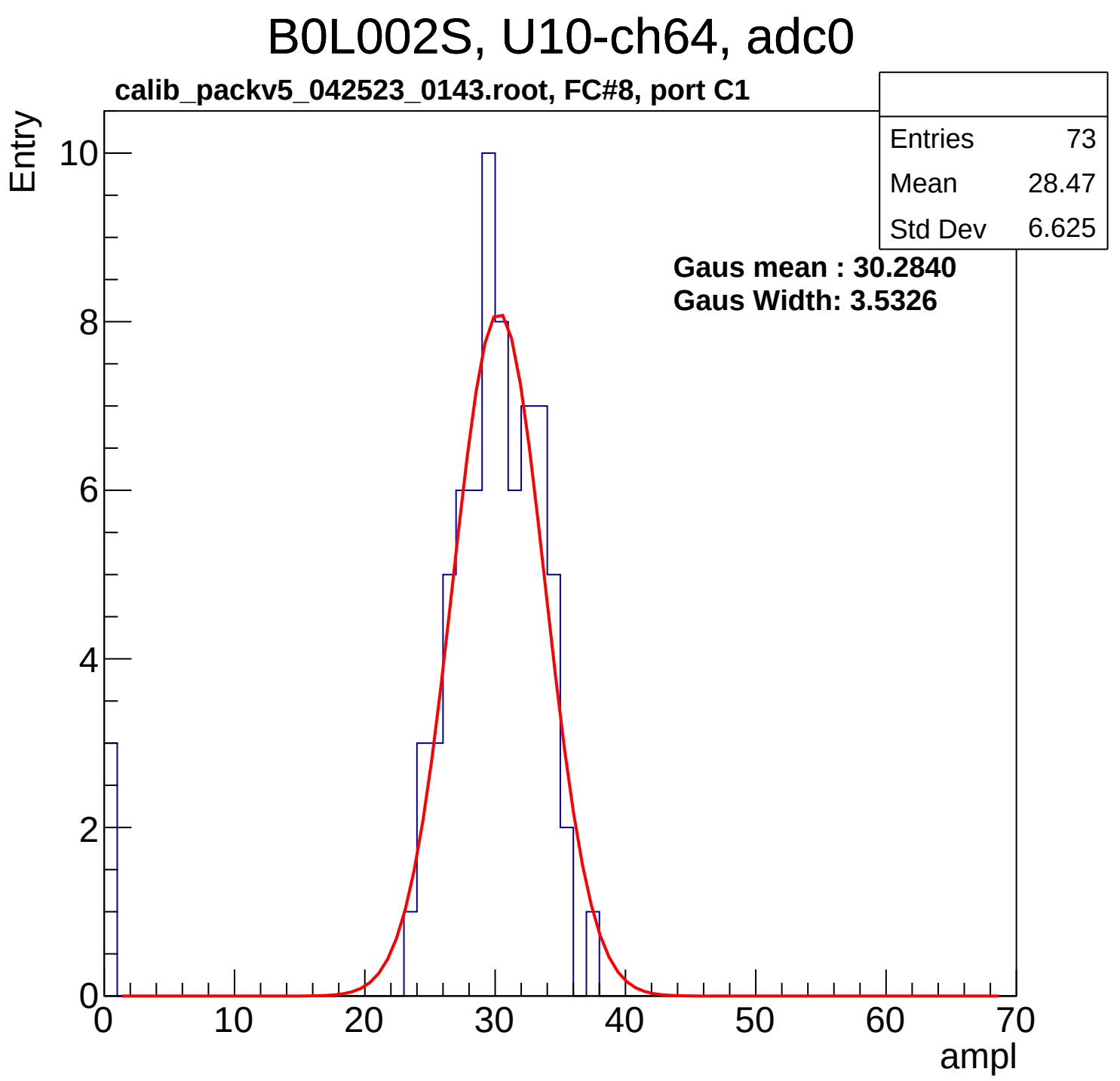

B0L002S, U10-ch63, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.85
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

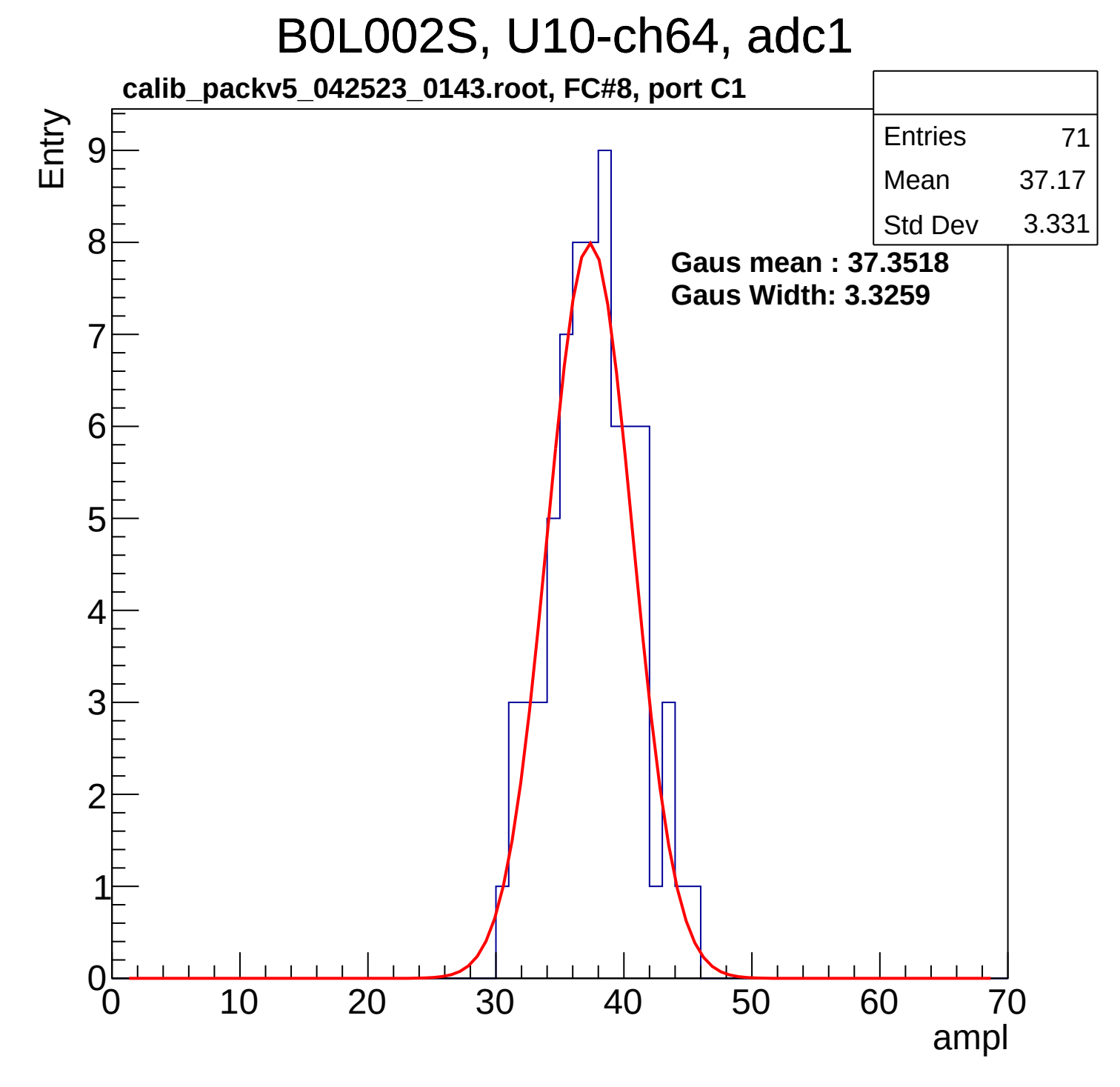

B0L002S, U10-ch63, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 57.09
Std Dev 3.185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

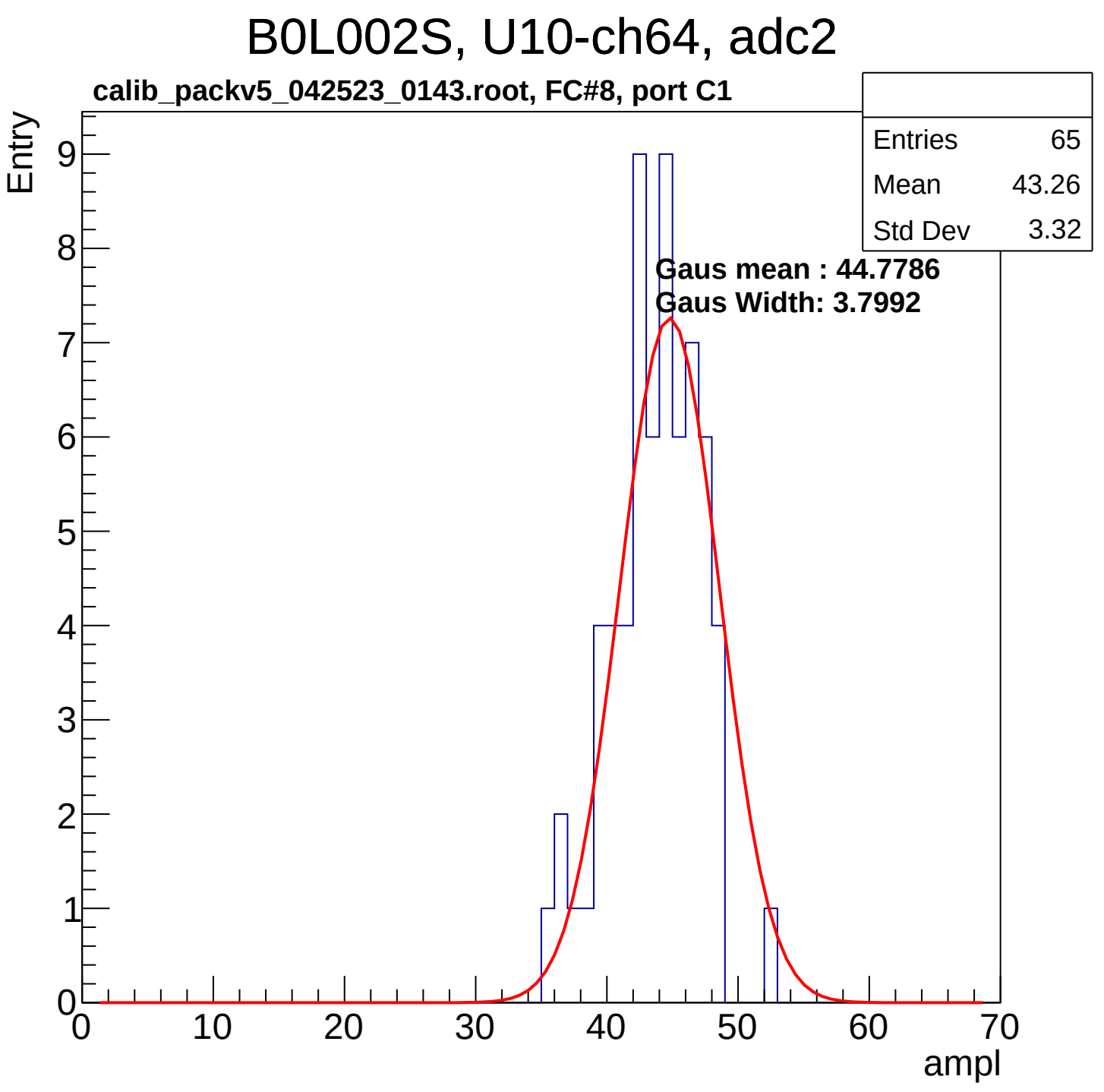

B0L002S, U10-ch63, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.4
Std Dev 12.09
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

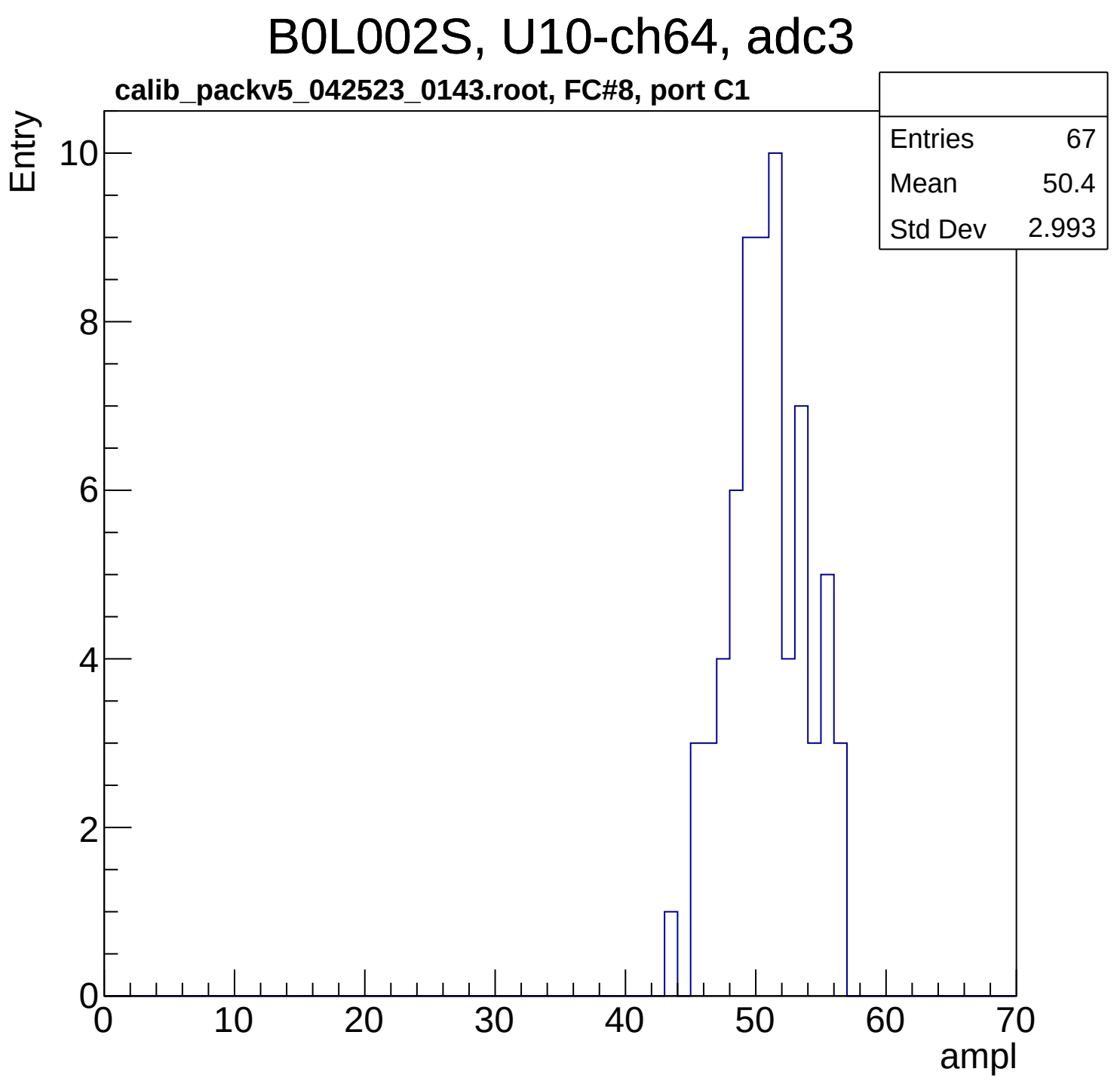

B0L002S, U10-ch63, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

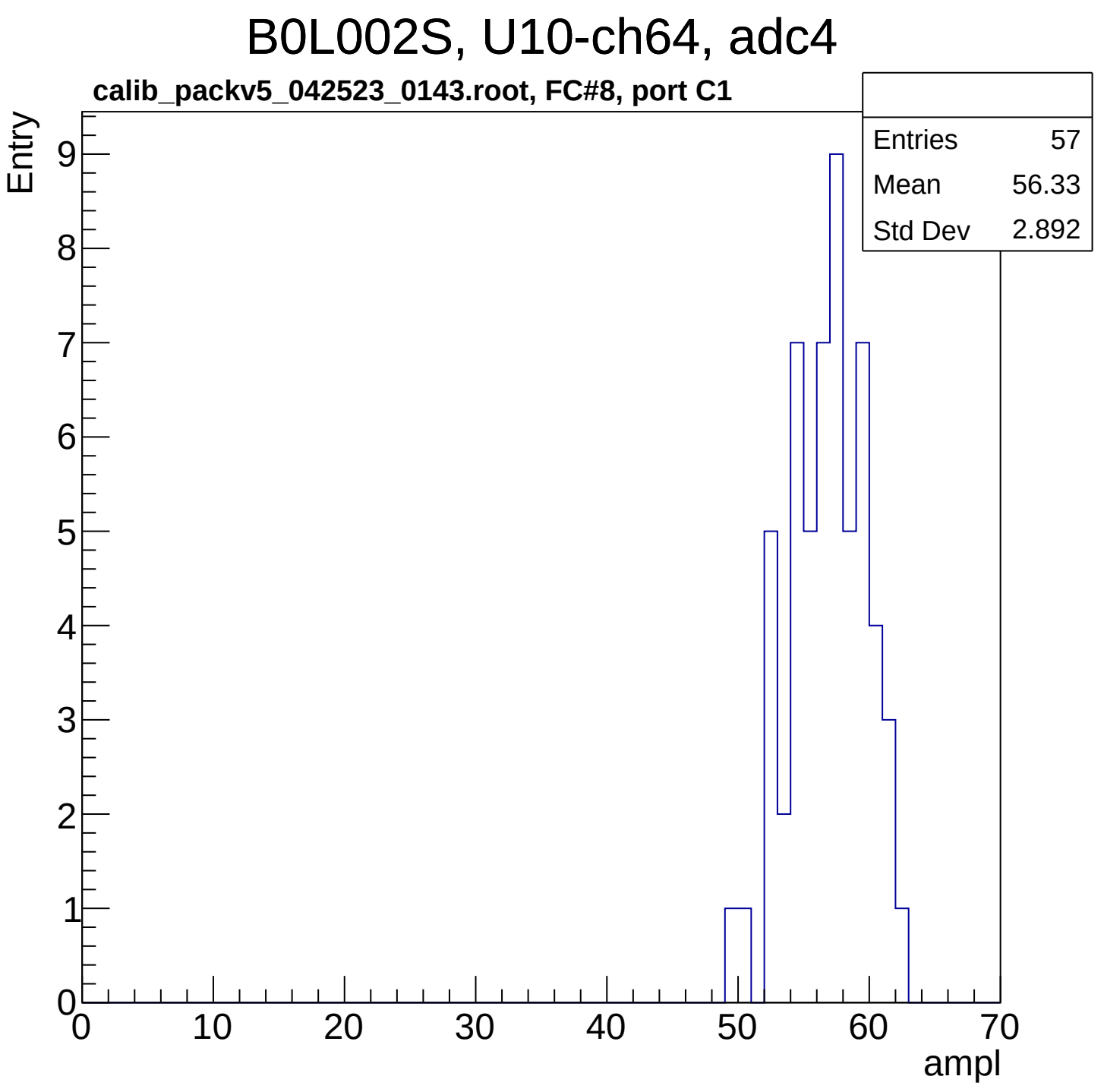

B0L002S, U10-ch63, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

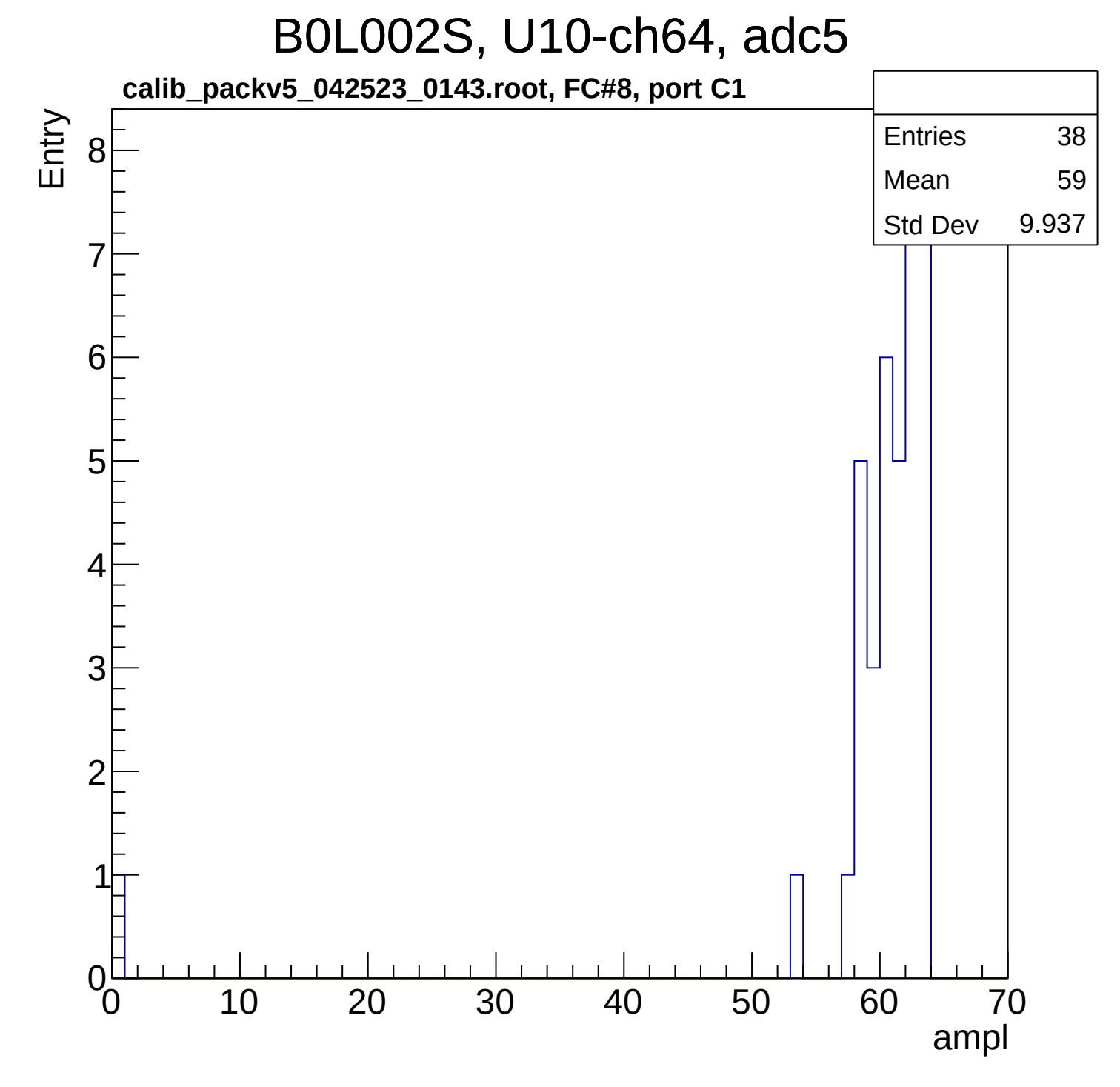

B0L002S, U10-ch64, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 28.47
Std Dev 6.625
Gaus mean : 30.2840
Gaus Width: 3.5326
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

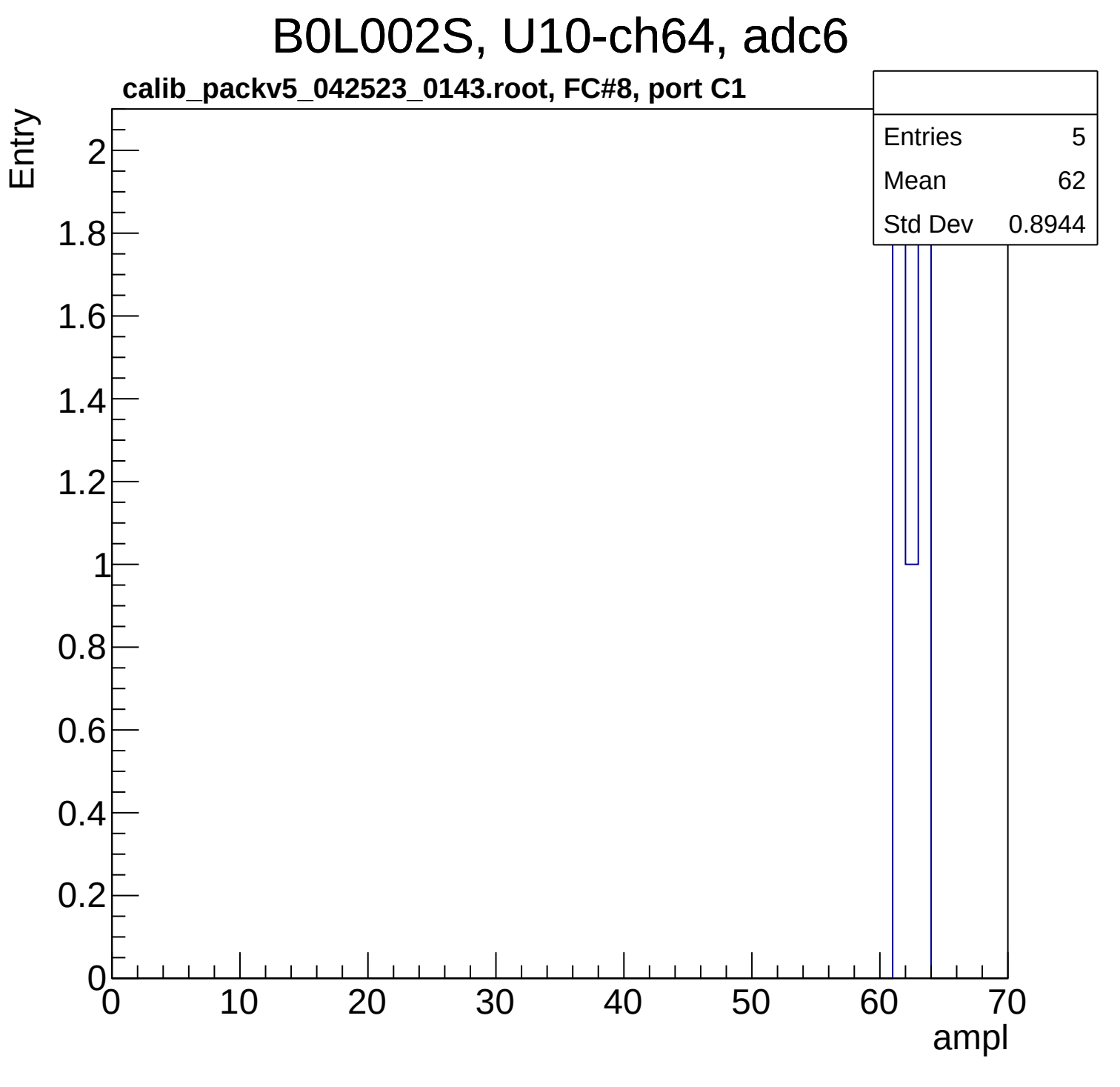

B0L002S, U10-ch64, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.17
Std Dev 3.331
Gaus mean : 37.3518
Gaus Width: 3.3259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

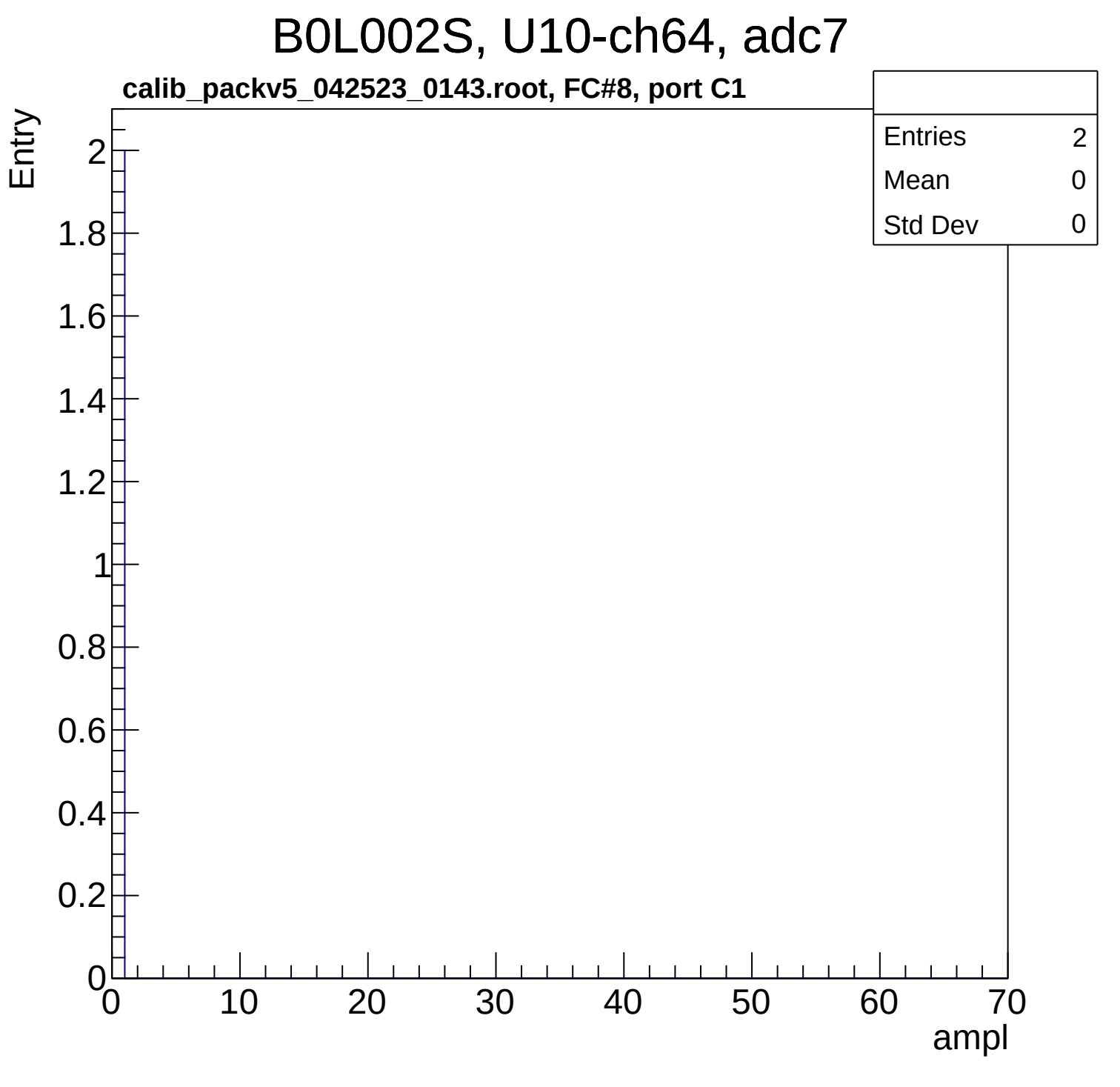

B0L002S, U10-ch64, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 43.26
Std Dev 3.32
Gaus mean : 44.7786
Gaus Width: 3.7992
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

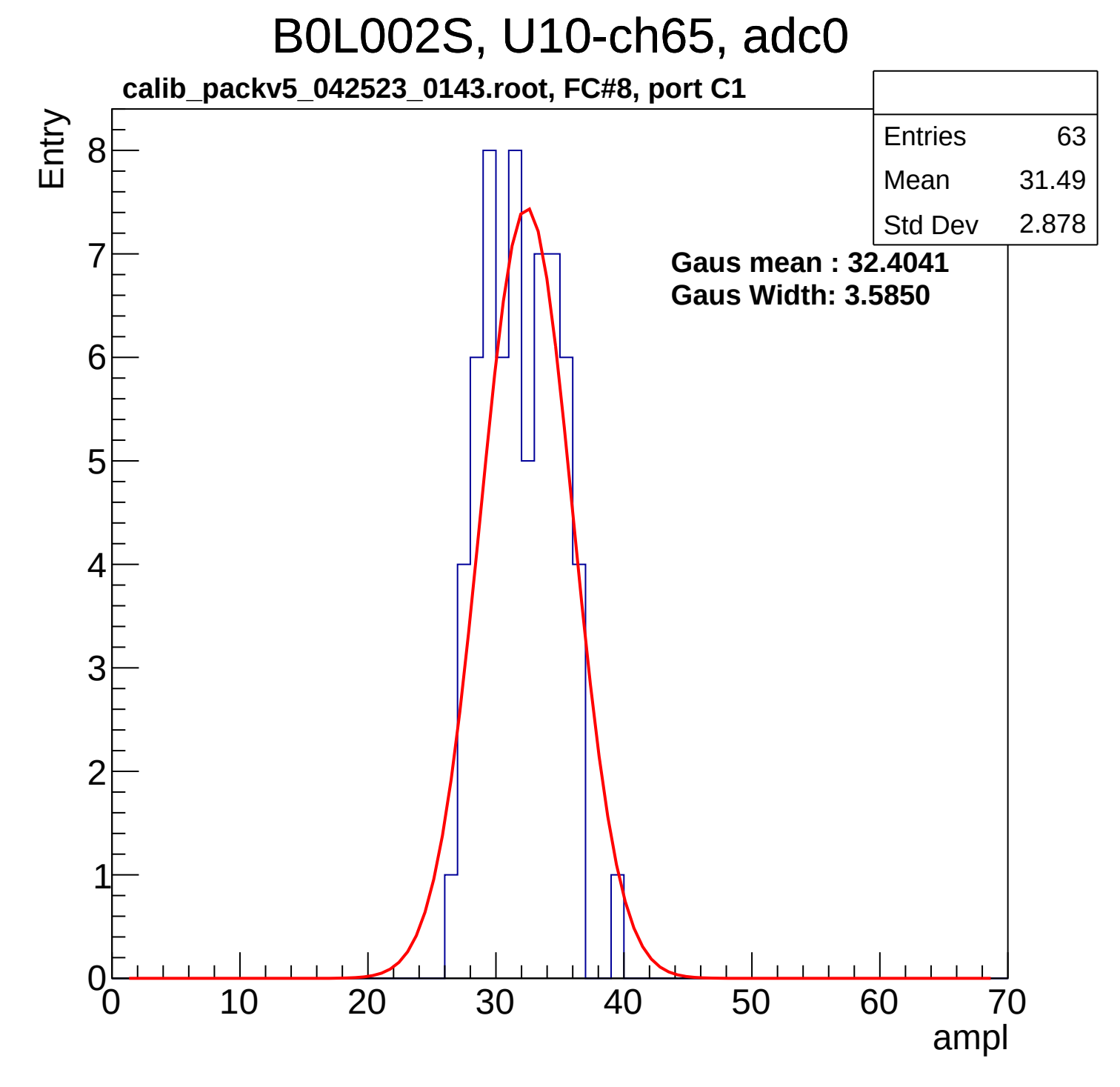

B0L002S, U10-ch64, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 50.4
Std Dev 2.993
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

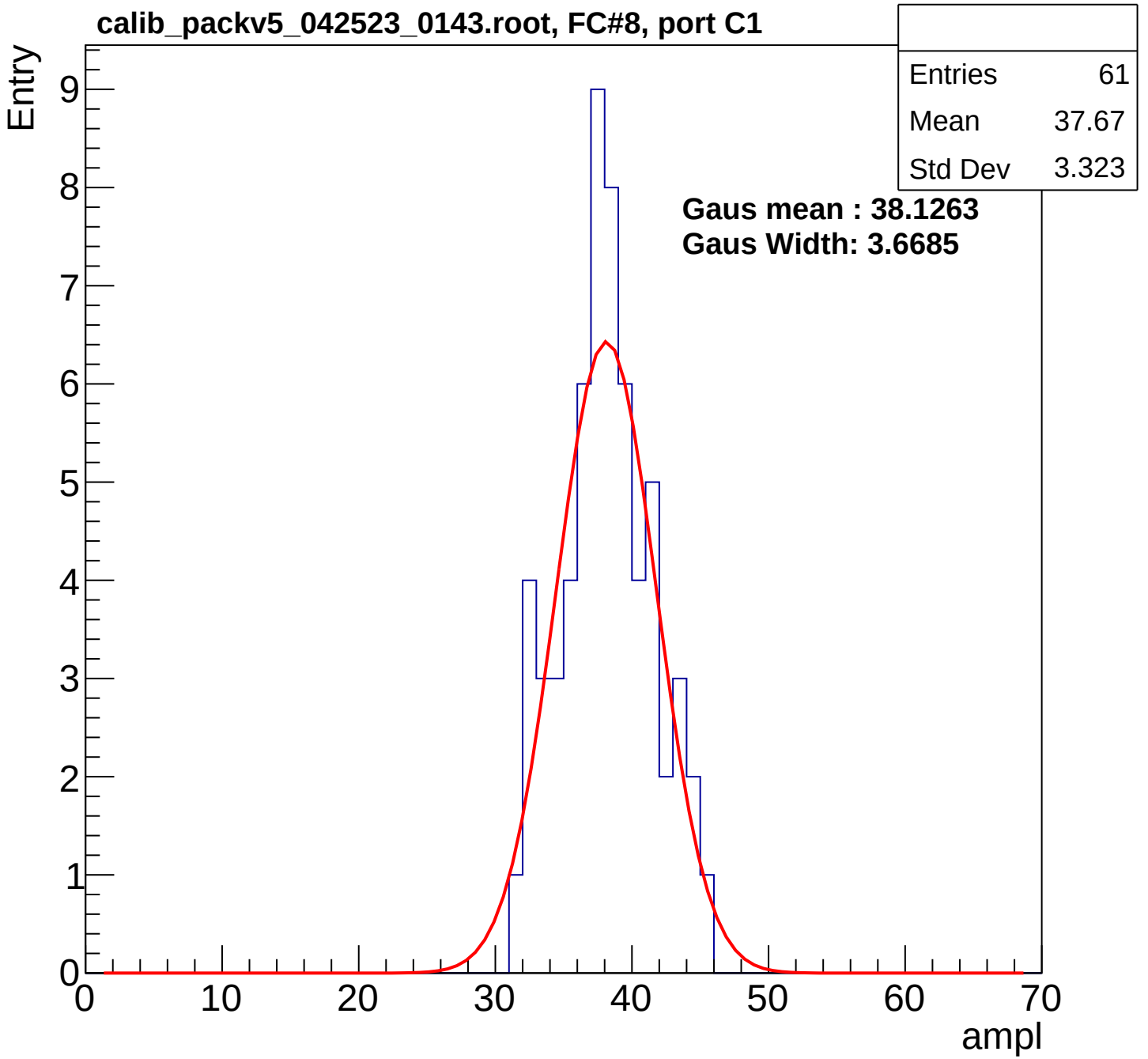

B0L002S, U10-ch64, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 56.33
Std Dev 2.892
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

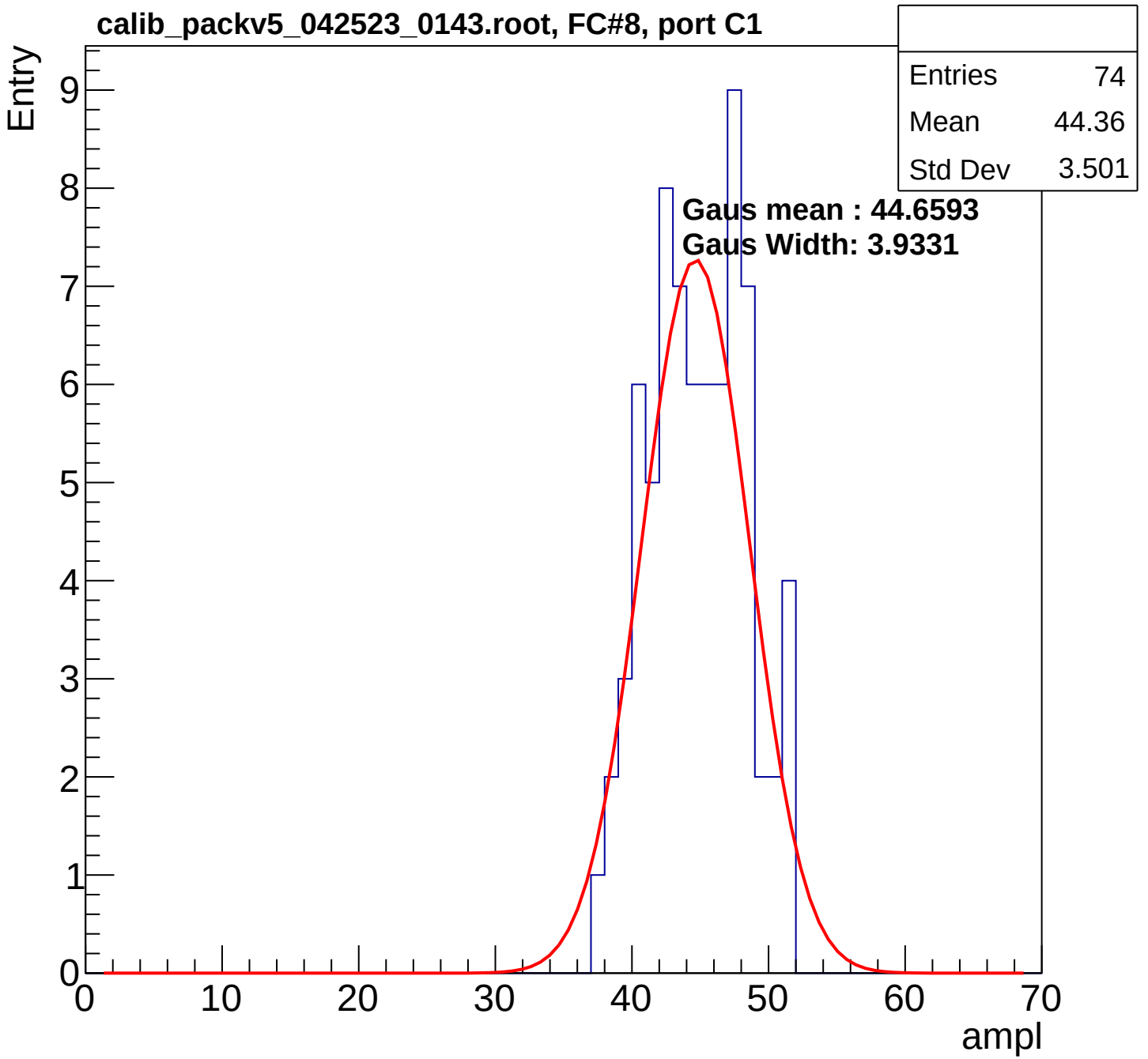

B0L002S, U10-ch64, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 59
Std Dev 9.937
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

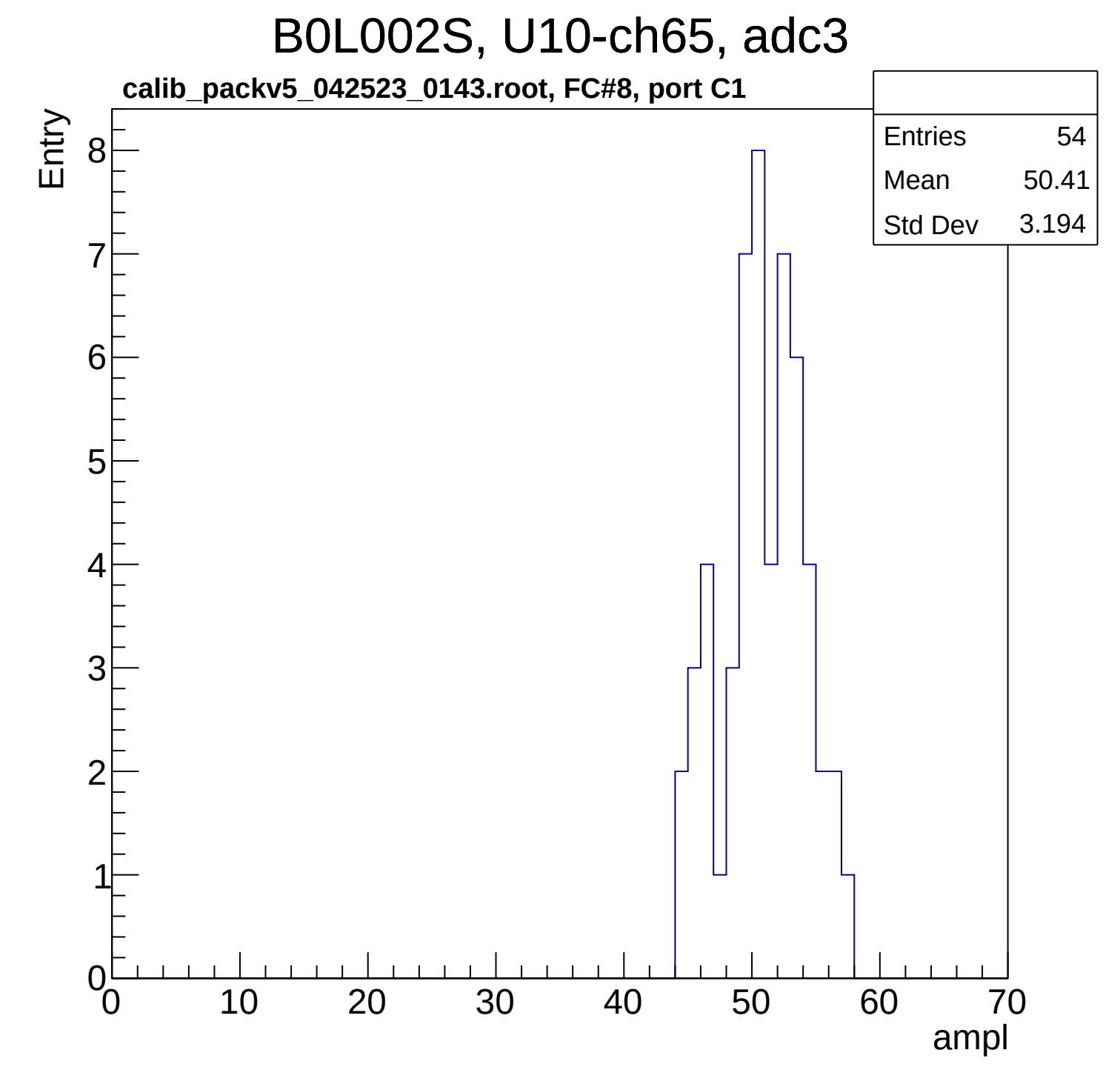

B0L002S, U10-ch64, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

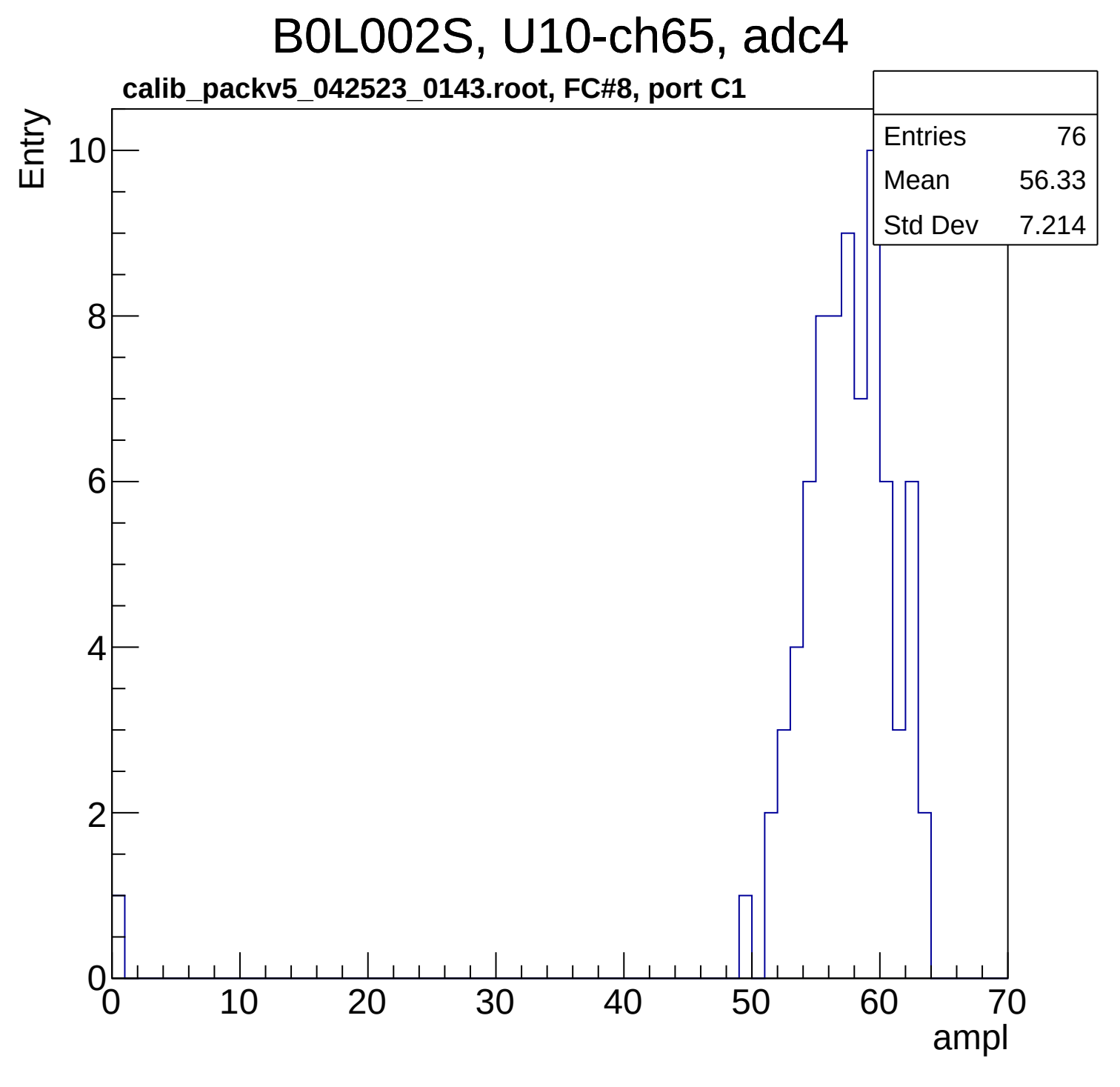

B0L002S, U10-ch64, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

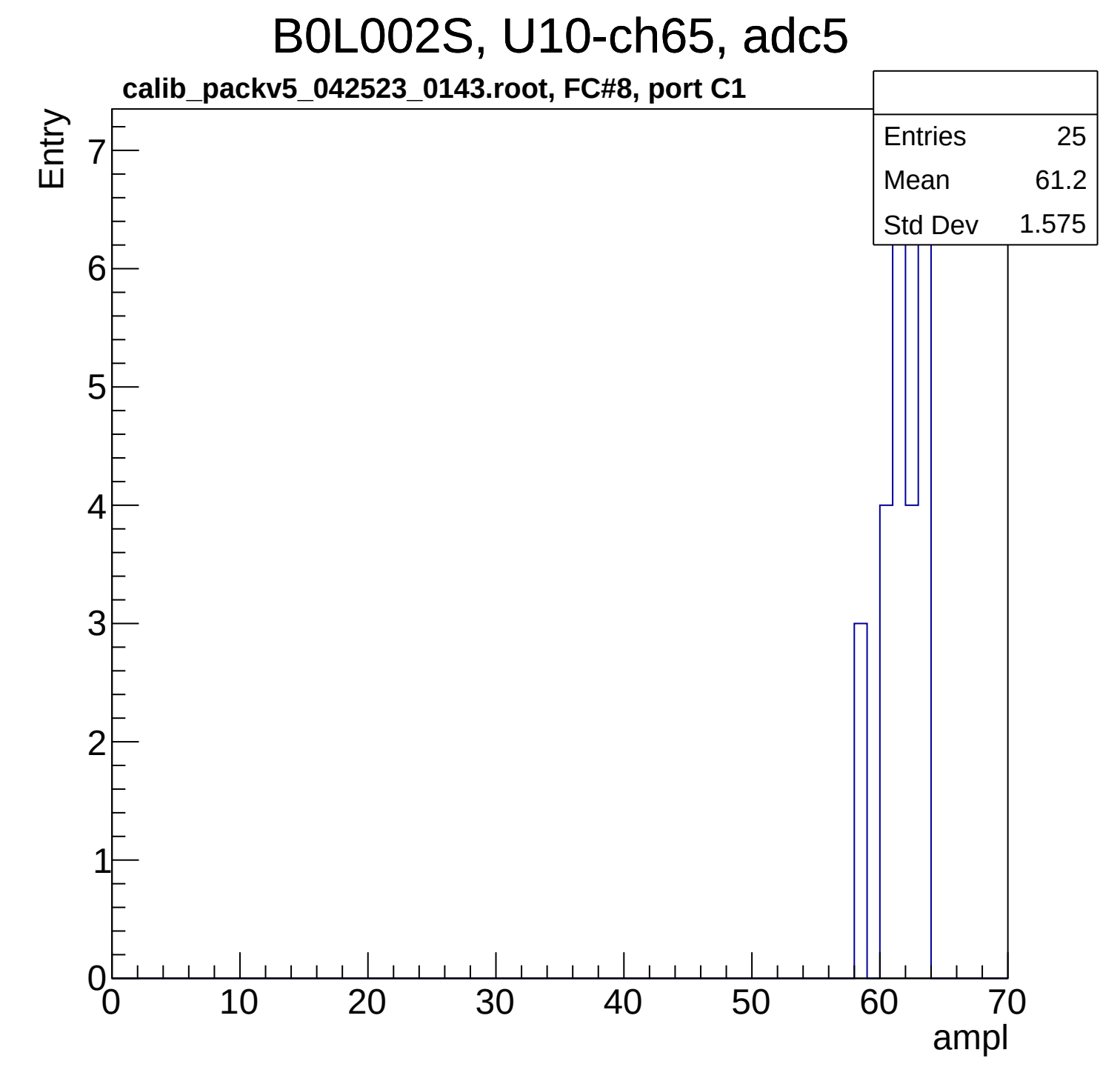

B0L002S, U10-ch65, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.49
Std Dev 2.878
Gaus mean : 32.4041
Gaus Width: 3.5850
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

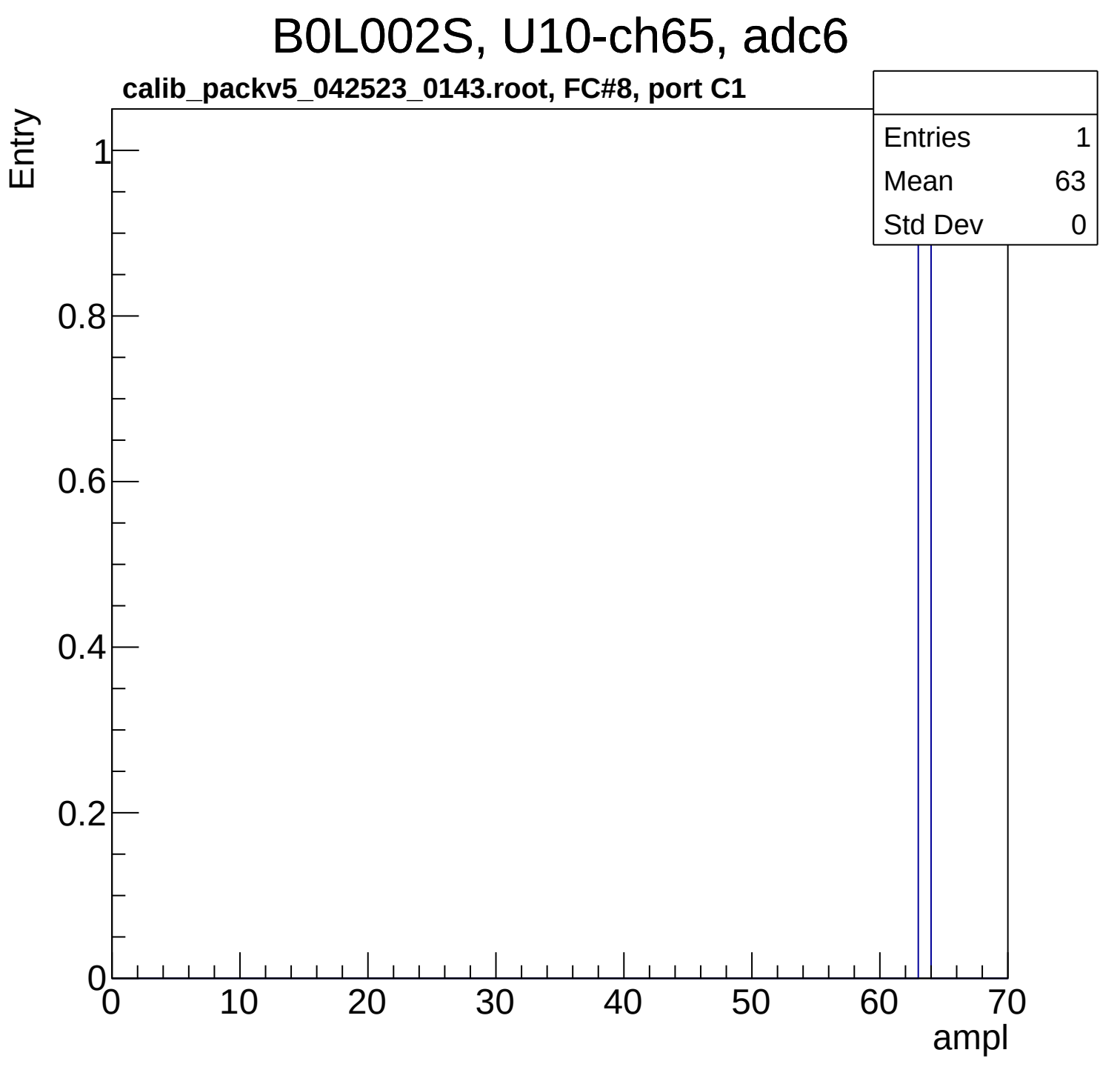

B0L002S, U10-ch65, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 37.67
Std Dev 3.323
Gaus mean : 38.1263
Gaus Width: 3.6685
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

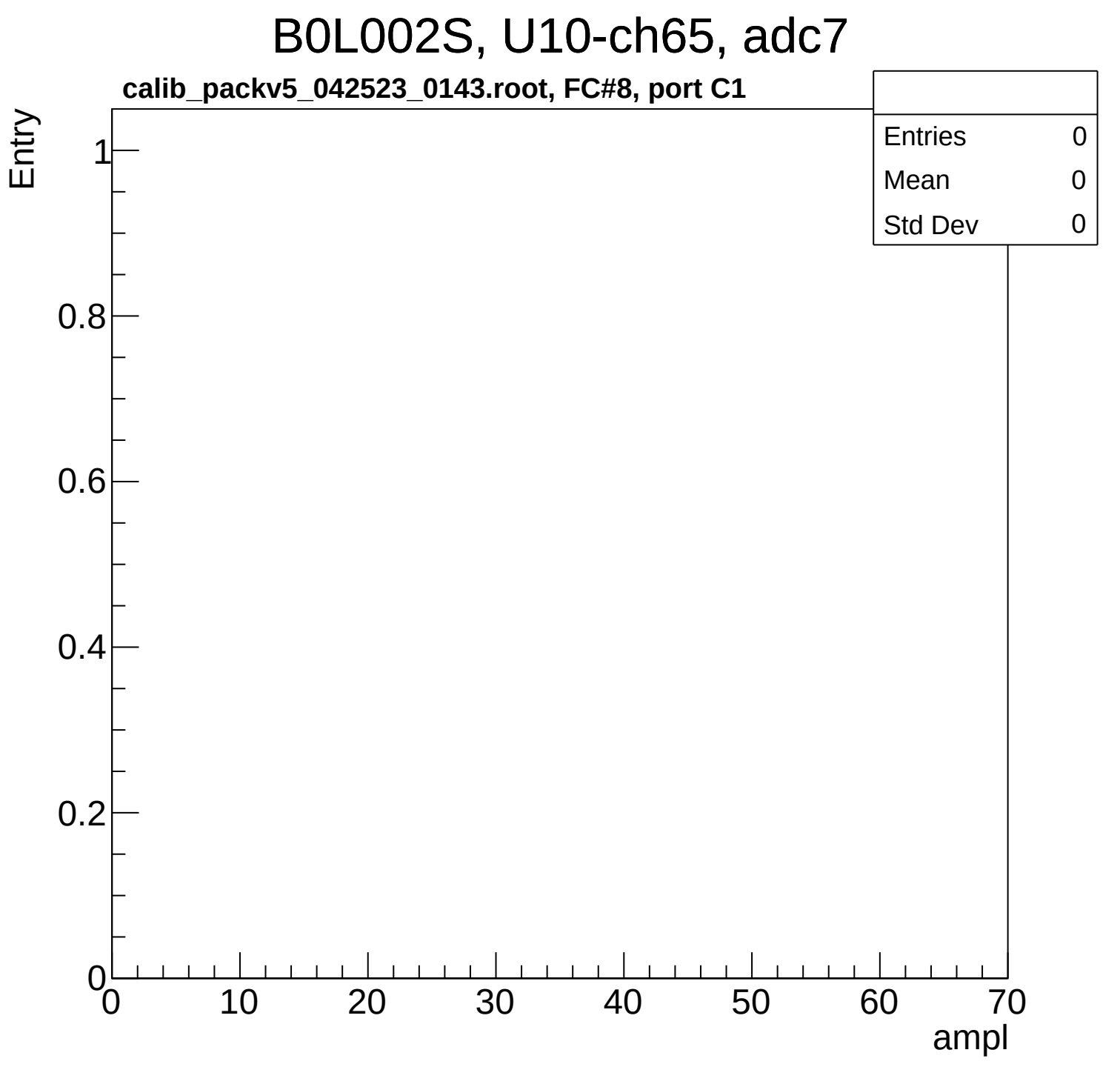

B0L002S, U10-ch65, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 44.36
Std Dev 3.501
Gaus mean : 44.6593
Gaus Width: 3.9331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

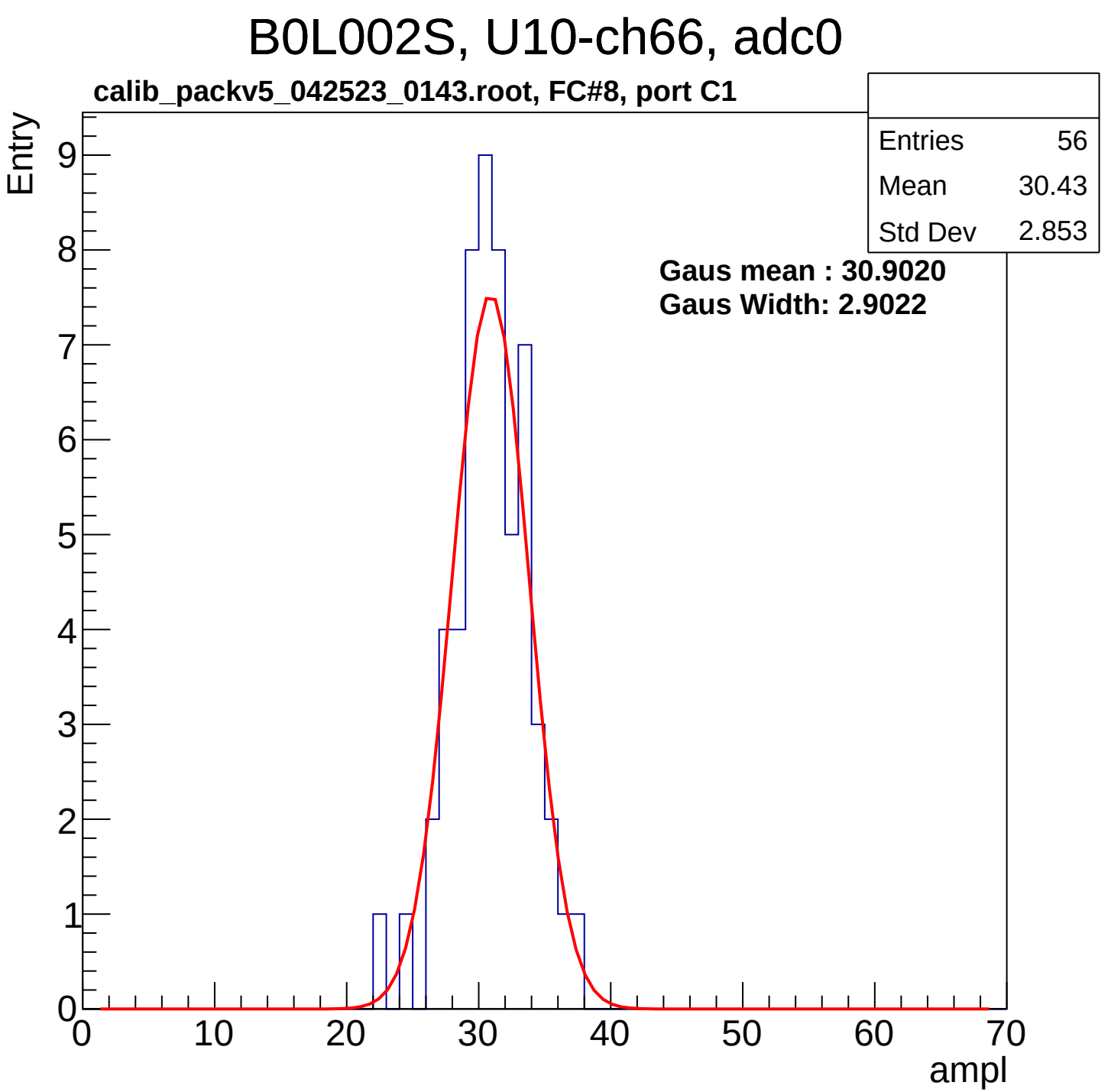

B0L002S, U10-ch65, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 50.41
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

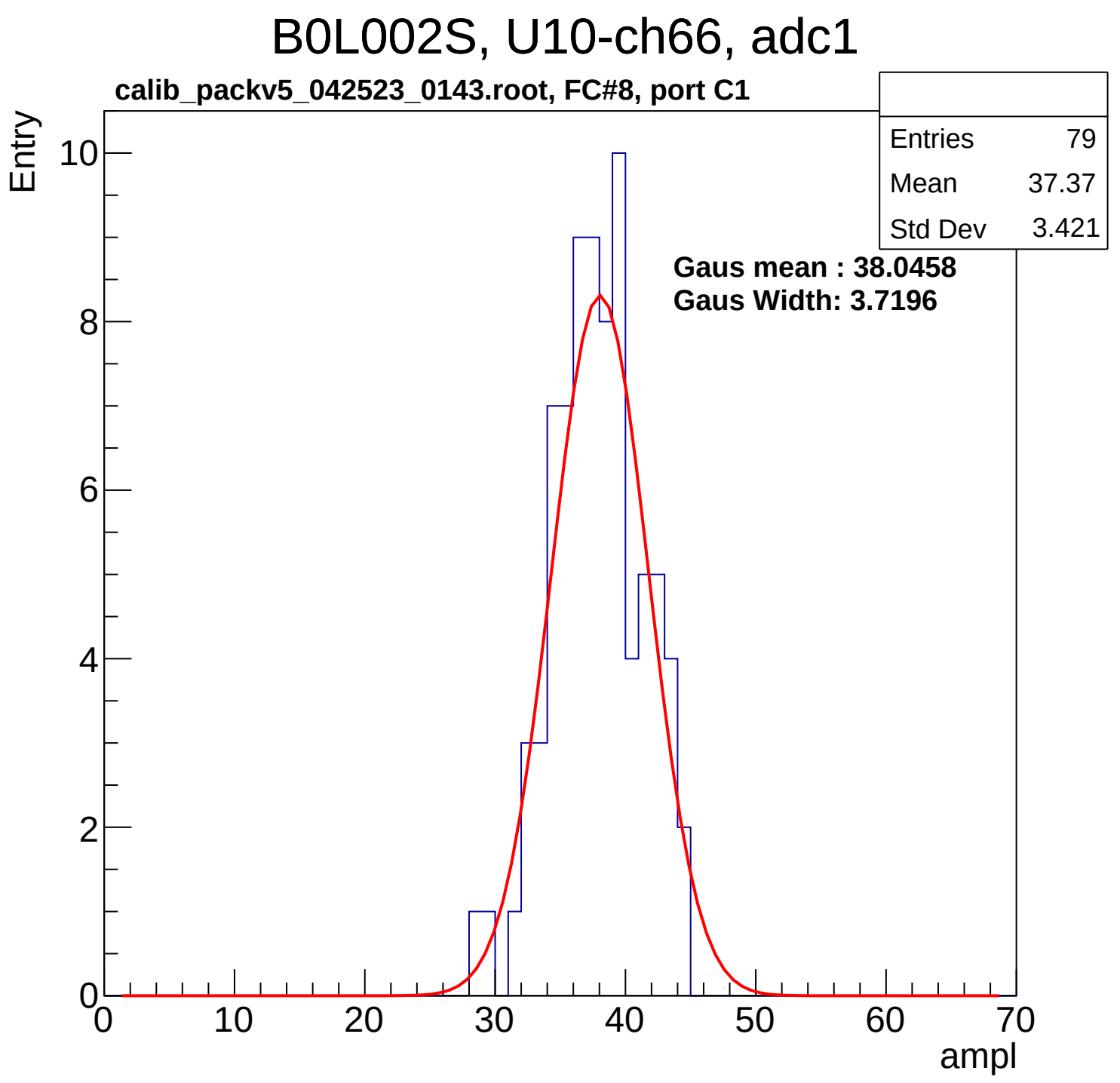

B0L002S, U10-ch65, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 56.33
Std Dev 7.214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

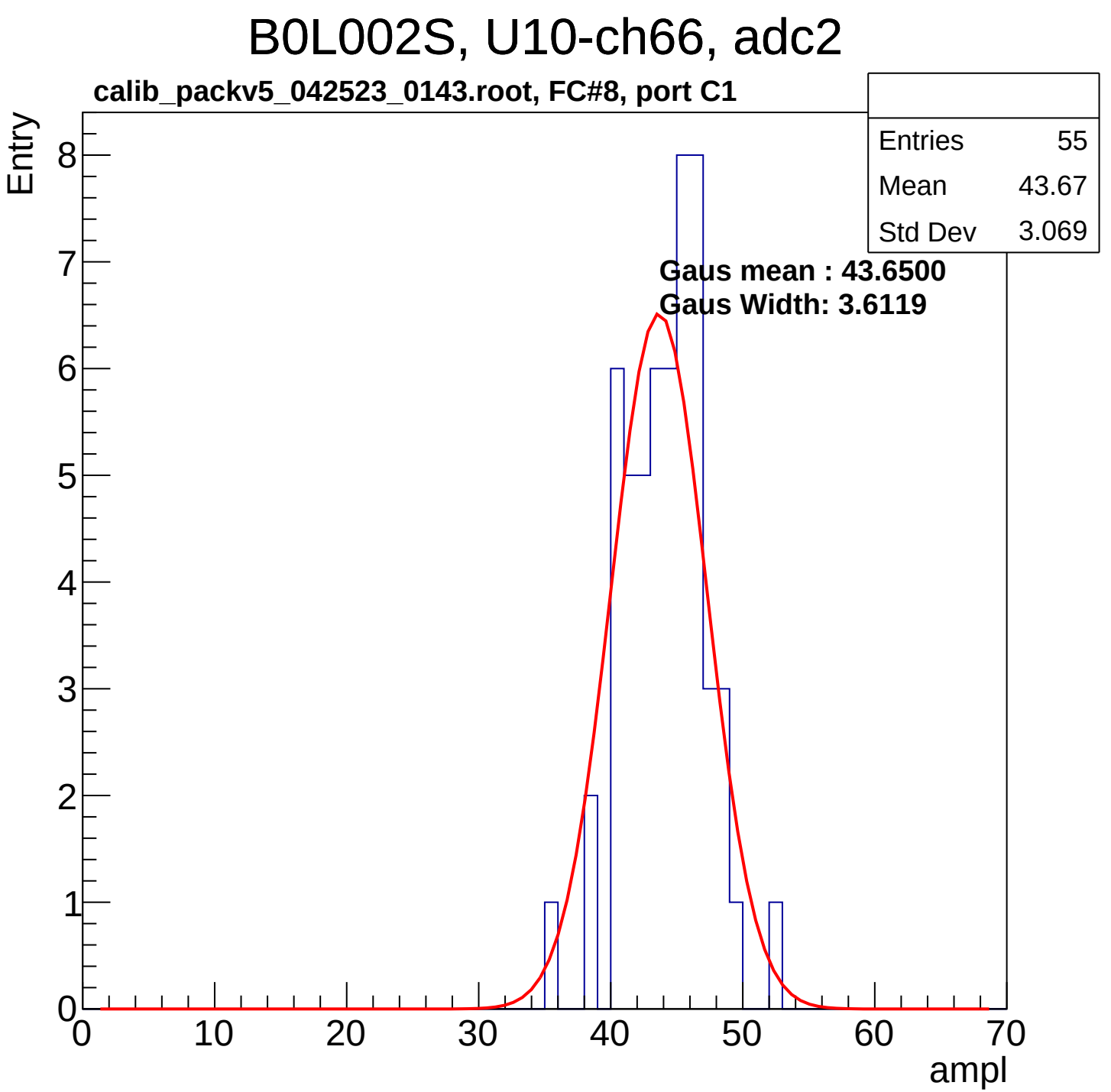

B0L002S, U10-ch65, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 61.2
Std Dev 1.575
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

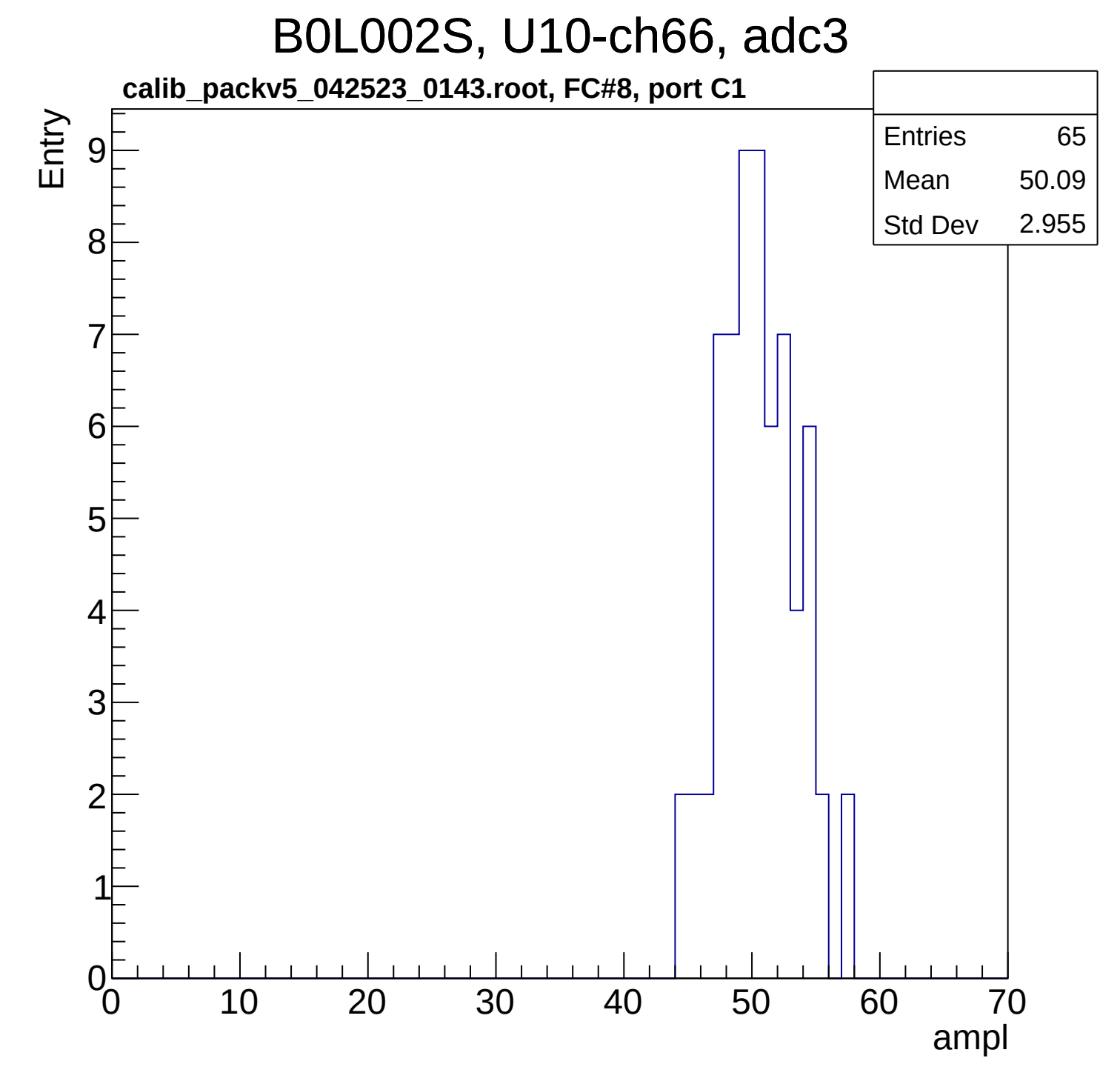

B0L002S, U10-ch65, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

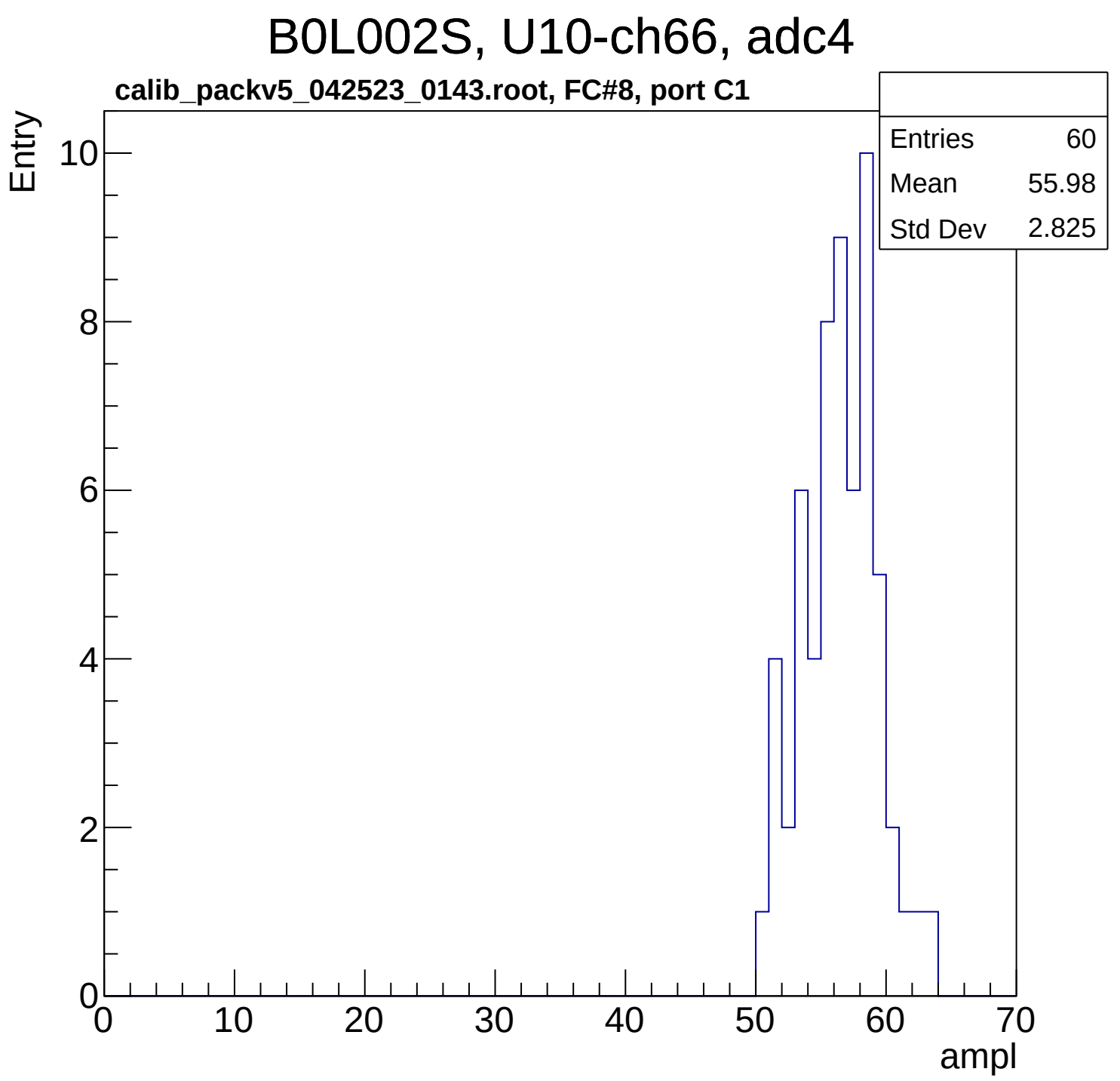

B0L002S, U10-ch65, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

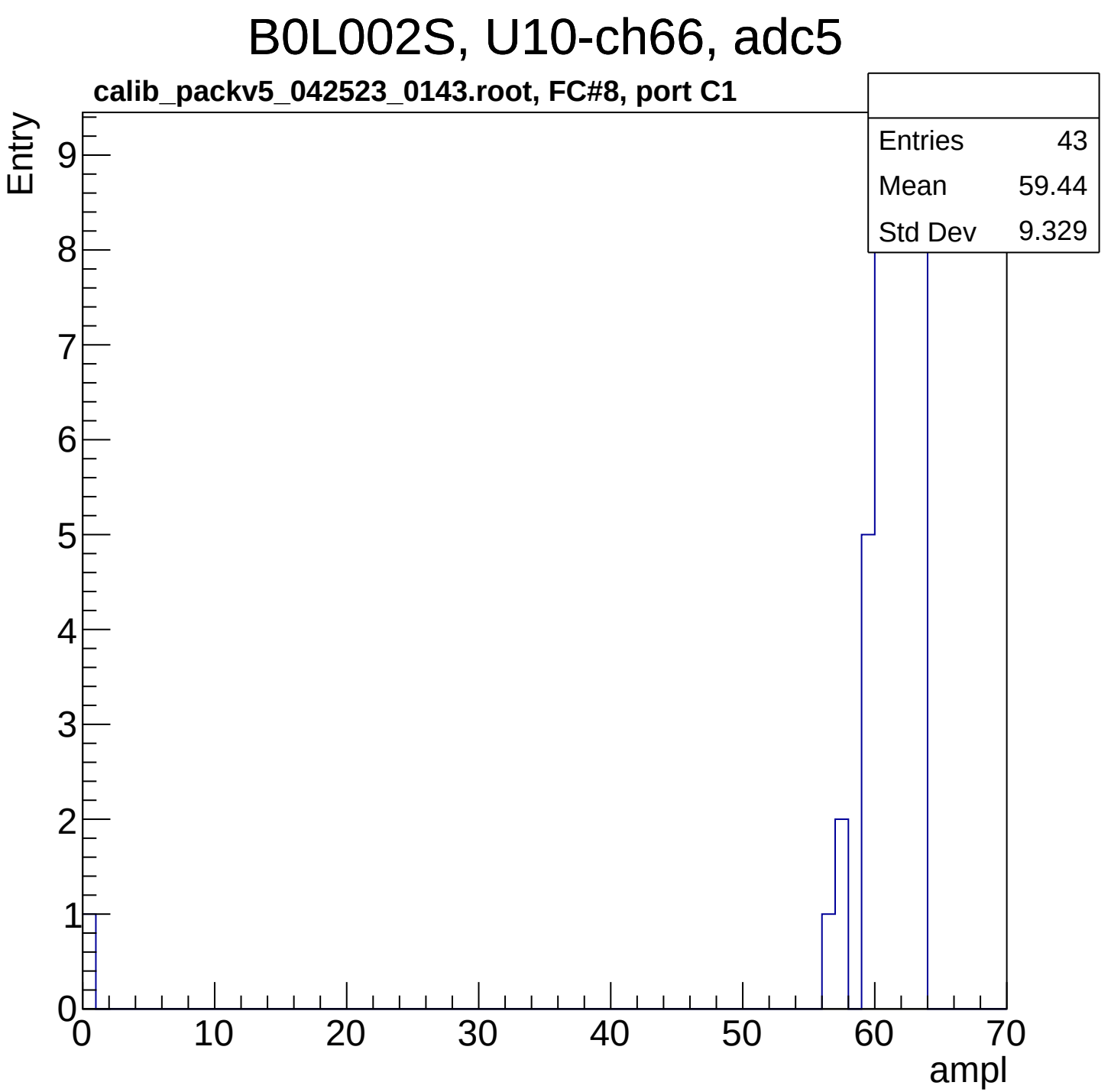

B0L002S, U10-ch66, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 30.43
Std Dev 2.853
Gaus mean : 30.9020
Gaus Width: 2.9022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

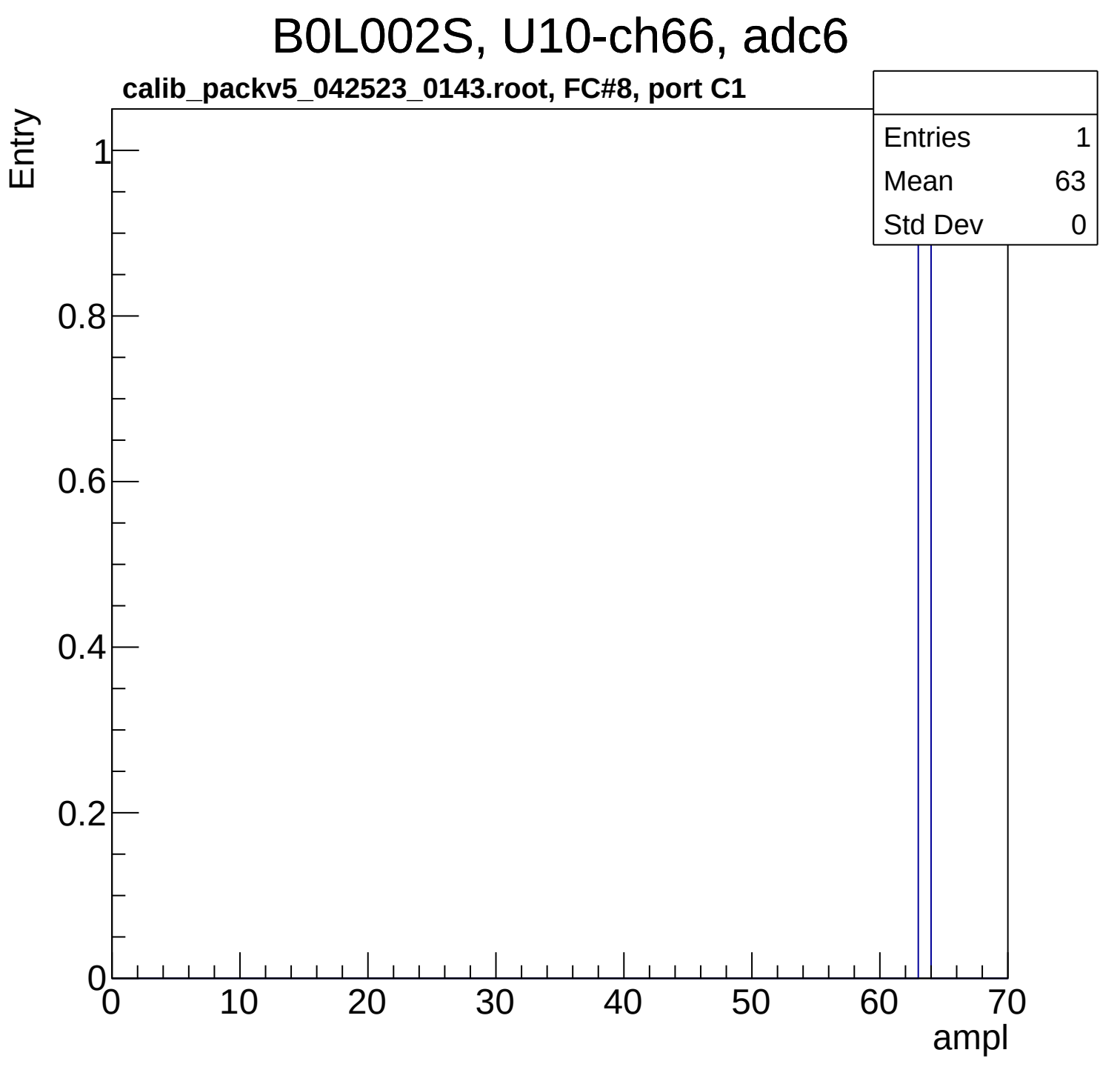

B0L002S, U10-ch66, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 37.37
Std Dev 3.421
Gaus mean : 38.0458
Gaus Width: 3.7196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

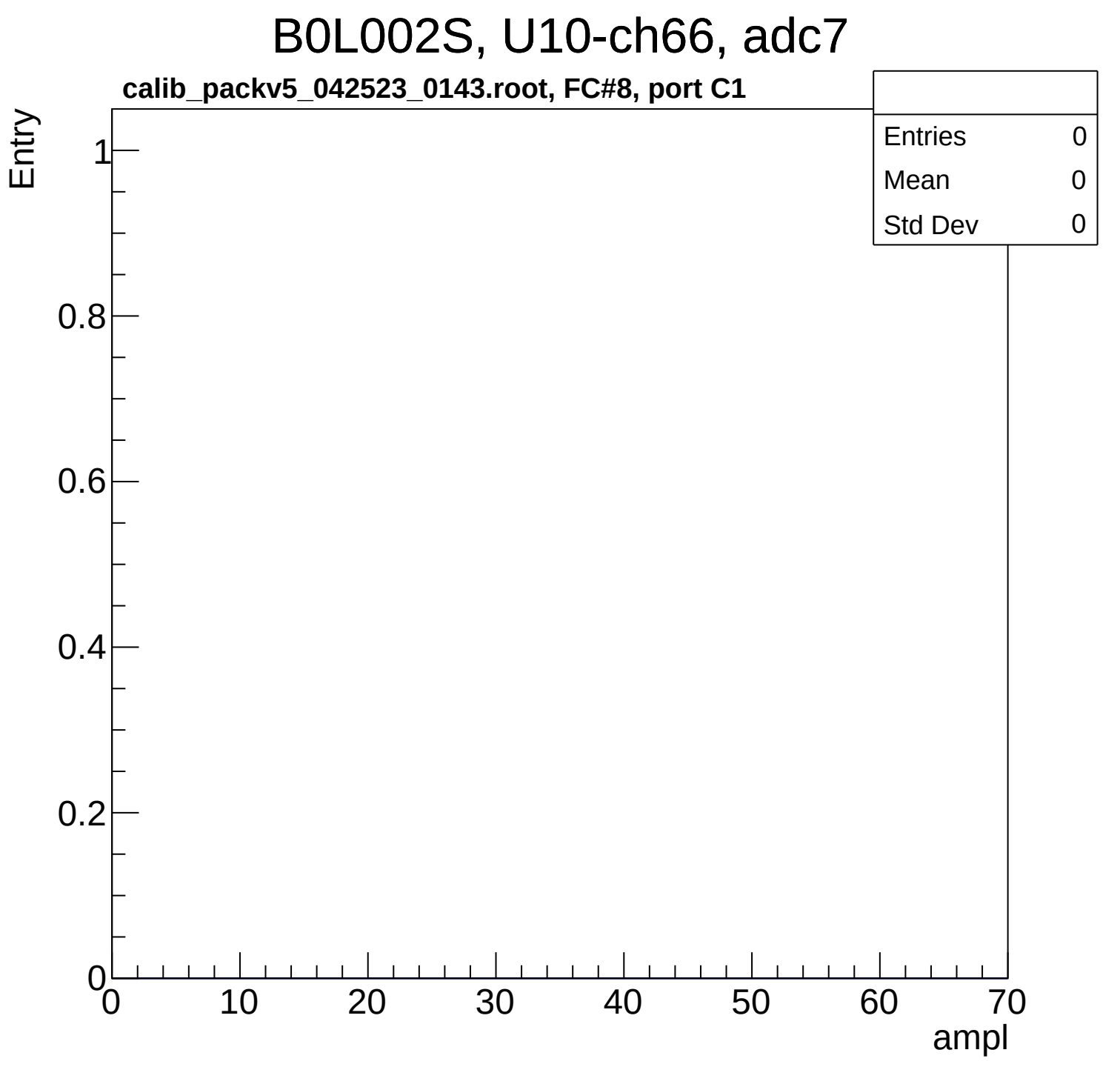

B0L002S, U10-ch66, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 43.67
Std Dev 3.069
Gaus mean : 43.6500
Gaus Width: 3.6119
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

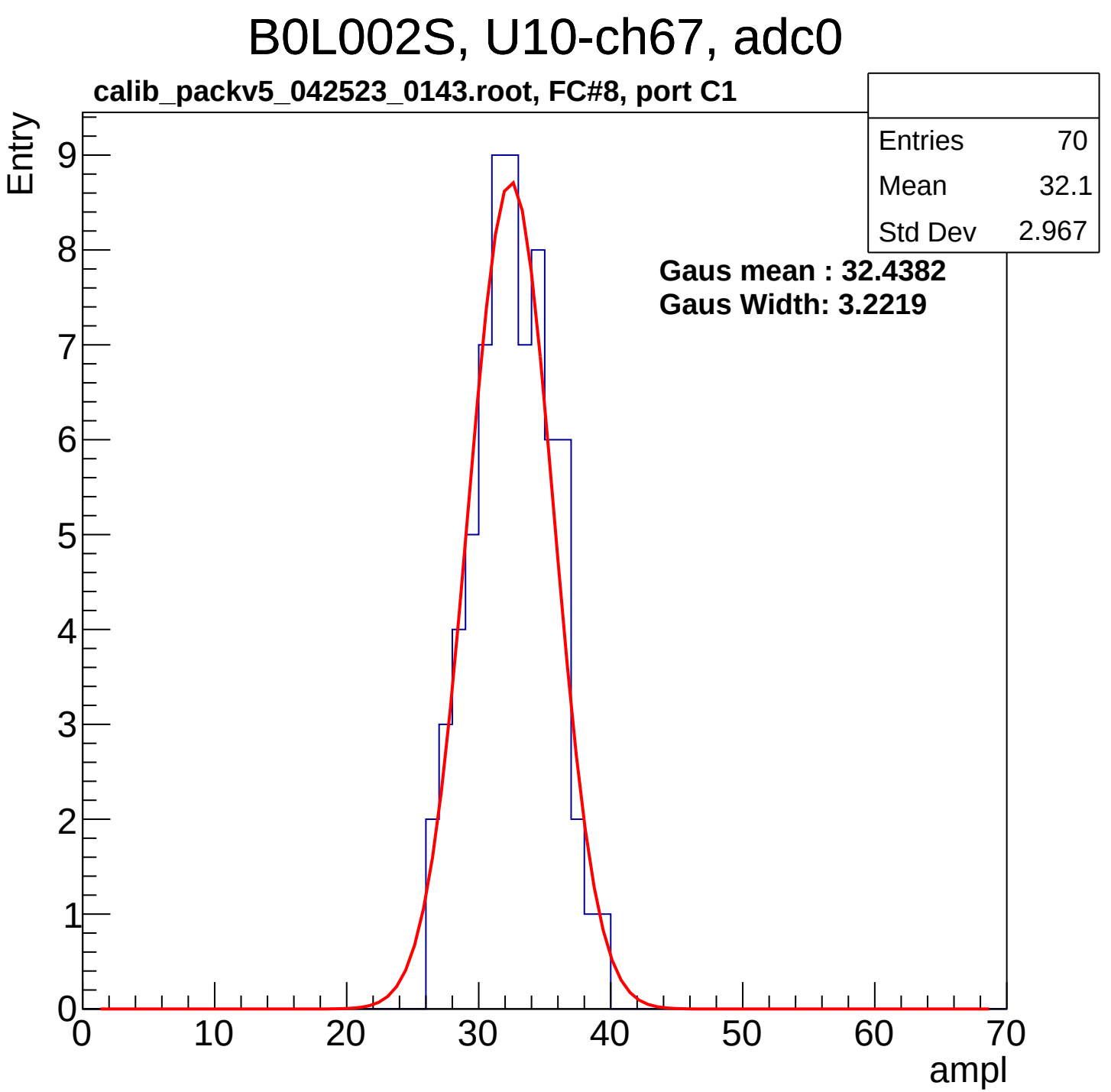

B0L002S, U10-ch66, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 50.09
Std Dev 2.955
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

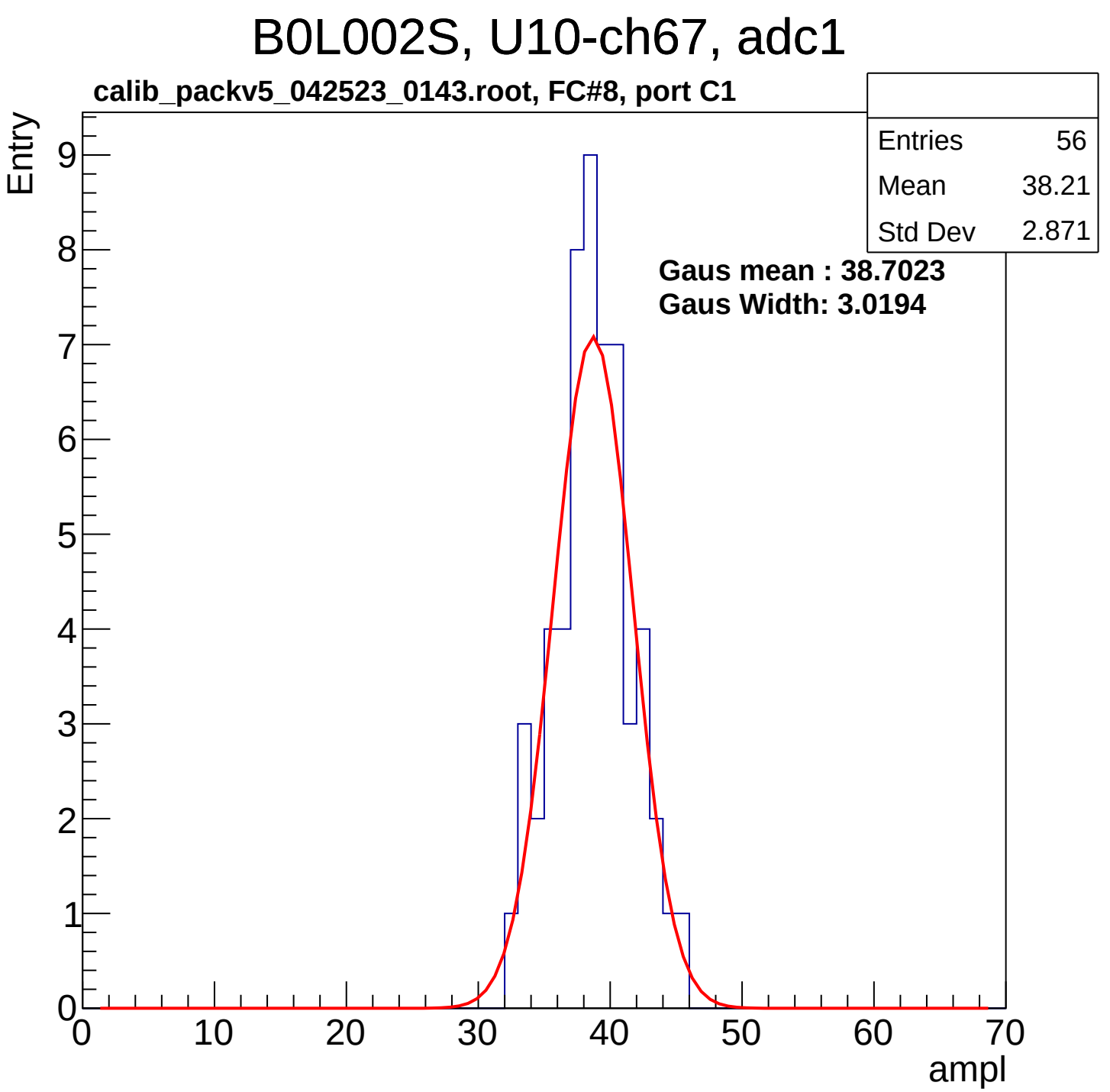

B0L002S, U10-ch66, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 55.98
Std Dev 2.825
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

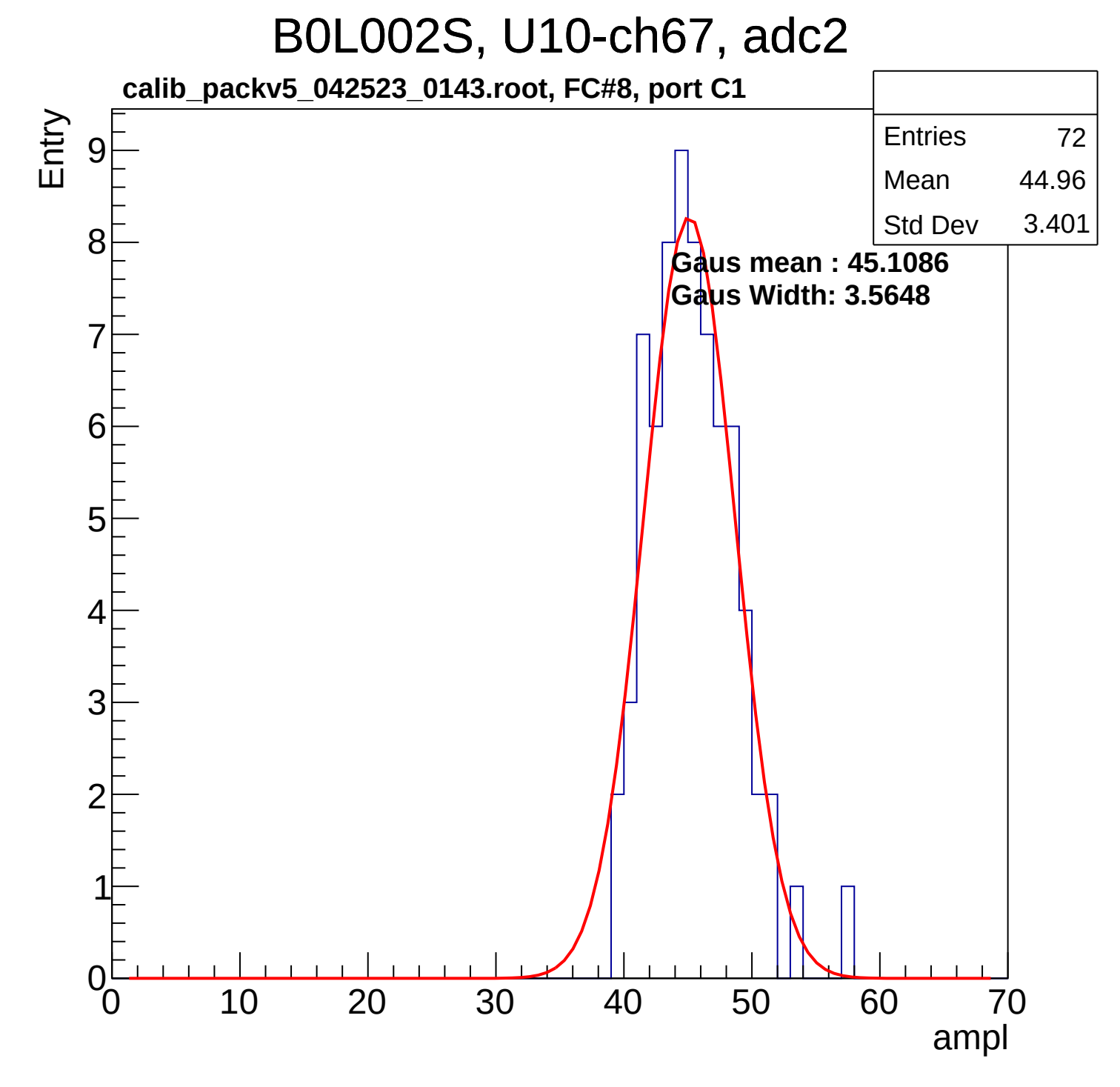

B0L002S, U10-ch66, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.44
Std Dev 9.329
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

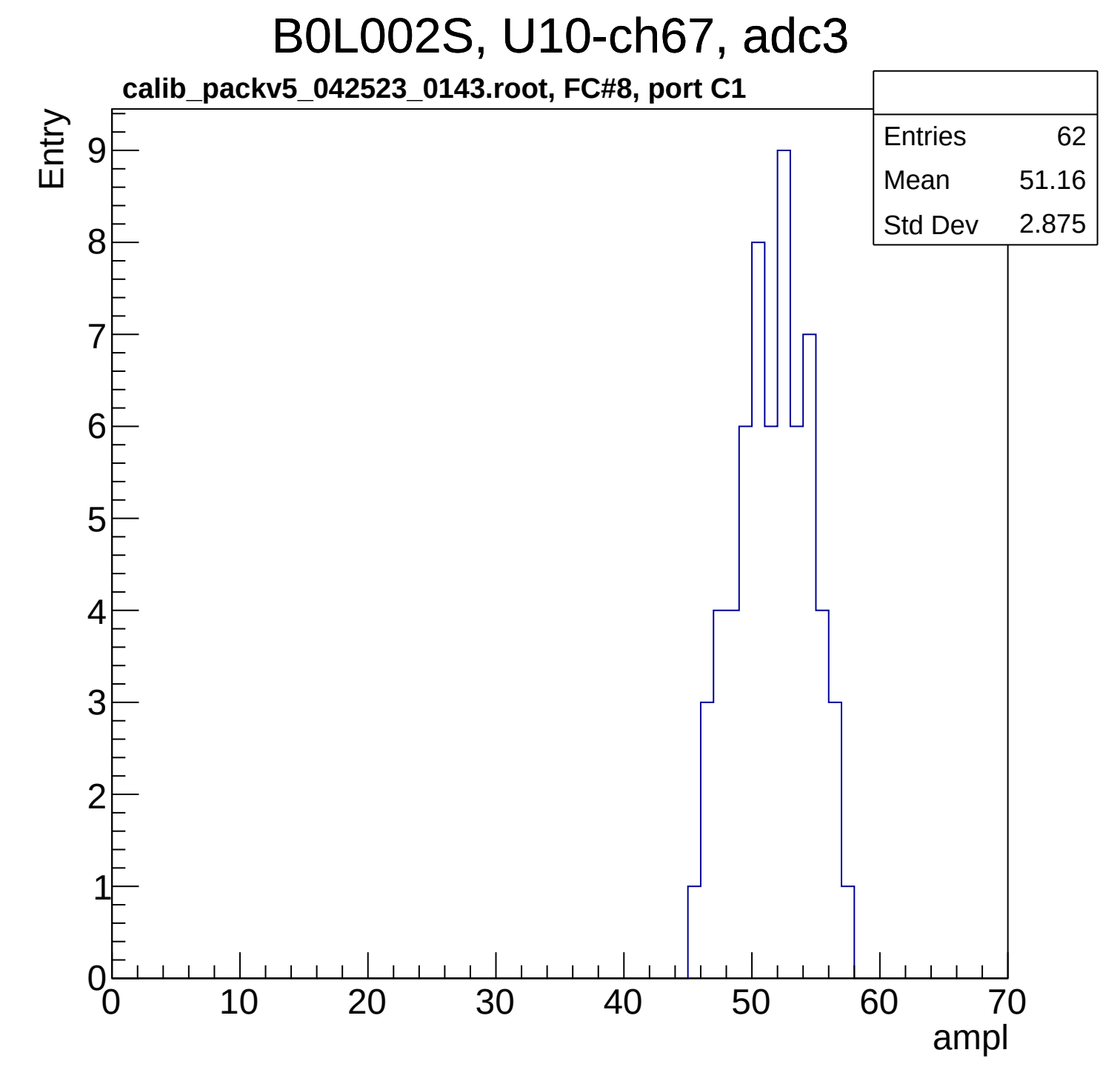

B0L002S, U10-ch66, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

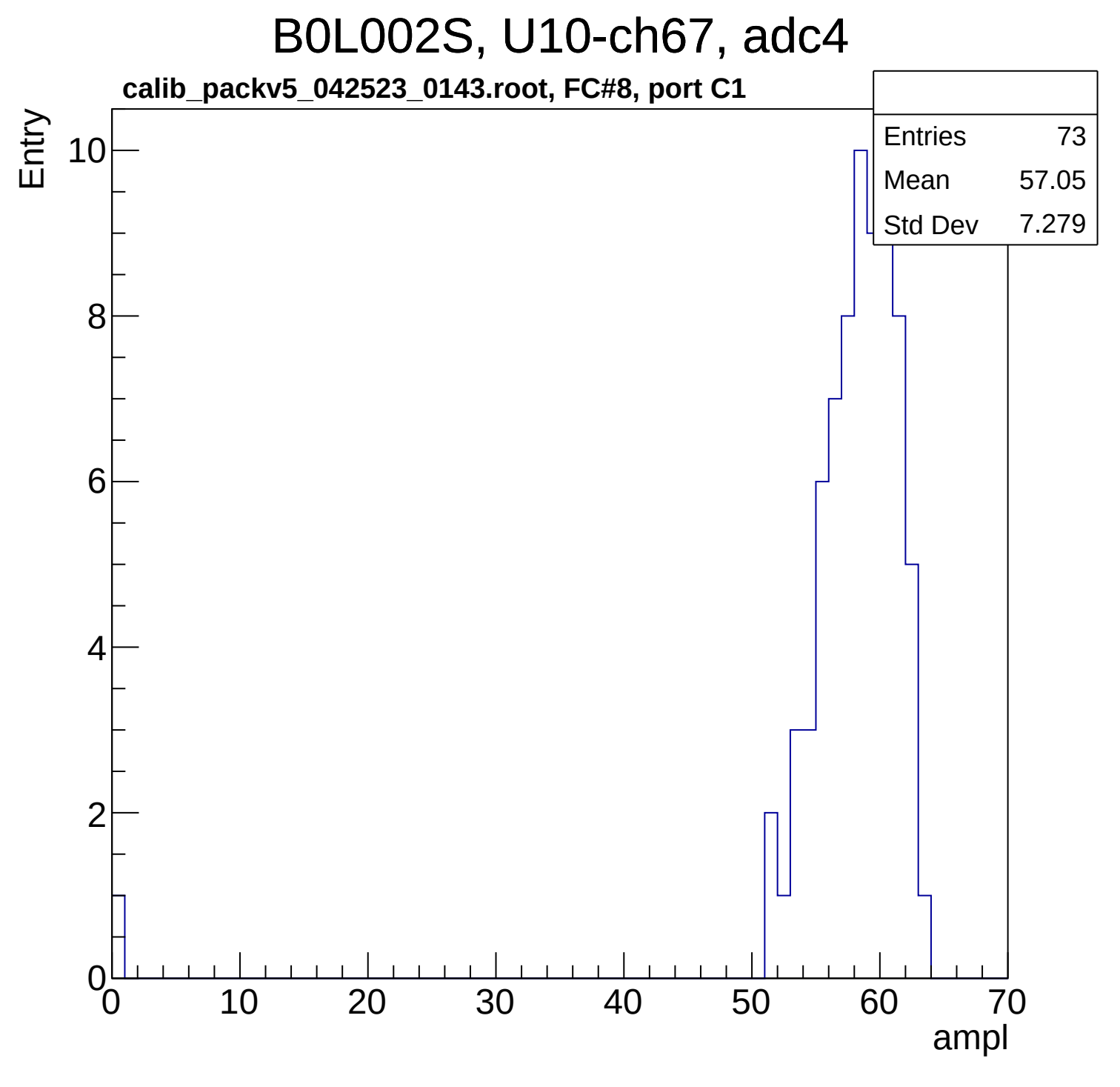

B0L002S, U10-ch66, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

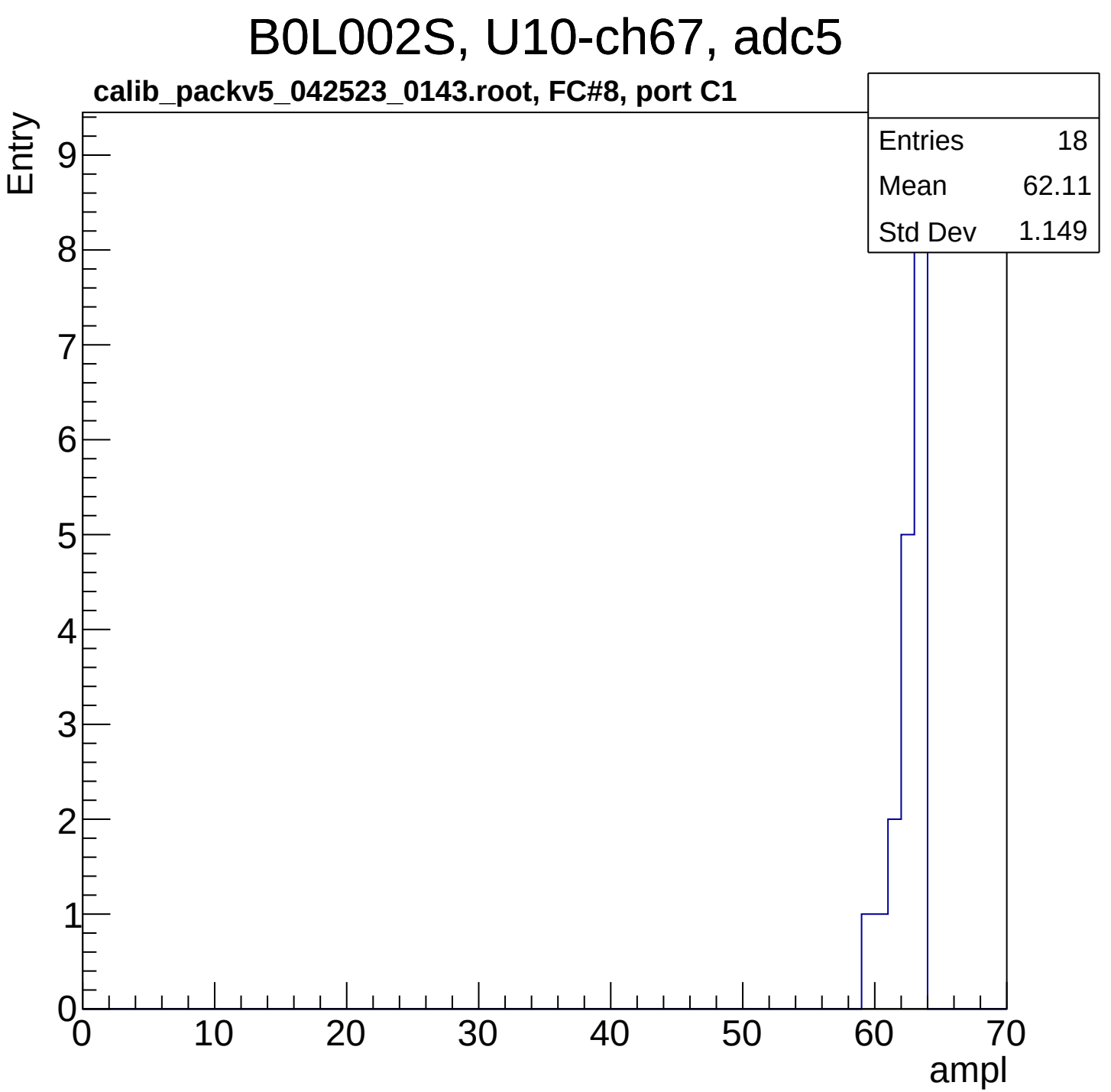

B0L002S, U10-ch67, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 32.1
Std Dev 2.967
Gaus mean : 32.4382
Gaus Width: 3.2219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

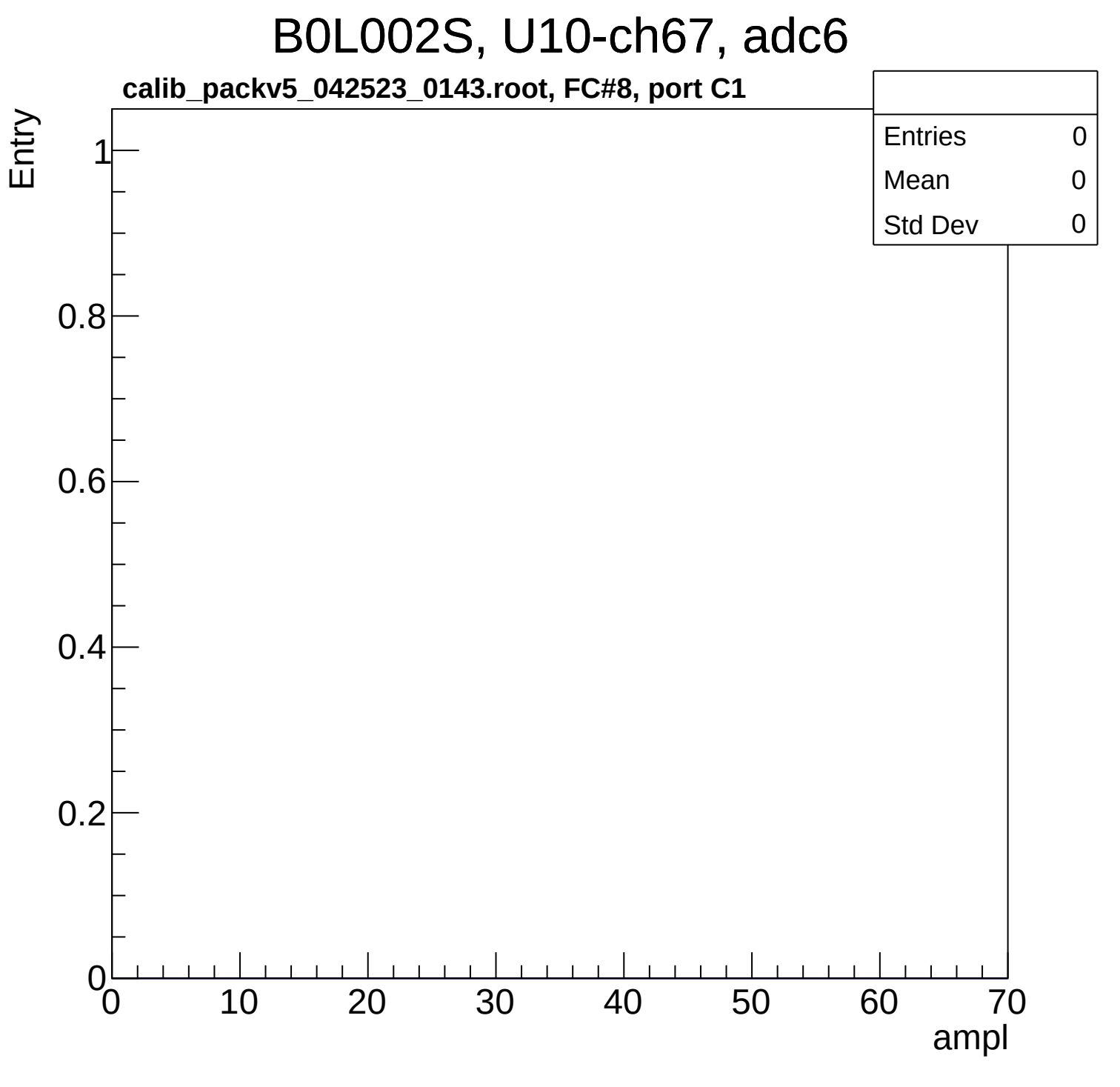

B0L002S, U10-ch67, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 38.21
Std Dev 2.871
Gaus mean : 38.7023
Gaus Width: 3.0194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

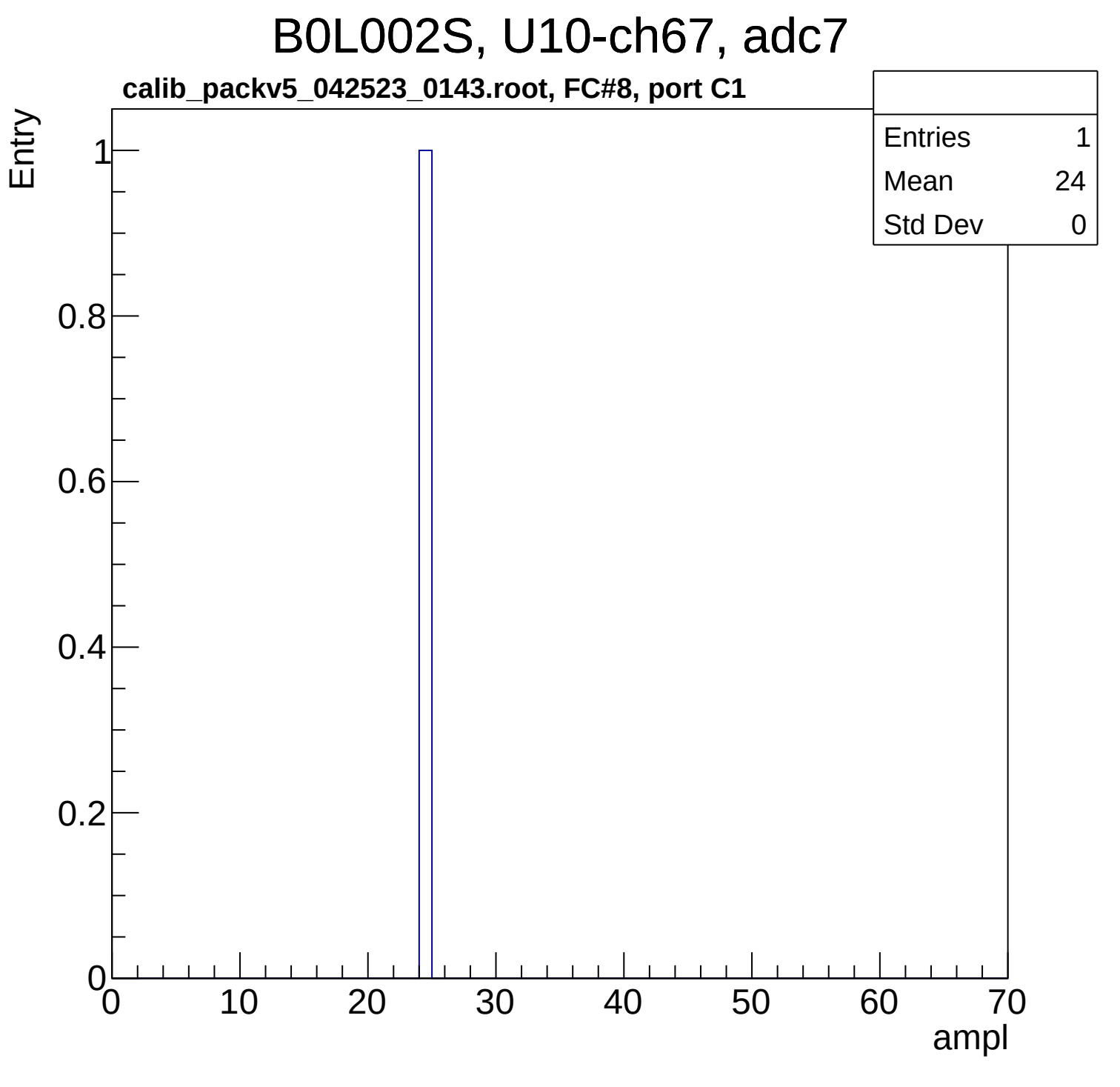

B0L002S, U10-ch67, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 44.96
Std Dev 3.401
Gaus mean : 45.1086
Gaus Width: 3.5648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

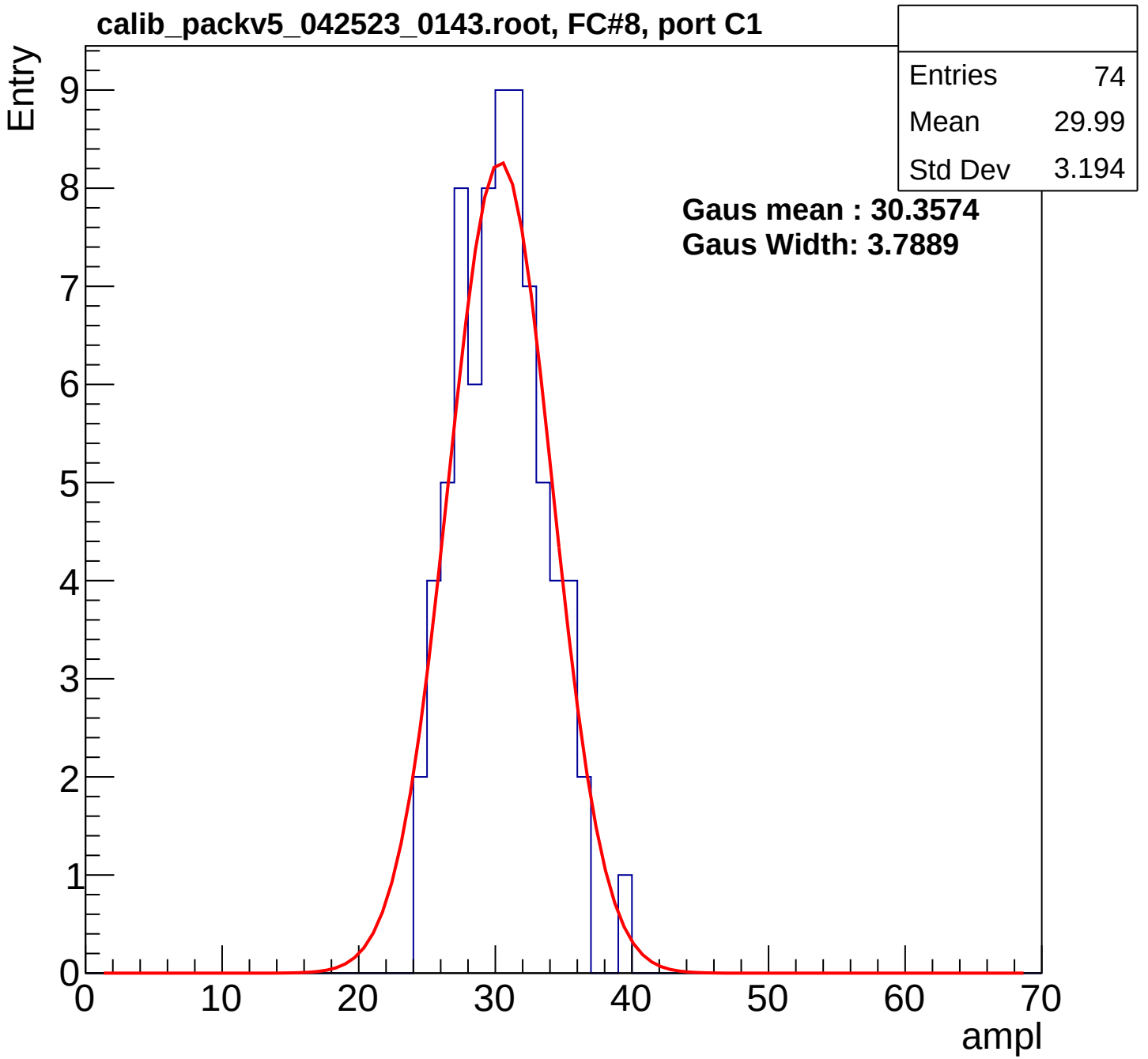

B0L002S, U10-ch67, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.16
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

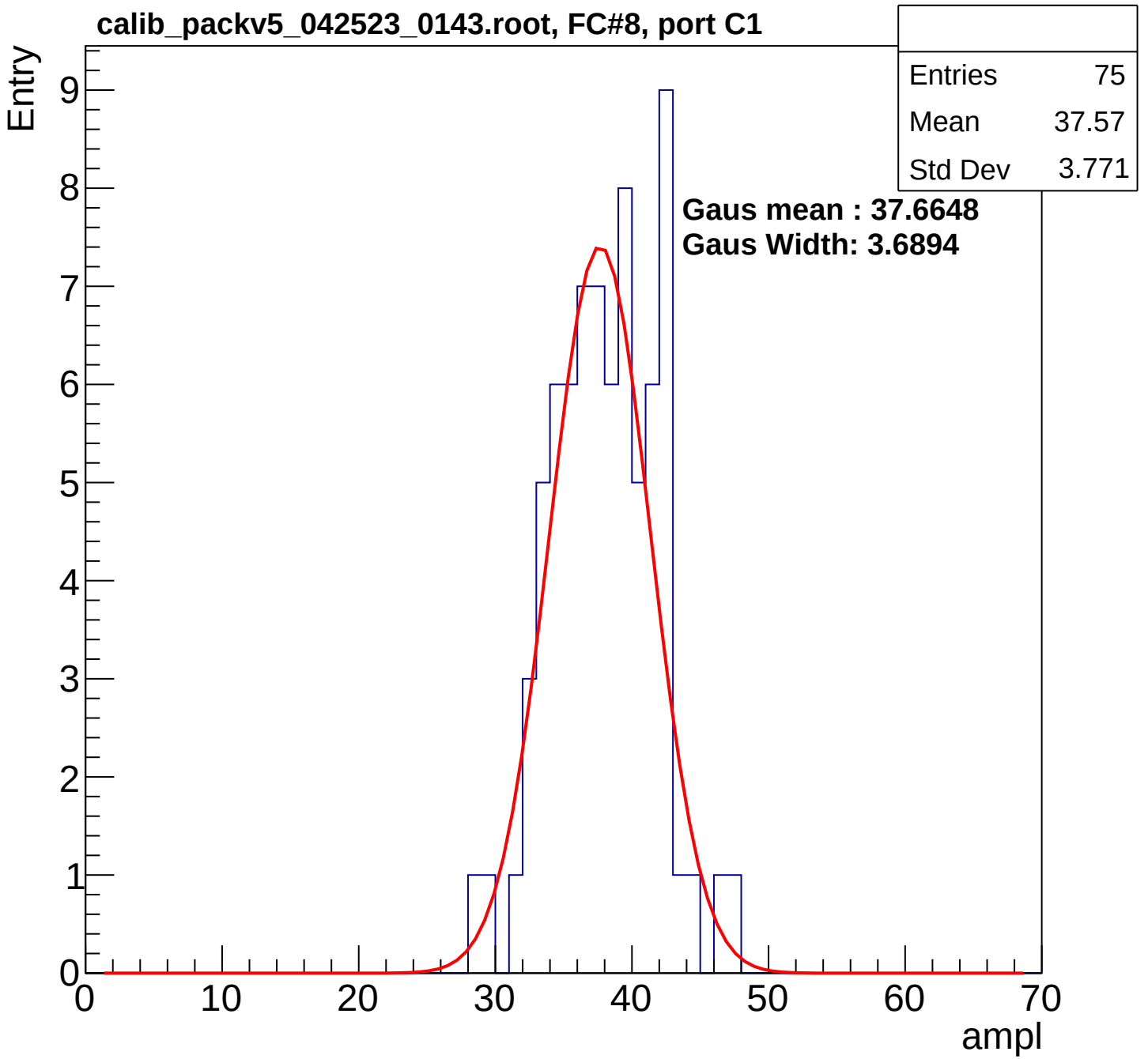

B0L002S, U10-ch67, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 57.05
Std Dev 7.279
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

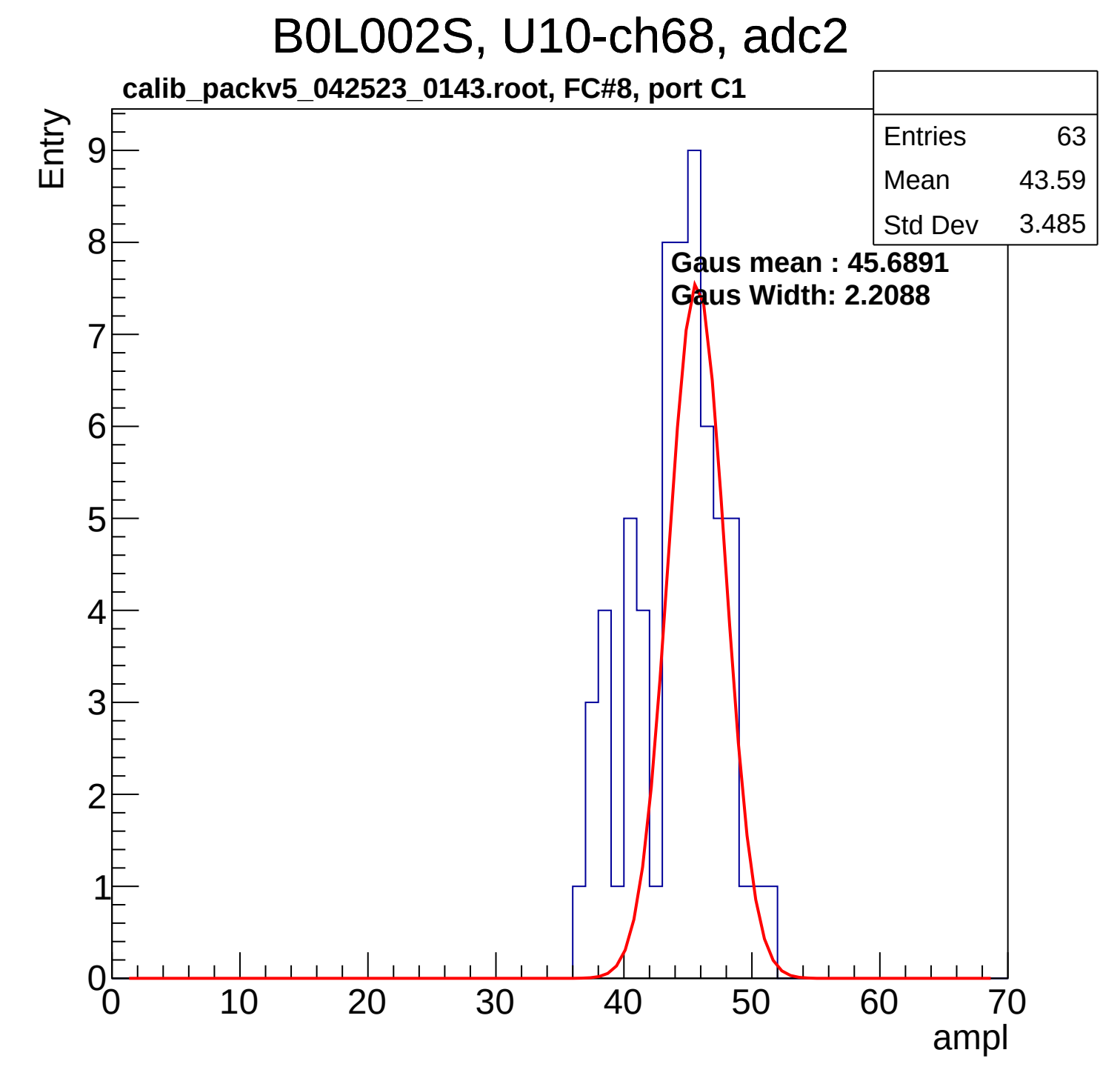

B0L002S, U10-ch67, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 62.11
Std Dev 1.149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

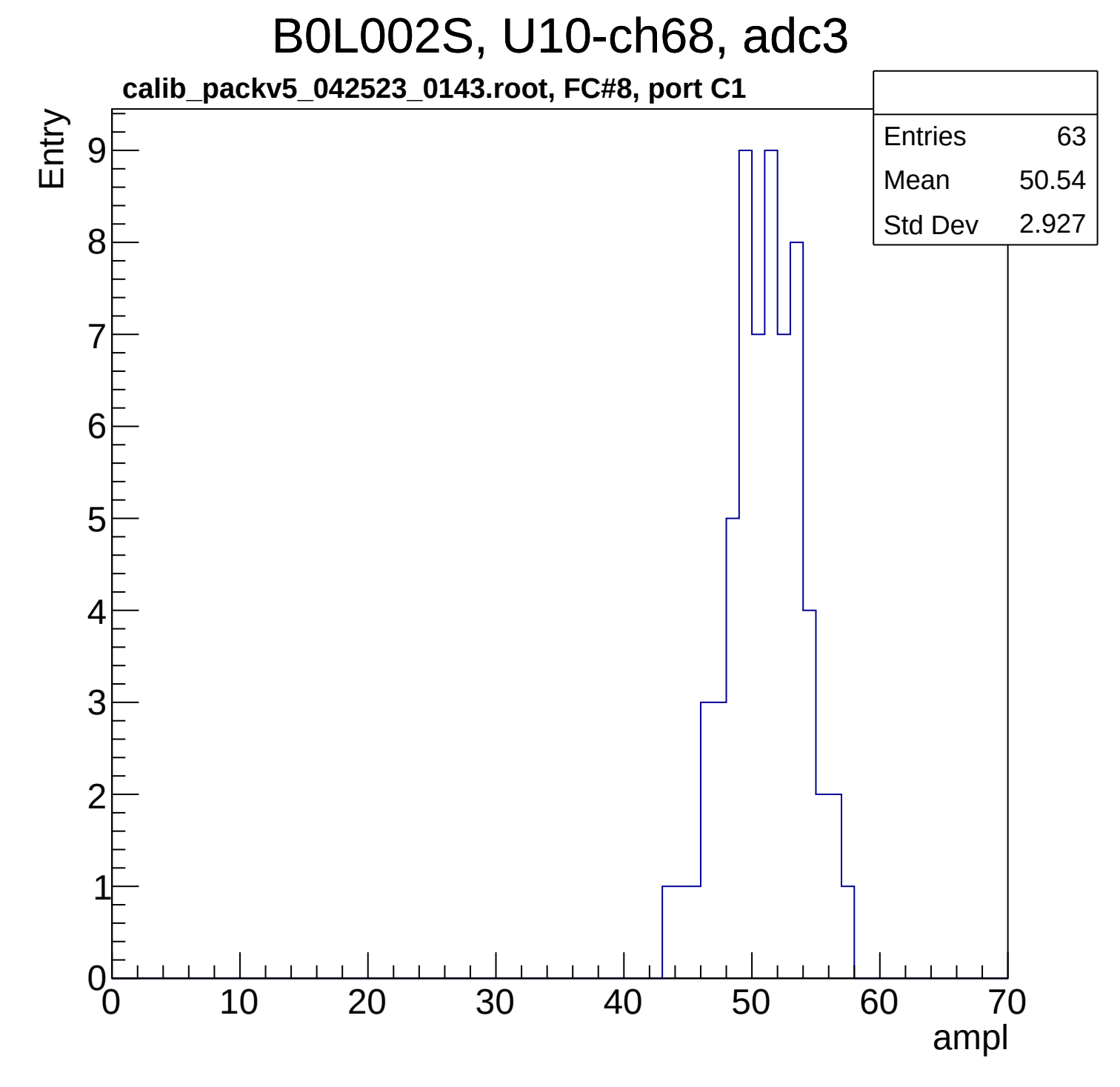

B0L002S, U10-ch67, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

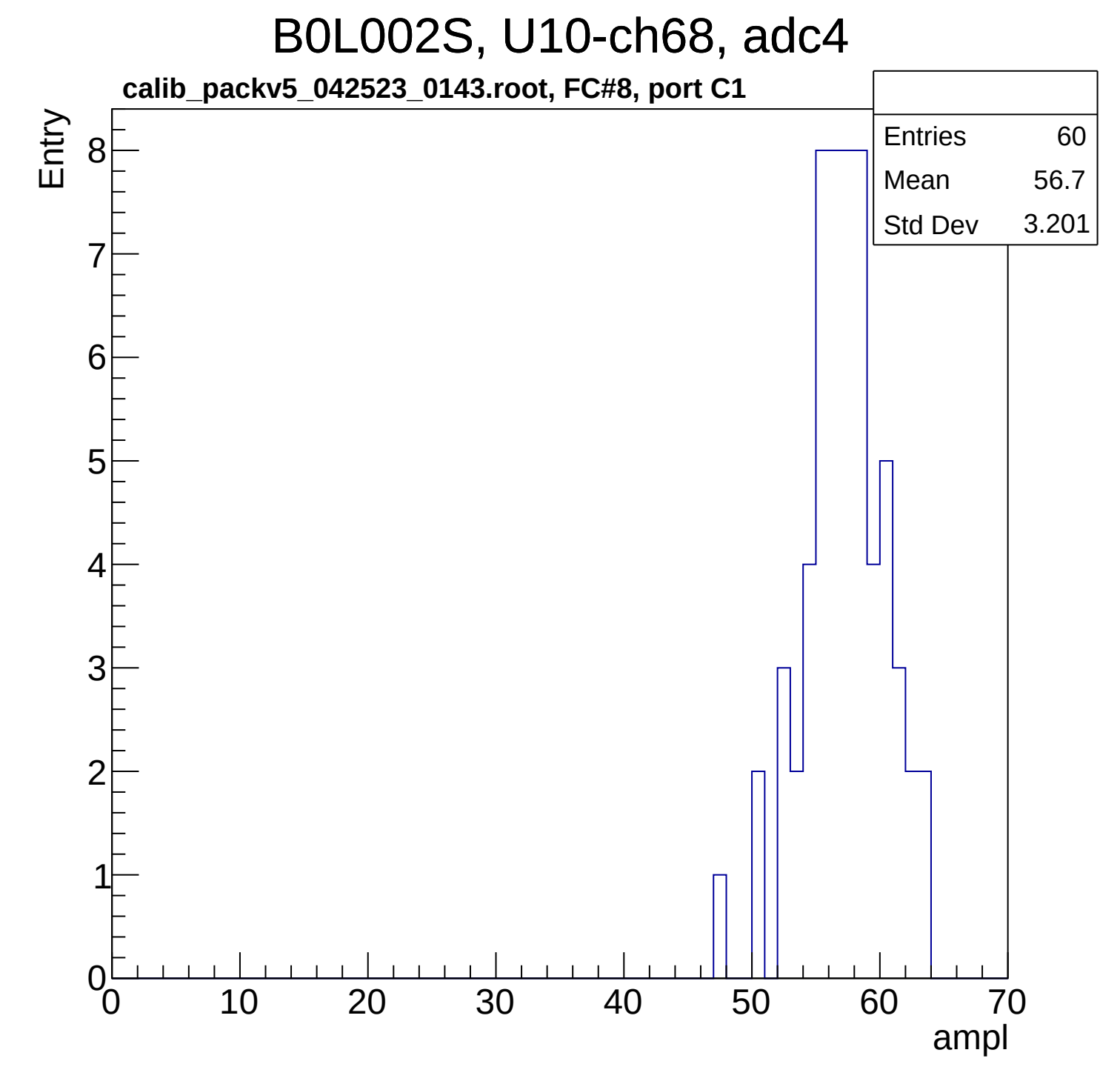

B0L002S, U10-ch67, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

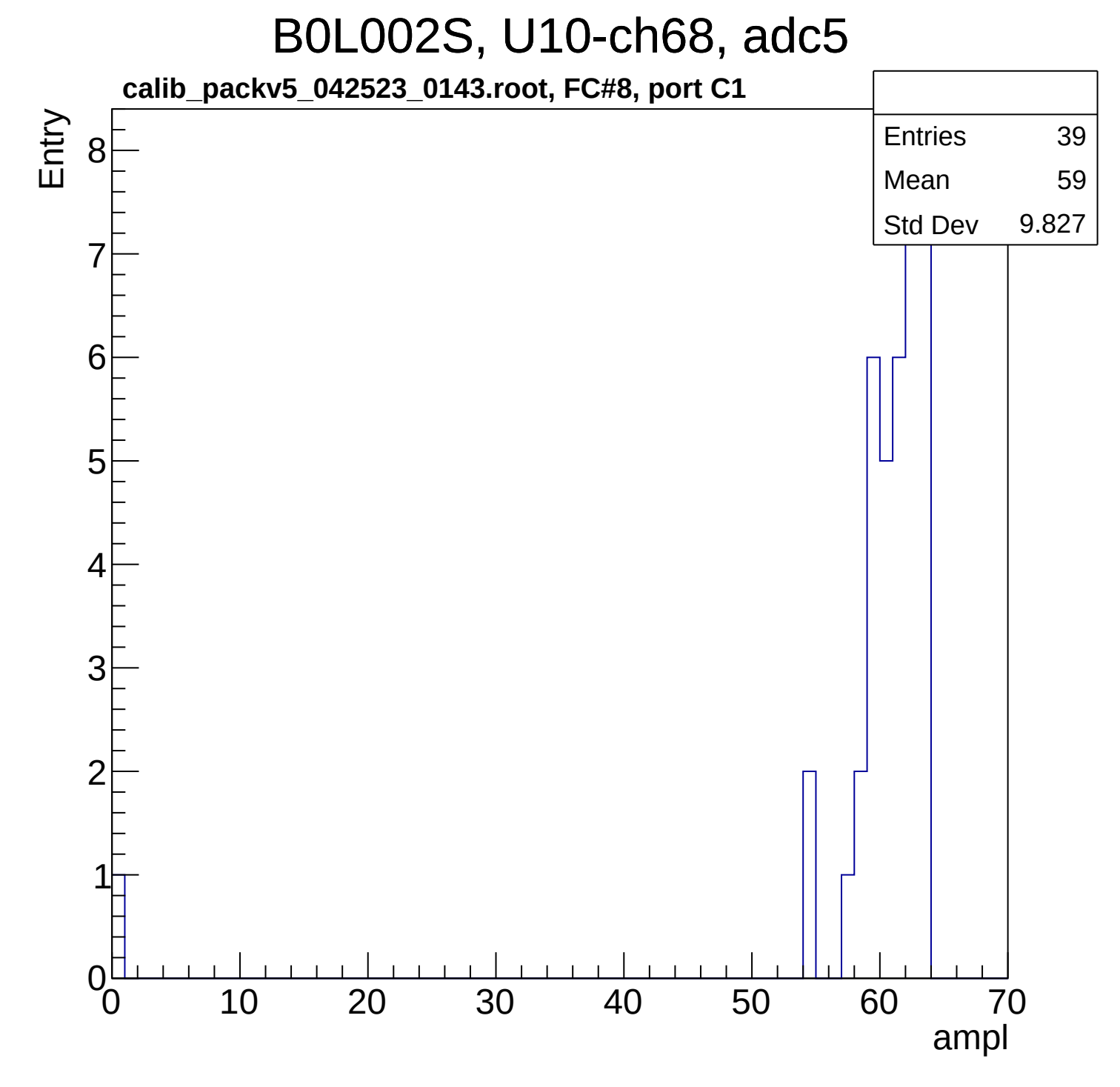

B0L002S, U10-ch68, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.99
Std Dev 3.194
Gaus mean : 30.3574
Gaus Width: 3.7889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

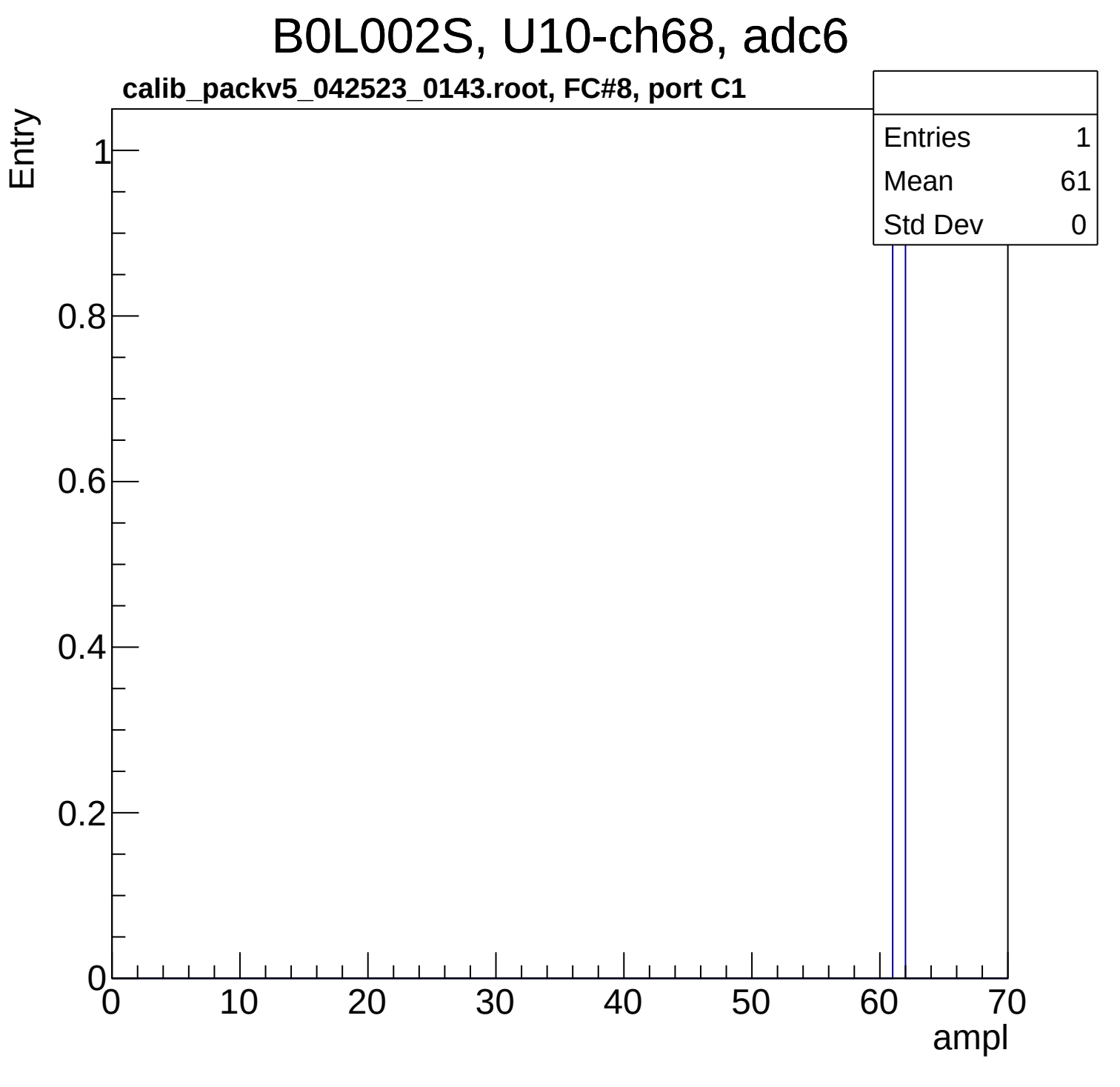

B0L002S, U10-ch68, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 37.57
Std Dev 3.771
Gaus mean : 37.6648
Gaus Width: 3.6894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

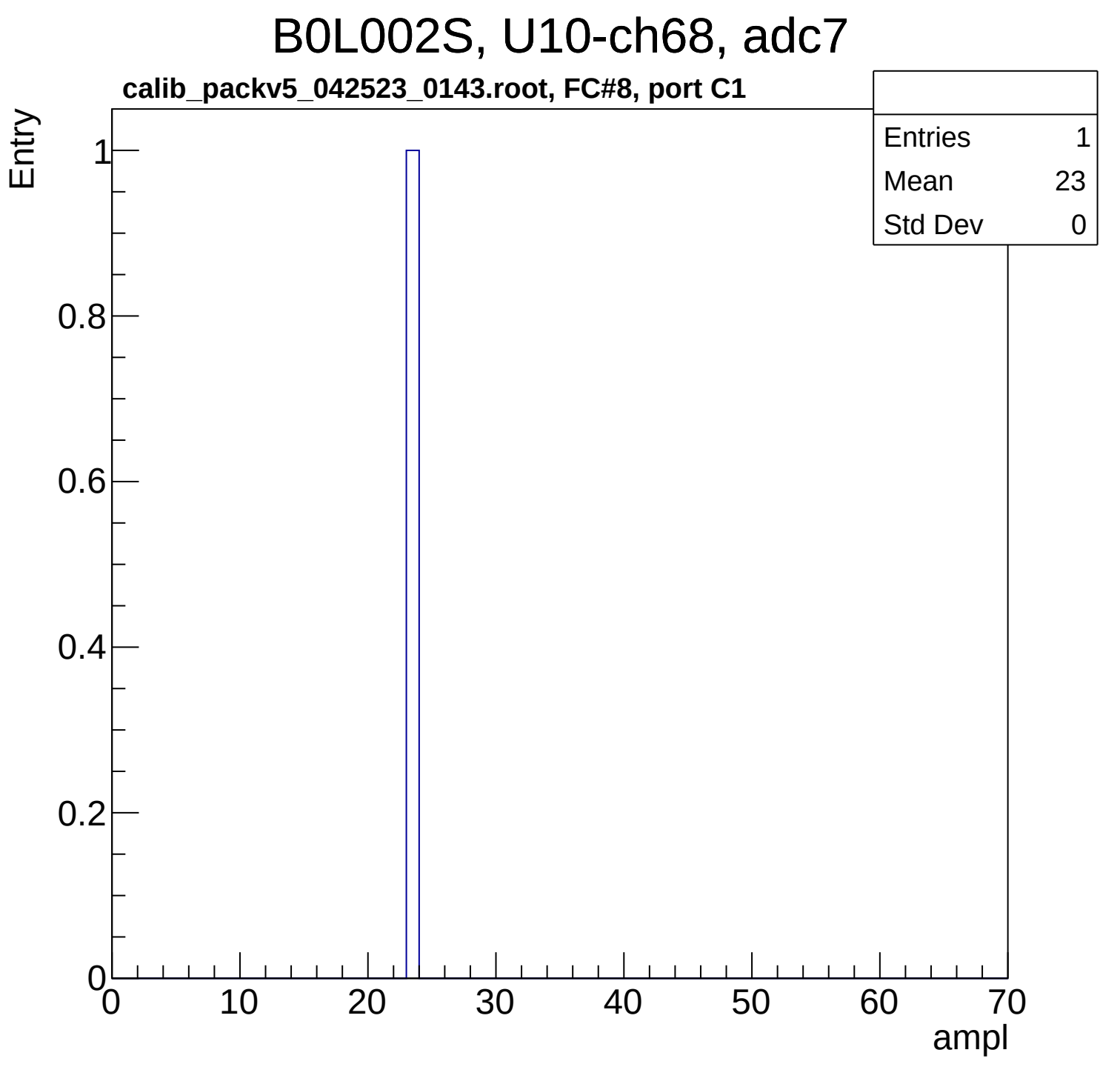

B0L002S, U10-ch68, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 43.59
Std Dev 3.485
Gaus mean : 45.6891
Gaus Width: 2.2088
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

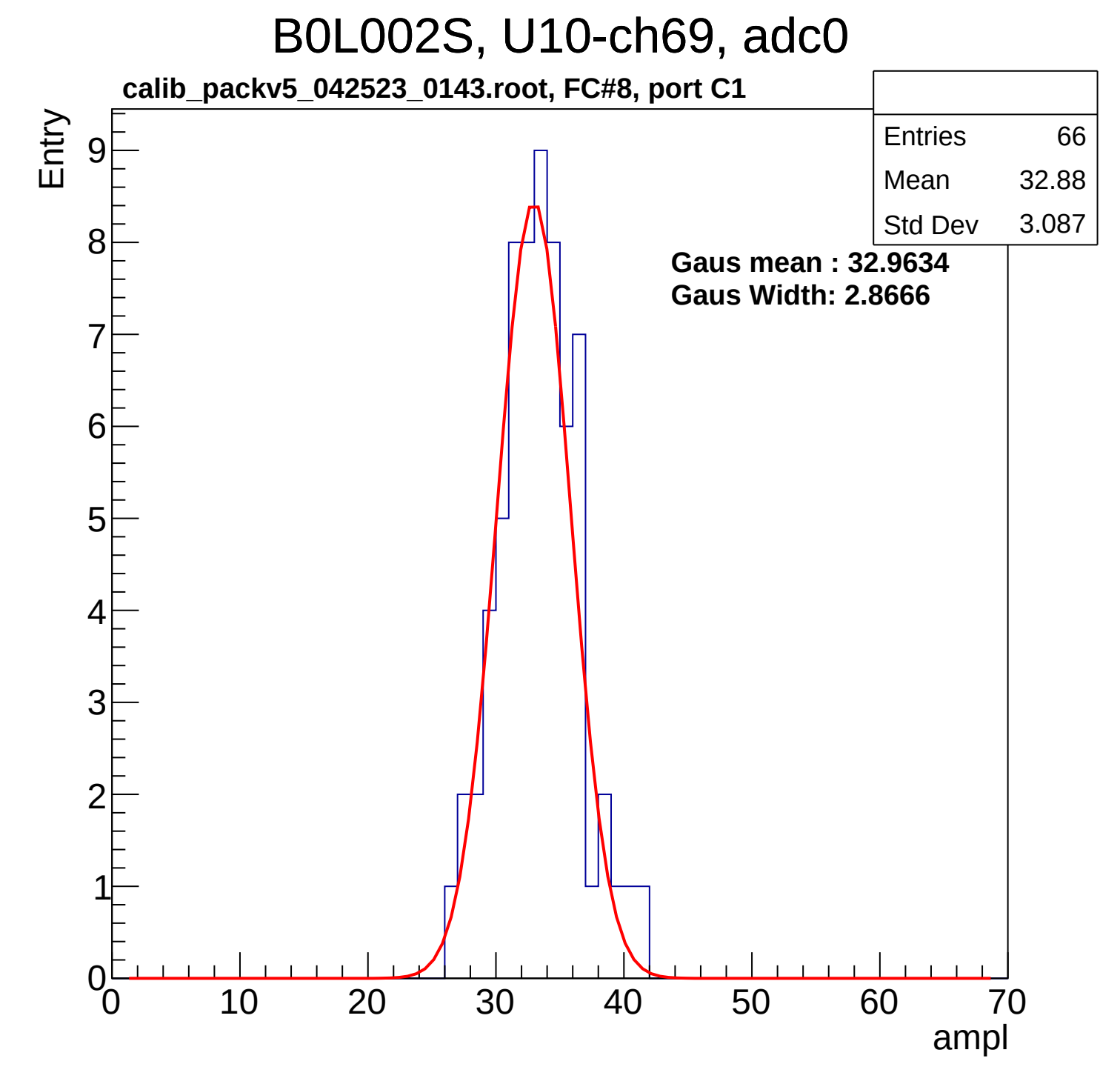

B0L002S, U10-ch68, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.54
Std Dev 2.927
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

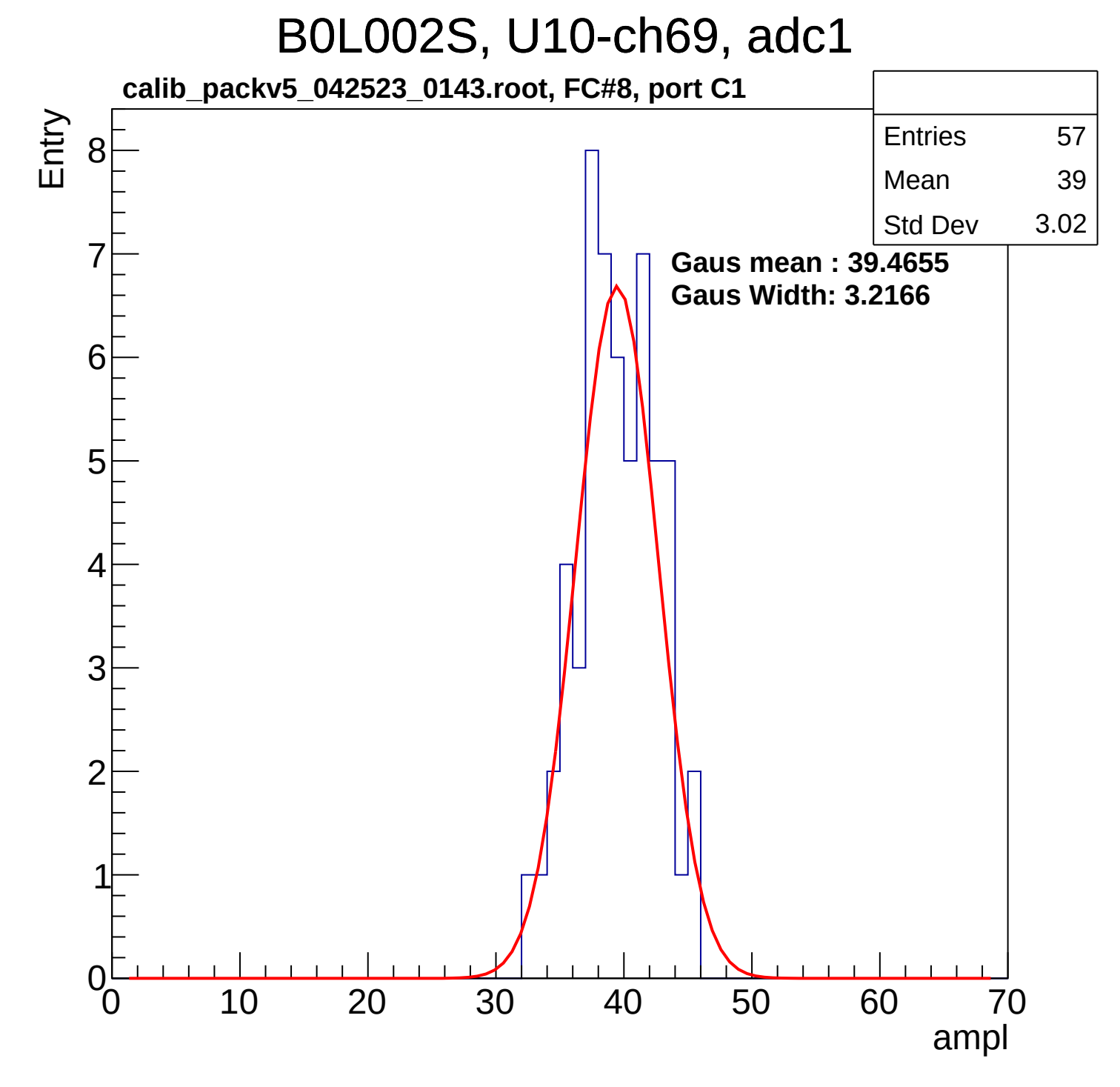

B0L002S, U10-ch68, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.7
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

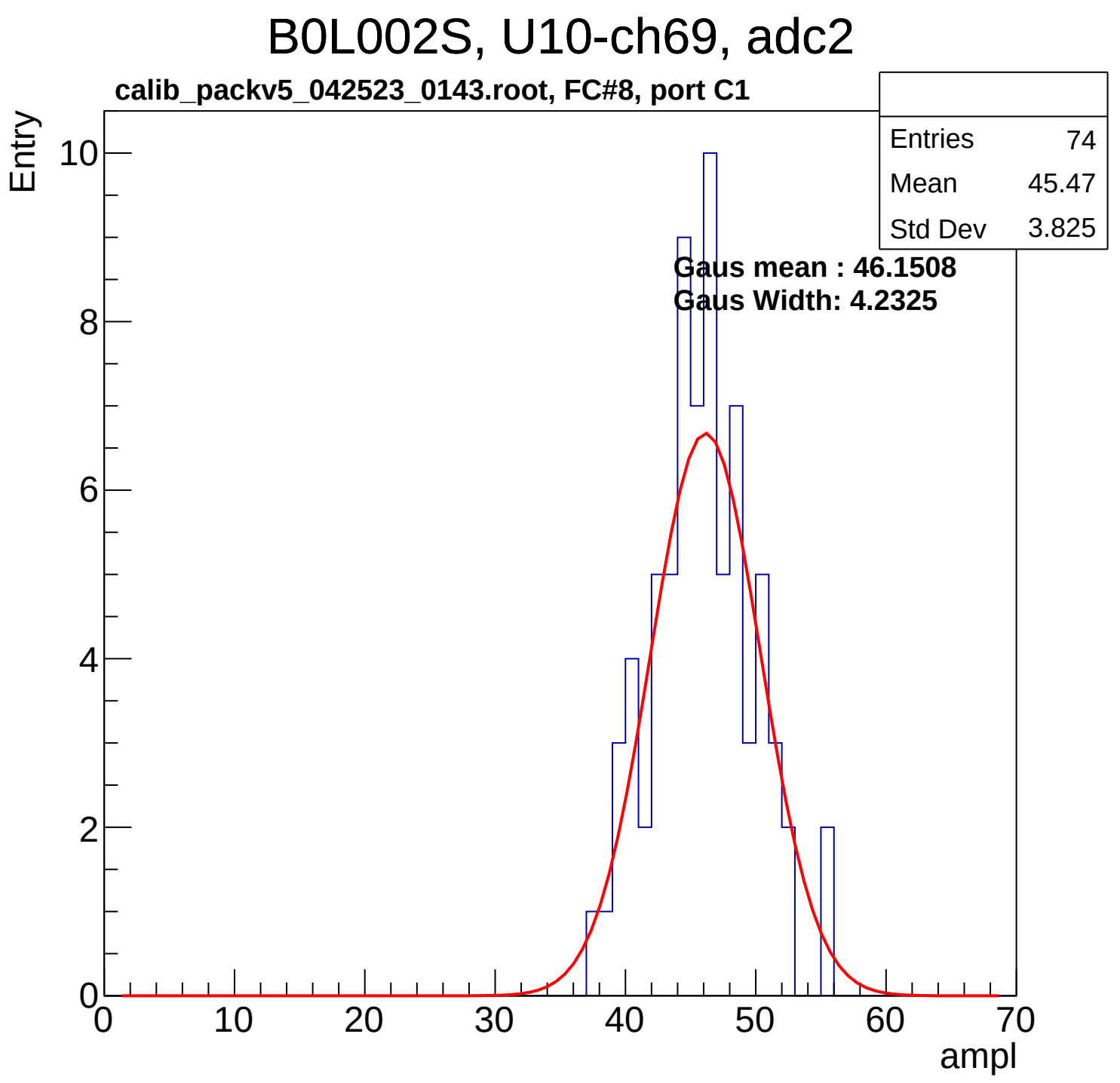

B0L002S, U10-ch68, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59
Std Dev 9.827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

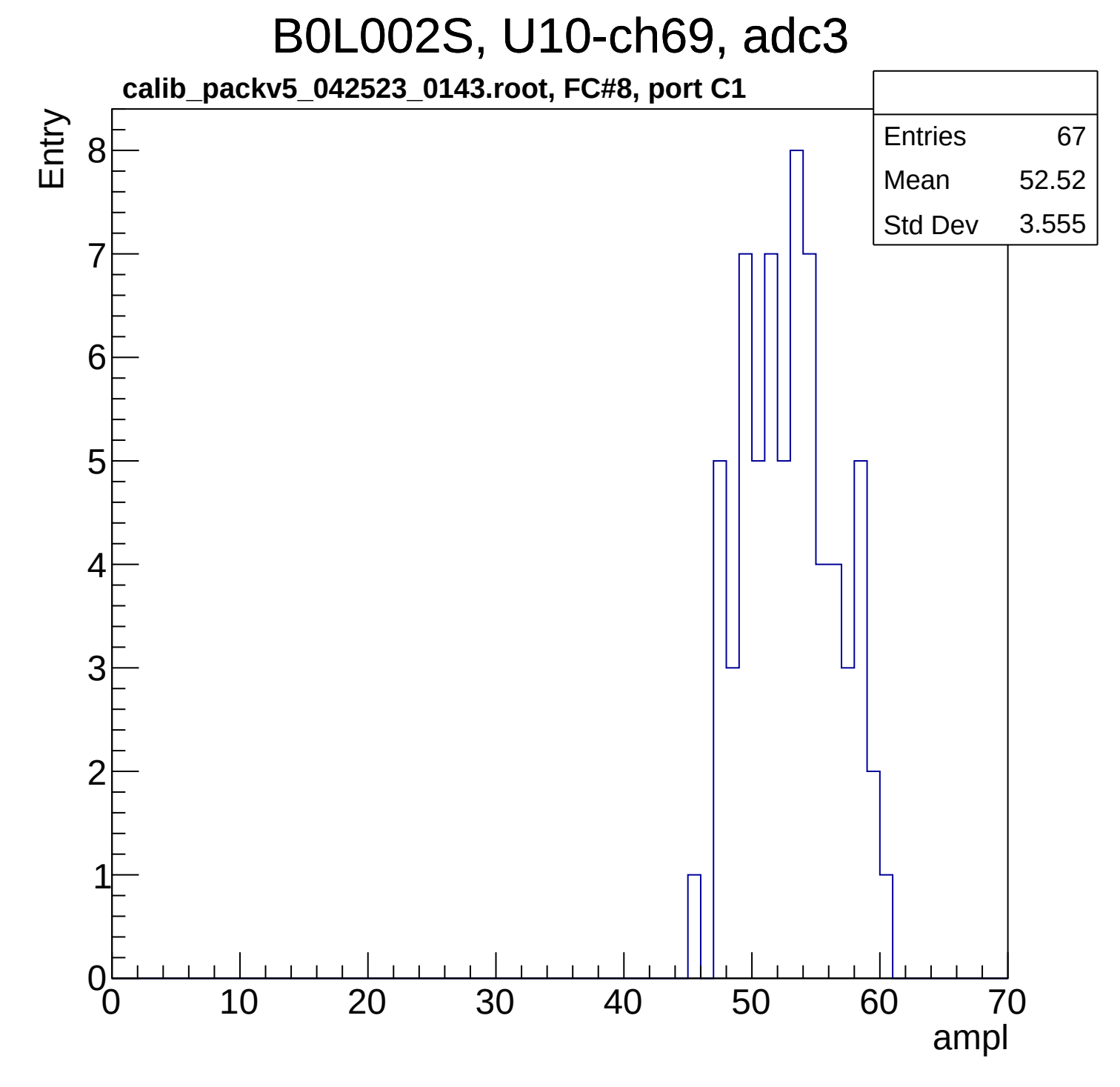

B0L002S, U10-ch68, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

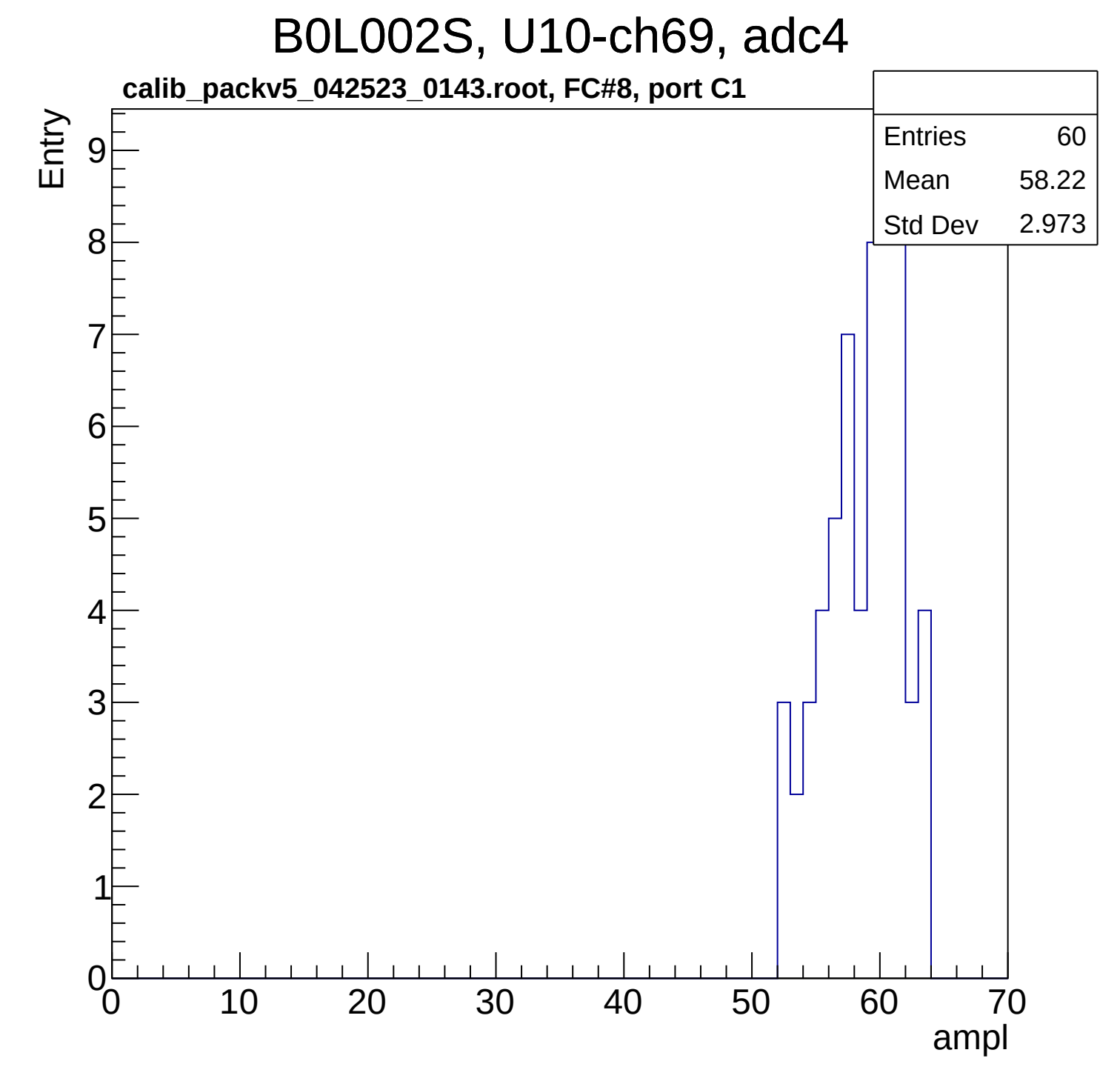

B0L002S, U10-ch68, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

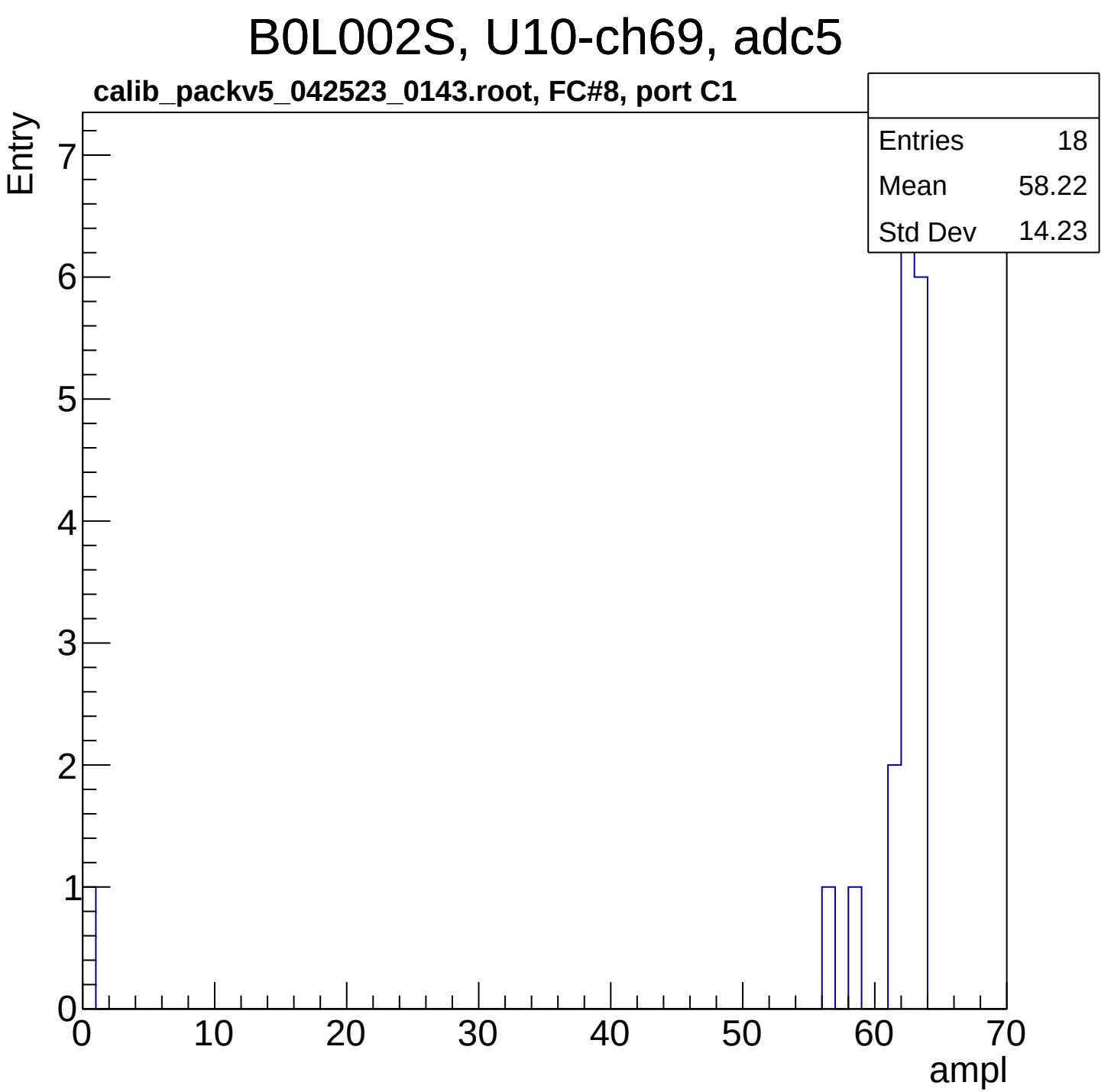

B0L002S, U10-ch69, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 32.88
Std Dev 3.087
Gaus mean : 32.9634
Gaus Width: 2.8666
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

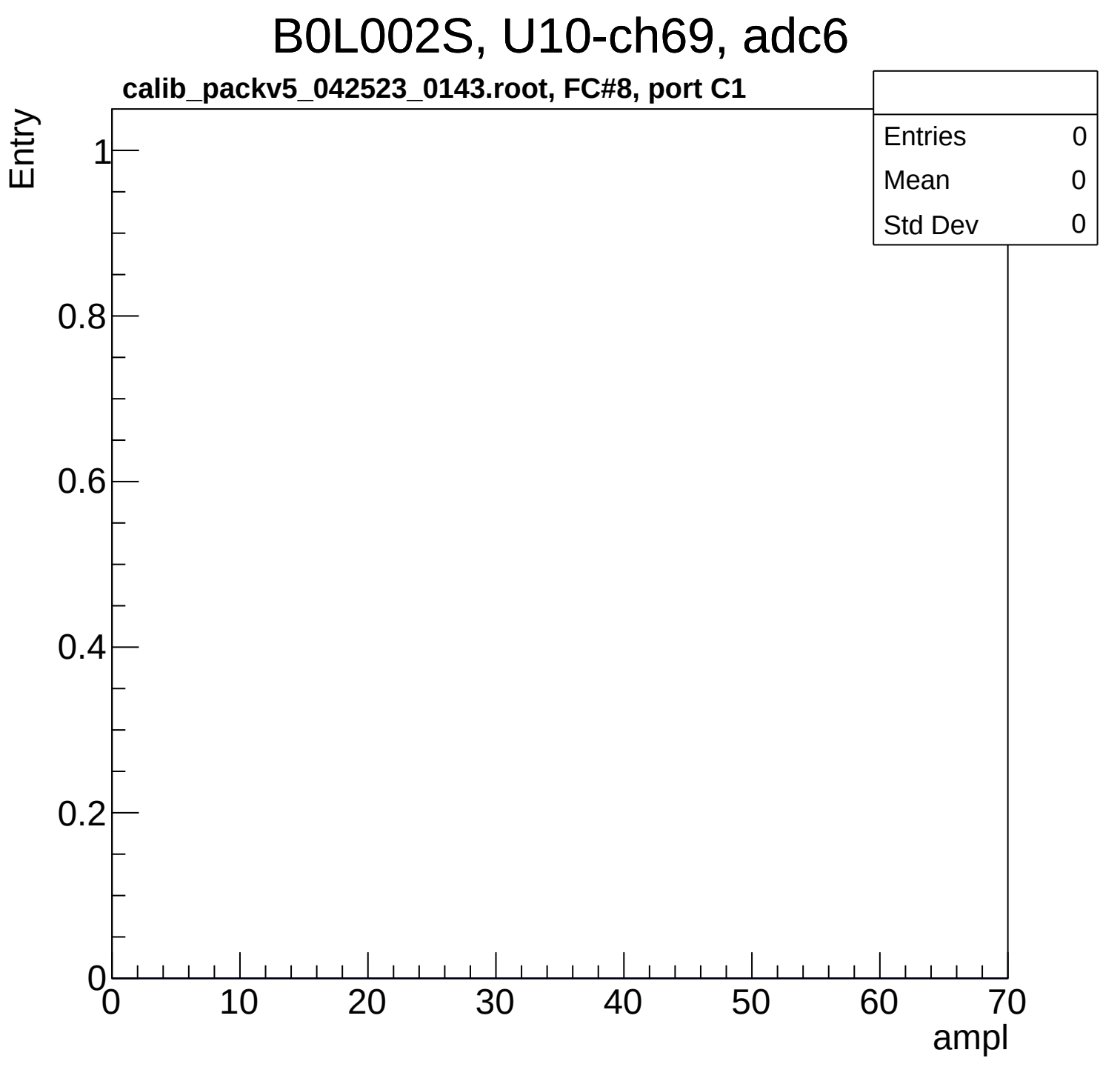

B0L002S, U10-ch69, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 39
Std Dev 3.02
Gaus mean : 39.4655
Gaus Width: 3.2166
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

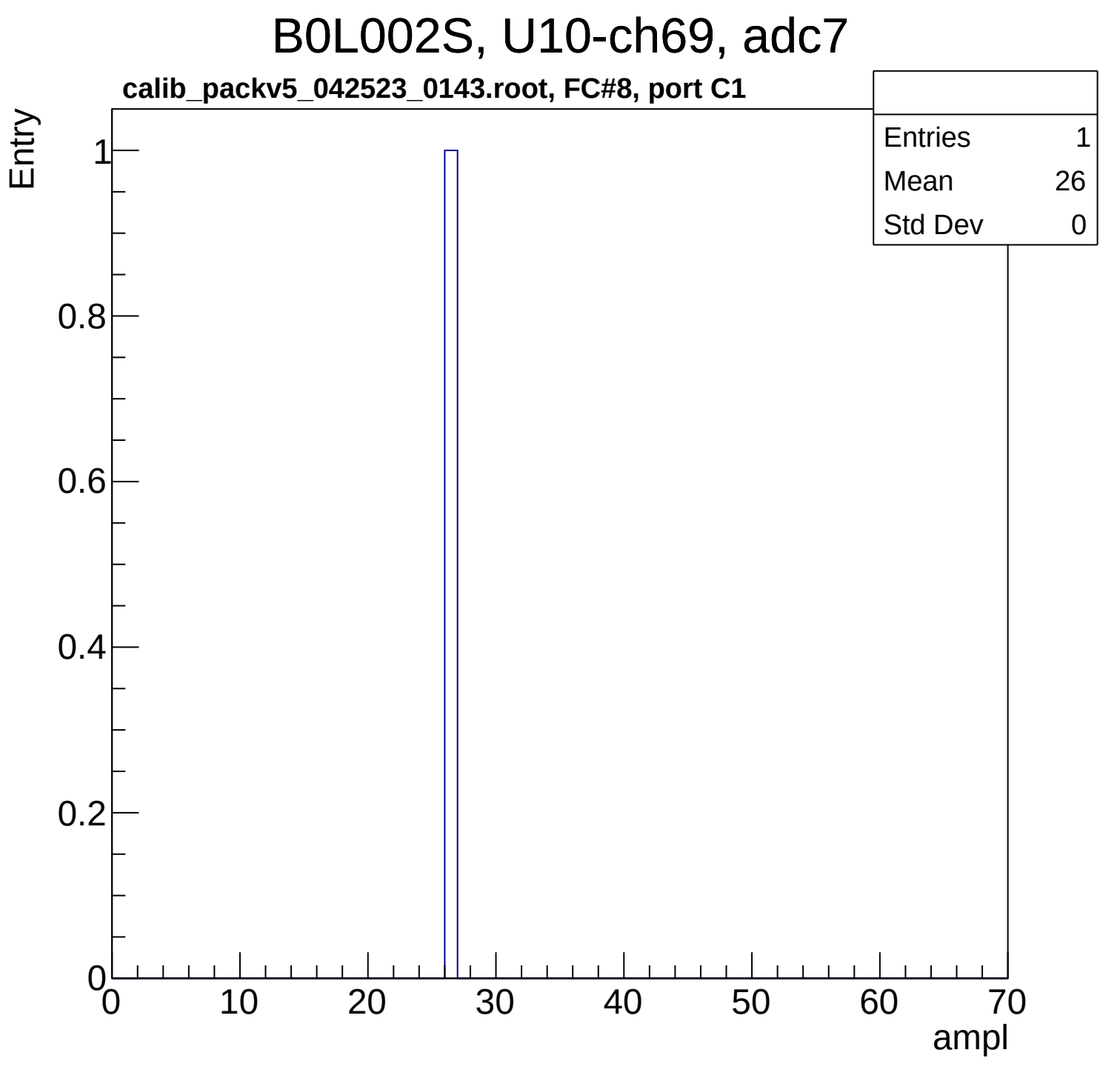

B0L002S, U10-ch69, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 45.47
Std Dev 3.825
Gaus mean : 46.1508
Gaus Width: 4.2325
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

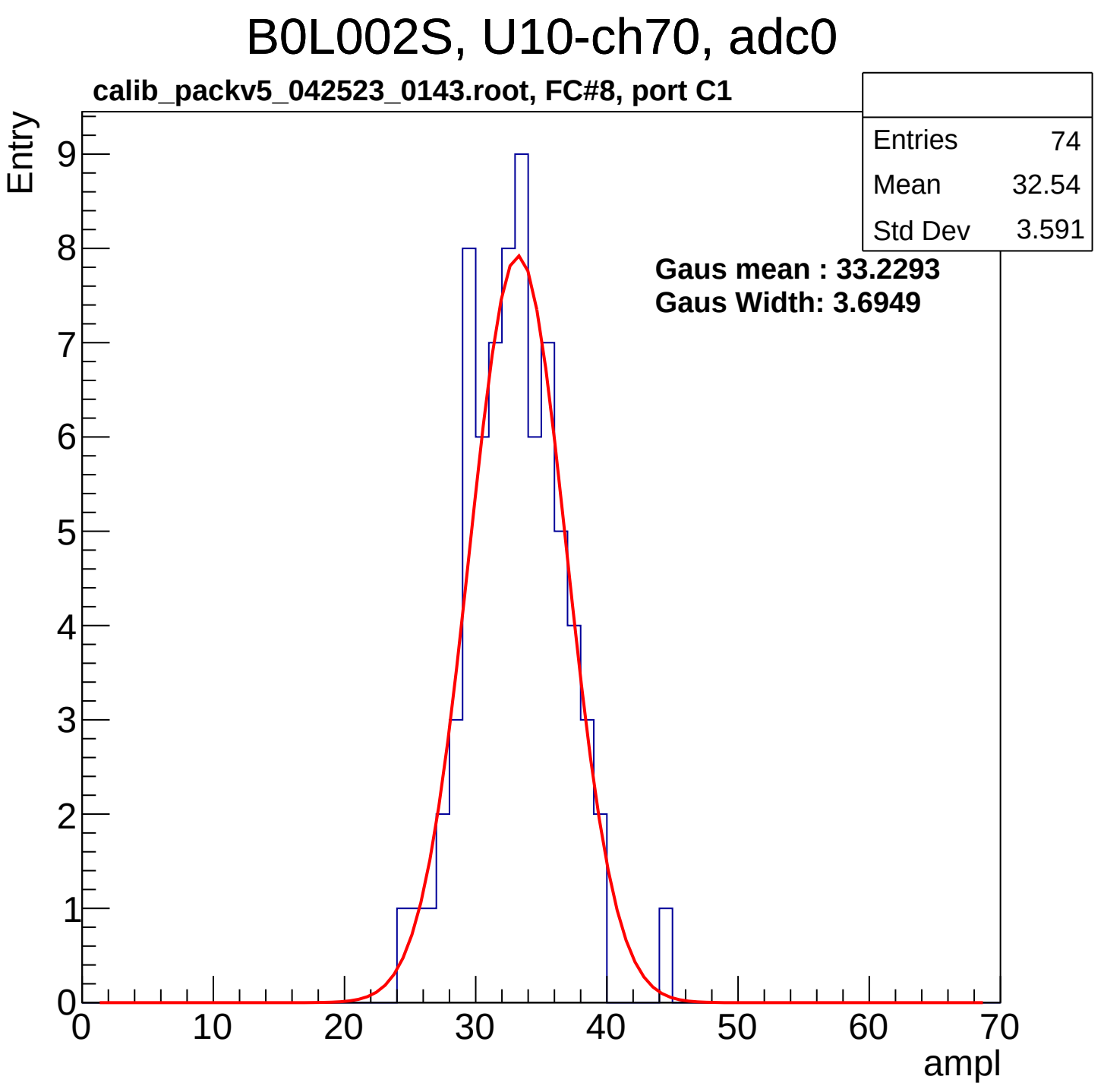

B0L002S, U10-ch69, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 52.52
Std Dev 3.555
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

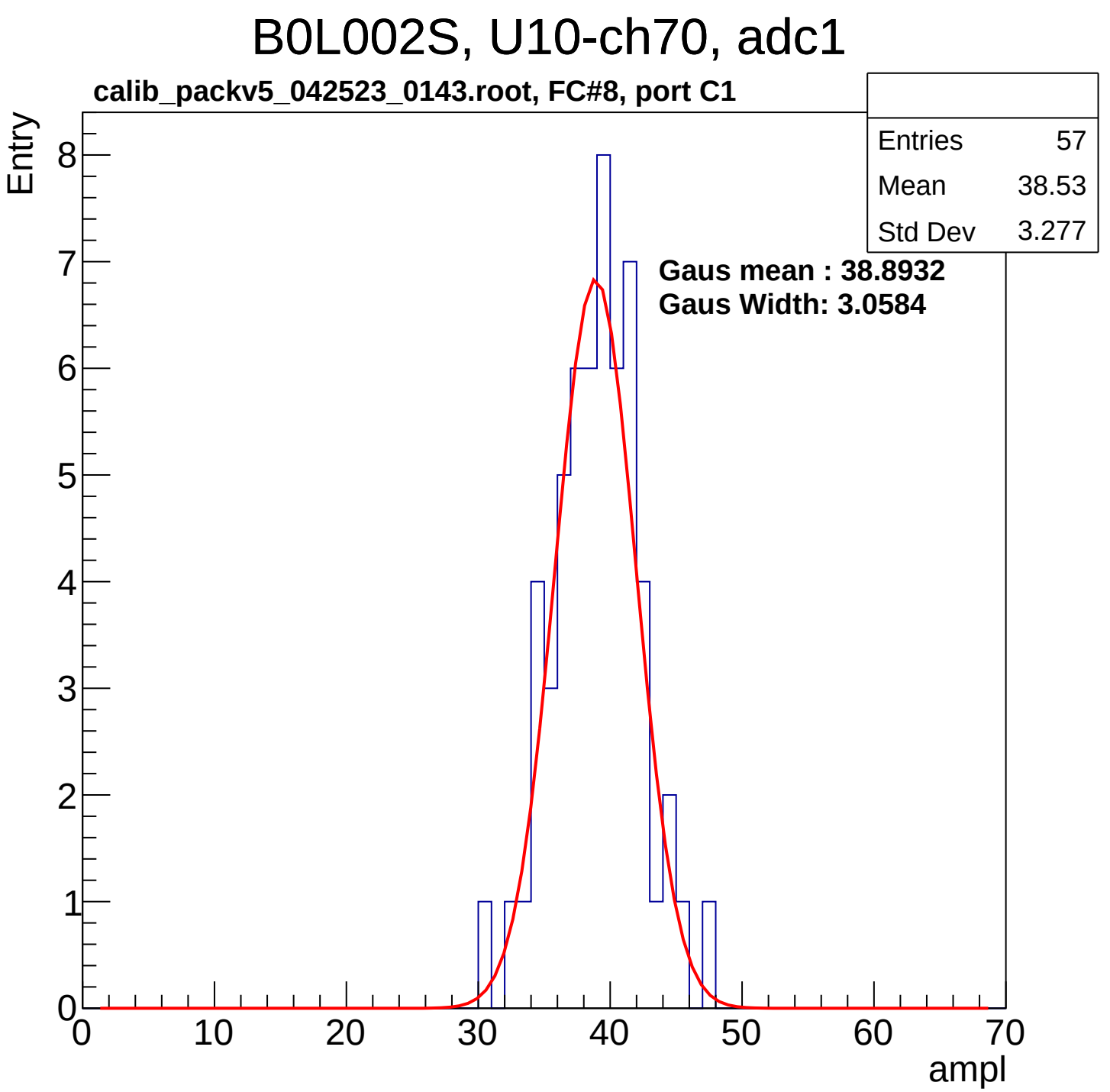

B0L002S, U10-ch69, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 58.22
Std Dev 2.973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

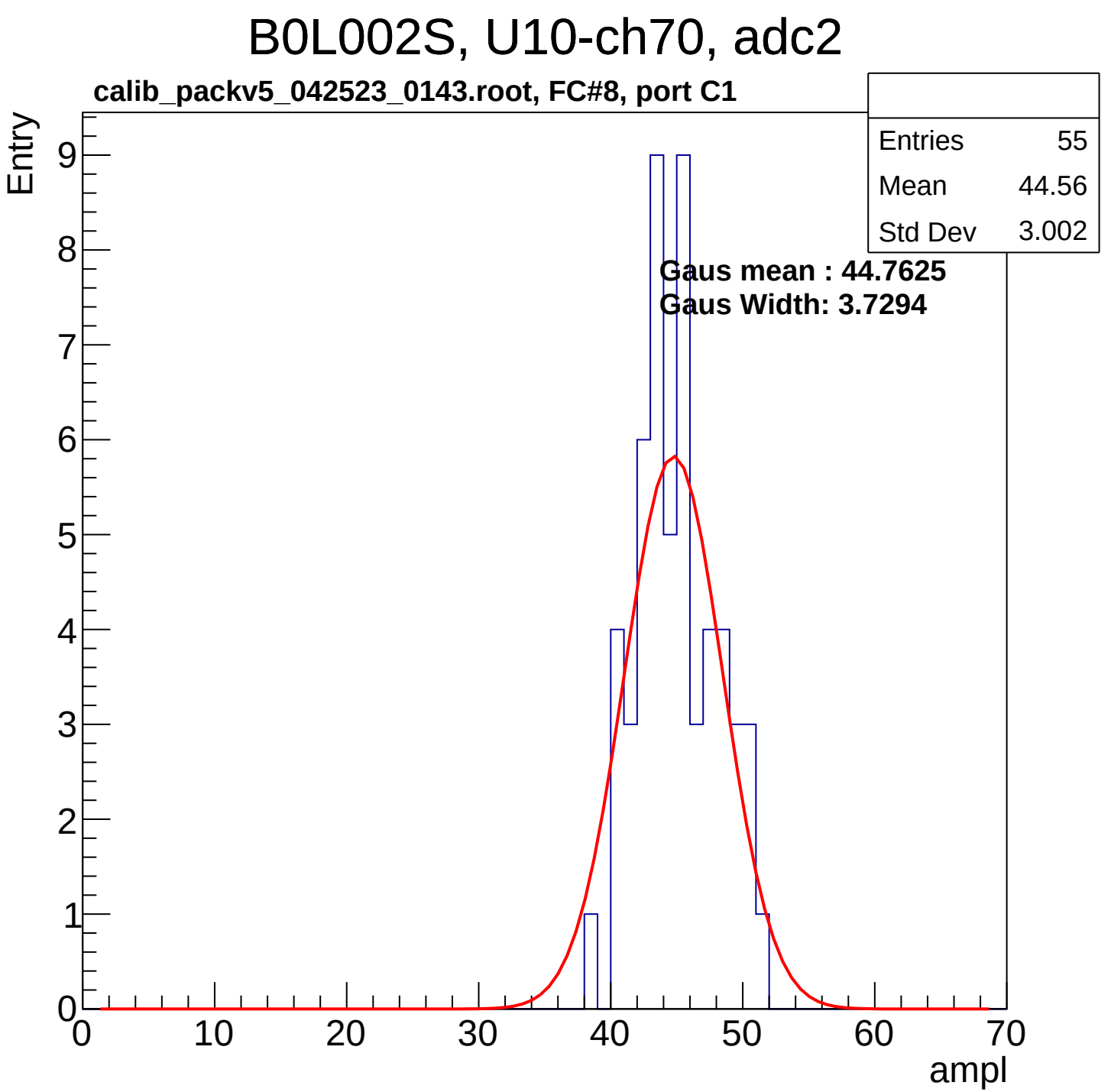

B0L002S, U10-ch69, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 58.22
Std Dev 14.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

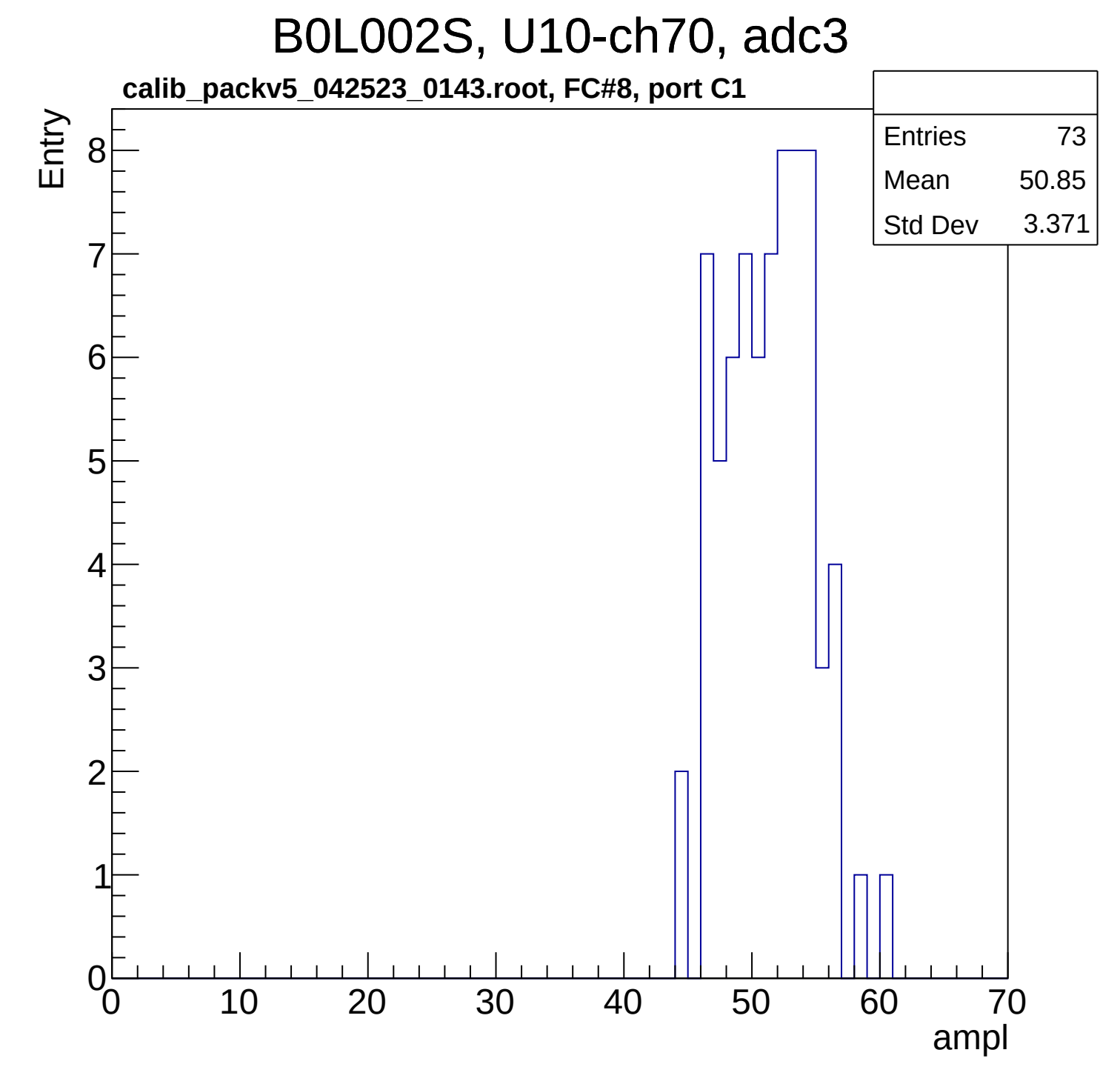

B0L002S, U10-ch69, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

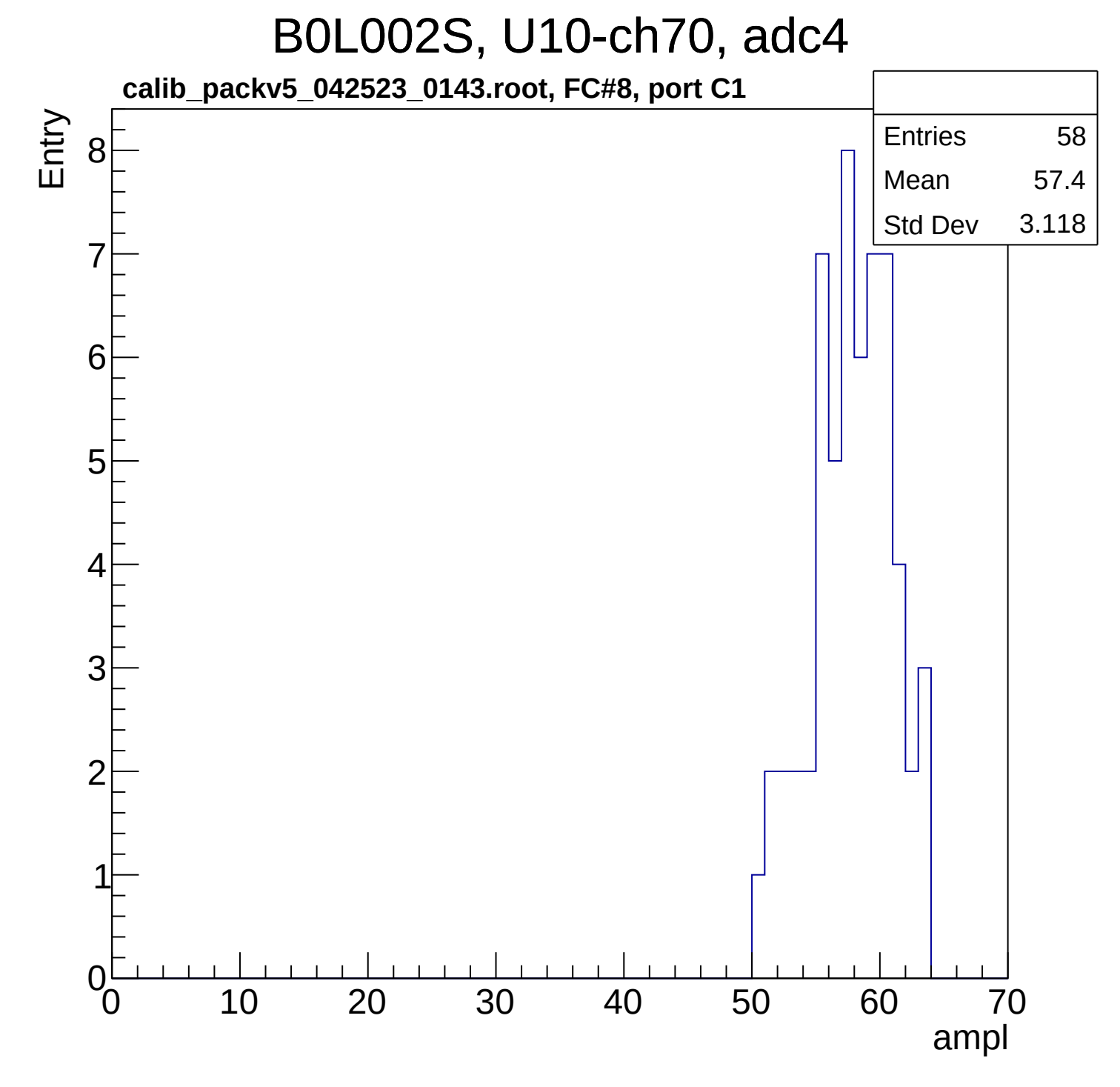

B0L002S, U10-ch69, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

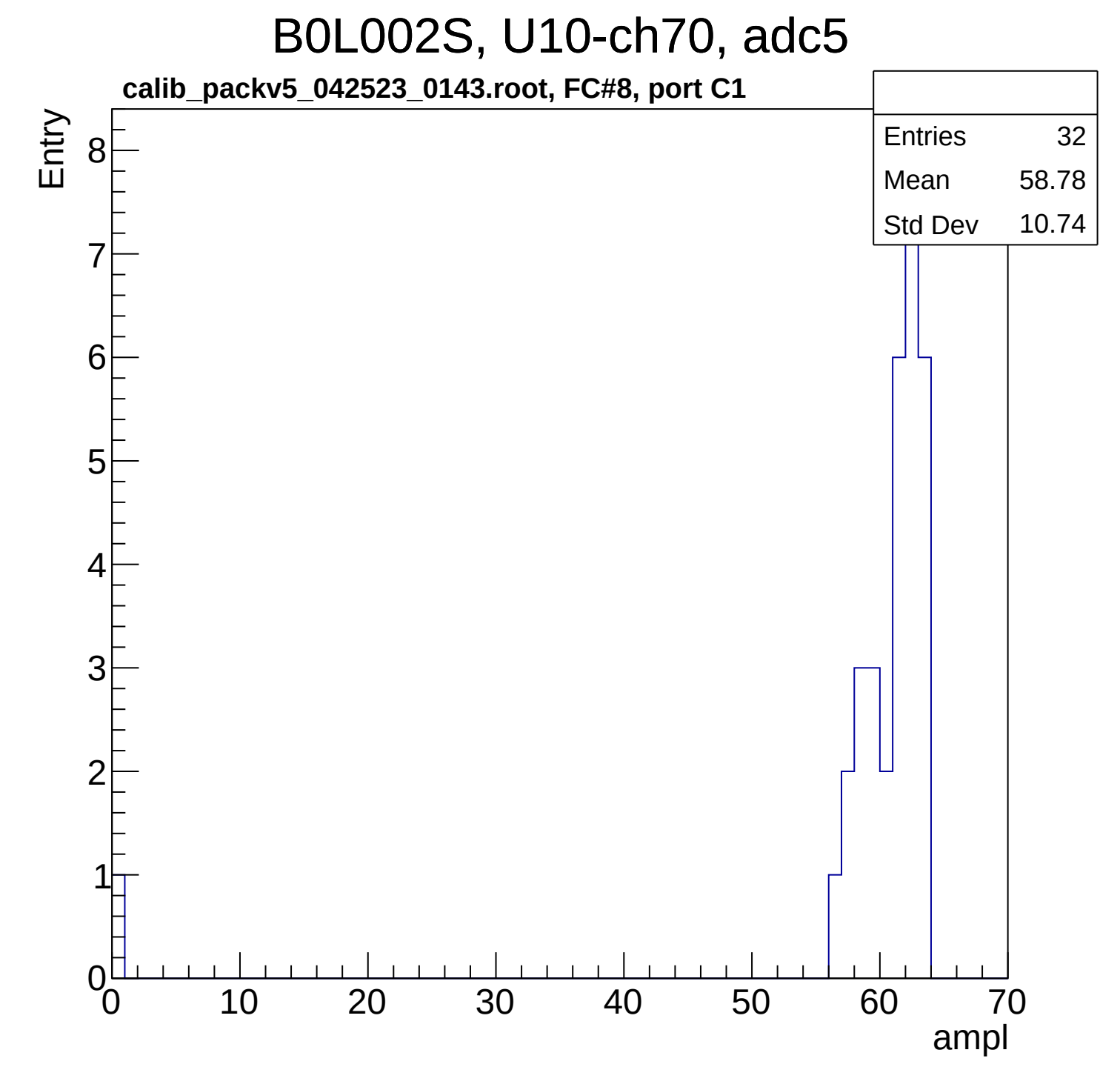

B0L002S, U10-ch70, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 32.54
Std Dev 3.591
Gaus mean : 33.2293
Gaus Width: 3.6949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

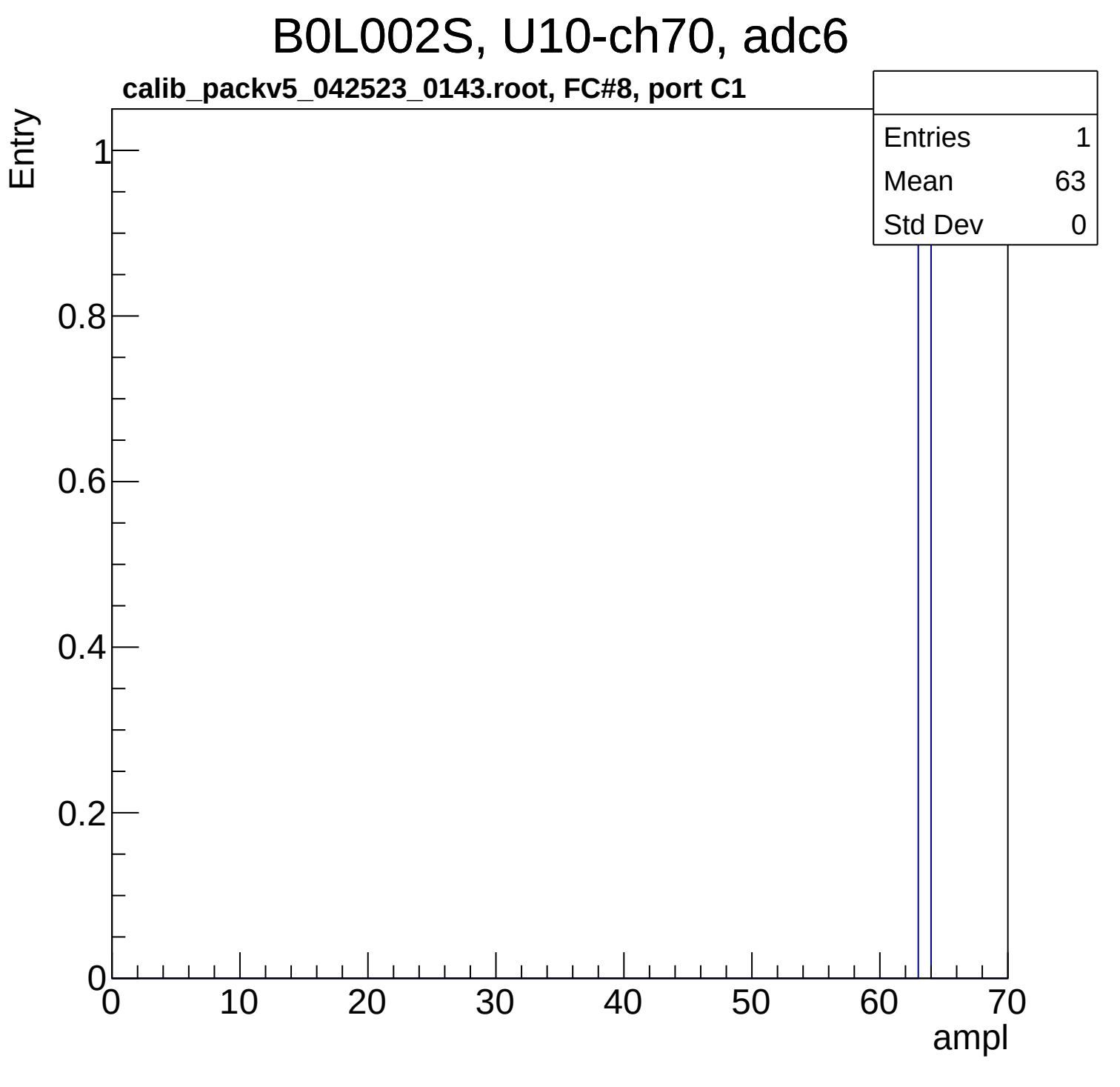

B0L002S, U10-ch70, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 38.53
Std Dev 3.277
Gaus mean : 38.8932
Gaus Width: 3.0584
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

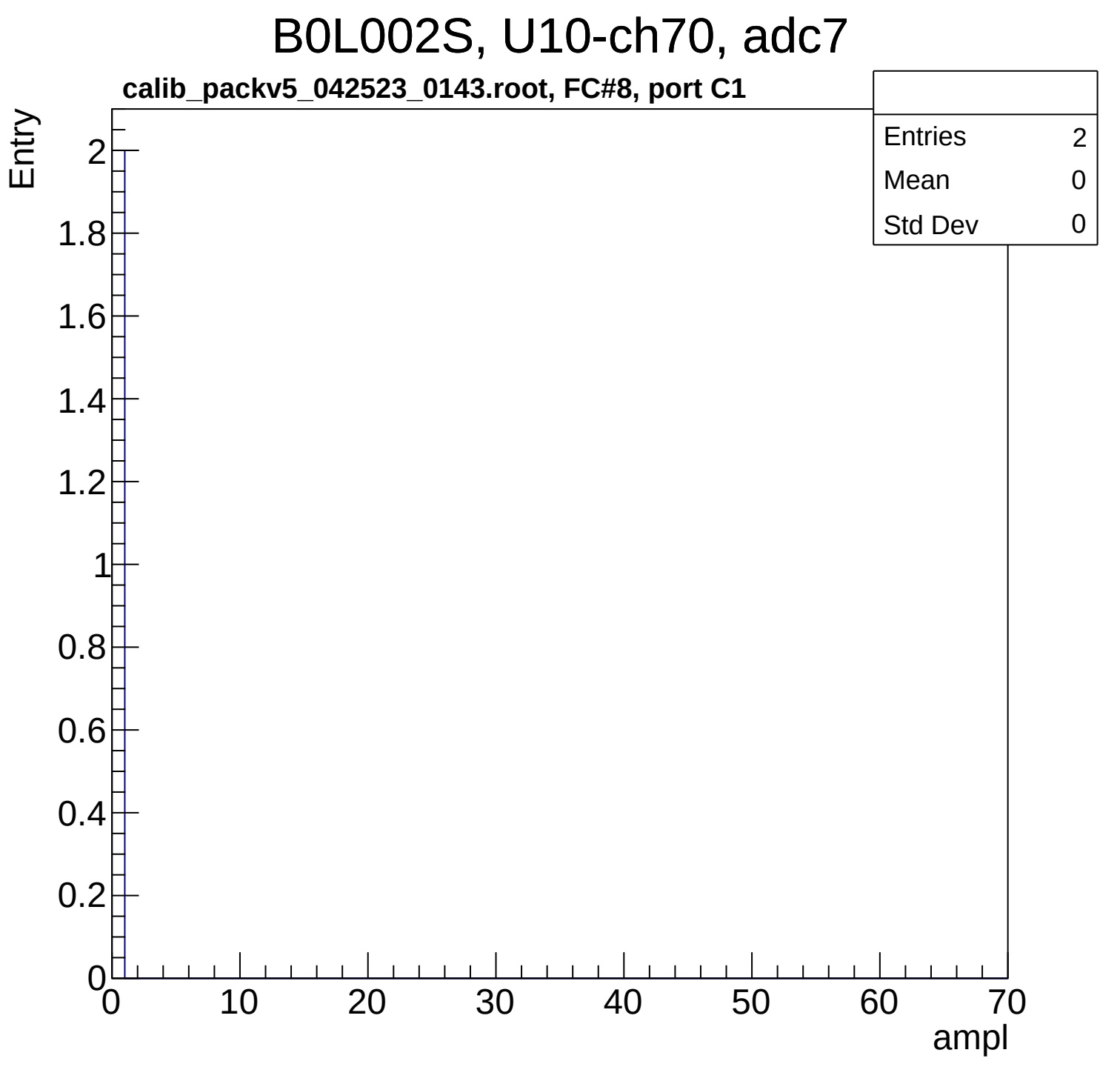

B0L002S, U10-ch70, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.56
Std Dev 3.002
Gaus mean : 44.7625
Gaus Width: 3.7294
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

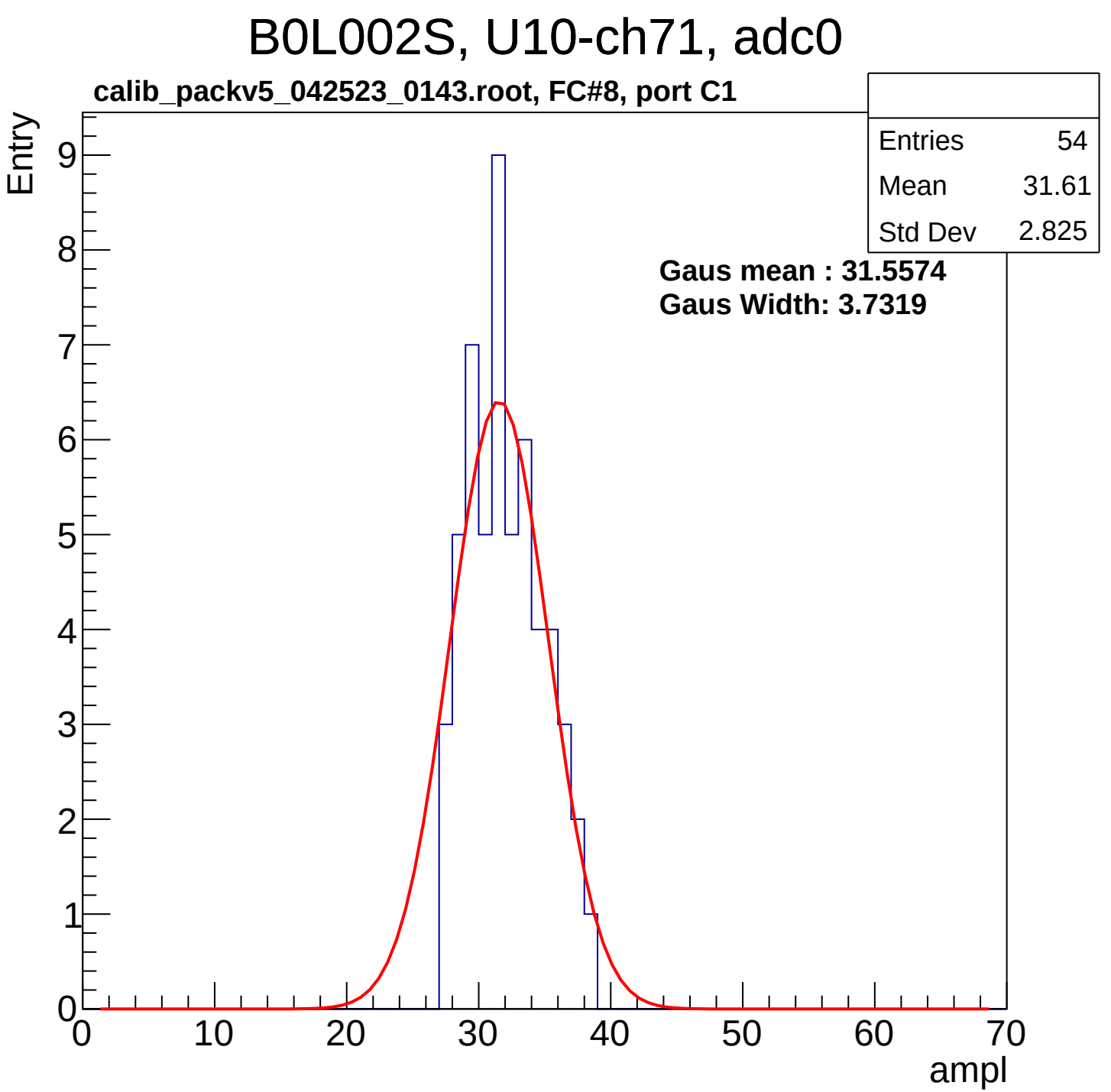

B0L002S, U10-ch70, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.85
Std Dev 3.371
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

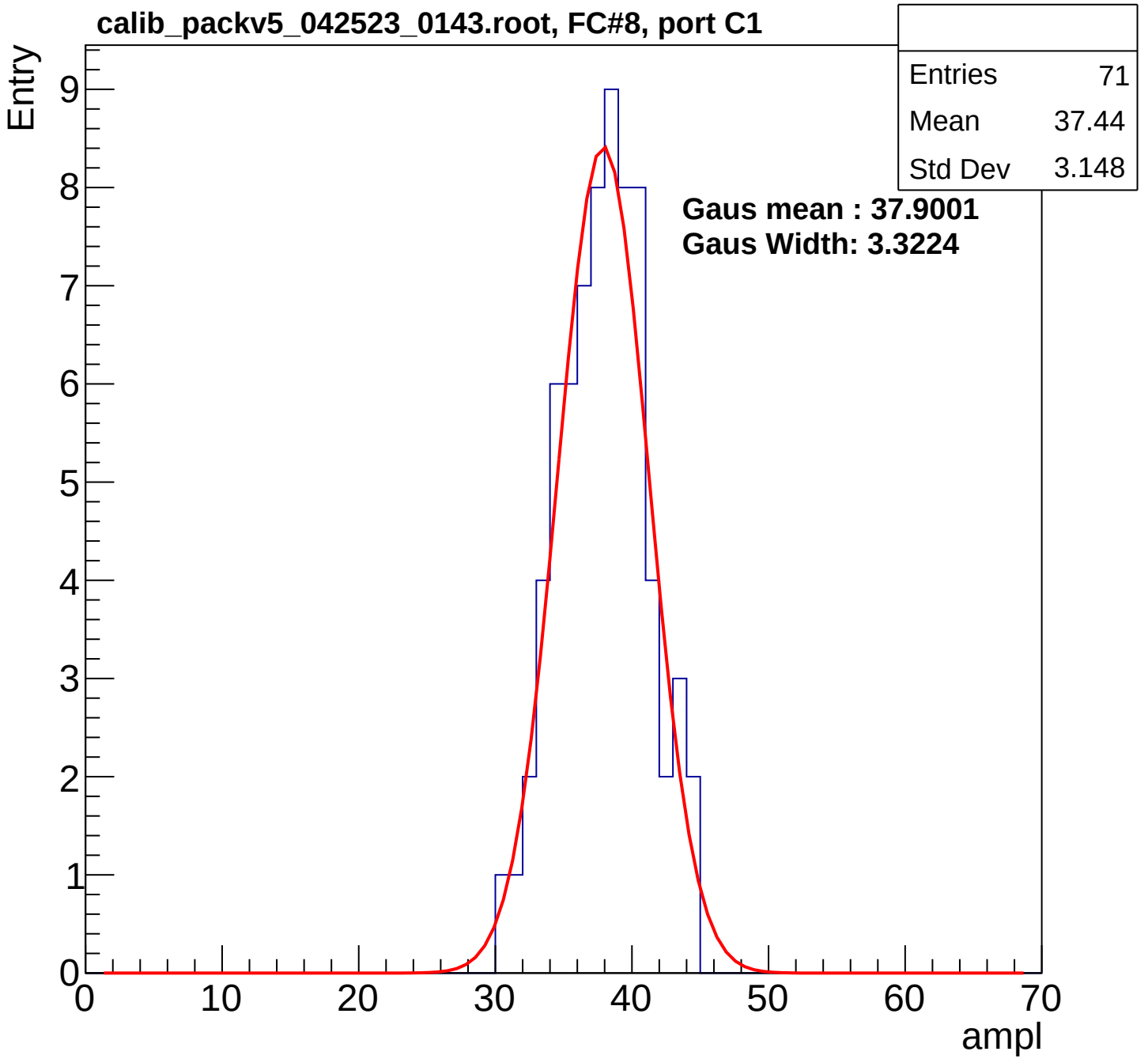

B0L002S, U10-ch70, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.4
Std Dev 3.118
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

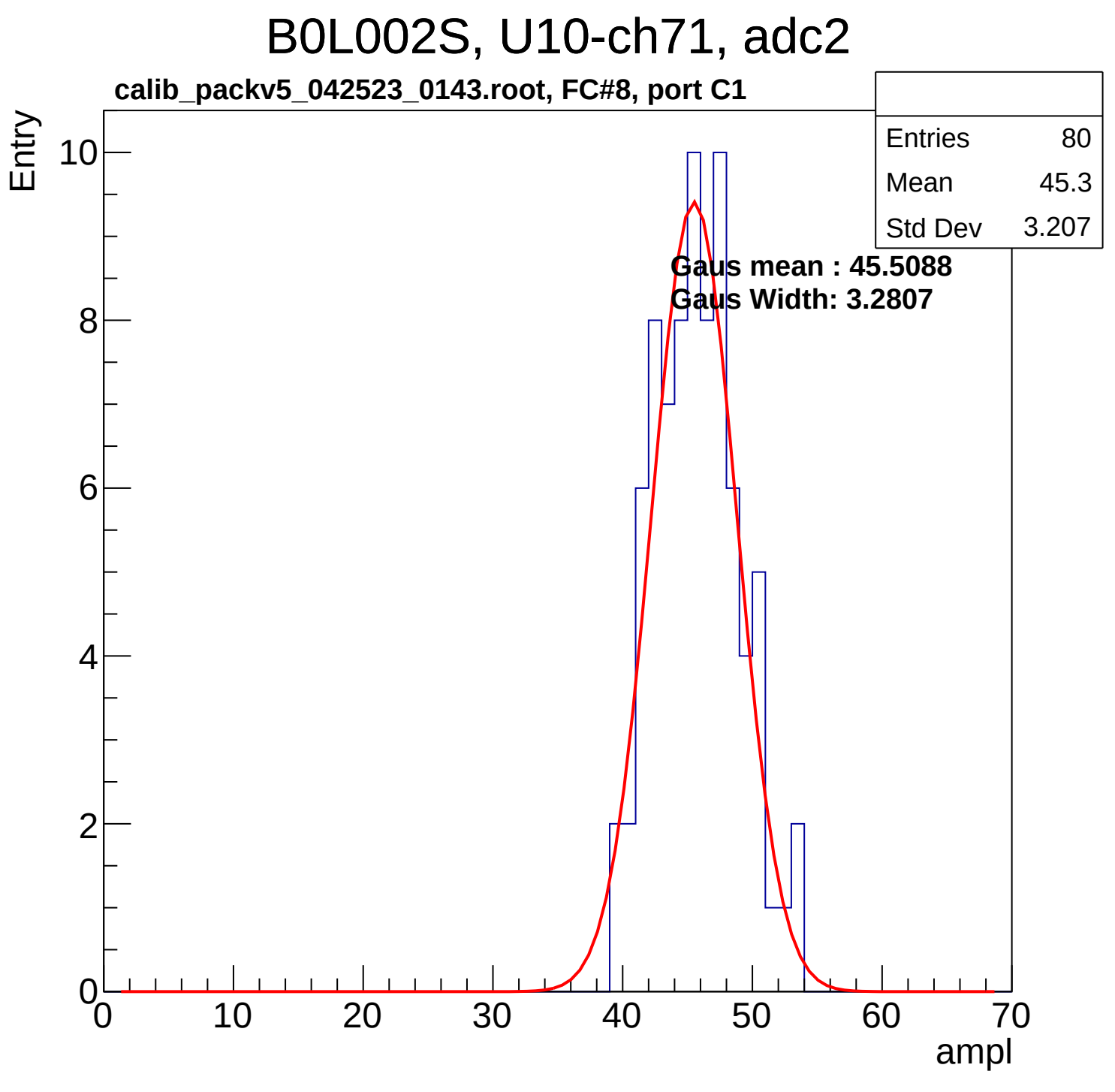

B0L002S, U10-ch70, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.78
Std Dev 10.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

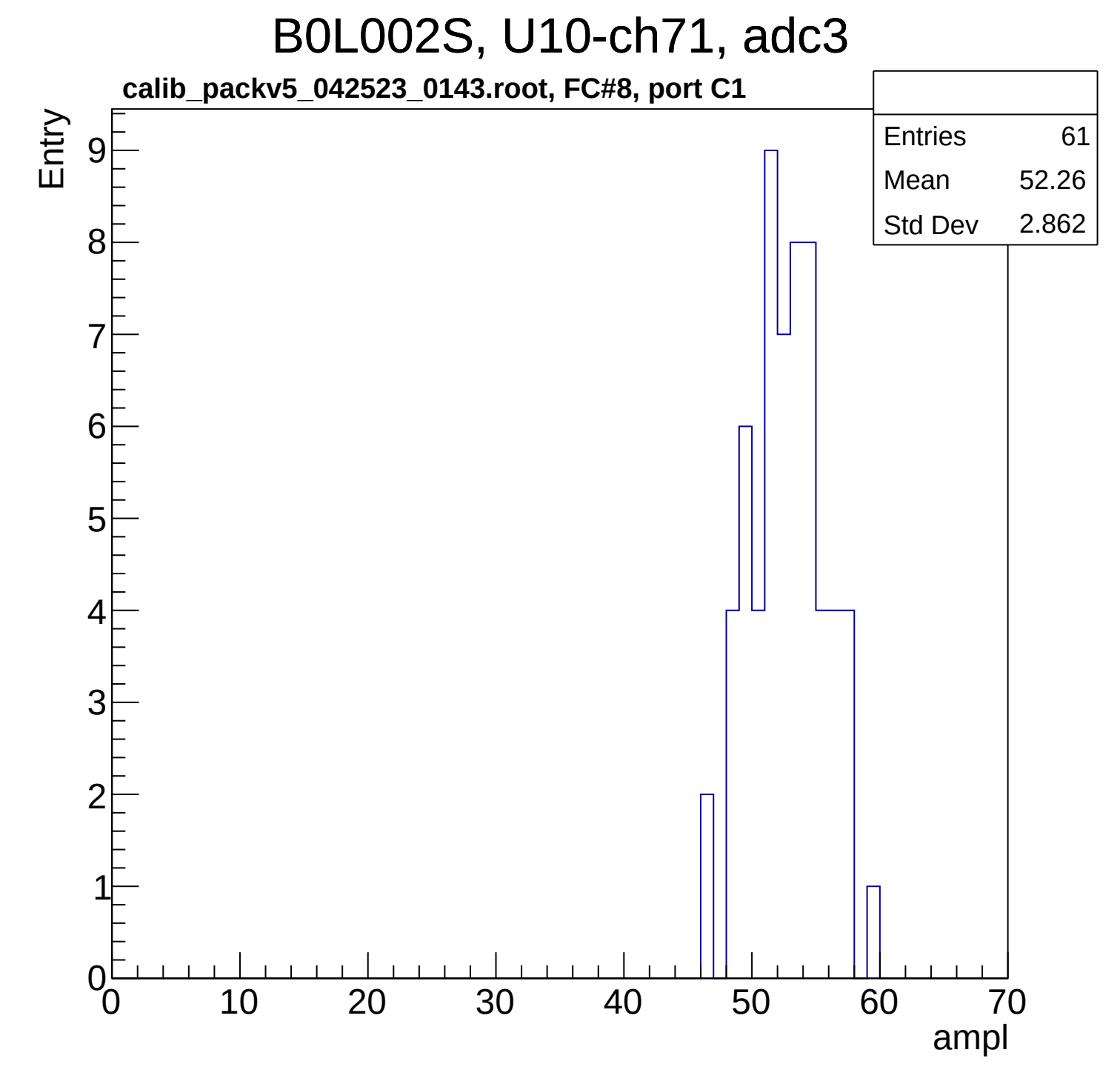

B0L002S, U10-ch70, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

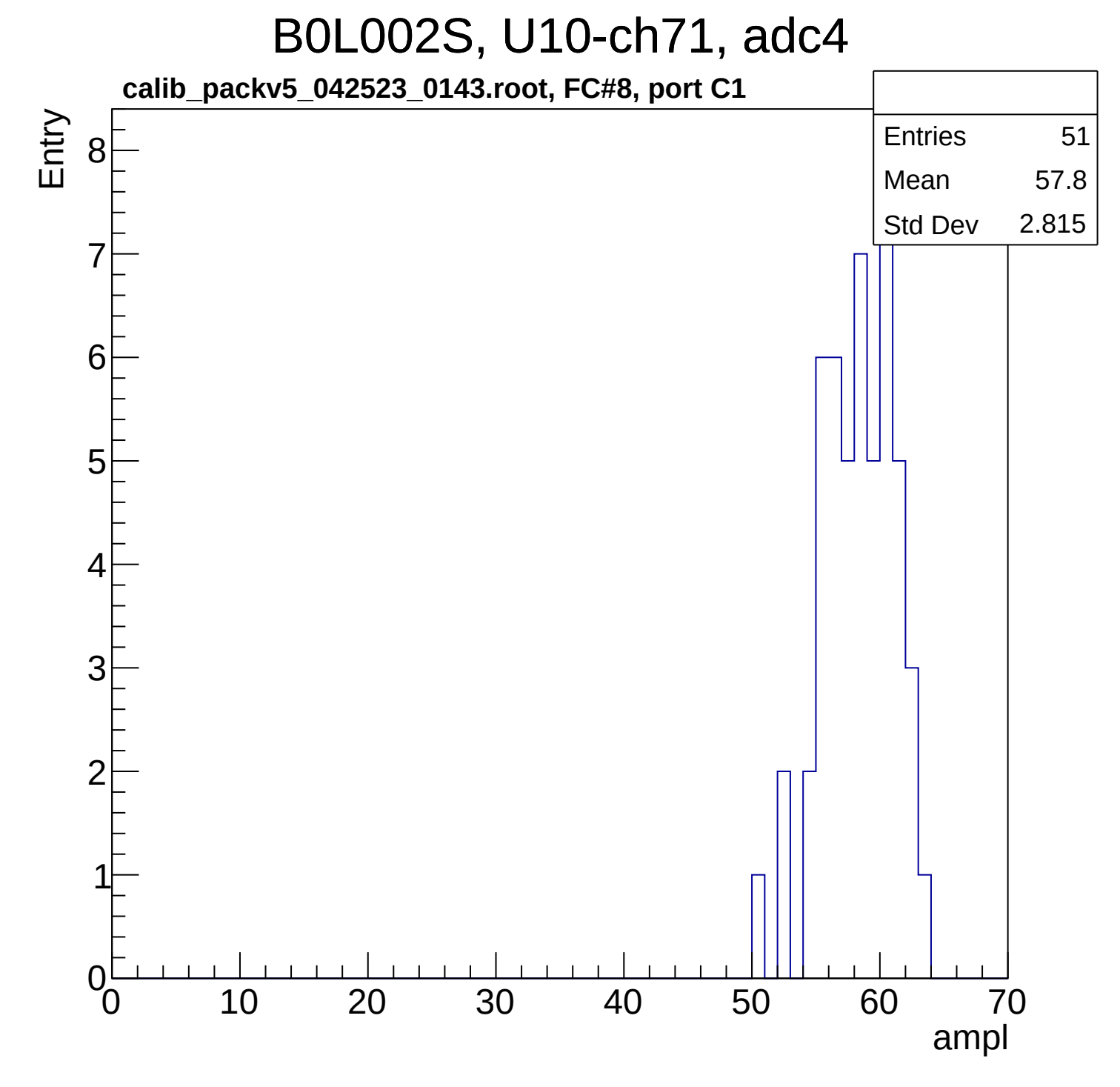

B0L002S, U10-ch70, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

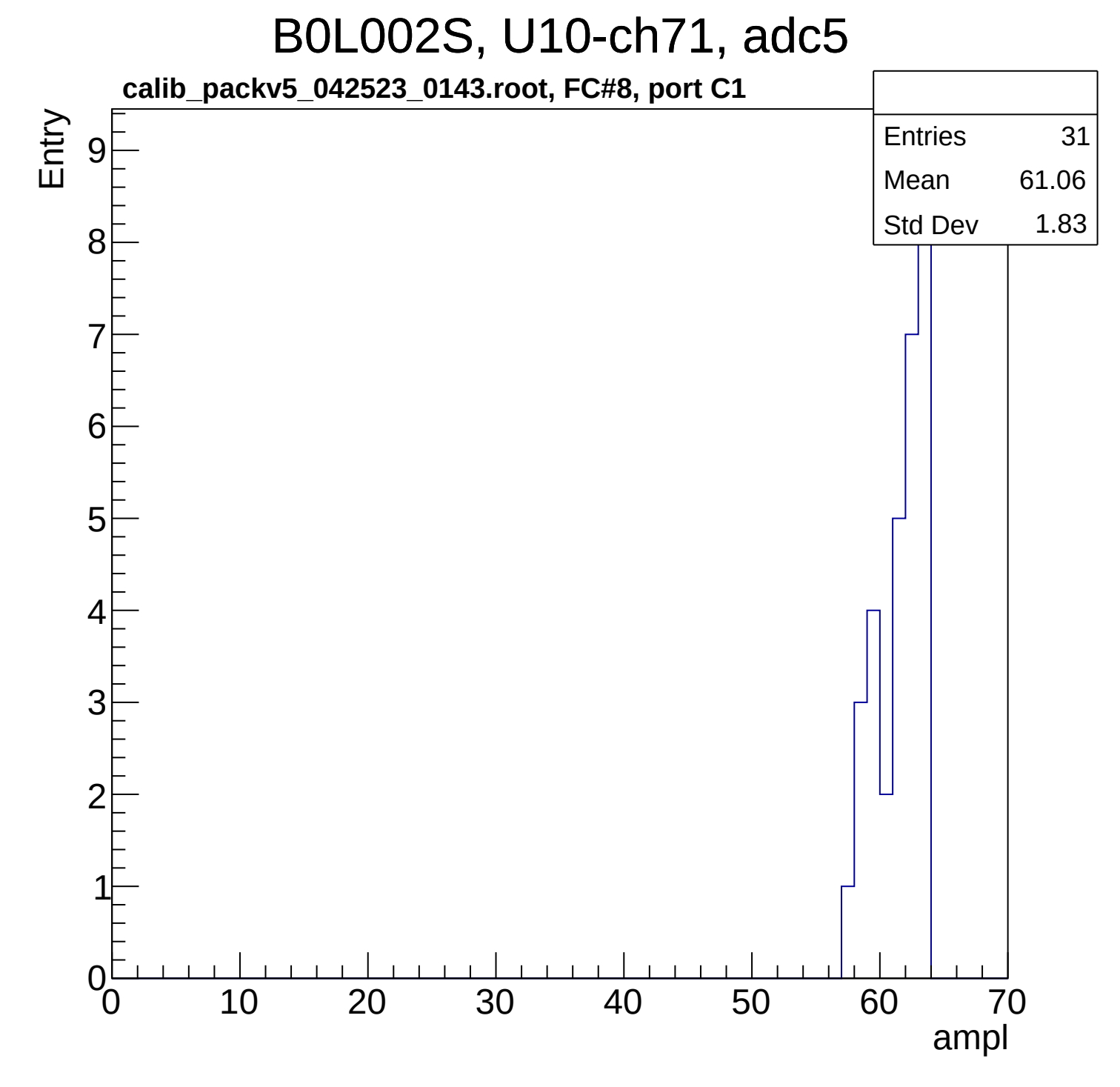

B0L002S, U10-ch71, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 31.61
Std Dev 2.825
Gaus mean : 31.5574
Gaus Width: 3.7319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

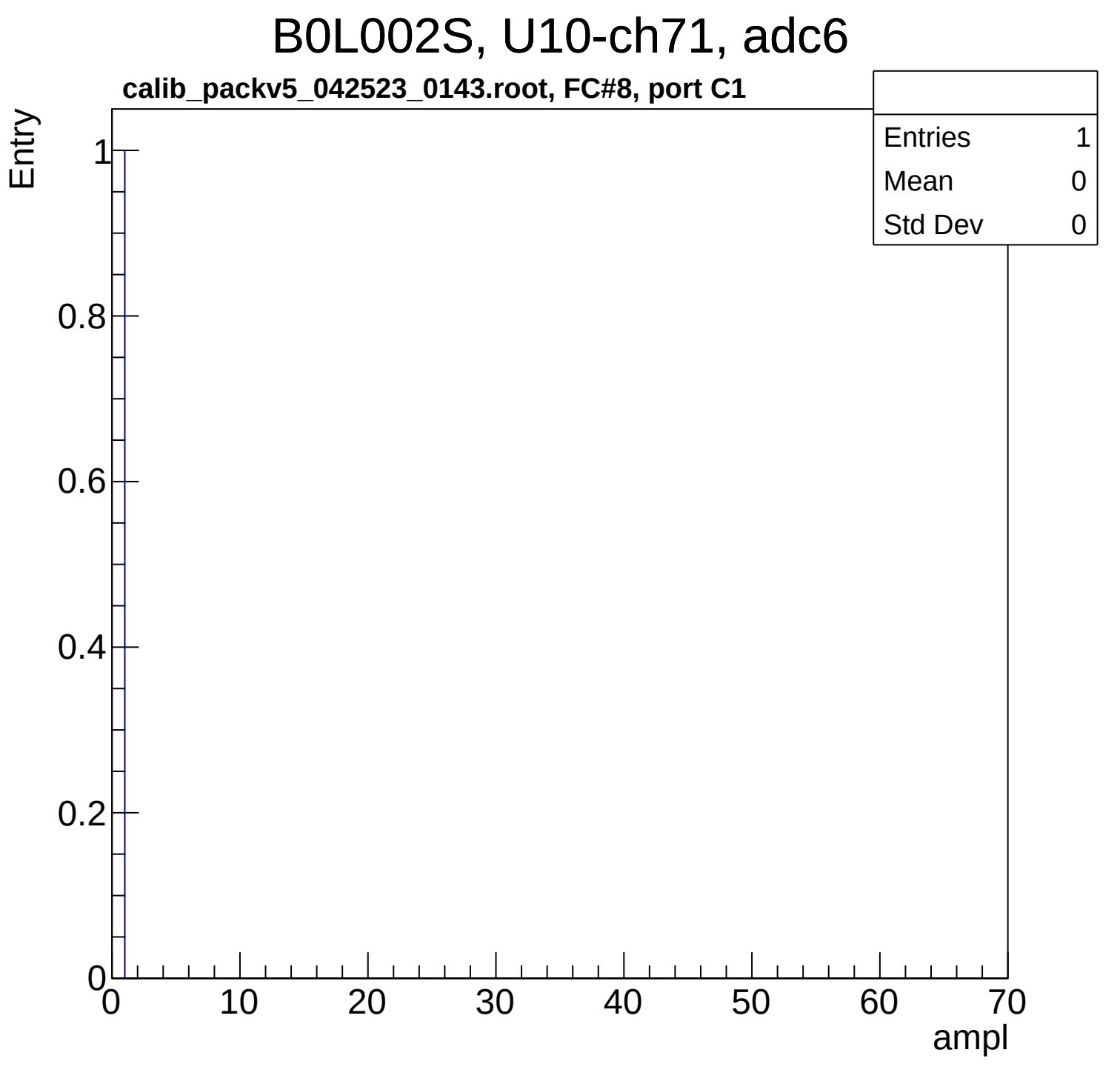

B0L002S, U10-ch71, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 37.44
Std Dev 3.148
Gaus mean : 37.9001
Gaus Width: 3.3224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

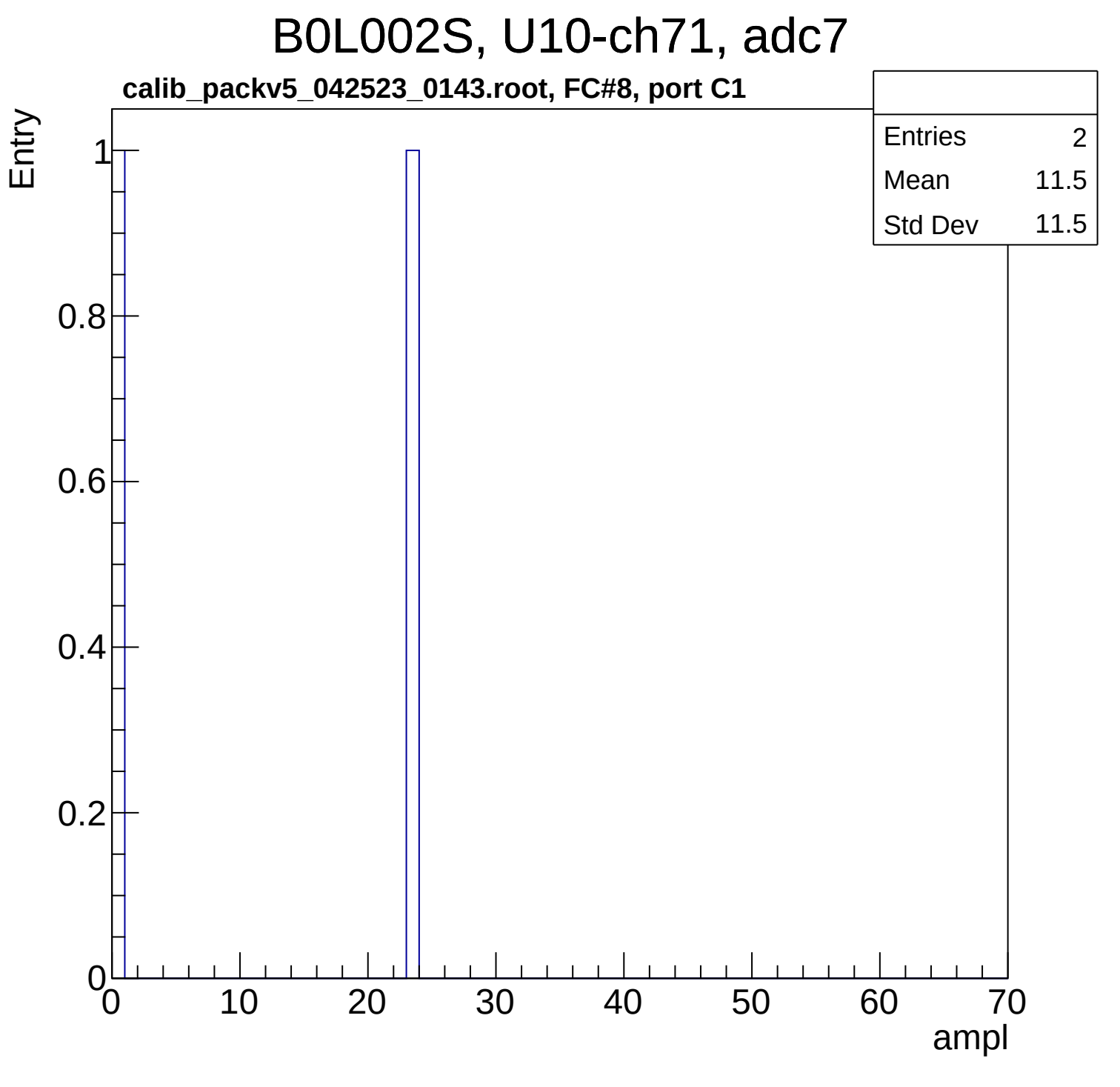

B0L002S, U10-ch71, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 45.3
Std Dev 3.207
Gaus mean : 45.5088
Gaus Width: 3.2807
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

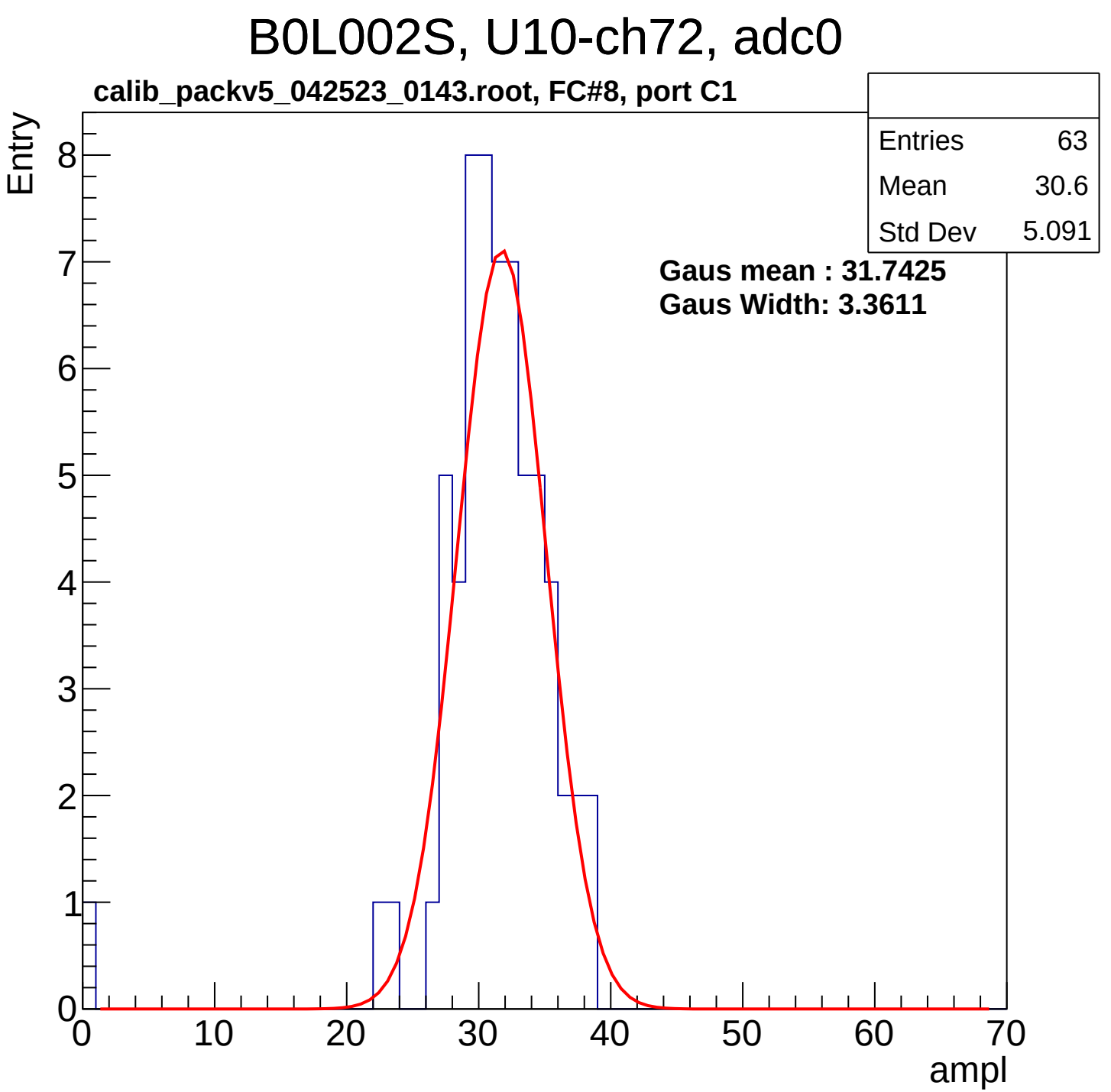

B0L002S, U10-ch71, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 52.26
Std Dev 2.862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

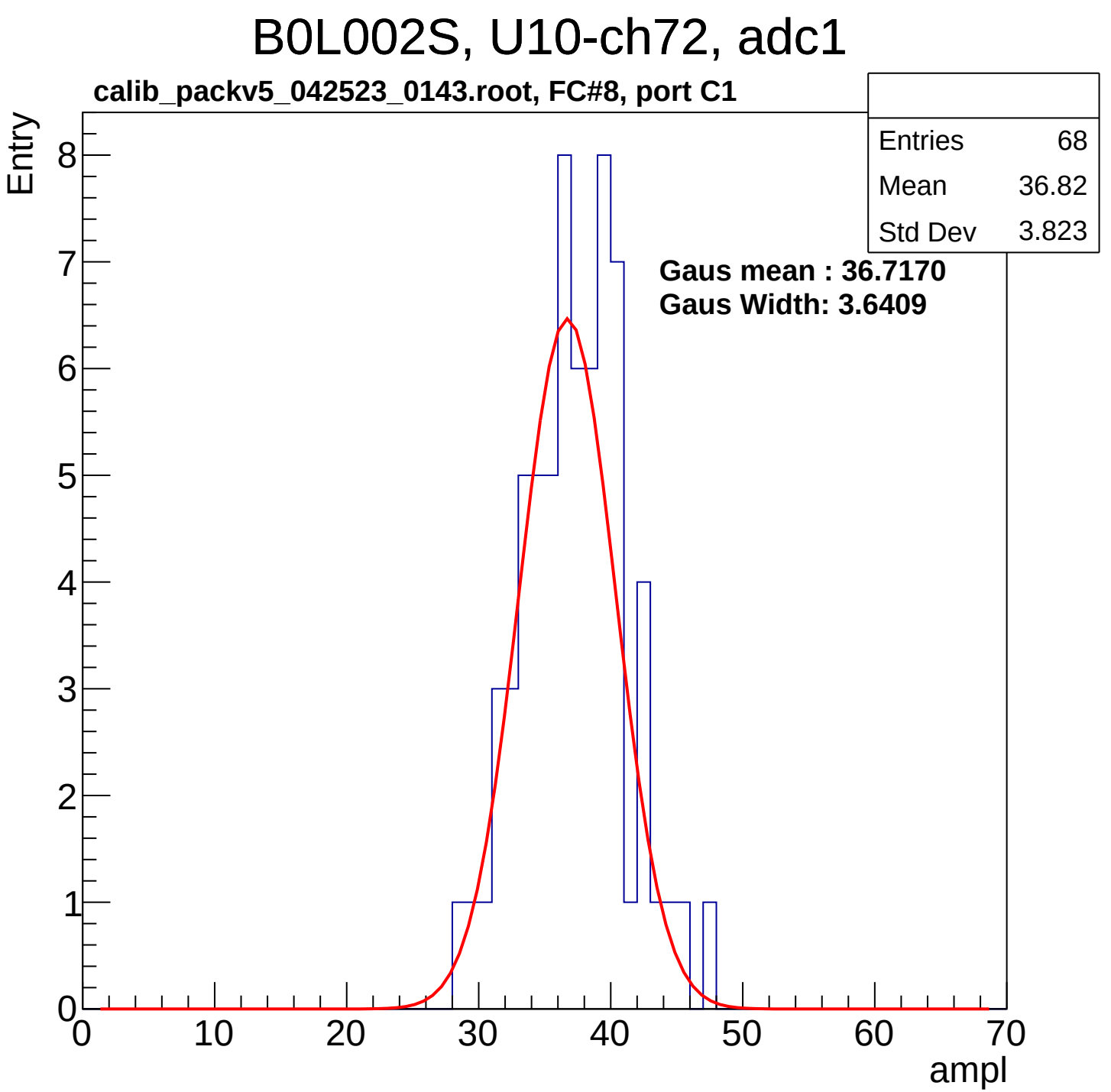

B0L002S, U10-ch71, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 57.8
Std Dev 2.815
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

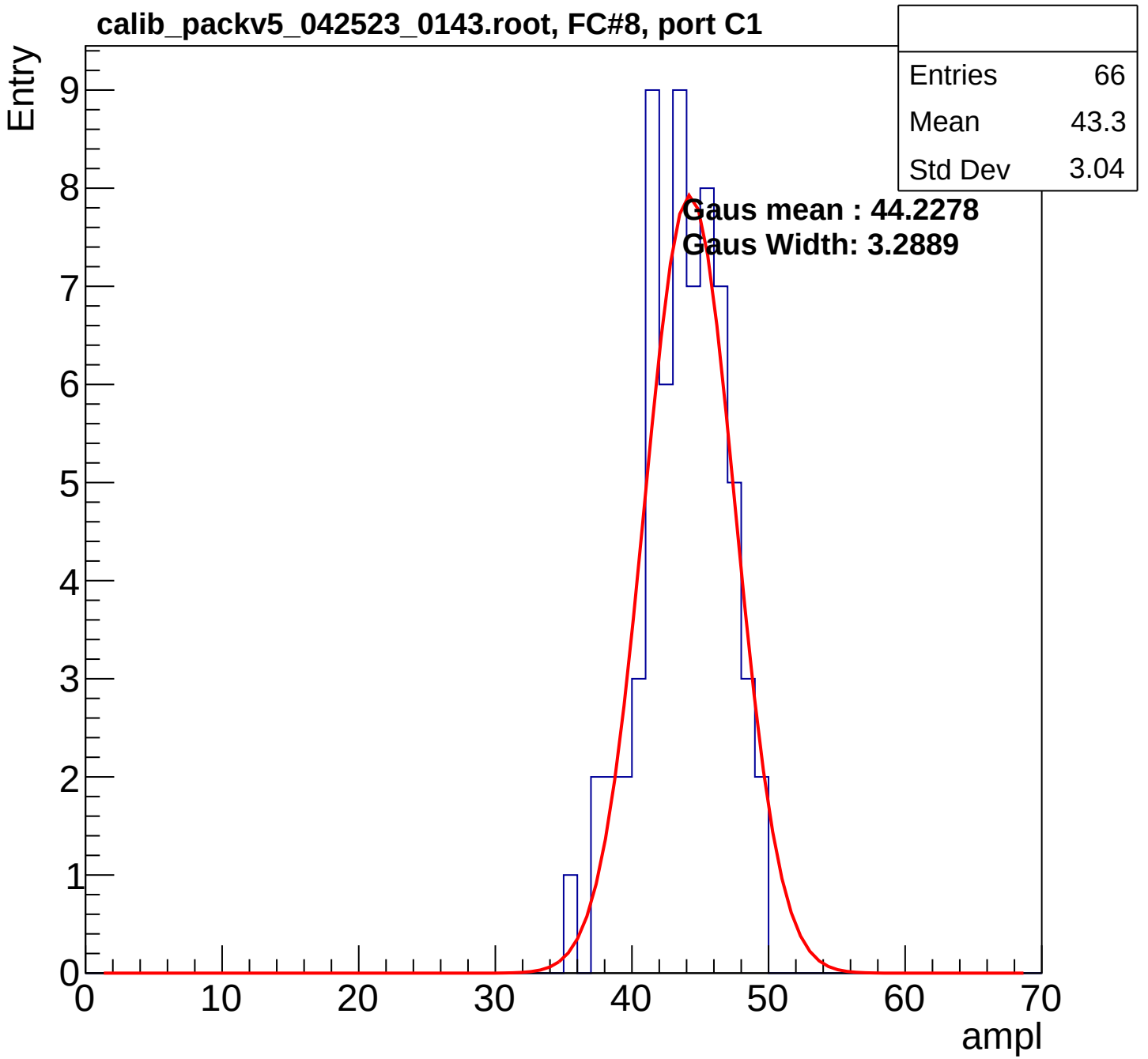

B0L002S, U10-ch71, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 61.06
Std Dev 1.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

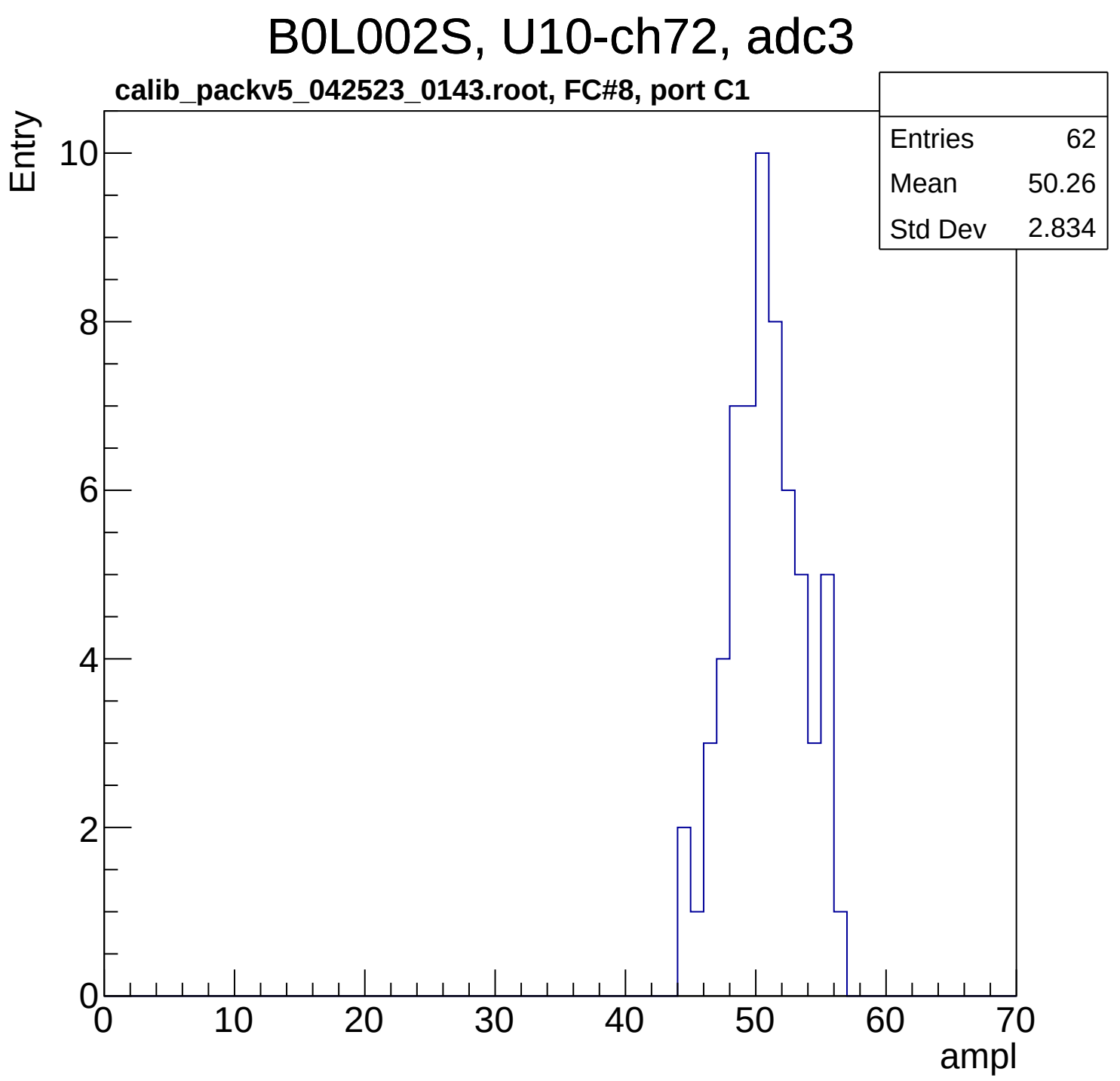

B0L002S, U10-ch71, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

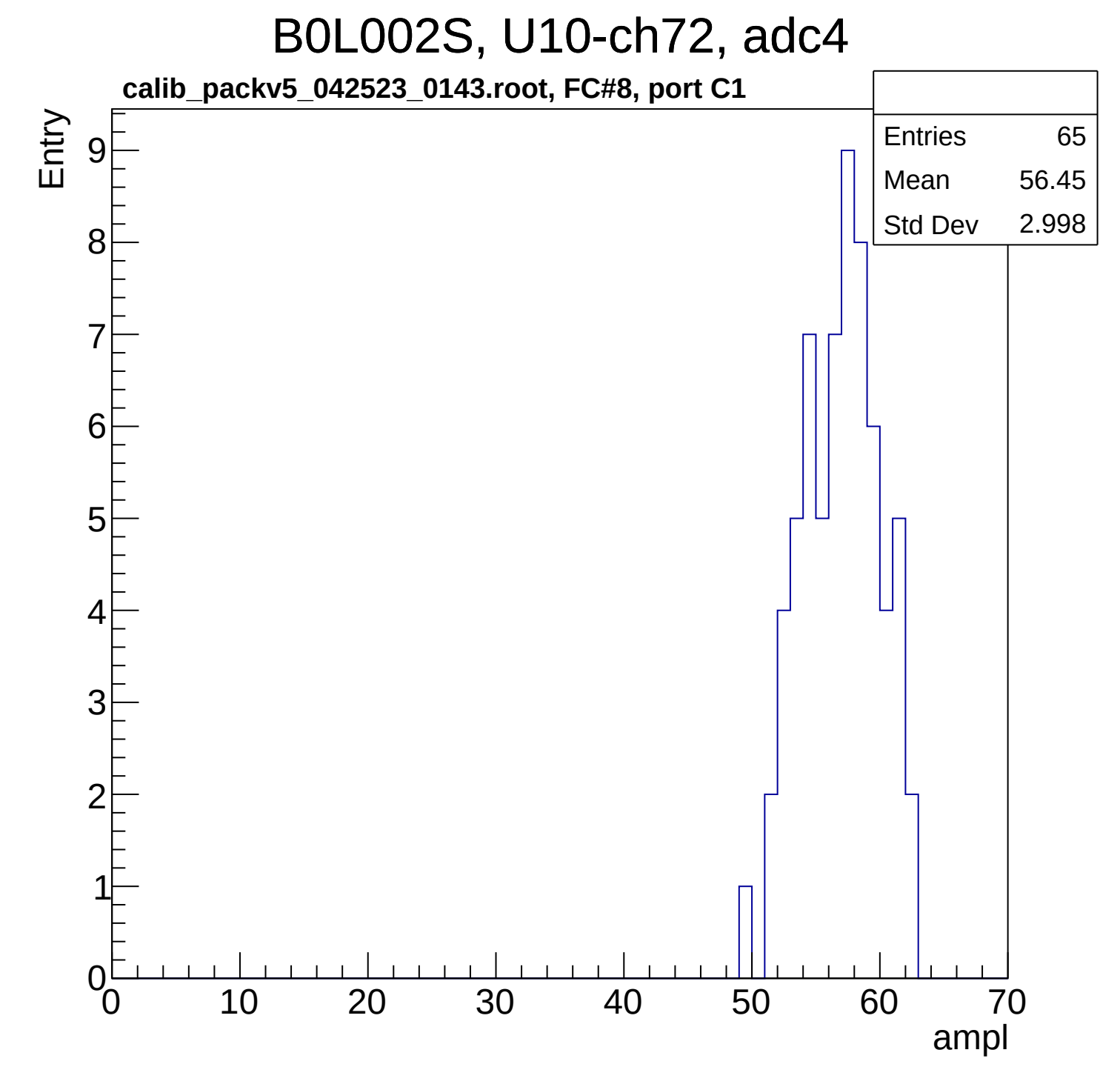

B0L002S, U10-ch71, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

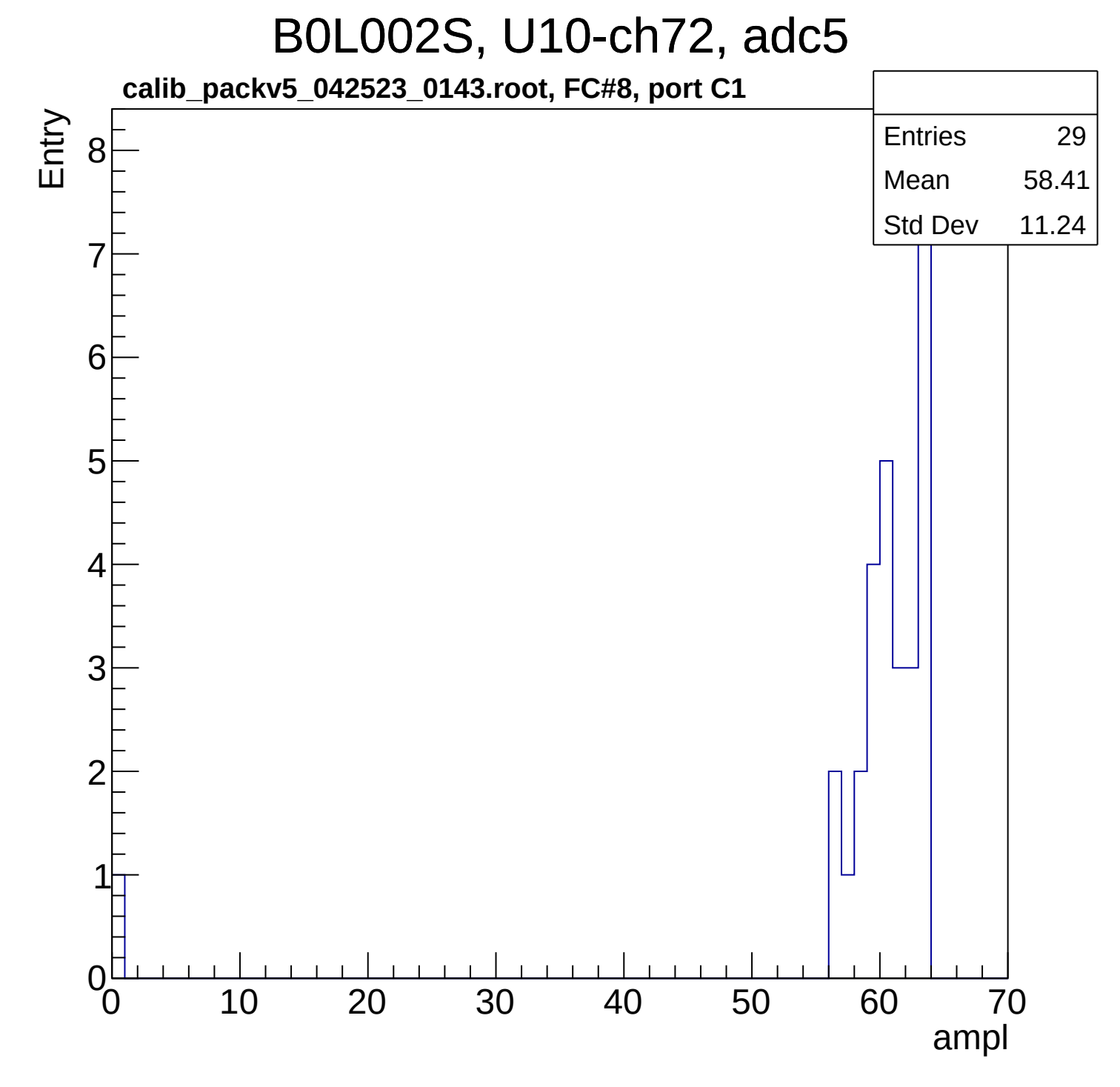

B0L002S, U10-ch72, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 30.6
Std Dev 5.091
Gaus mean : 31.7425
Gaus Width: 3.3611
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

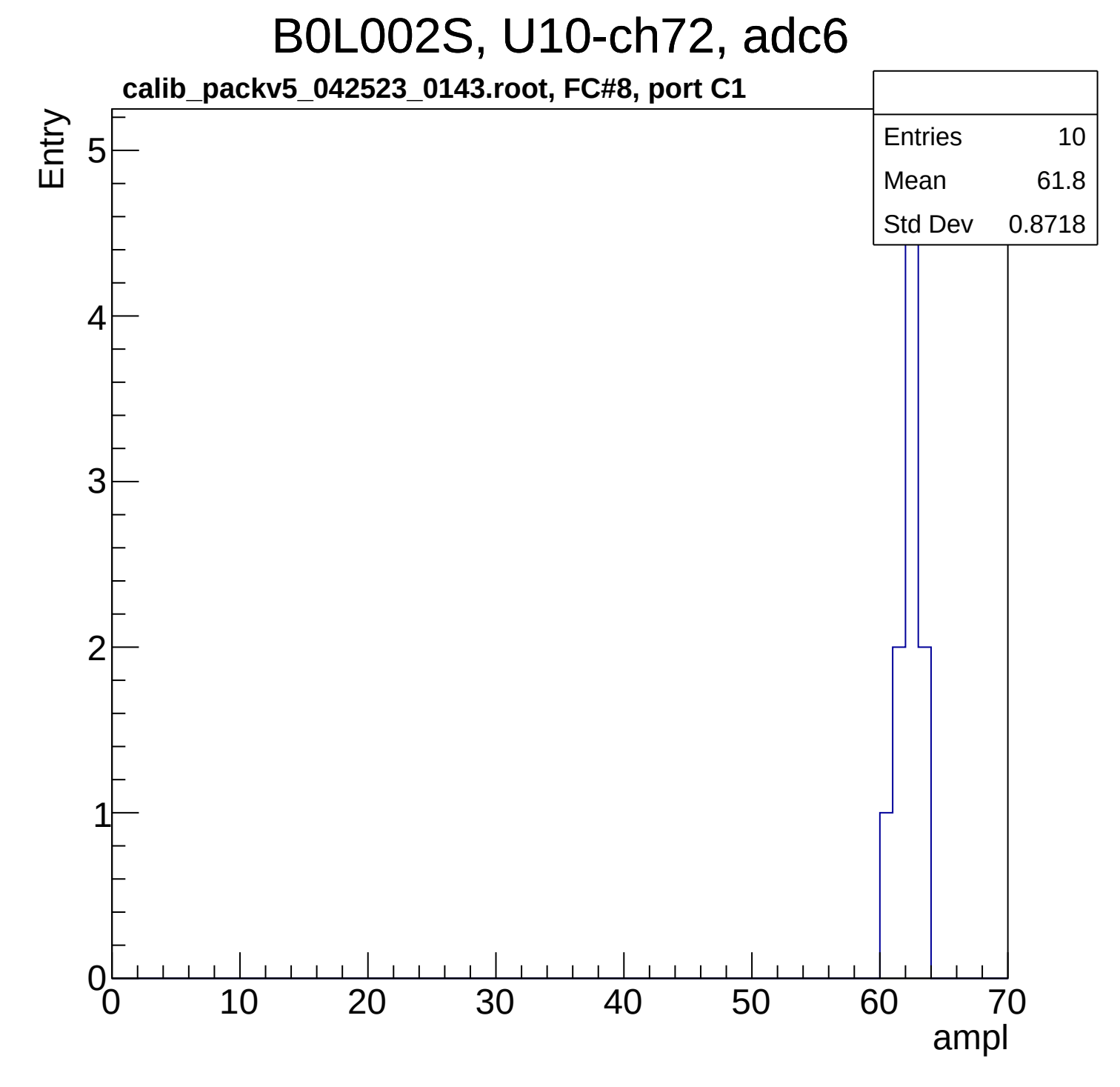

B0L002S, U10-ch72, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 36.82
Std Dev 3.823
Gaus mean : 36.7170
Gaus Width: 3.6409
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

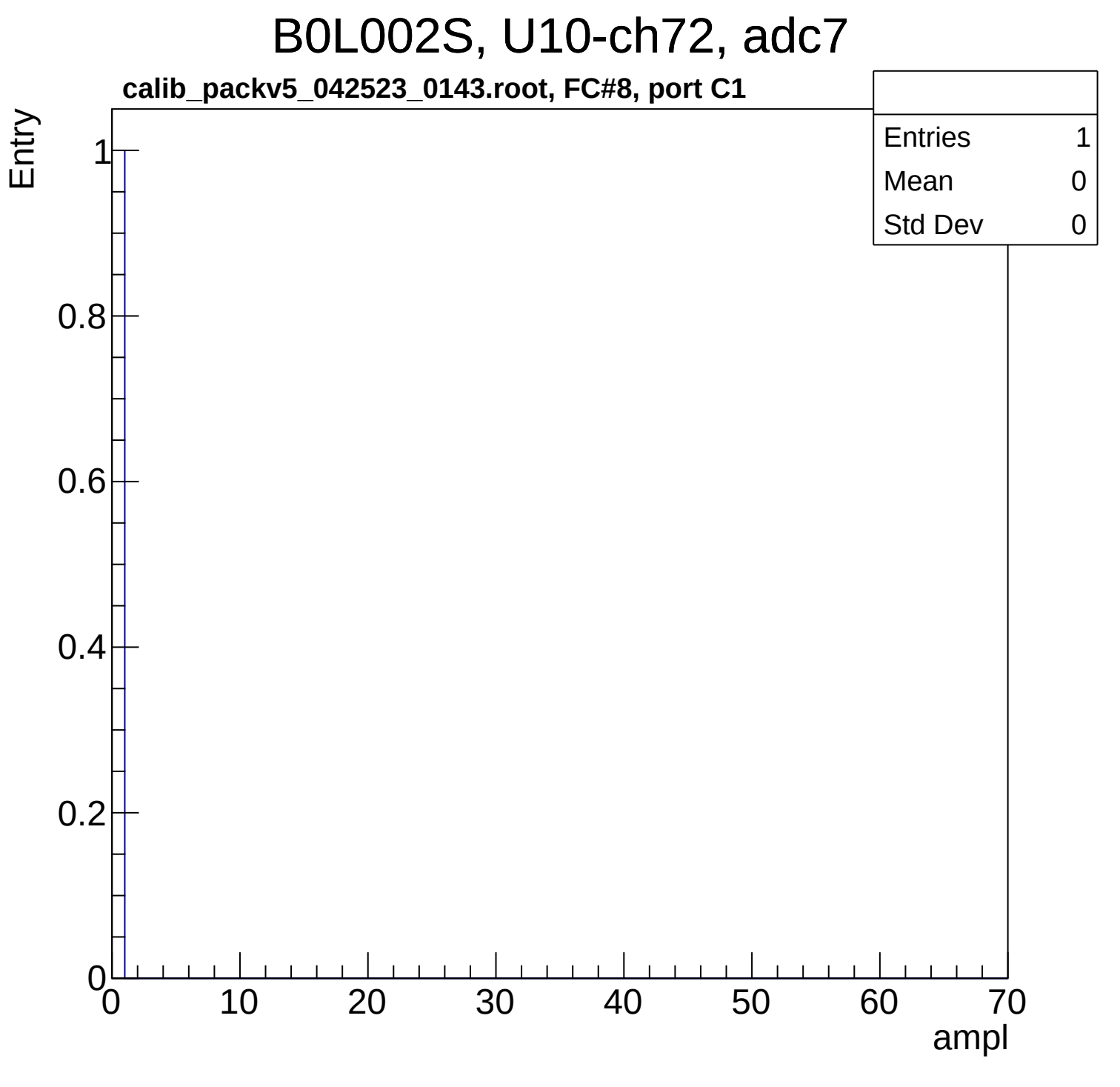

B0L002S, U10-ch72, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 43.3
Std Dev 3.04
Gaus mean : 44.2278
Gaus Width: 3.2889
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

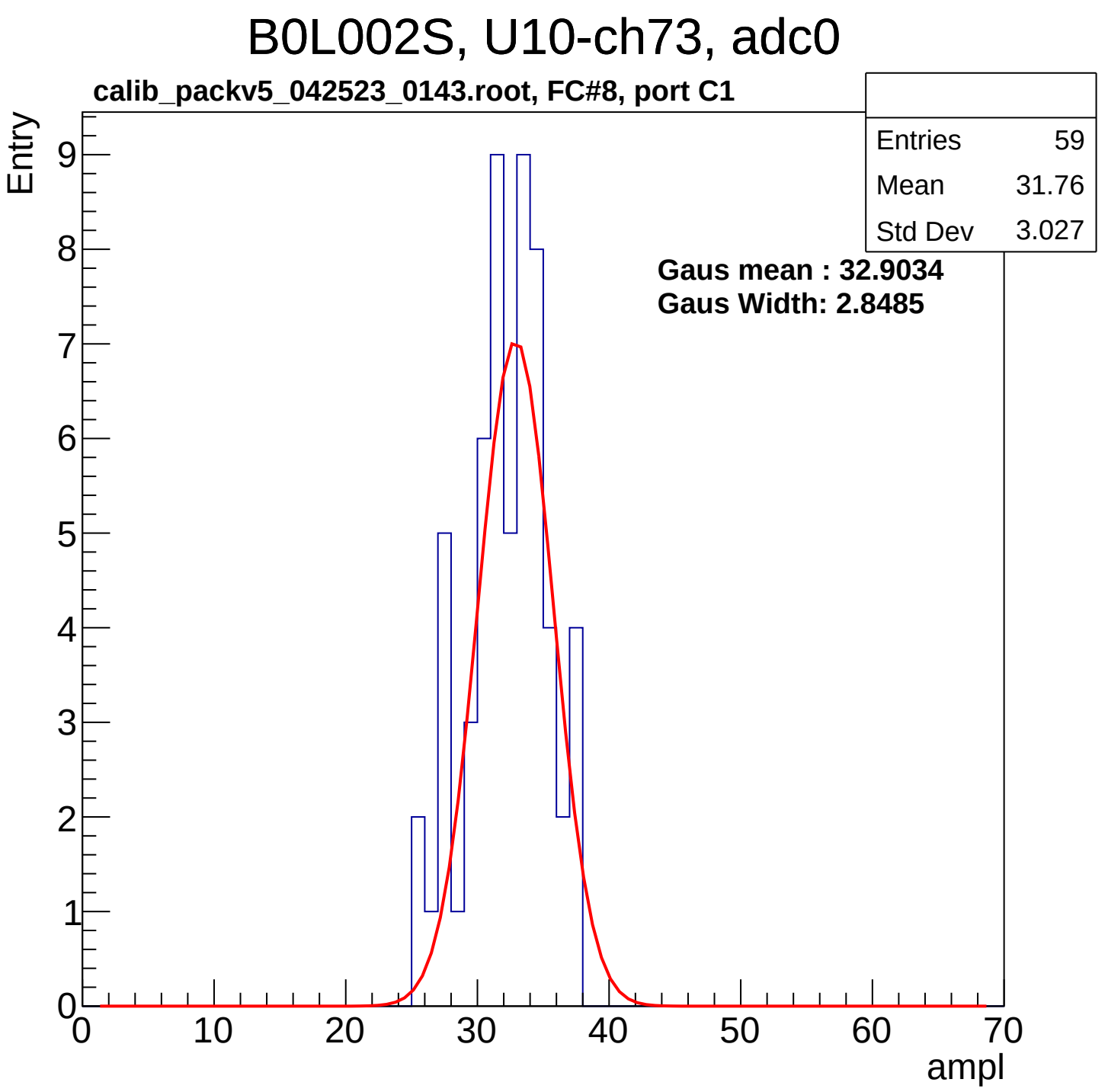

B0L002S, U10-ch72, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 50.26
Std Dev 2.834
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

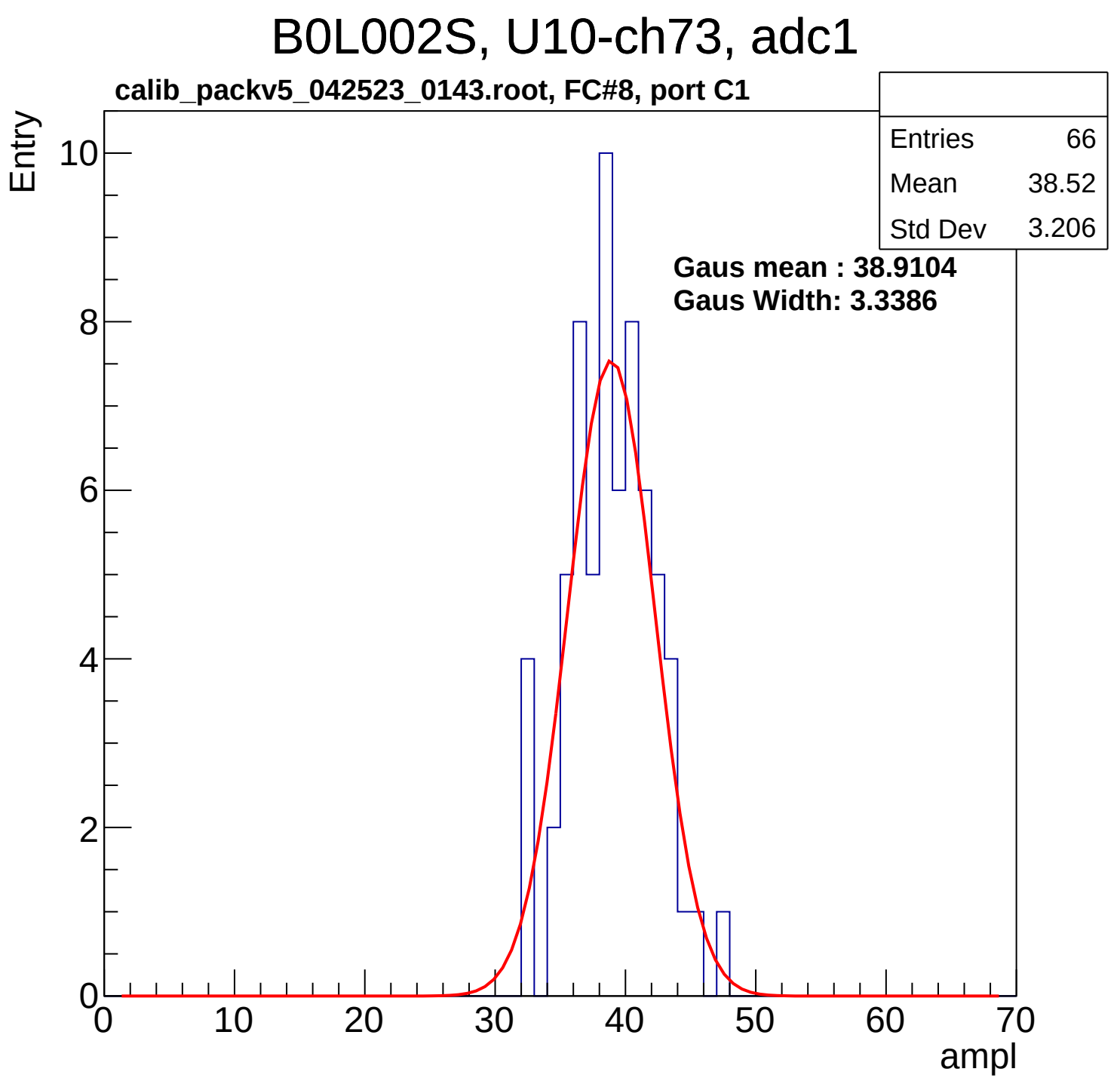

B0L002S, U10-ch72, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.45
Std Dev 2.998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

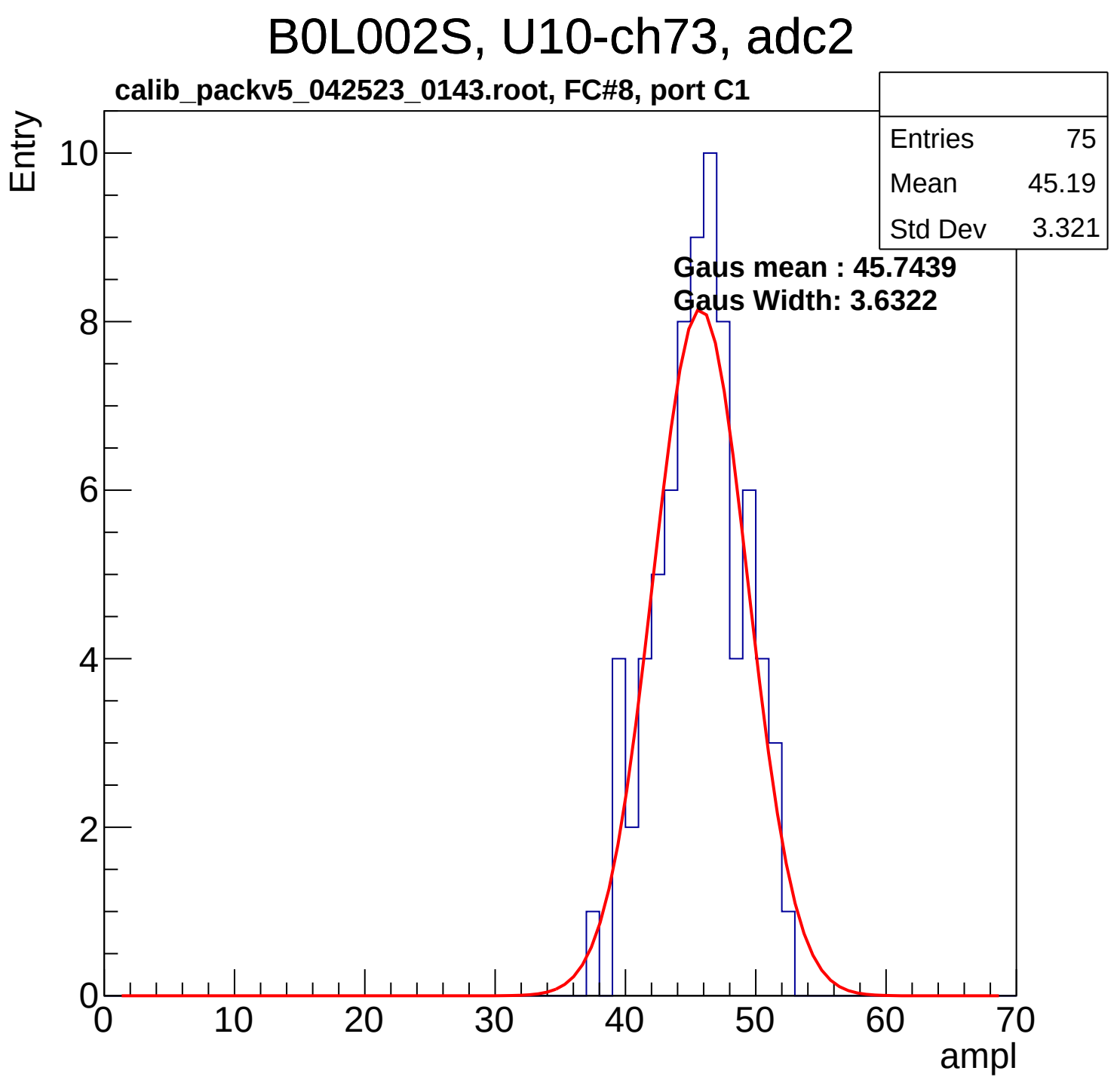

B0L002S, U10-ch72, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.41
Std Dev 11.24
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

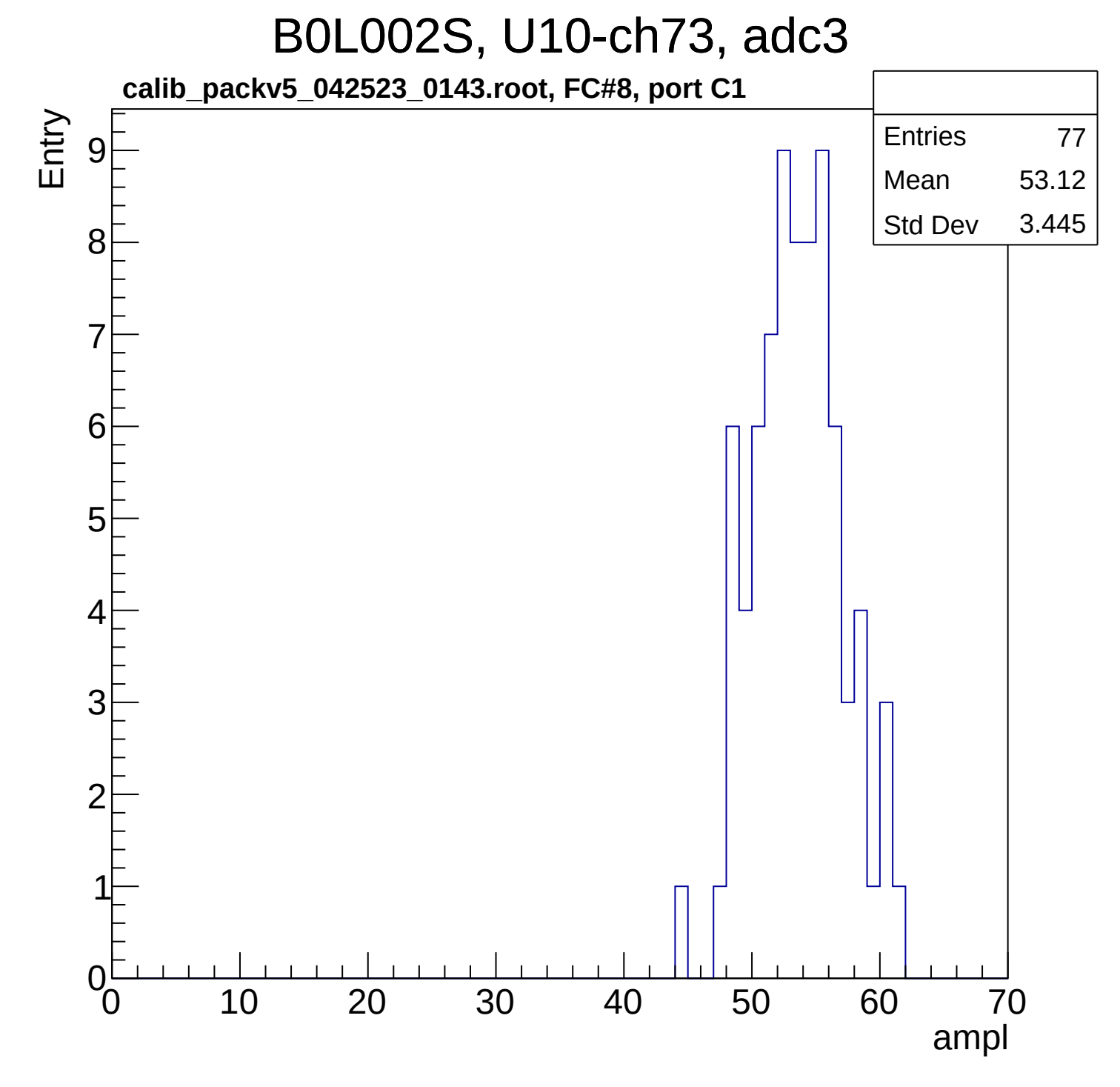

B0L002S, U10-ch72, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.8
Std Dev 0.8718
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

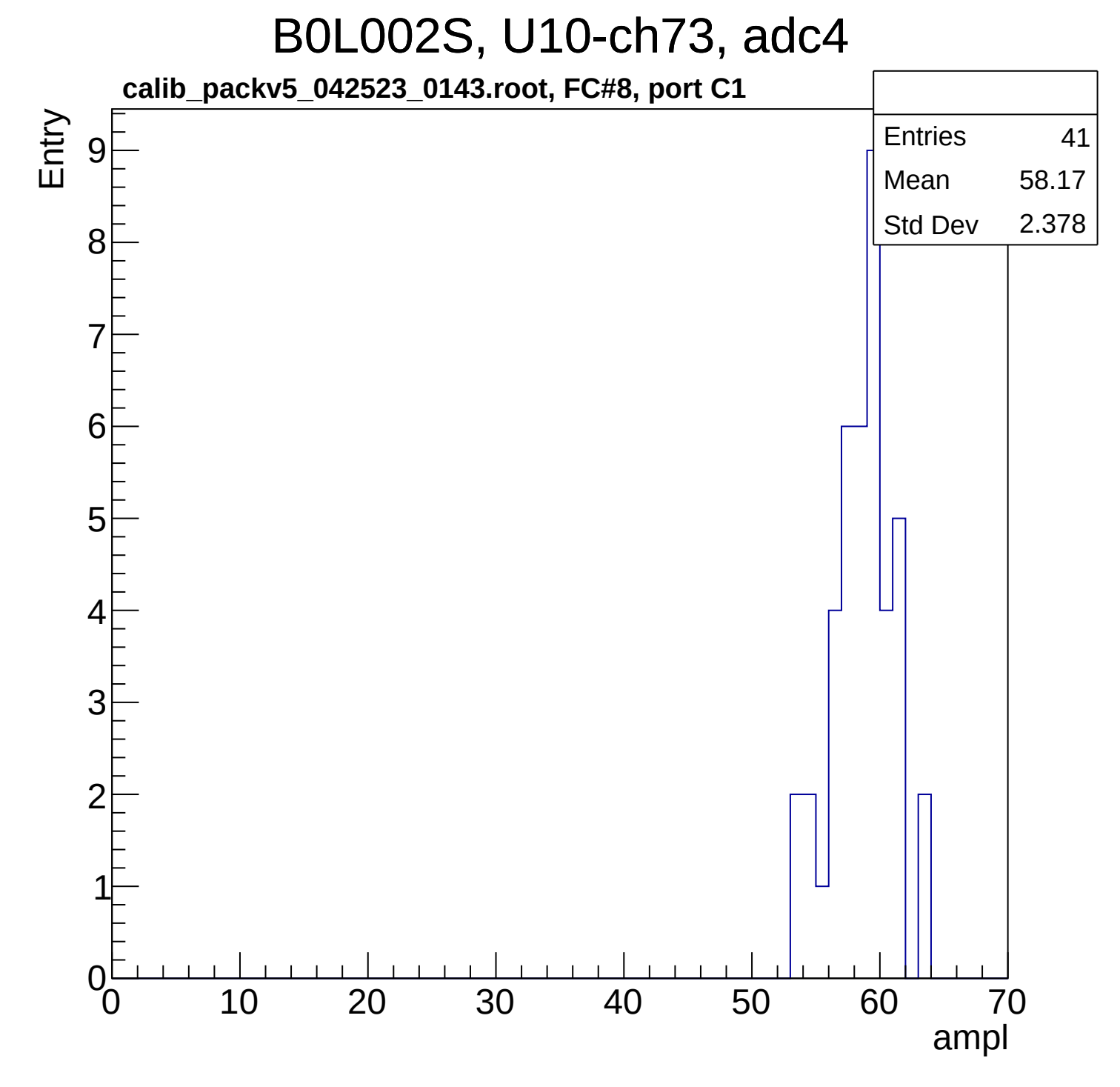

B0L002S, U10-ch72, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

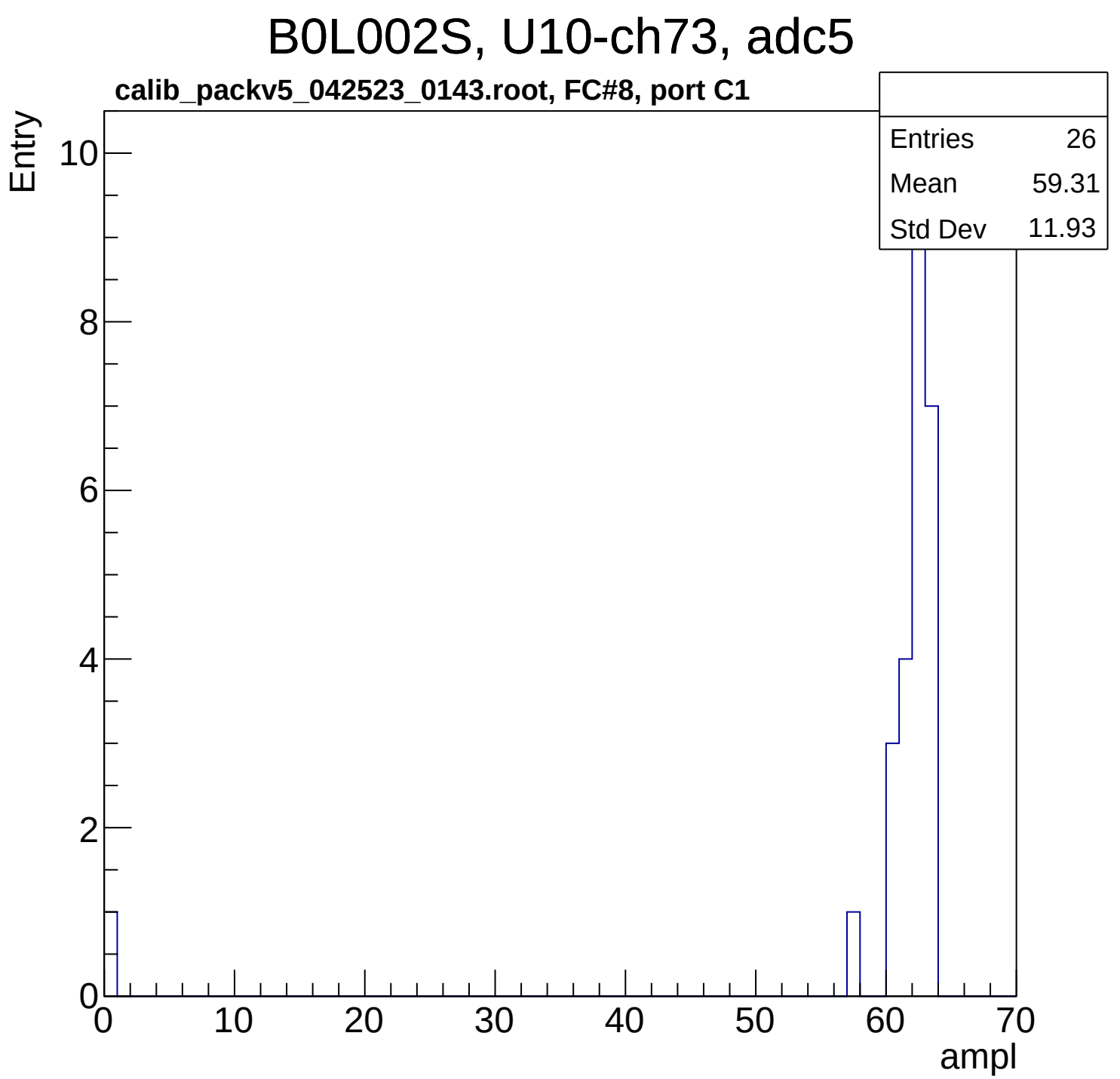

B0L002S, U10-ch73, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 31.76
Std Dev 3.027
Gaus mean : 32.9034
Gaus Width: 2.8485
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

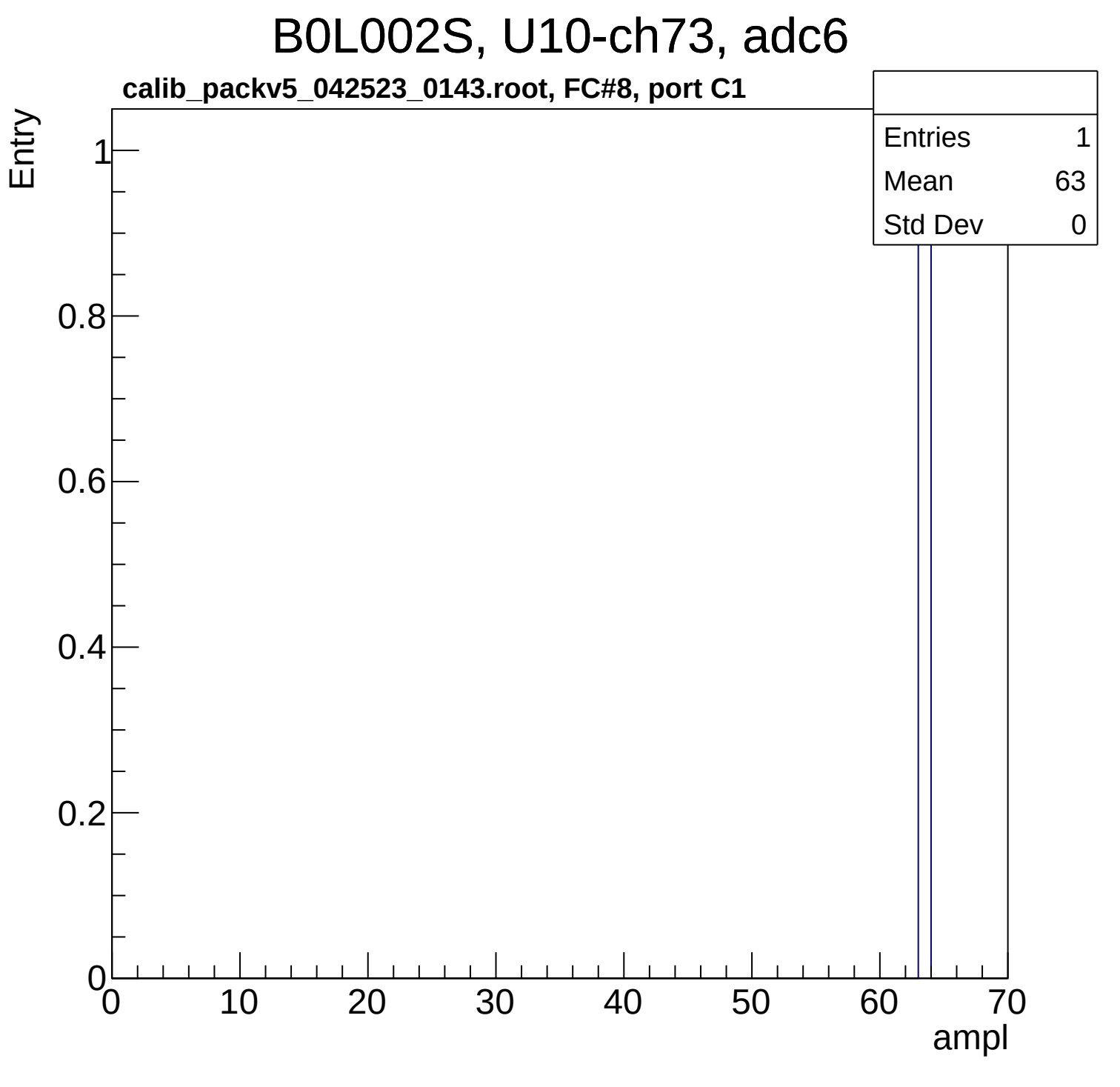

B0L002S, U10-ch73, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 38.52
Std Dev 3.206
Gaus mean : 38.9104
Gaus Width: 3.3386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

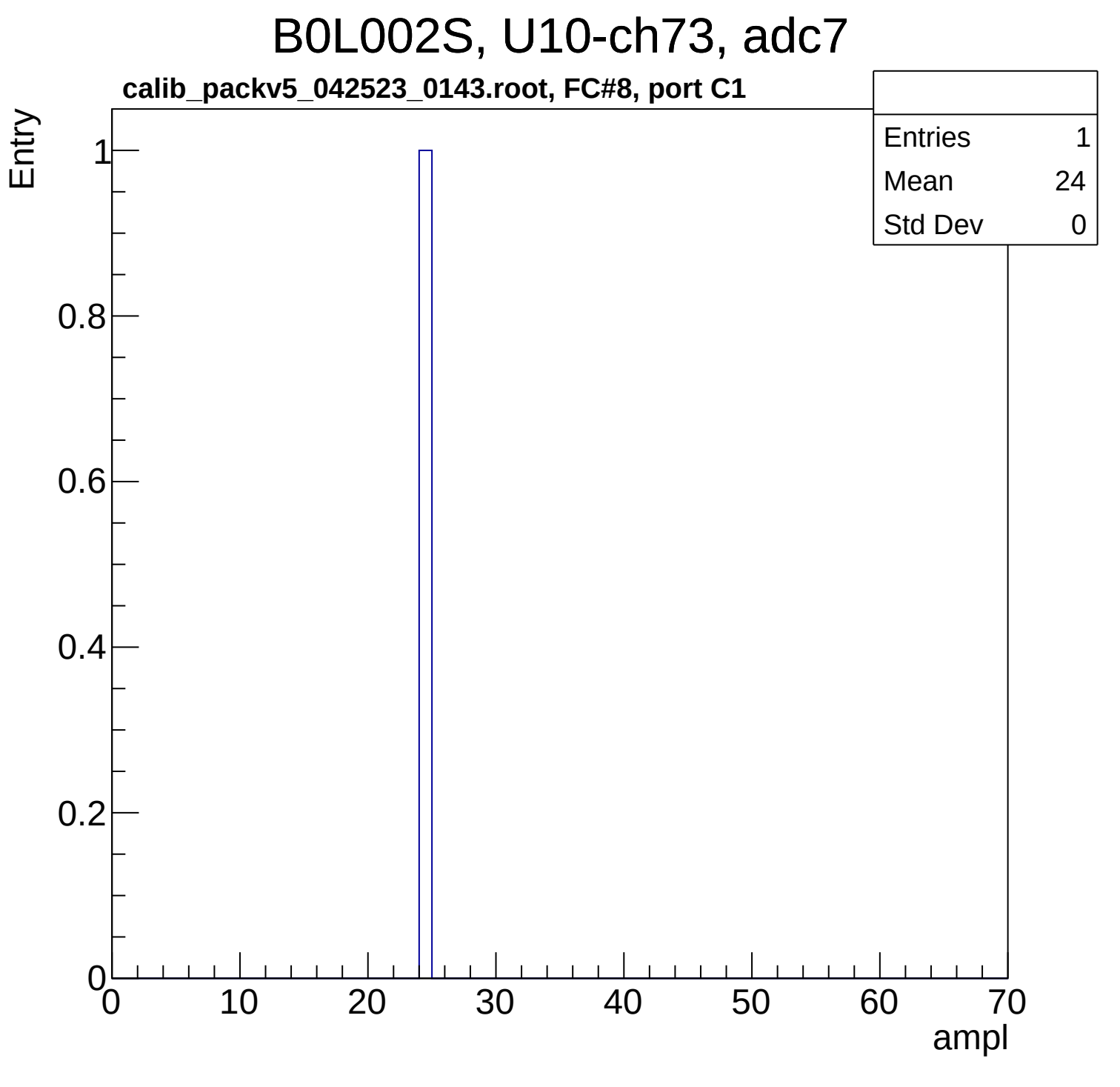

B0L002S, U10-ch73, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 45.19
Std Dev 3.321
Gaus mean : 45.7439
Gaus Width: 3.6322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

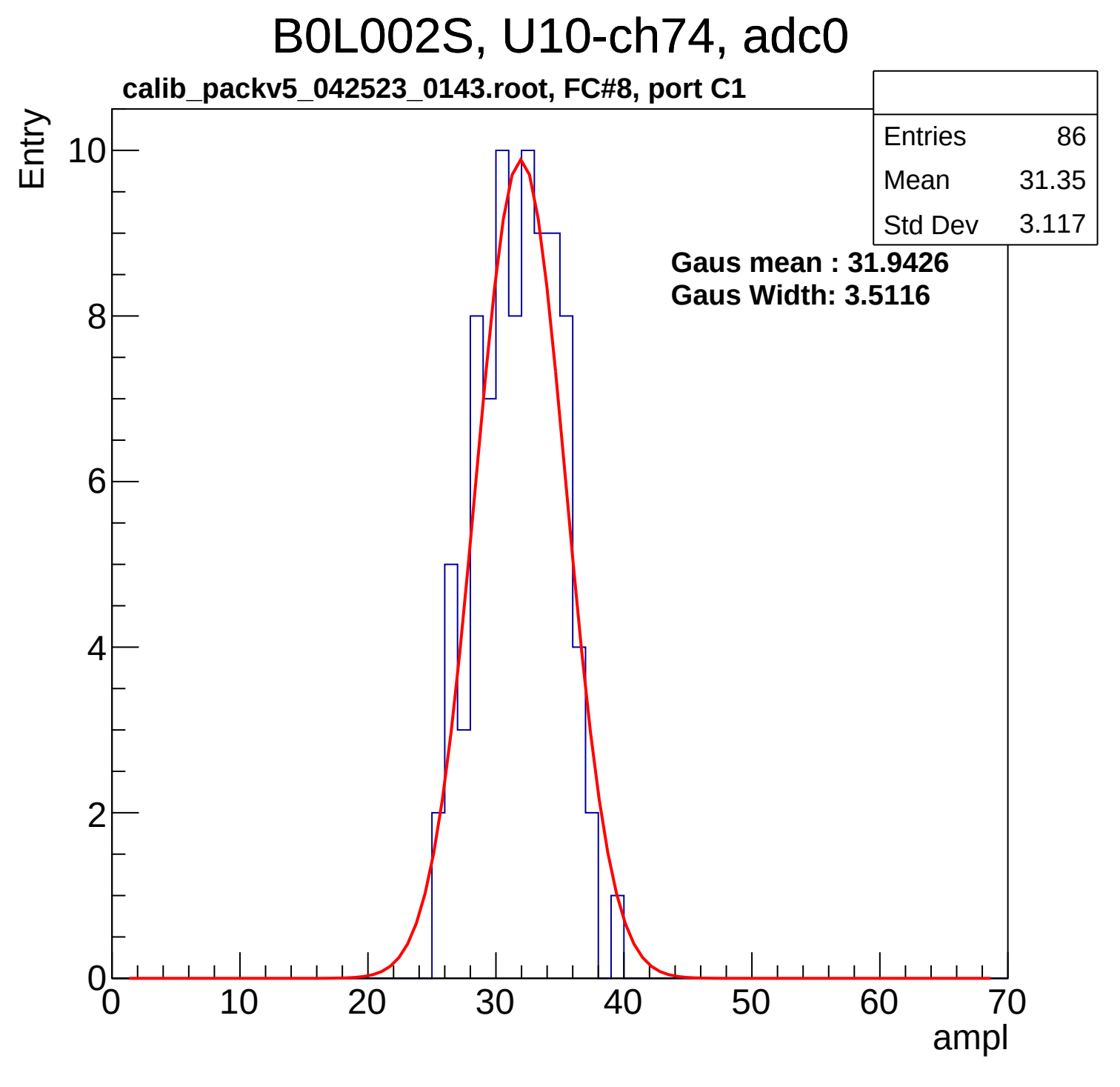

B0L002S, U10-ch73, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 53.12
Std Dev 3.445
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

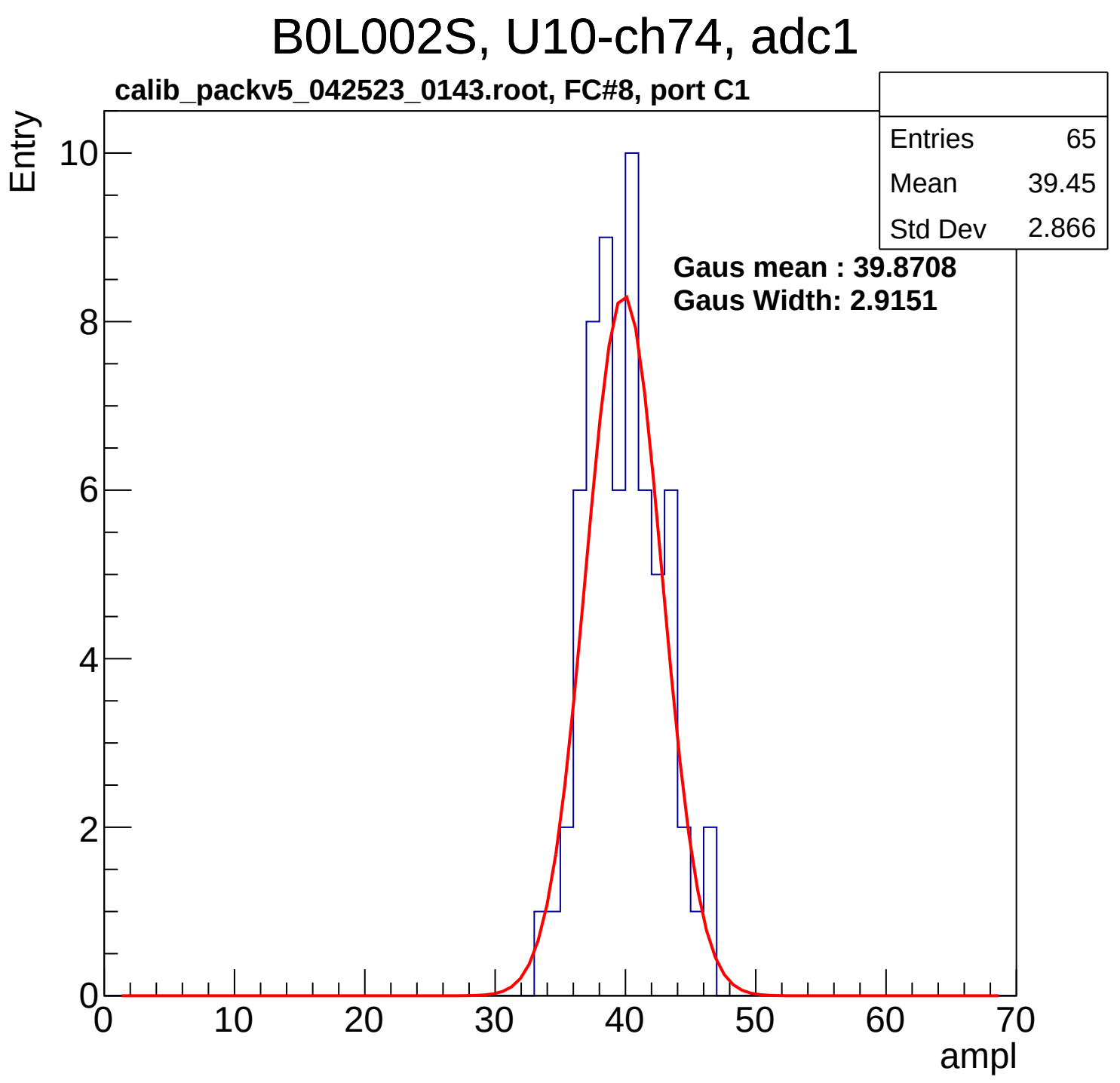

B0L002S, U10-ch73, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.17
Std Dev 2.378
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

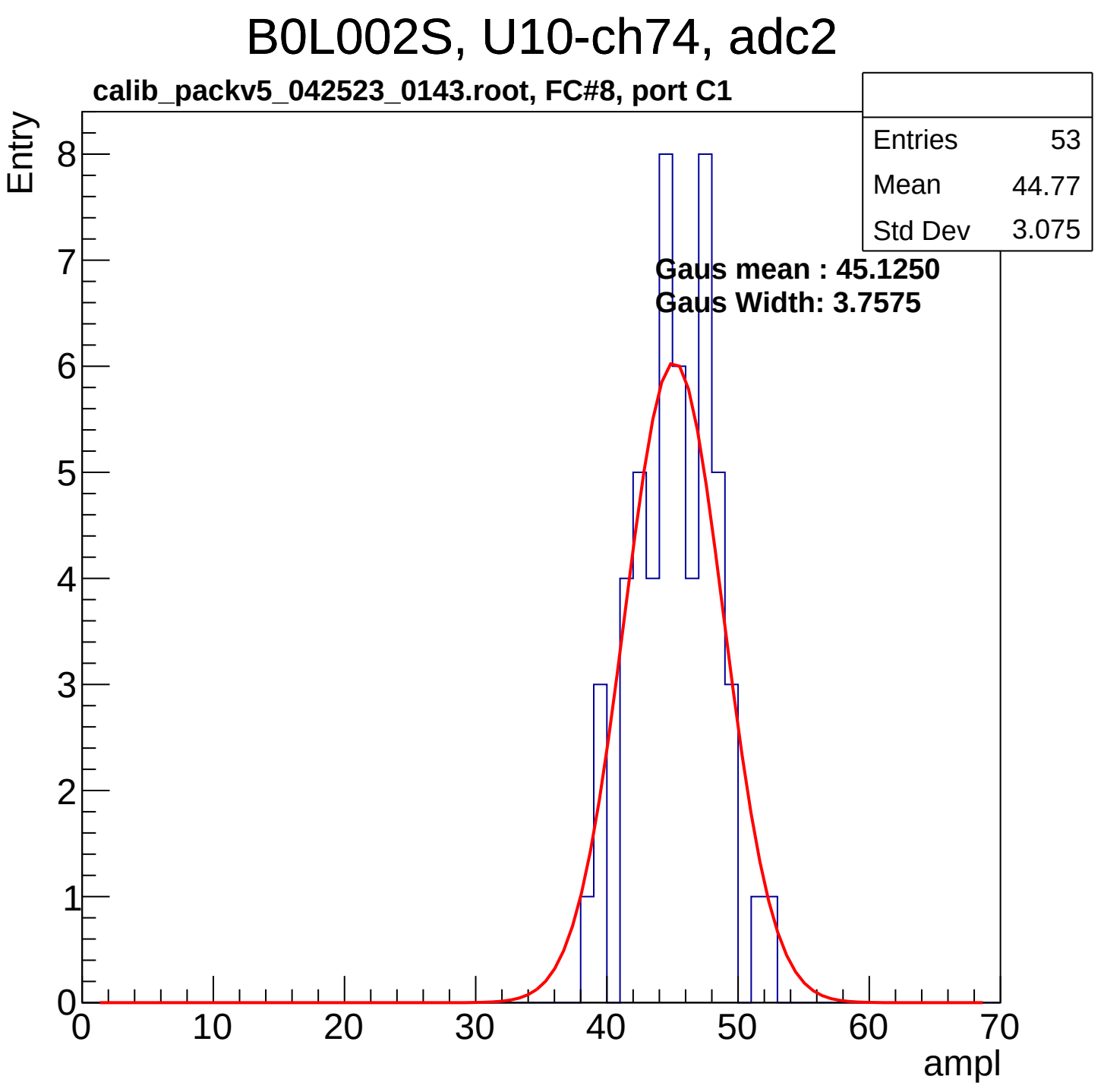

B0L002S, U10-ch73, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 59.31
Std Dev 11.93
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

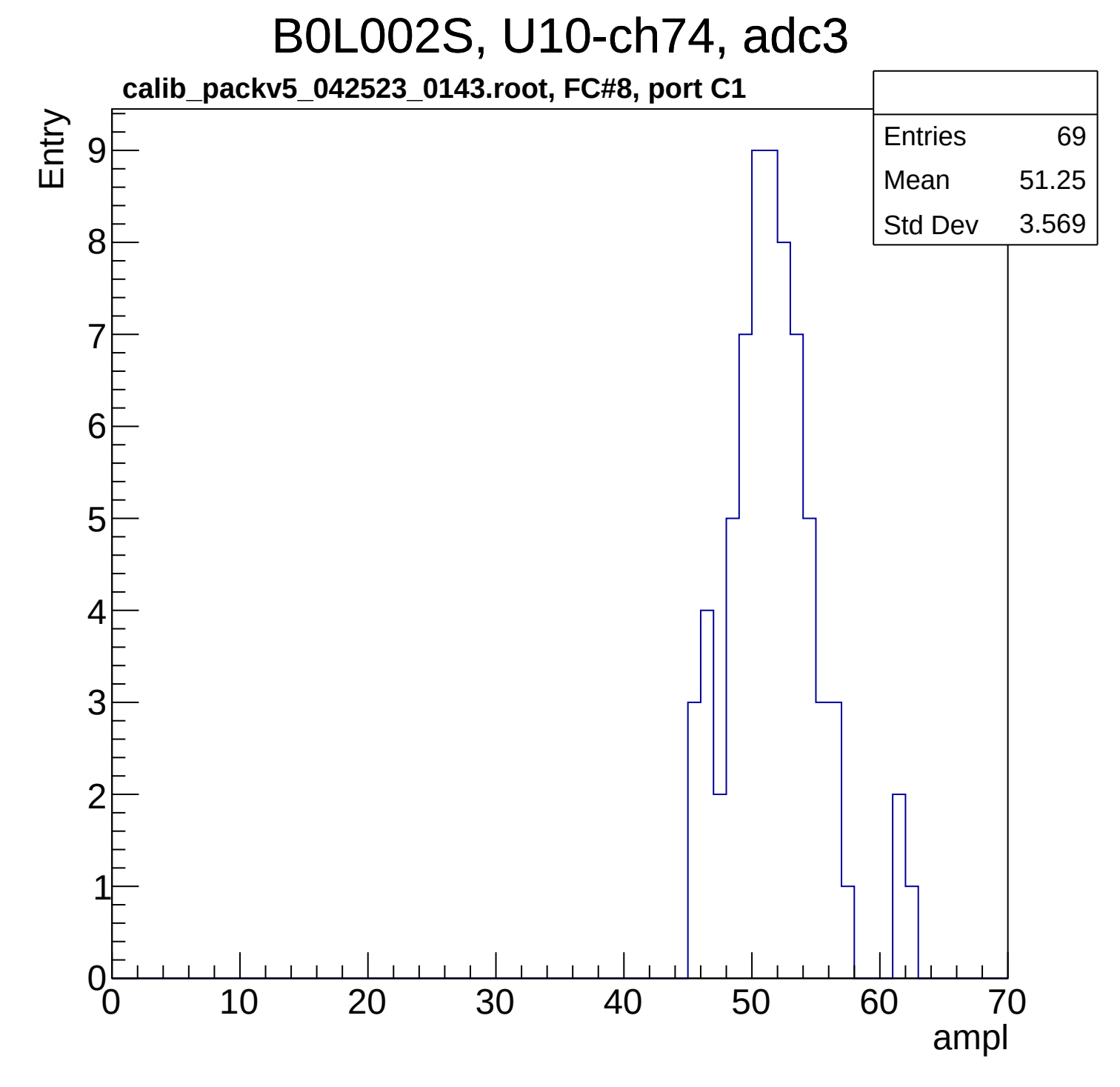

B0L002S, U10-ch73, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

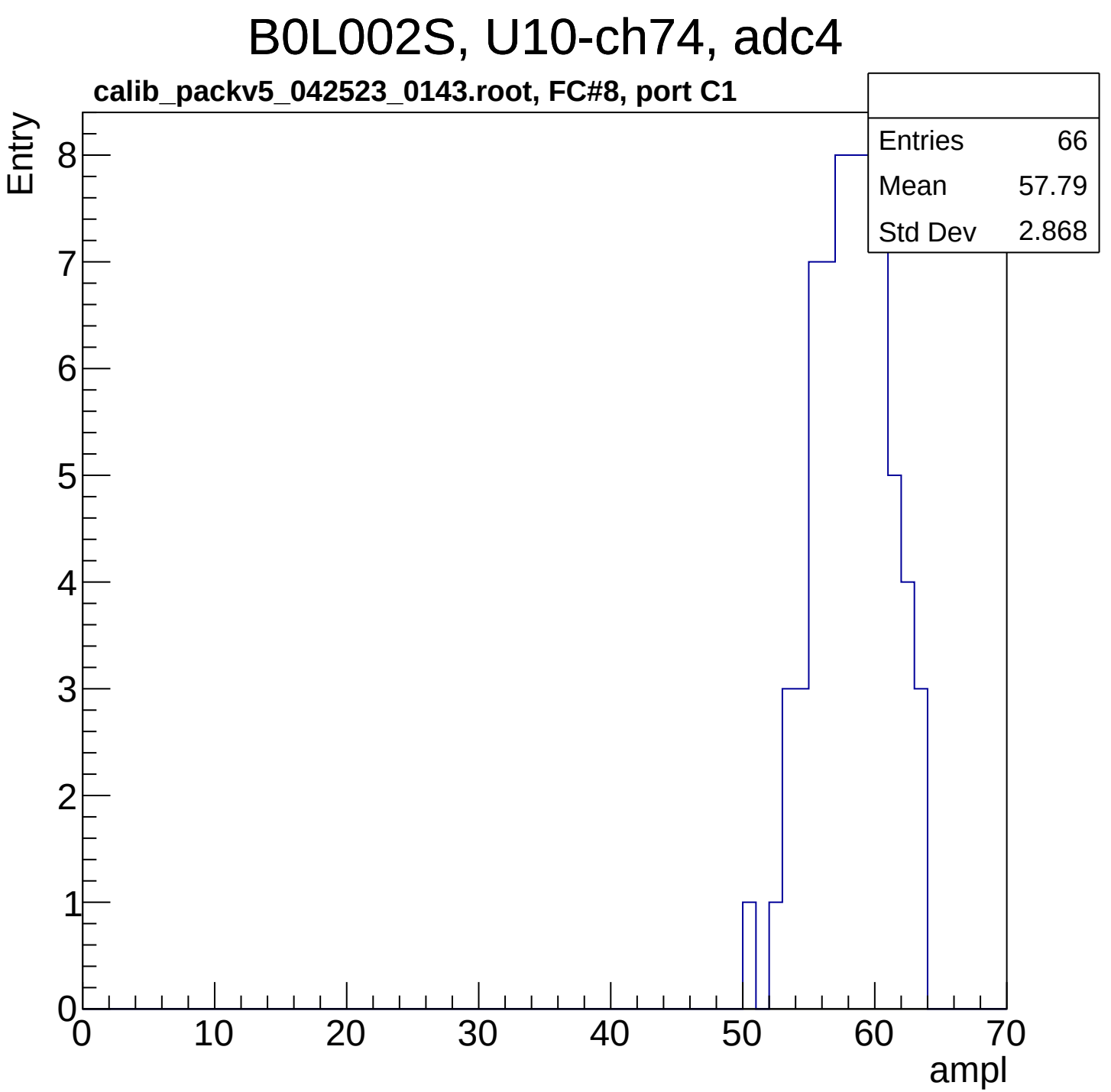

B0L002S, U10-ch73, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

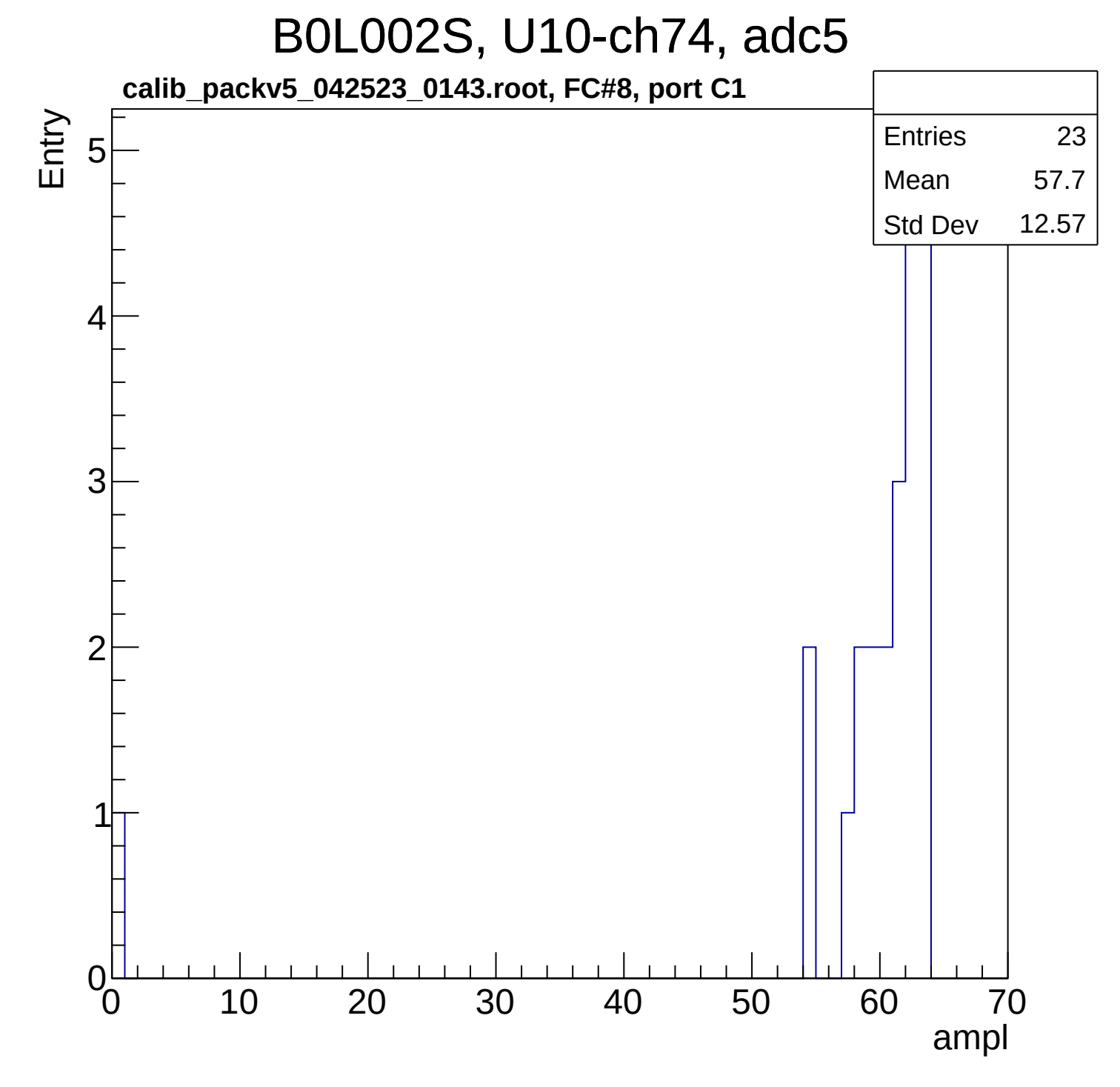

B0L002S, U10-ch74, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 31.35
Std Dev 3.117
Gaus mean : 31.9426
Gaus Width: 3.5116
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

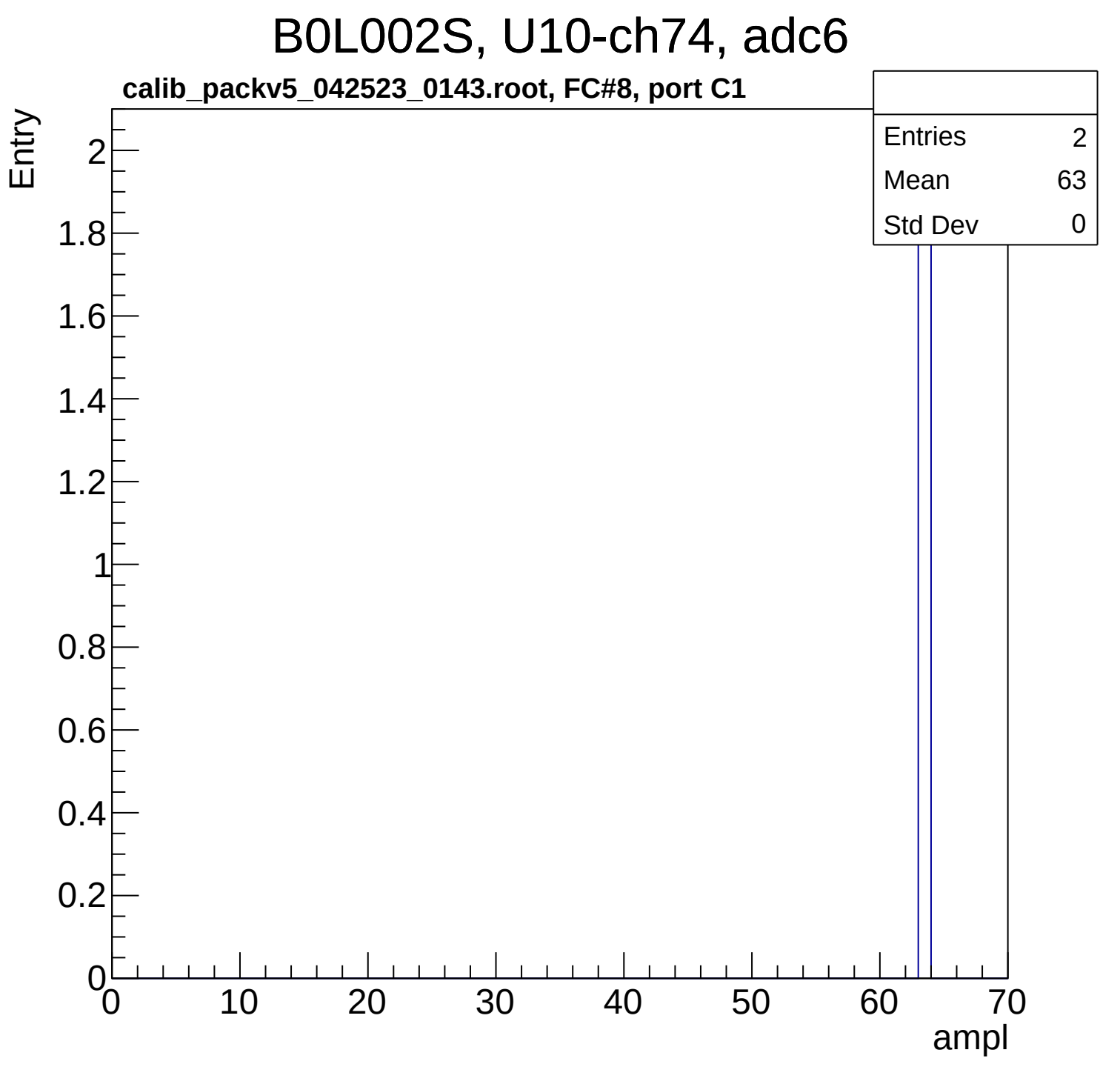

B0L002S, U10-ch74, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 39.45
Std Dev 2.866
Gaus mean : 39.8708
Gaus Width: 2.9151
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

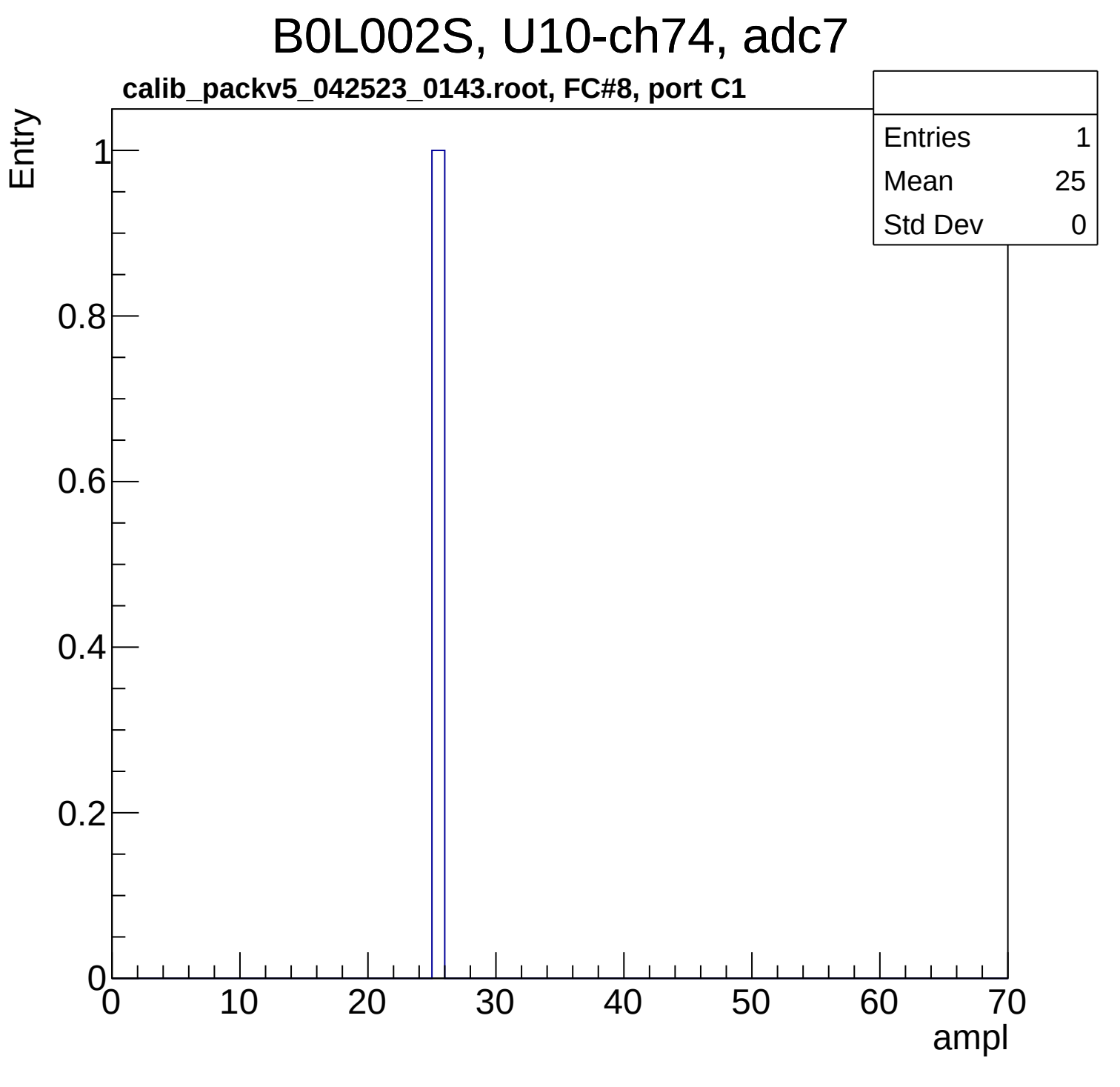

B0L002S, U10-ch74, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 44.77
Std Dev 3.075
Gaus mean : 45.1250
Gaus Width: 3.7575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

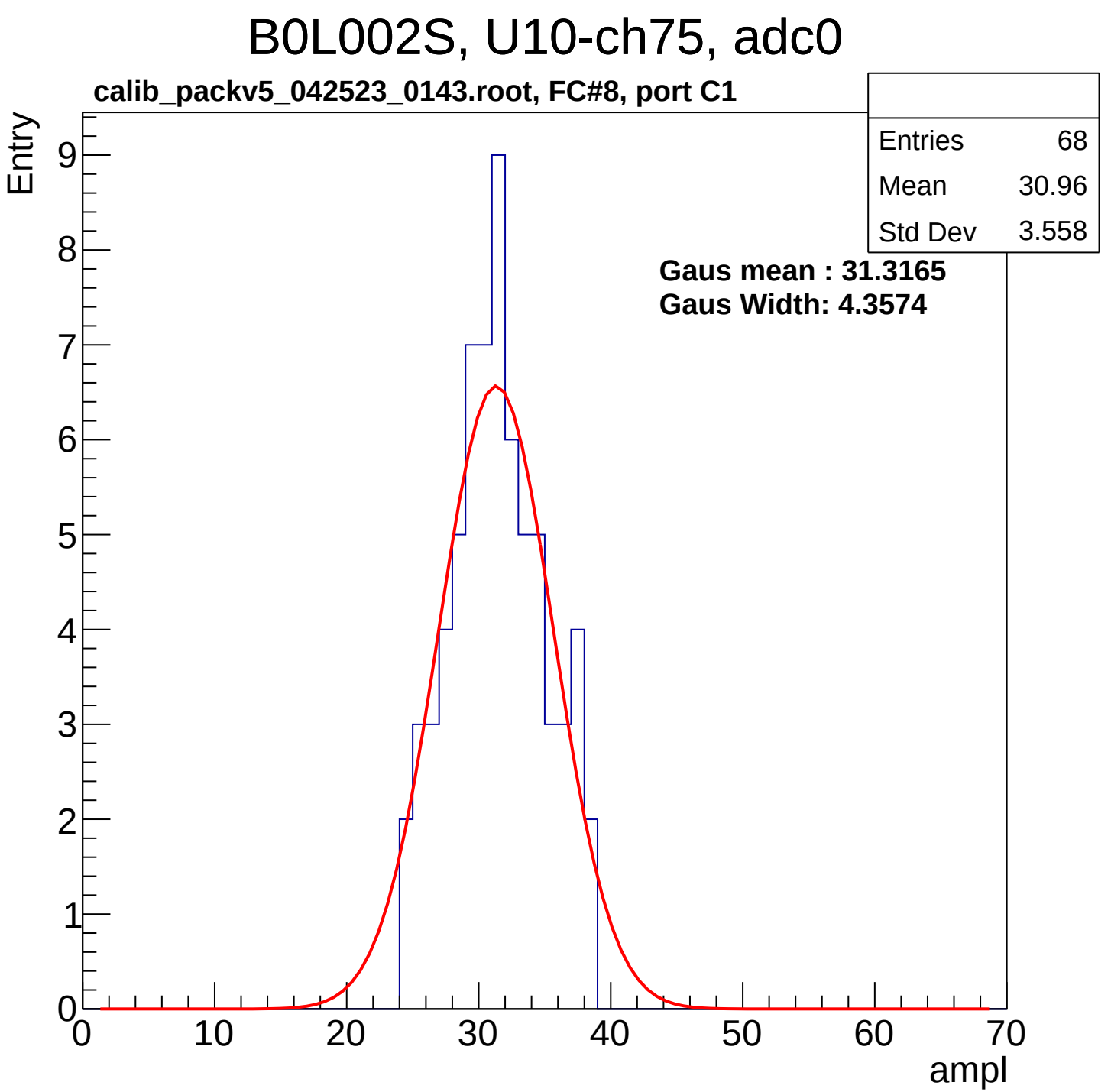

B0L002S, U10-ch74, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 51.25
Std Dev 3.569
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

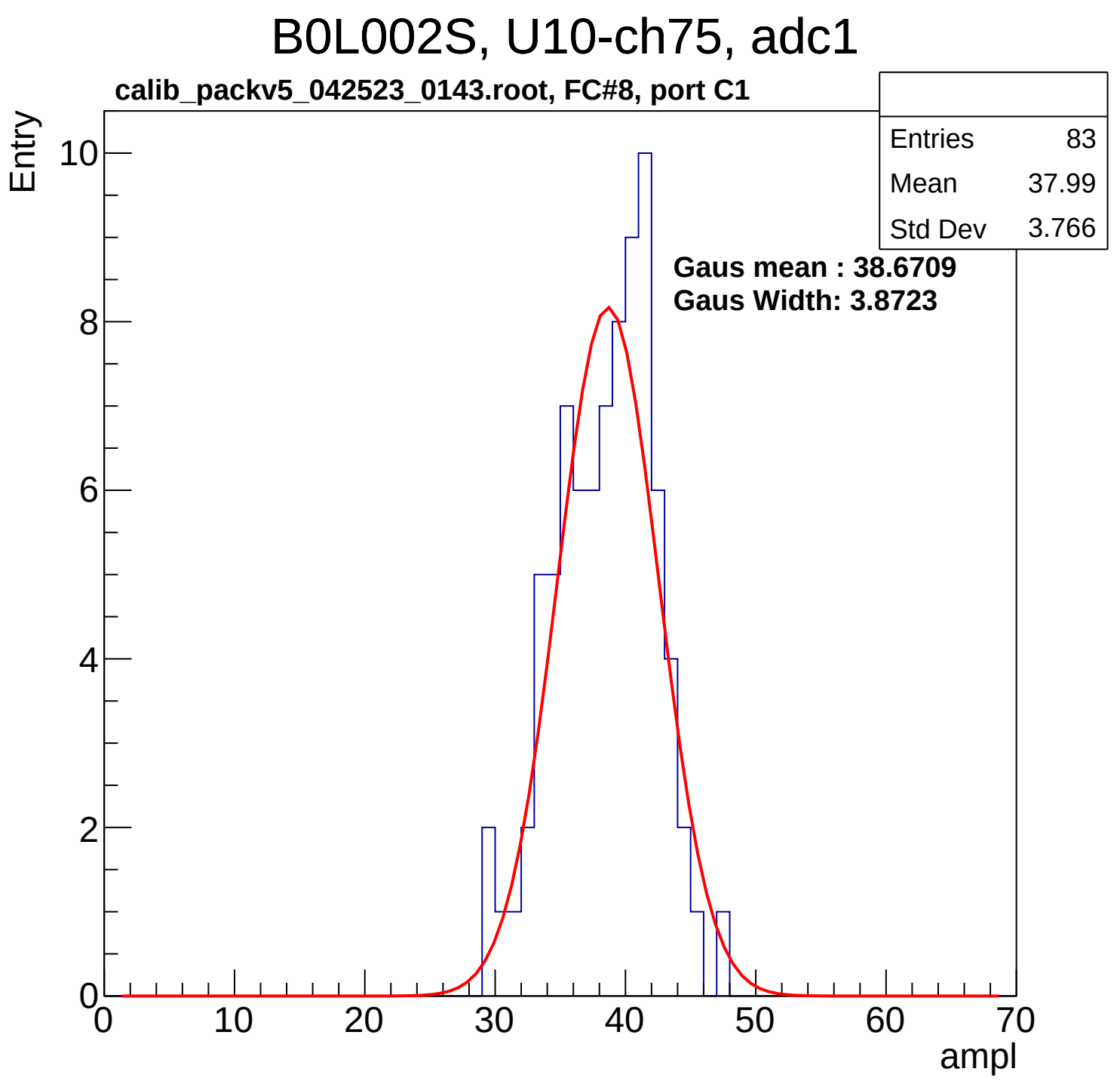

B0L002S, U10-ch74, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 57.79
Std Dev 2.868
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

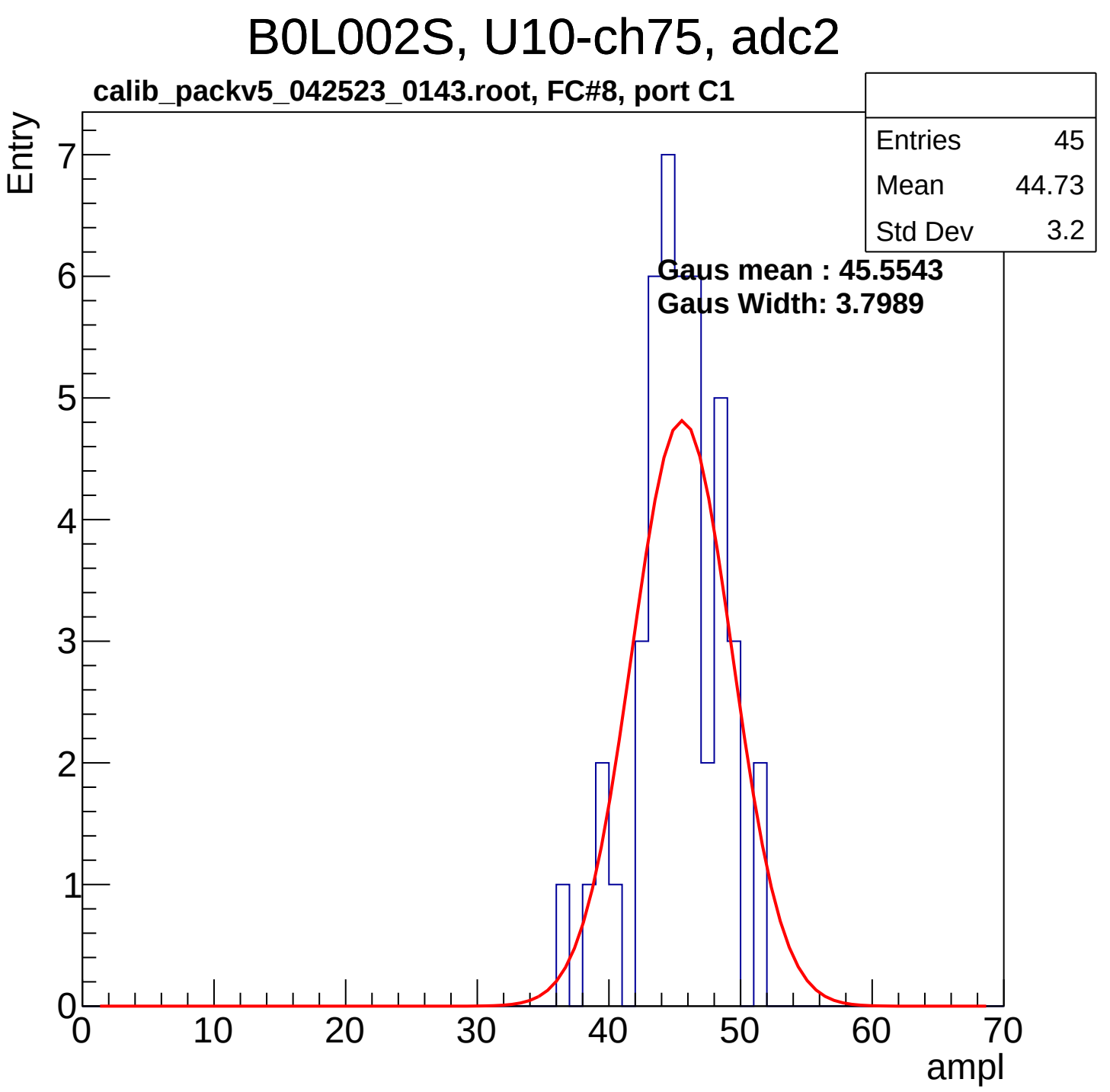

B0L002S, U10-ch74, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 57.7
Std Dev 12.57
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

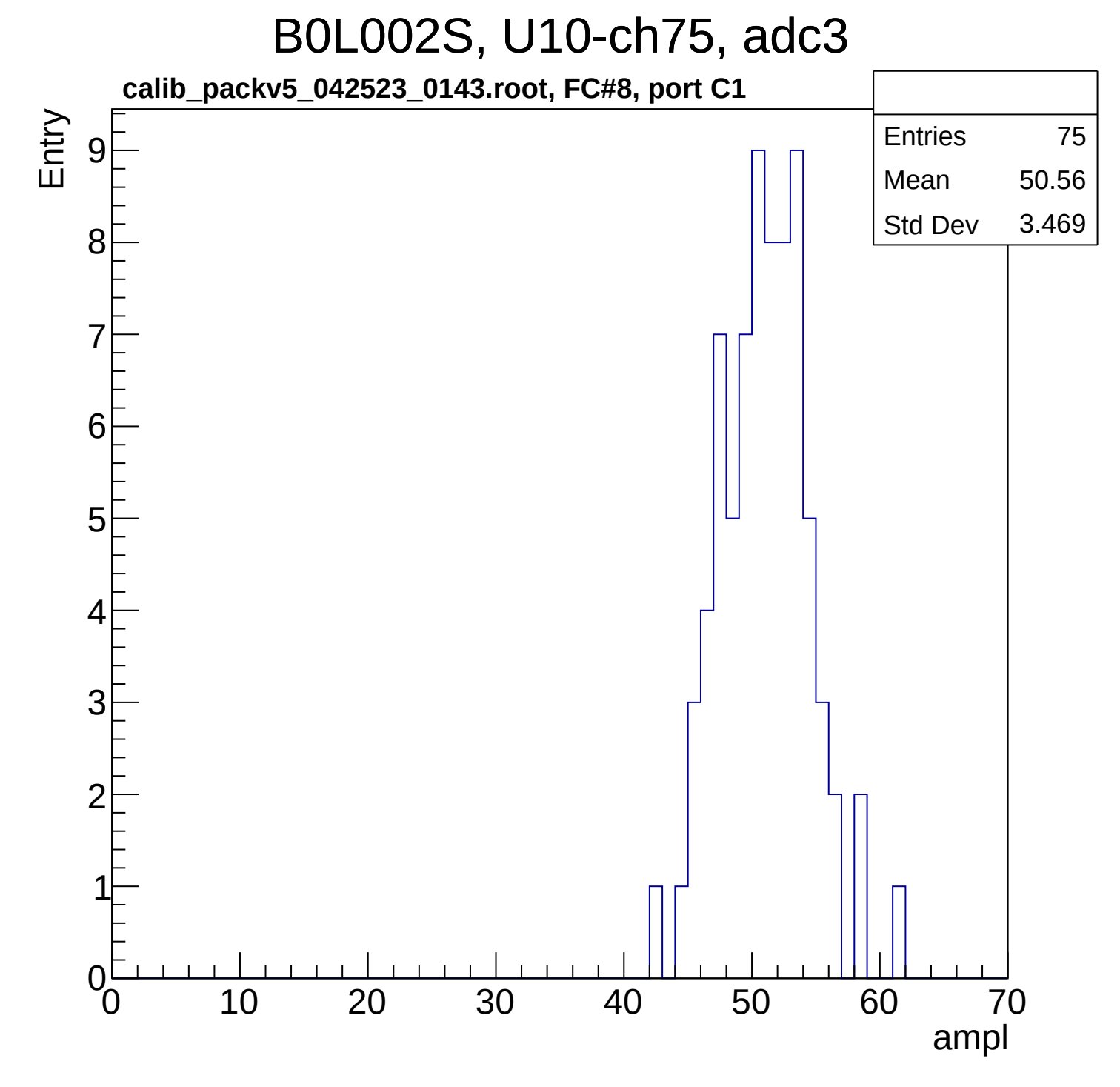

B0L002S, U10-ch74, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

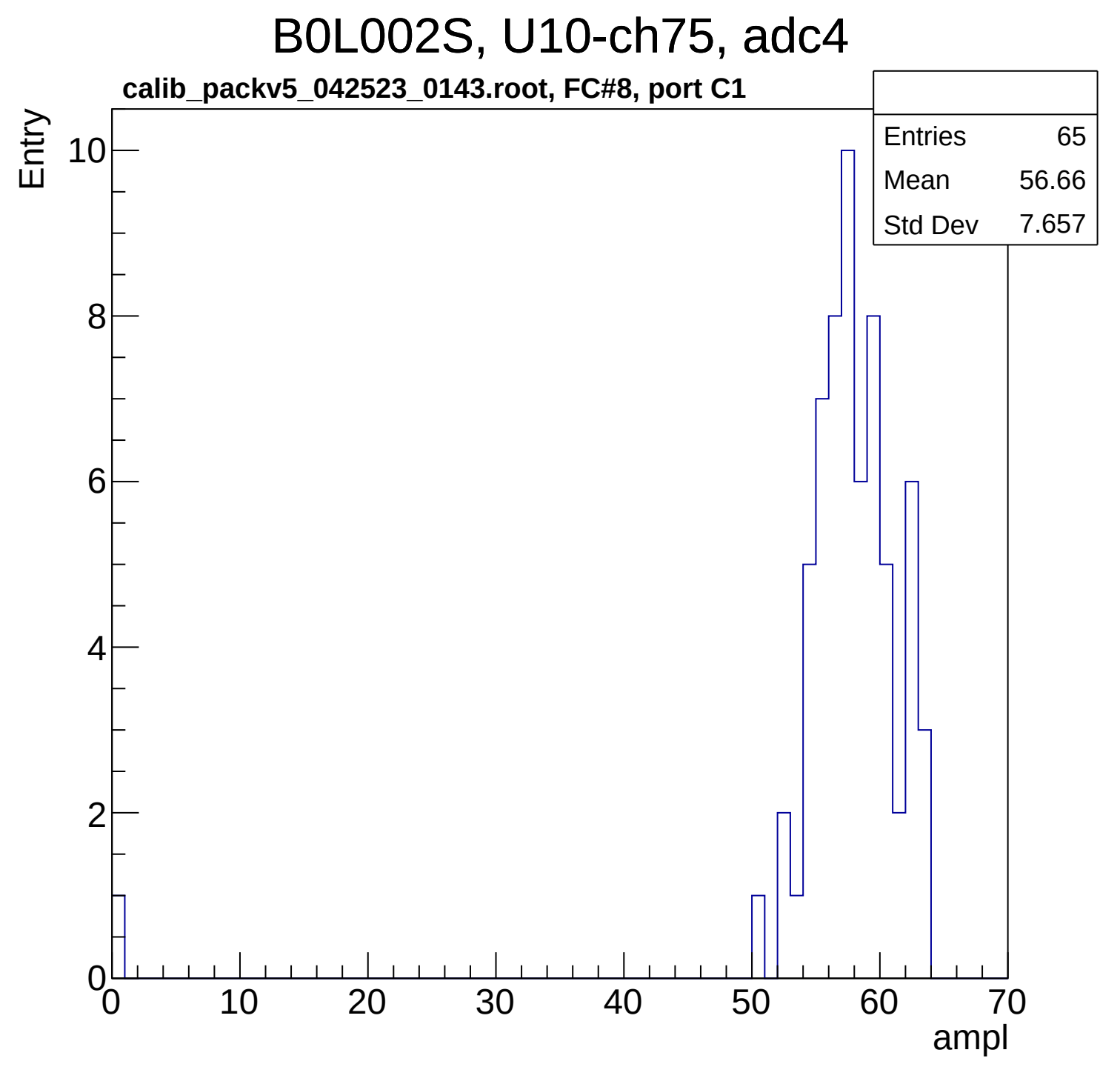

B0L002S, U10-ch74, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

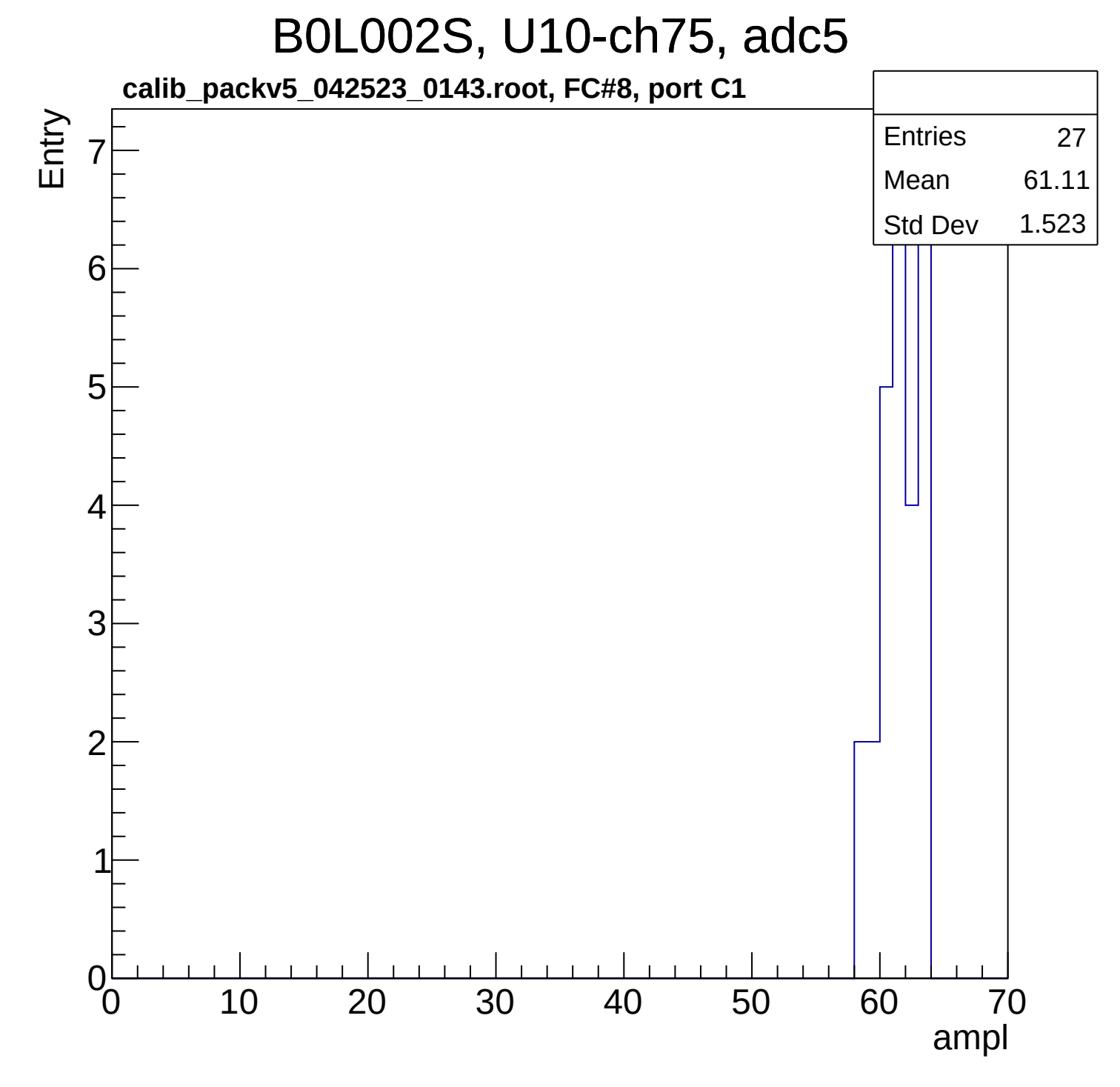

B0L002S, U10-ch75, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.96
Std Dev 3.558
Gaus mean : 31.3165
Gaus Width: 4.3574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

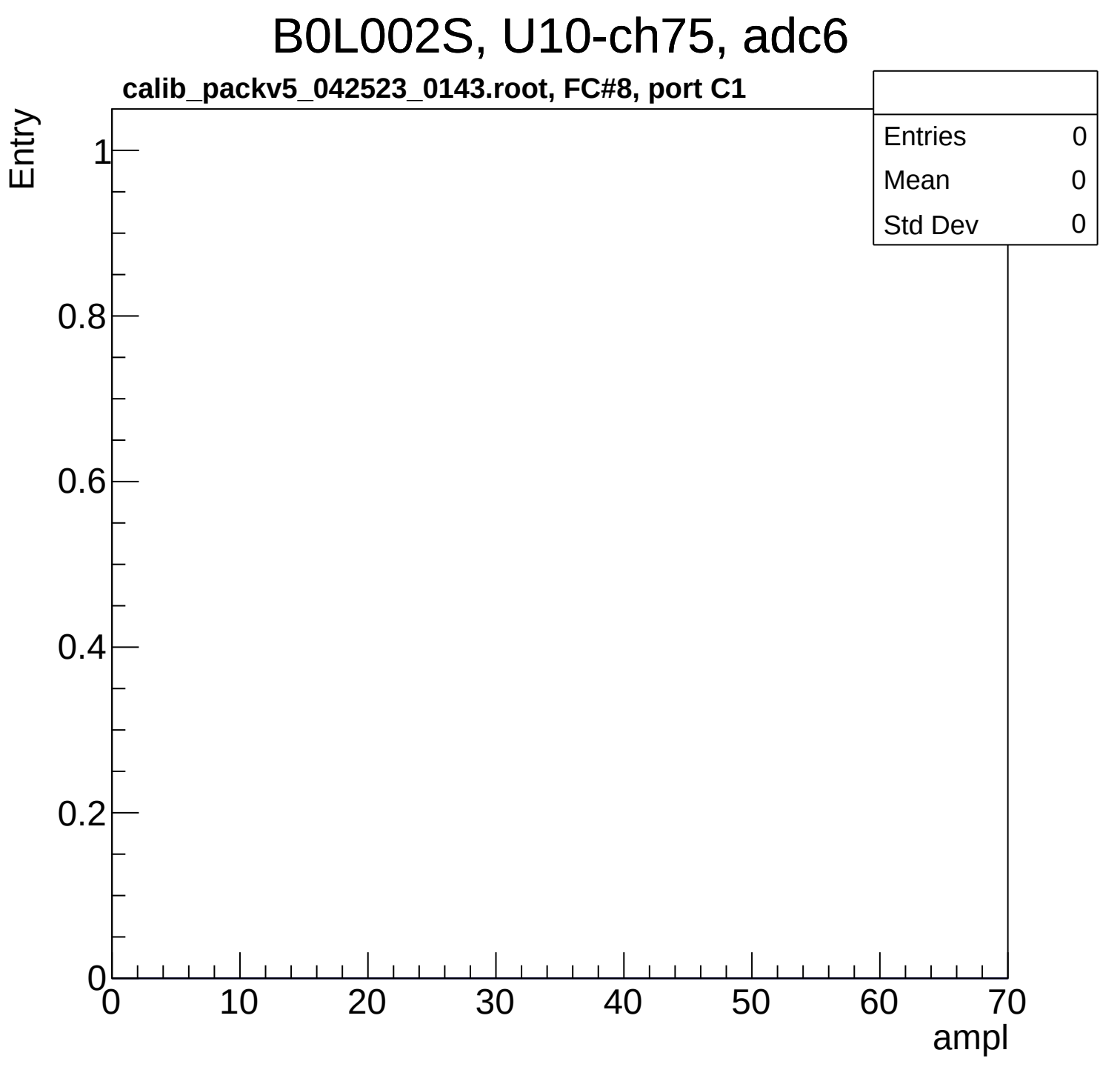

B0L002S, U10-ch75, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 37.99
Std Dev 3.766
Gaus mean : 38.6709
Gaus Width: 3.8723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

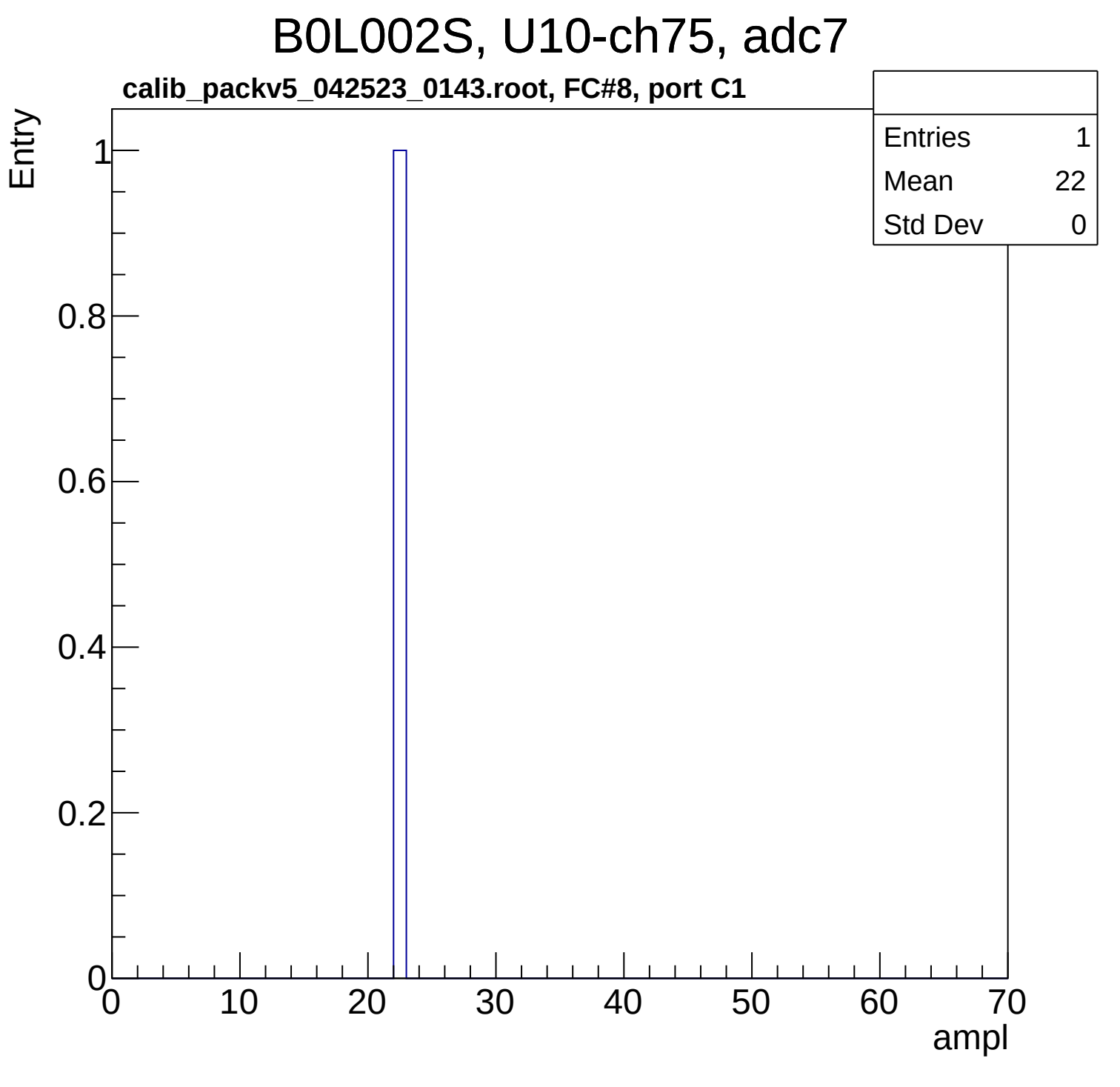

B0L002S, U10-ch75, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 44.73
Std Dev 3.2
Gaus mean : 45.5543
Gaus Width: 3.7989
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

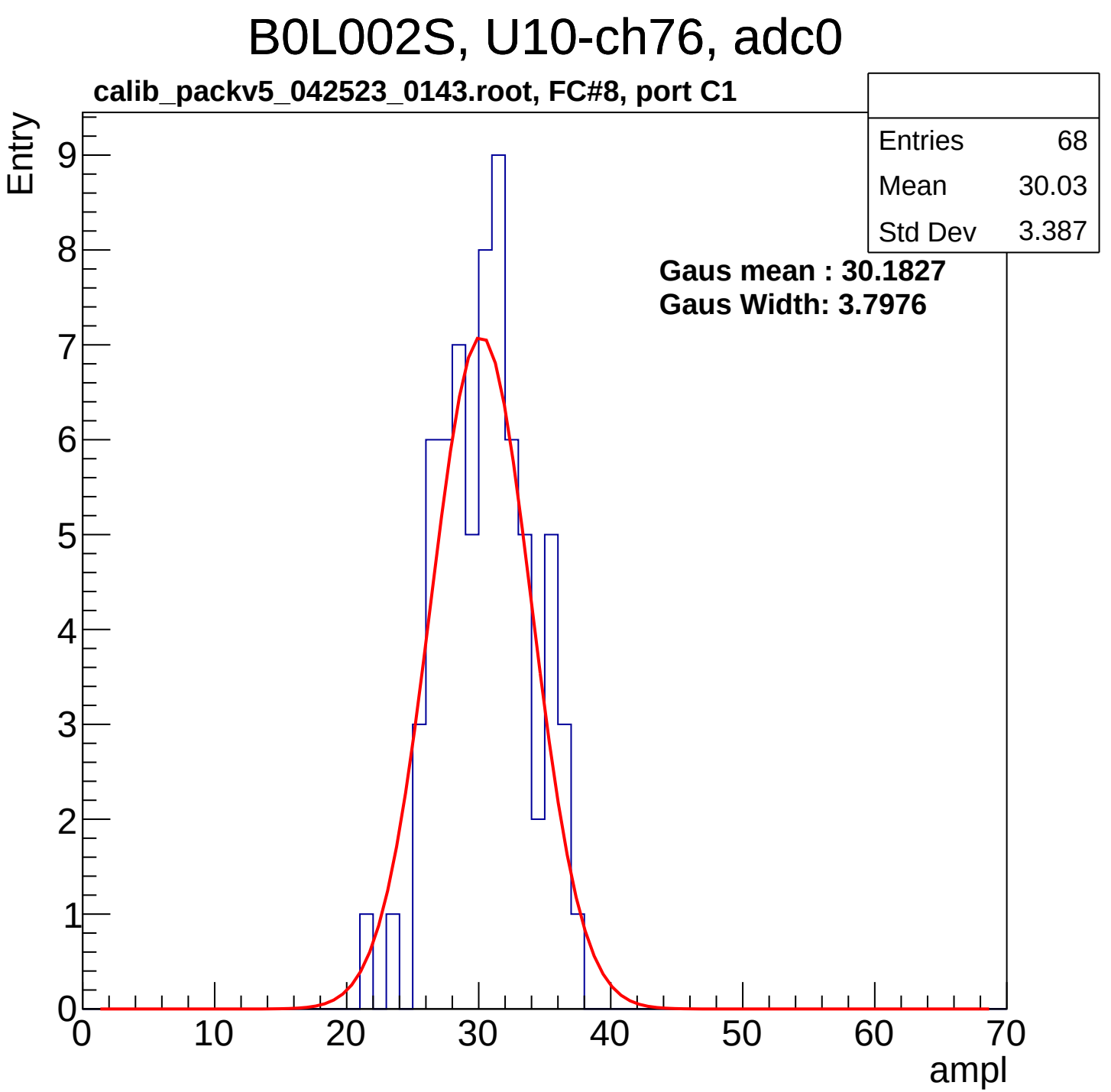

B0L002S, U10-ch75, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.56
Std Dev 3.469
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

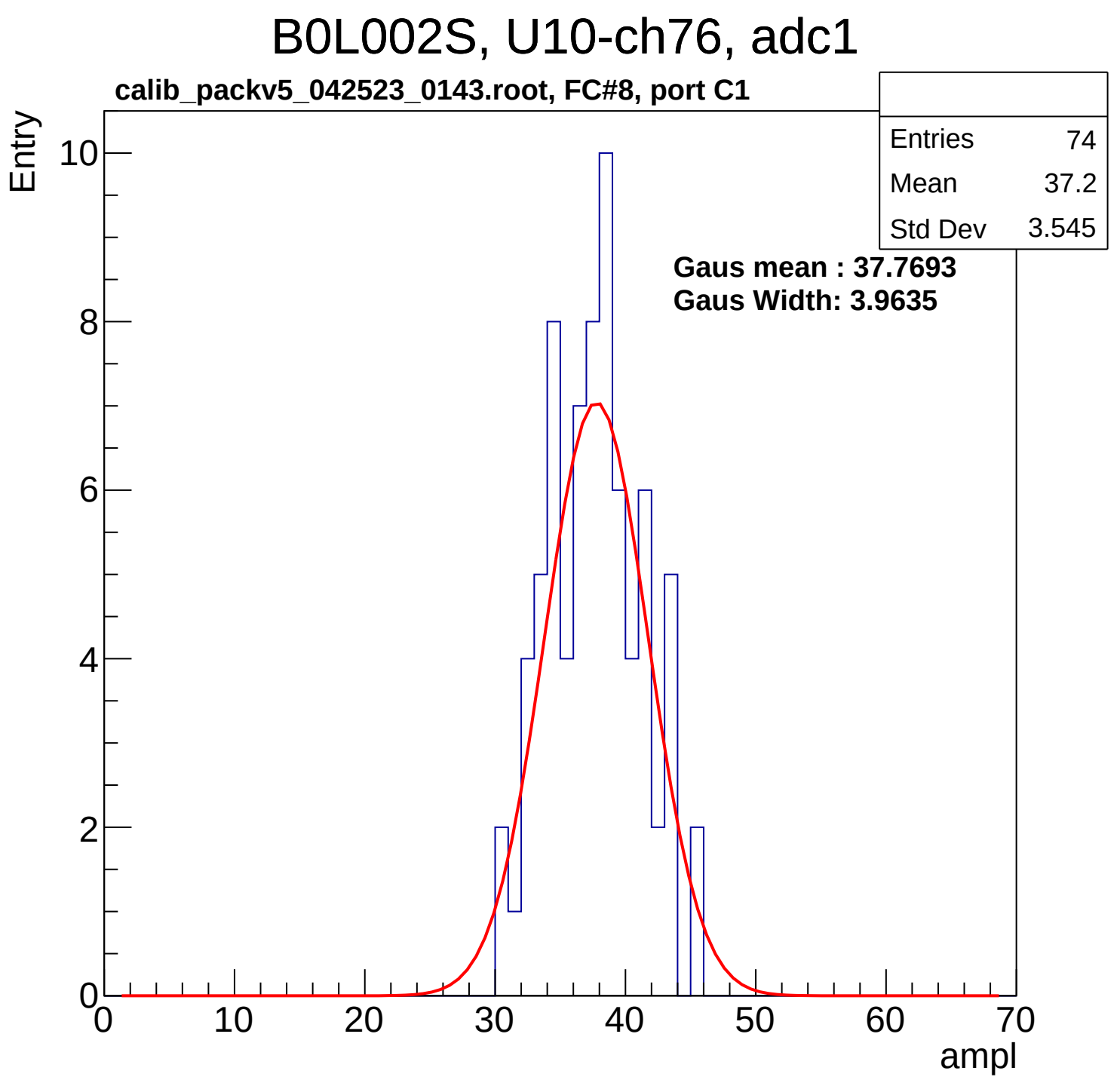

B0L002S, U10-ch75, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.66
Std Dev 7.657
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

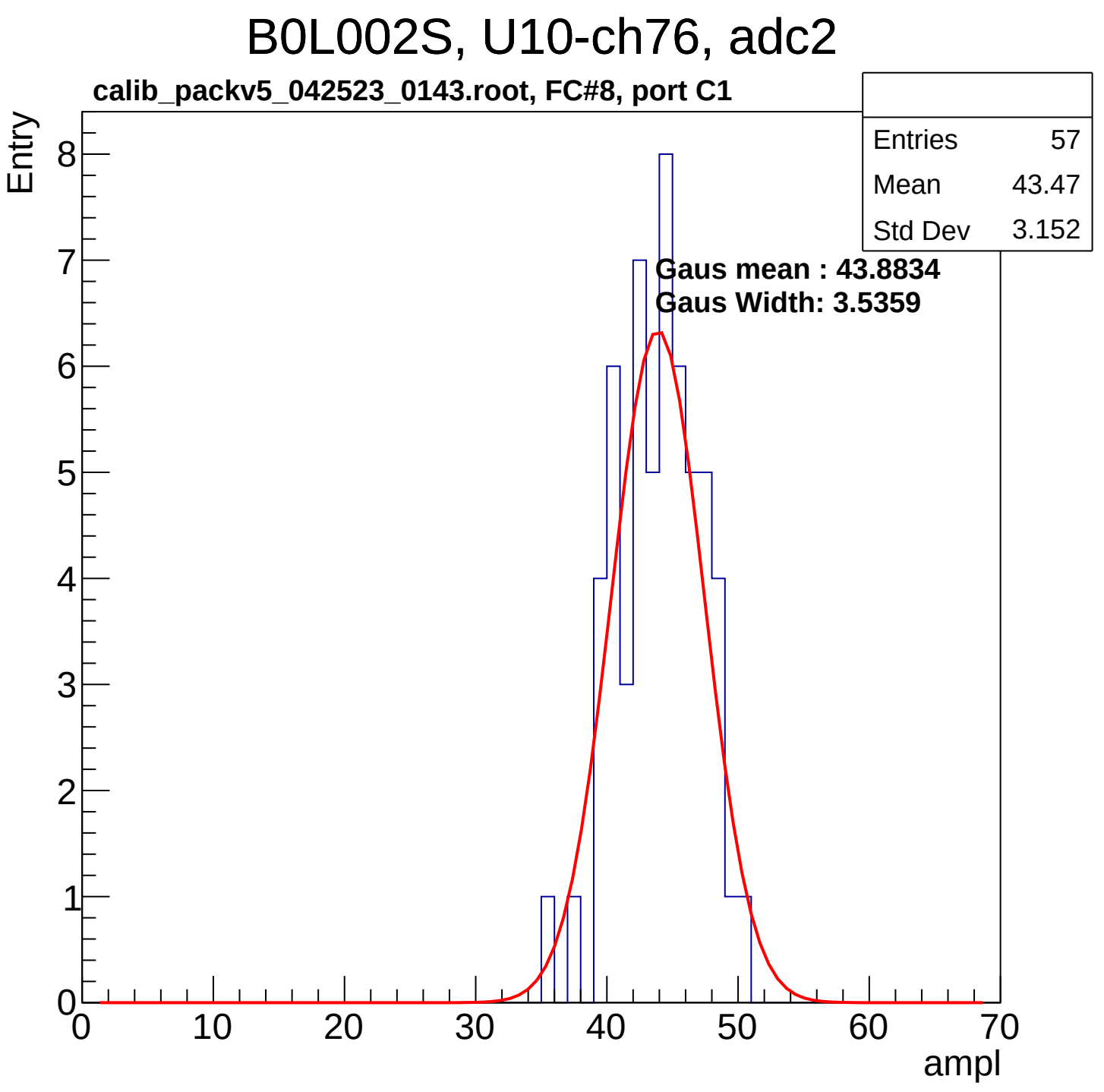

B0L002S, U10-ch75, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 27
Mean 61.11
Std Dev 1.523
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

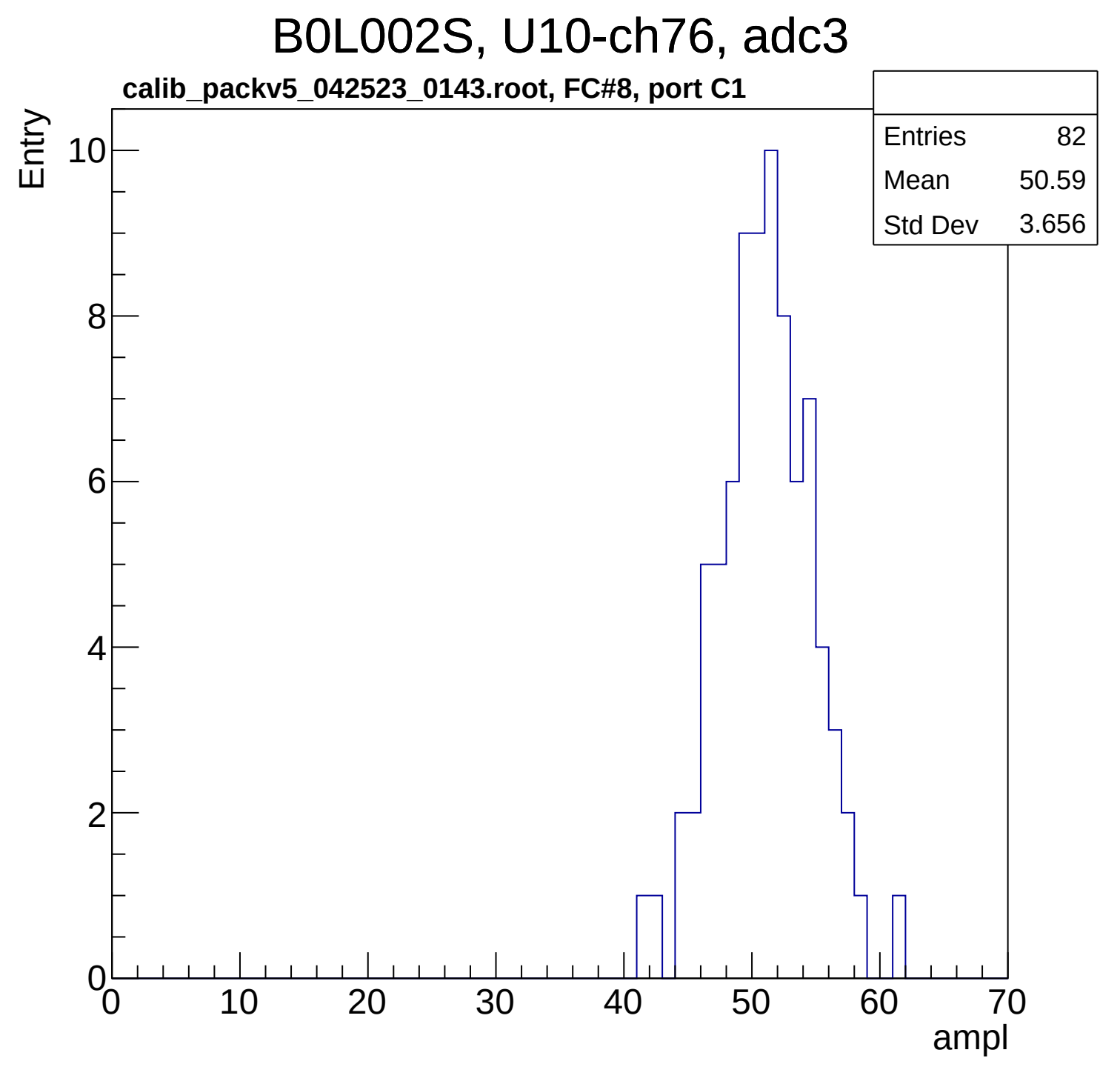

B0L002S, U10-ch75, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

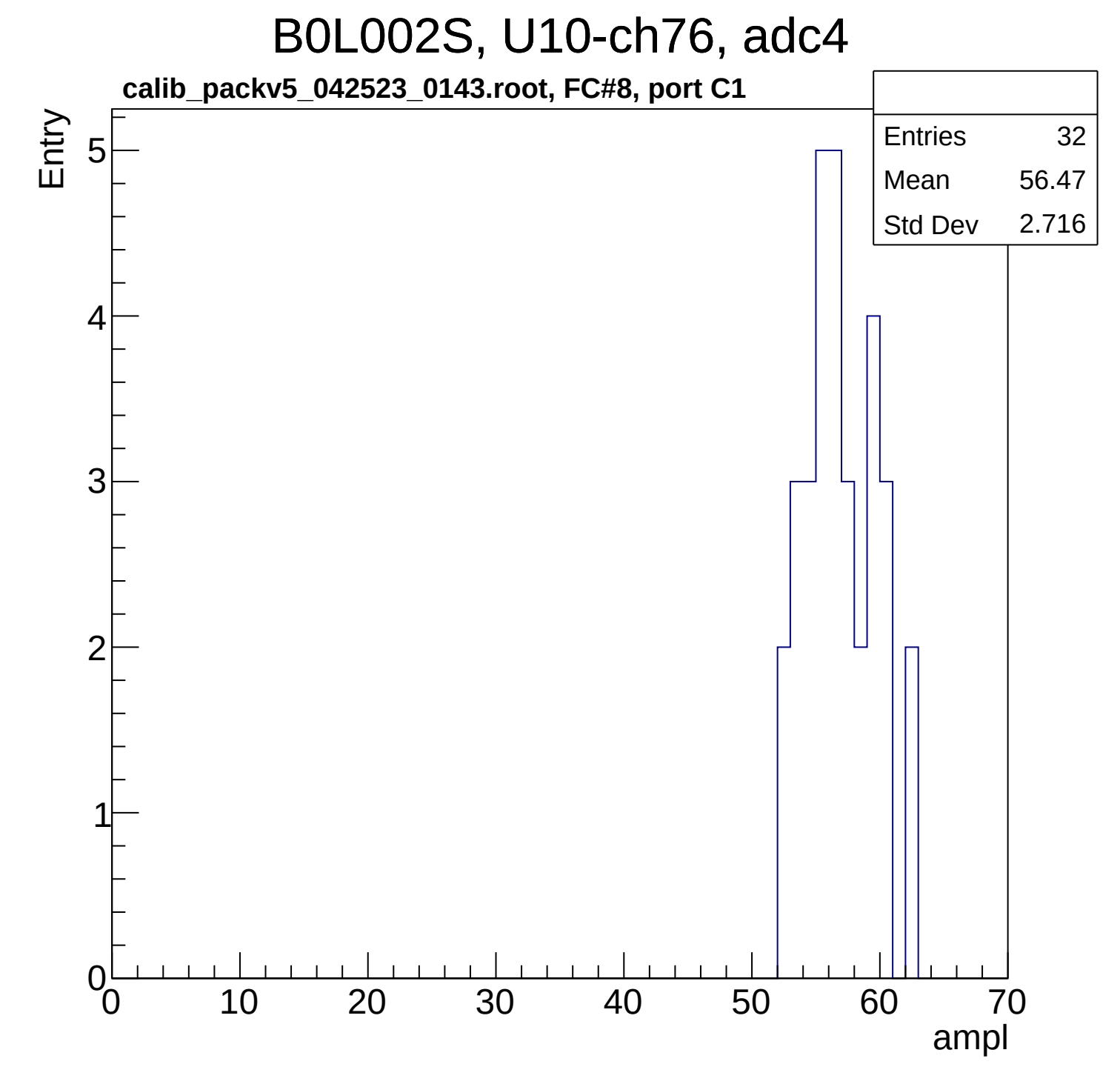

B0L002S, U10-ch75, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

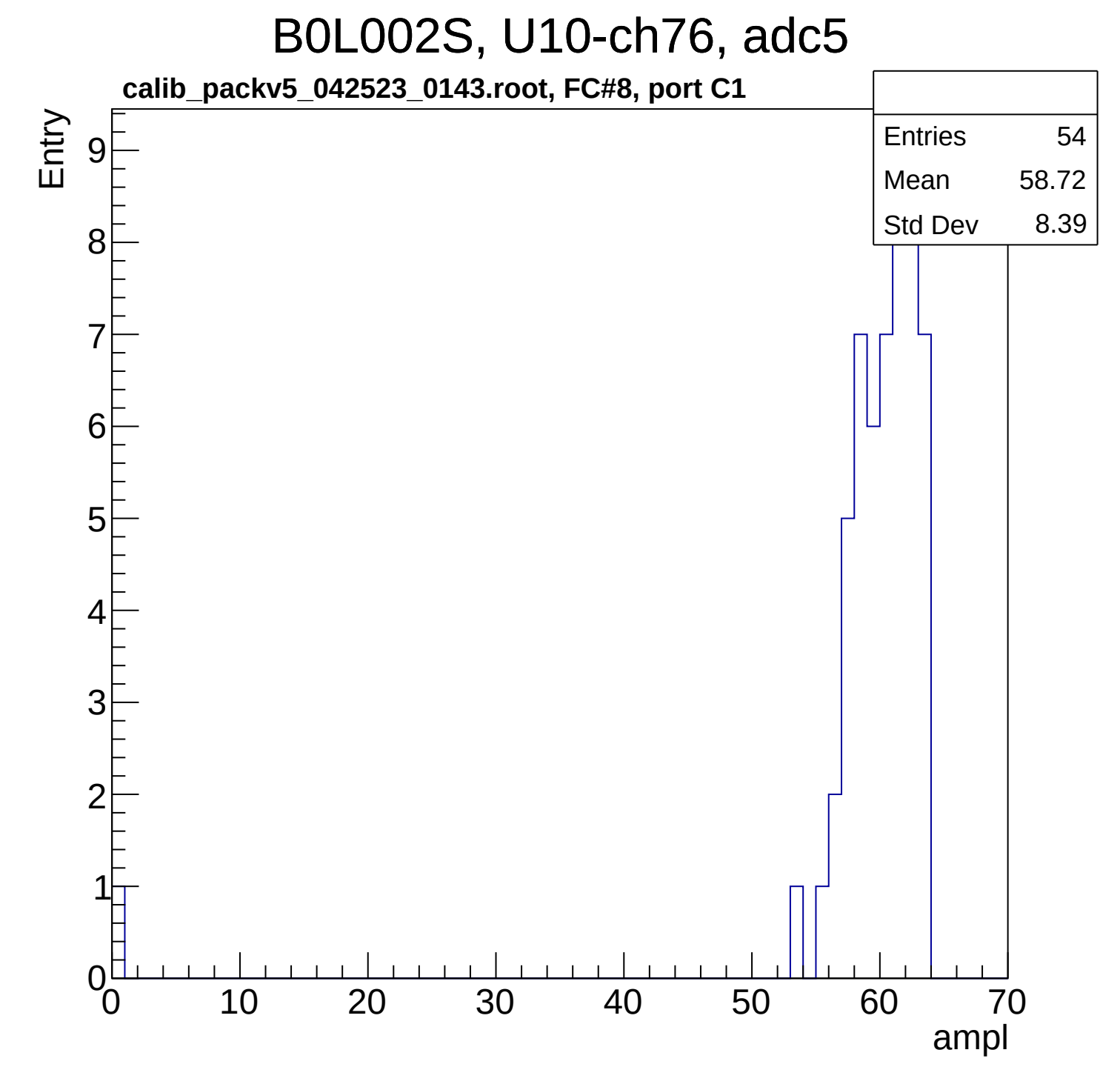

B0L002S, U10-ch76, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 30.03
Std Dev 3.387
Gaus mean : 30.1827
Gaus Width: 3.7976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

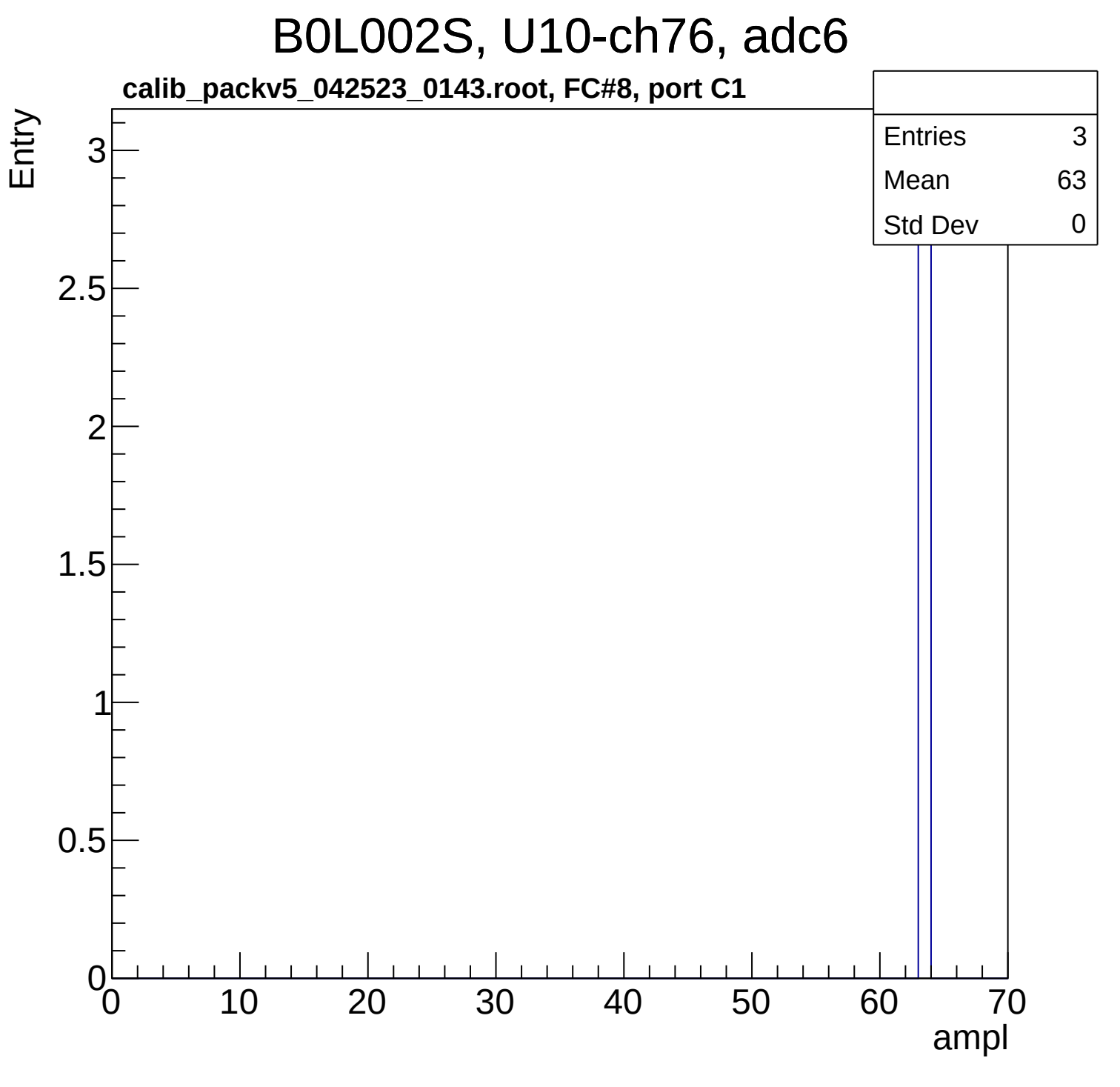

B0L002S, U10-ch76, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.2
Std Dev 3.545
Gaus mean : 37.7693
Gaus Width: 3.9635
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

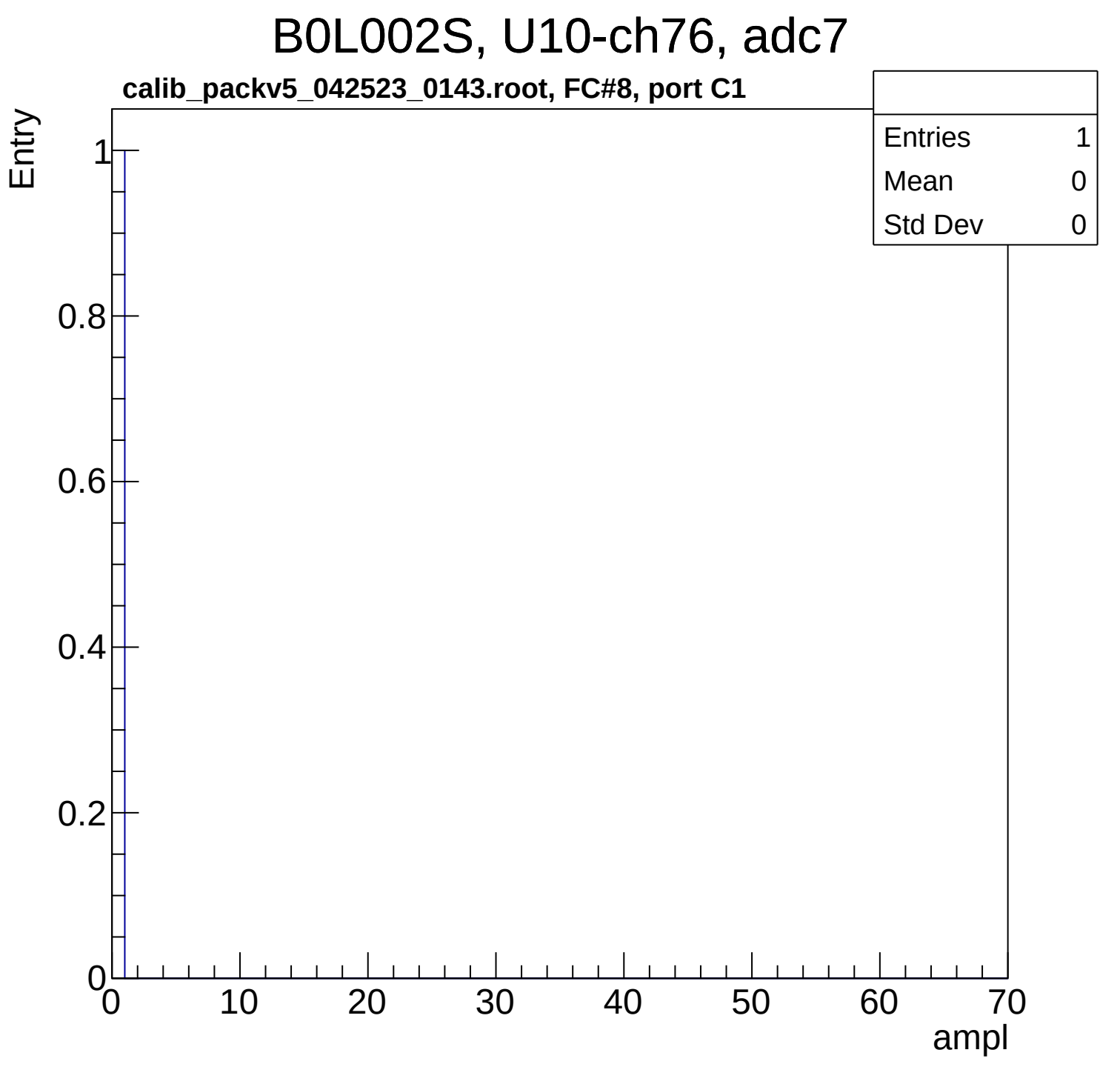

B0L002S, U10-ch76, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 43.47
Std Dev 3.152
Gaus mean : 43.8834
Gaus Width: 3.5359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

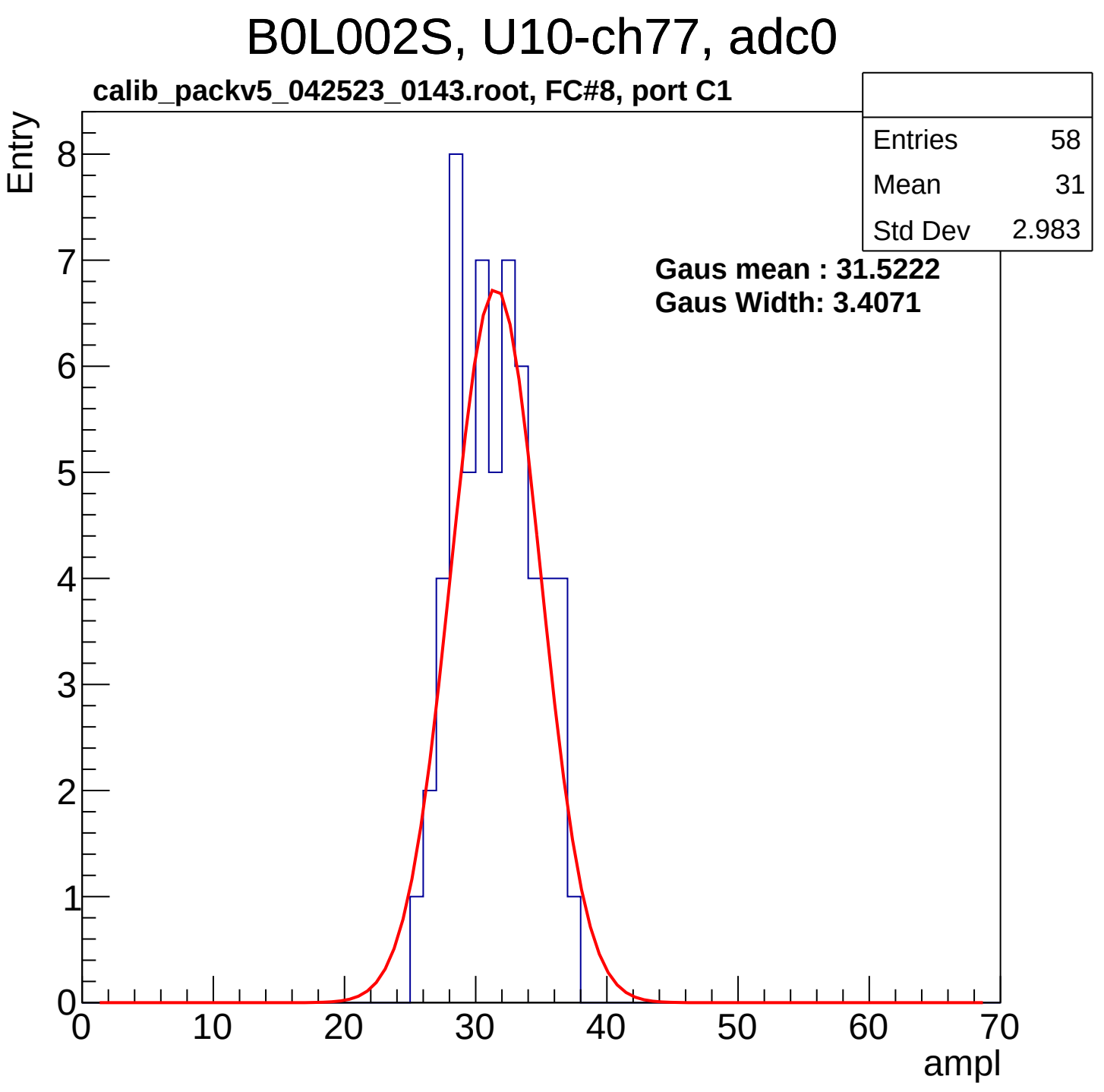

B0L002S, U10-ch76, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 50.59
Std Dev 3.656
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

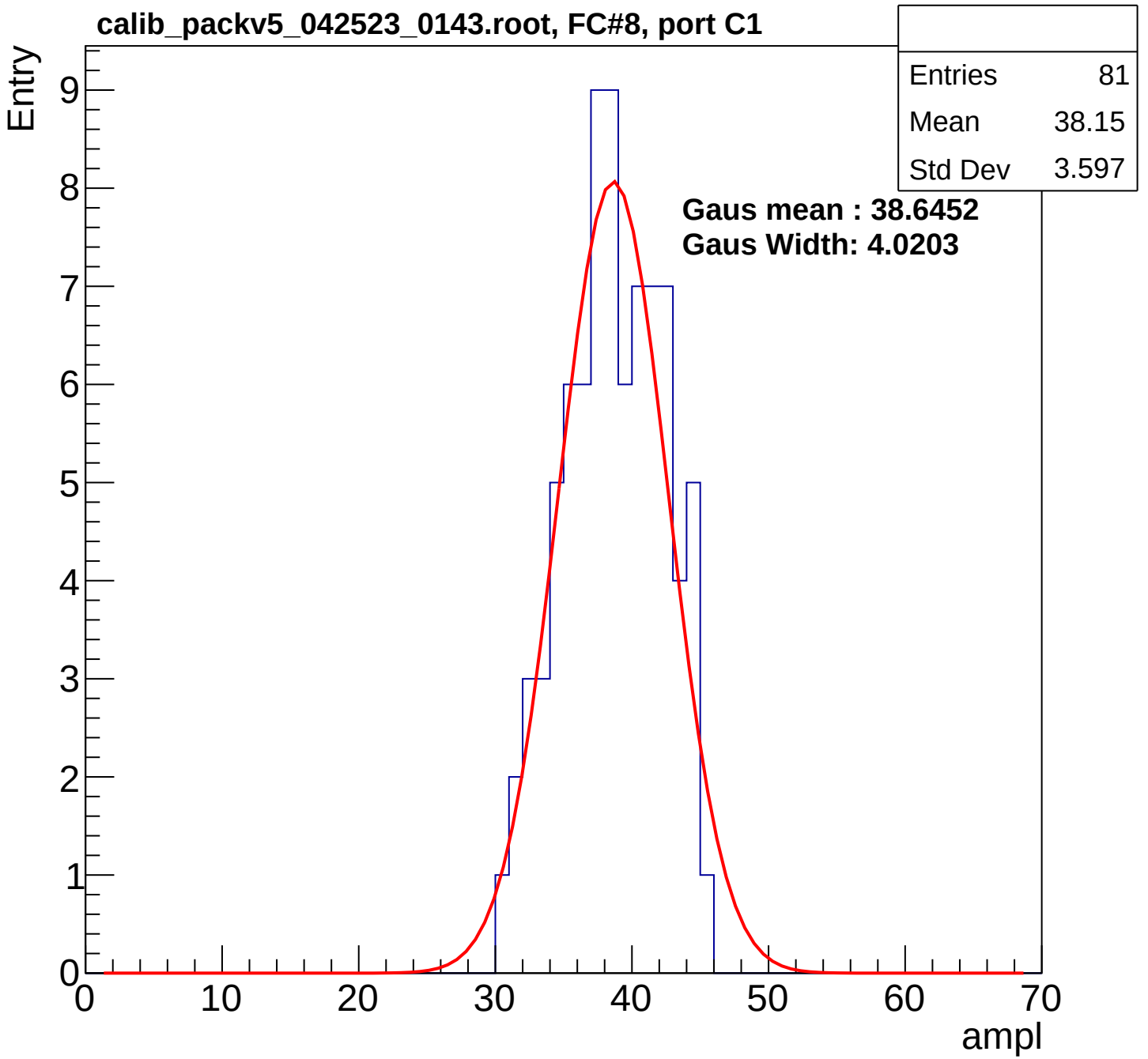

B0L002S, U10-ch76, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 56.47
Std Dev 2.716
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

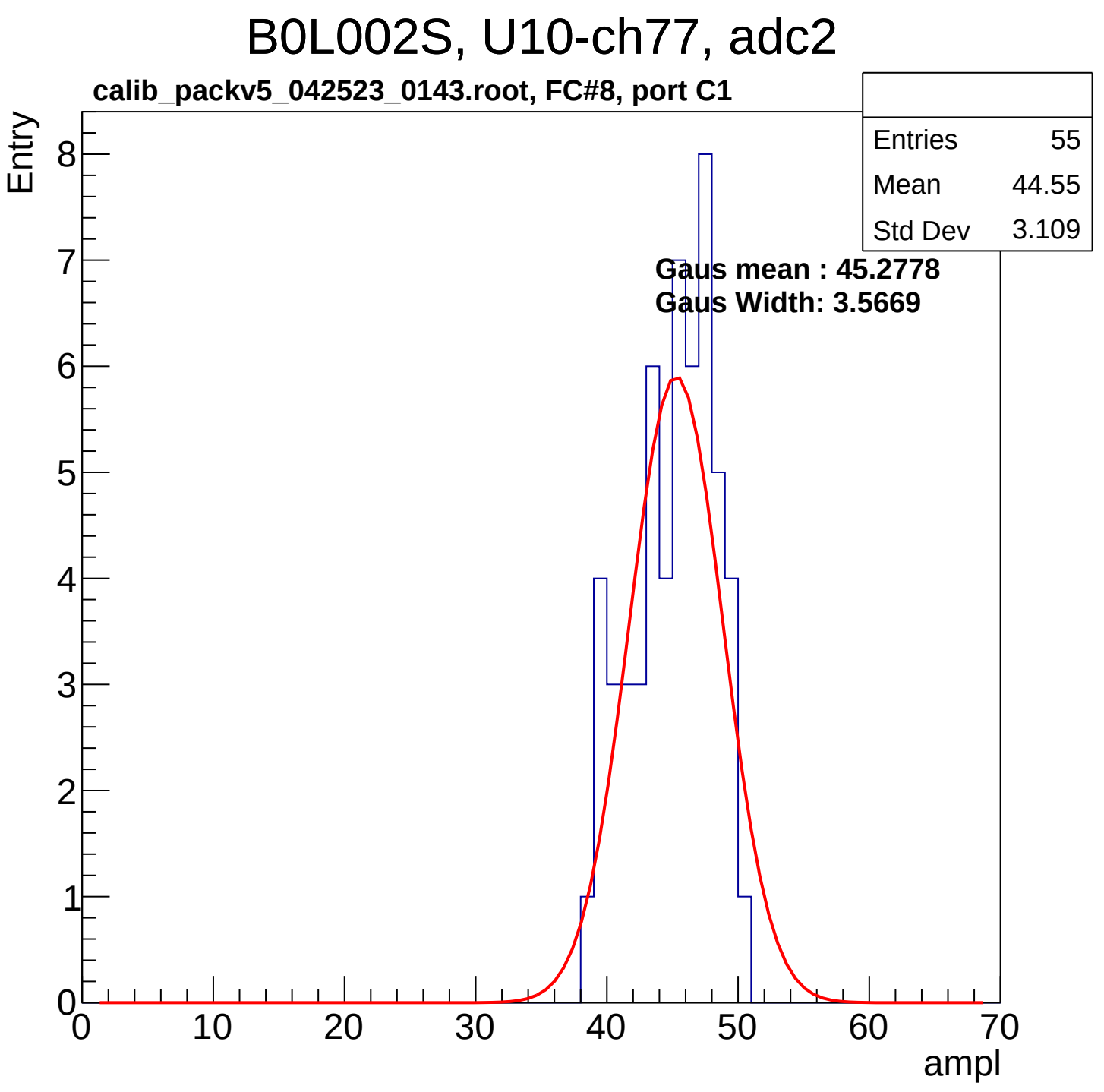

B0L002S, U10-ch76, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 58.72
Std Dev 8.39
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

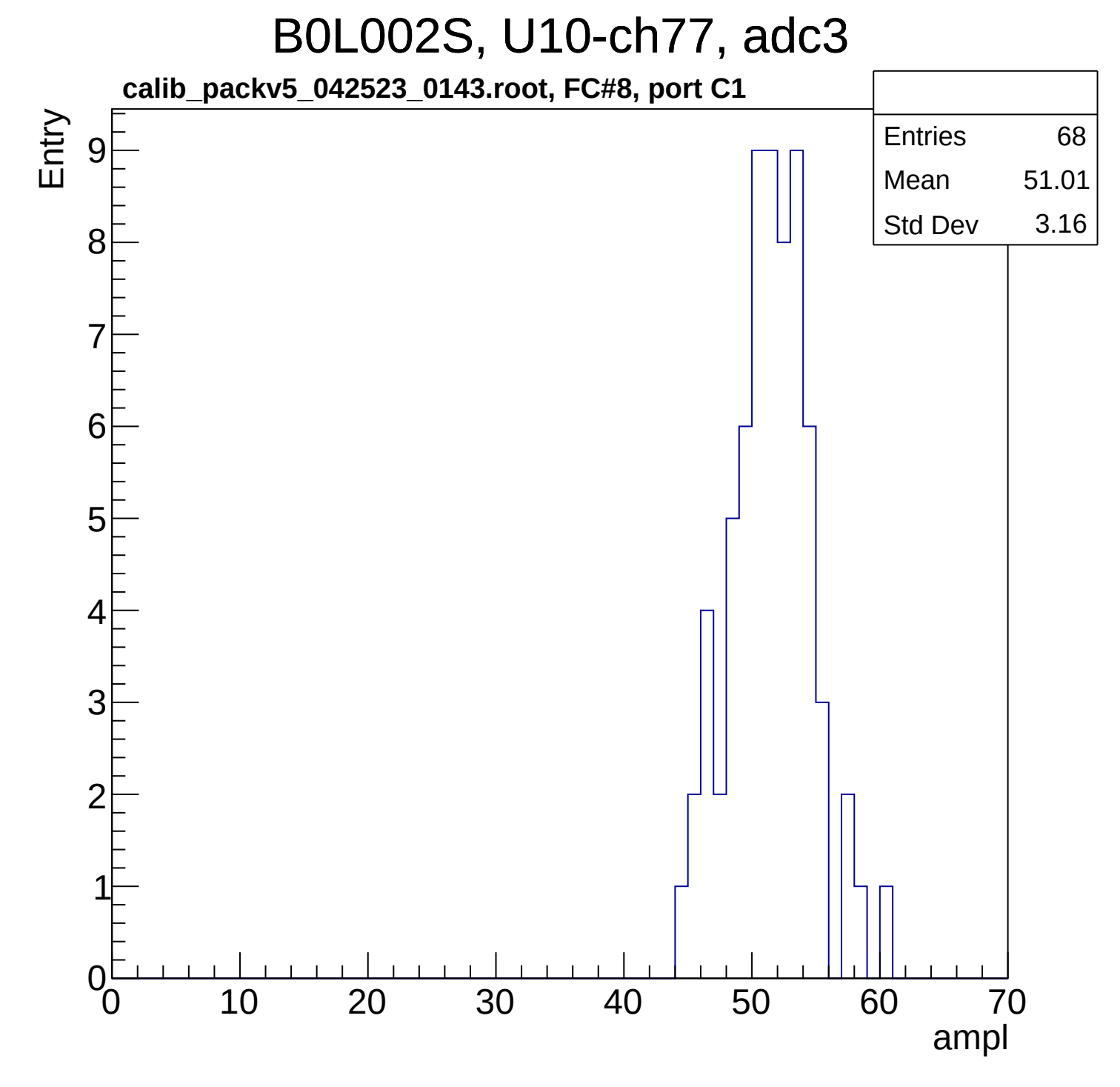

B0L002S, U10-ch76, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

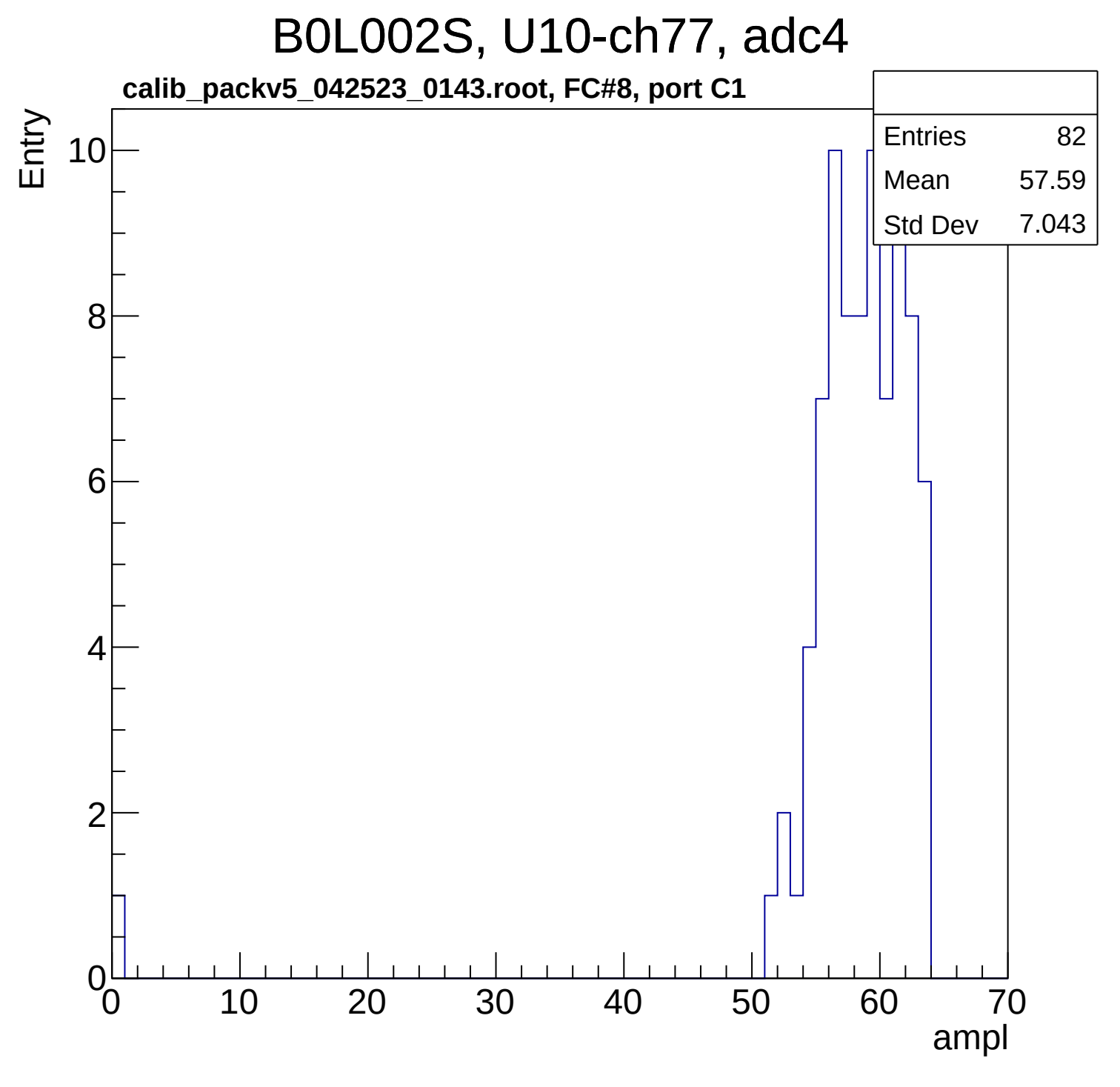

B0L002S, U10-ch76, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

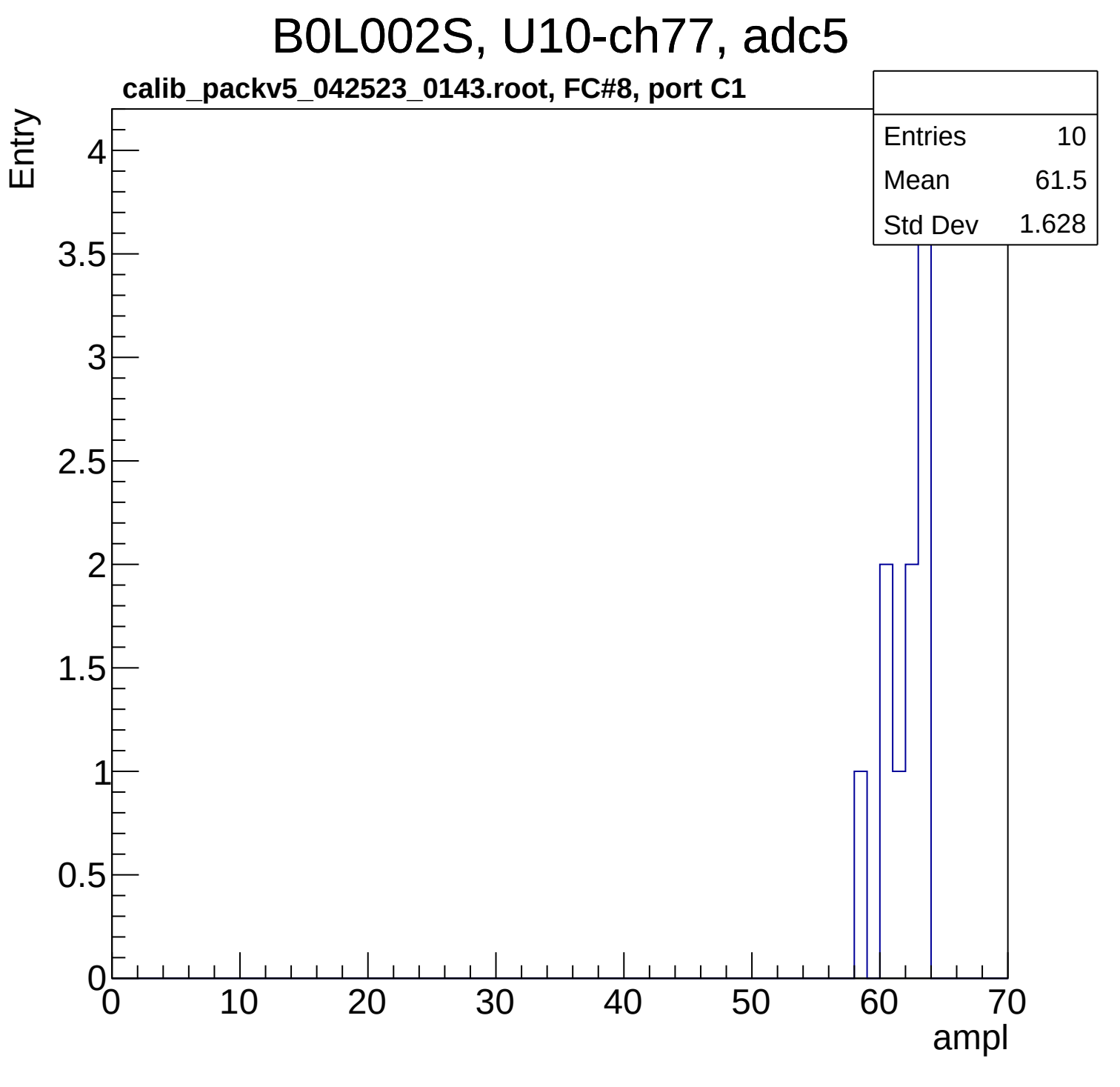

B0L002S, U10-ch77, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31
Std Dev 2.983
Gaus mean : 31.5222
Gaus Width: 3.4071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

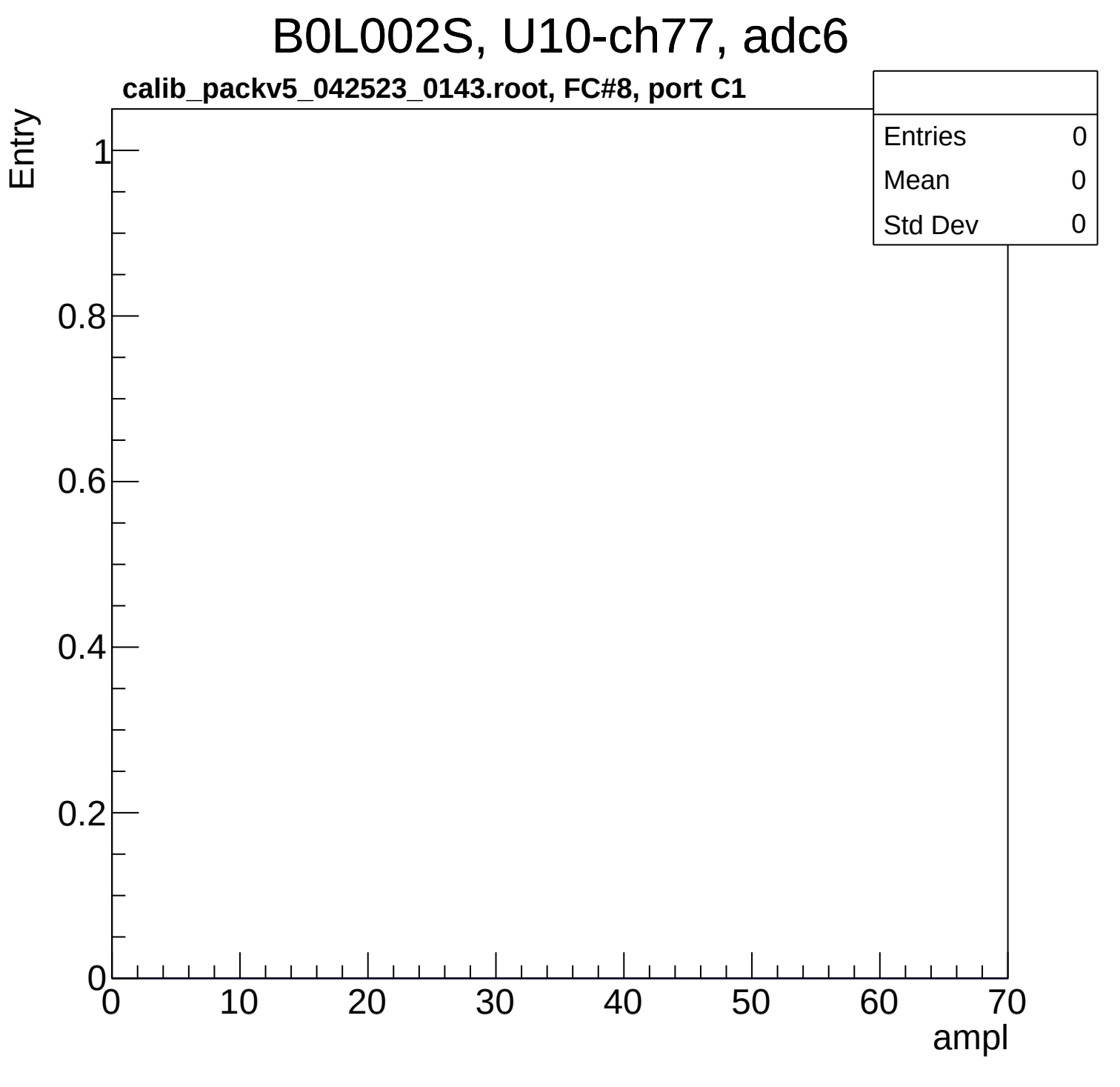

B0L002S, U10-ch77, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 38.15
Std Dev 3.597
Gaus mean : 38.6452
Gaus Width: 4.0203
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

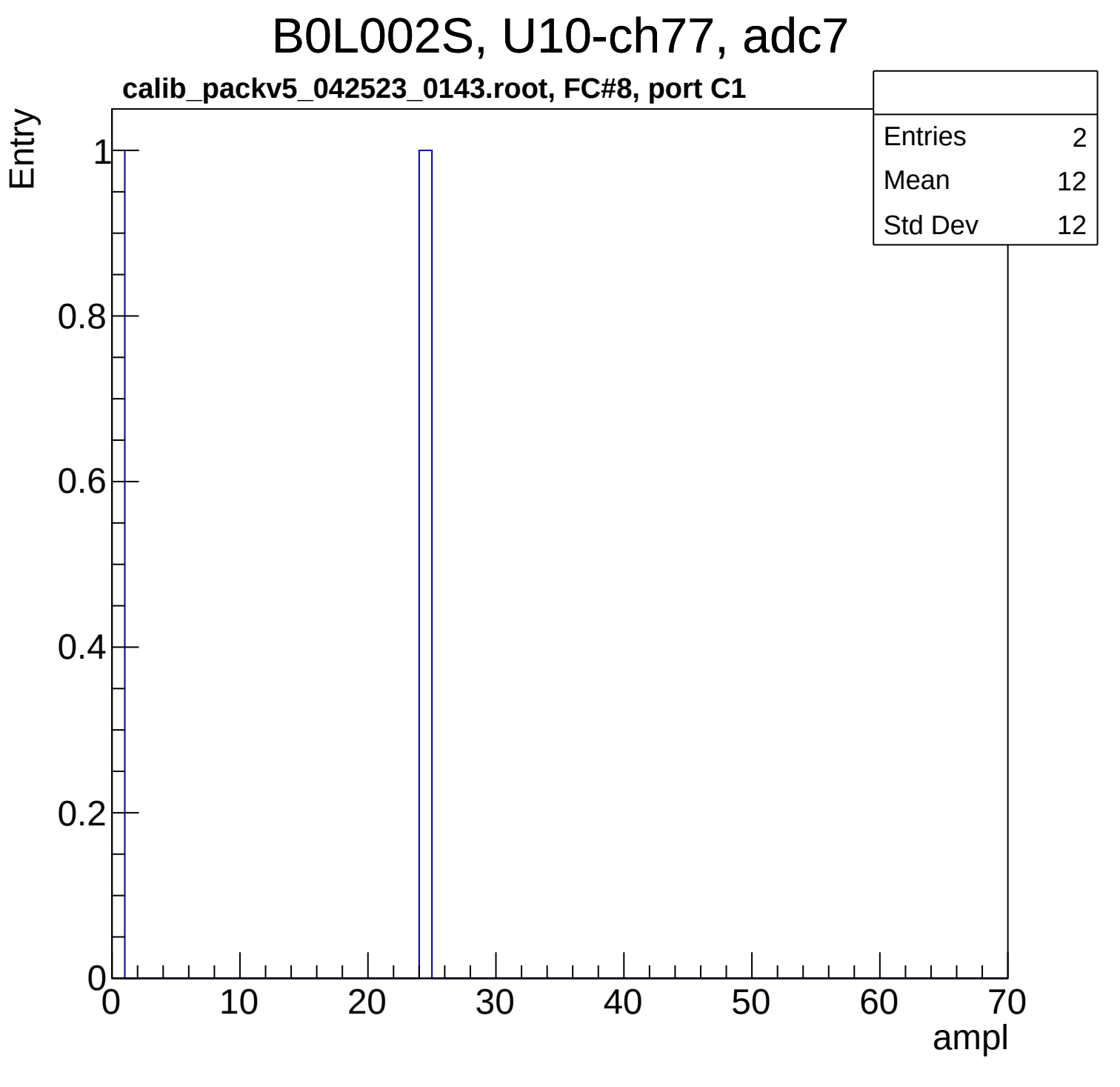

B0L002S, U10-ch77, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 44.55
Std Dev 3.109
Gaus mean : 45.2778
Gaus Width: 3.5669
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

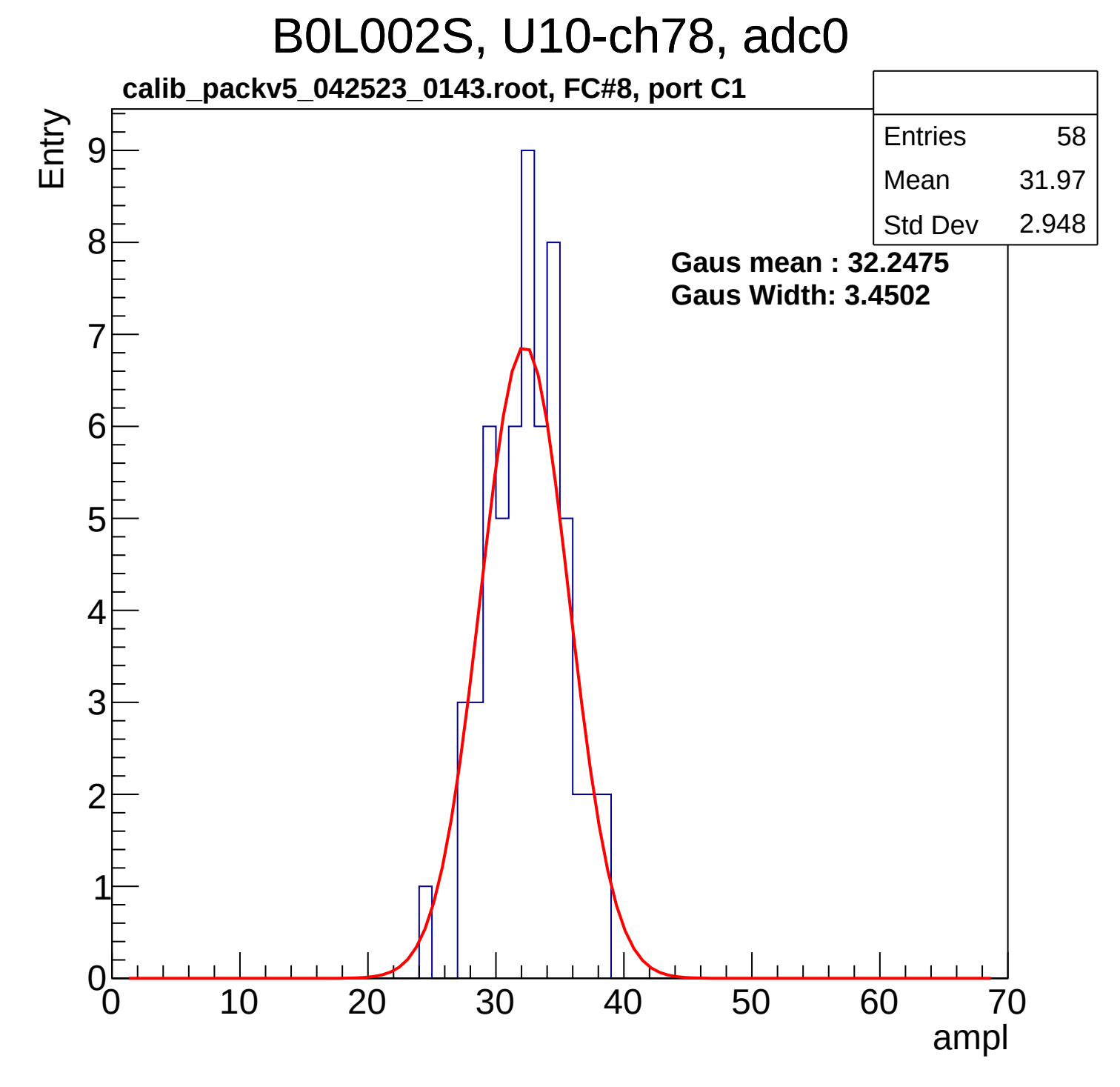

B0L002S, U10-ch77, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.01
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

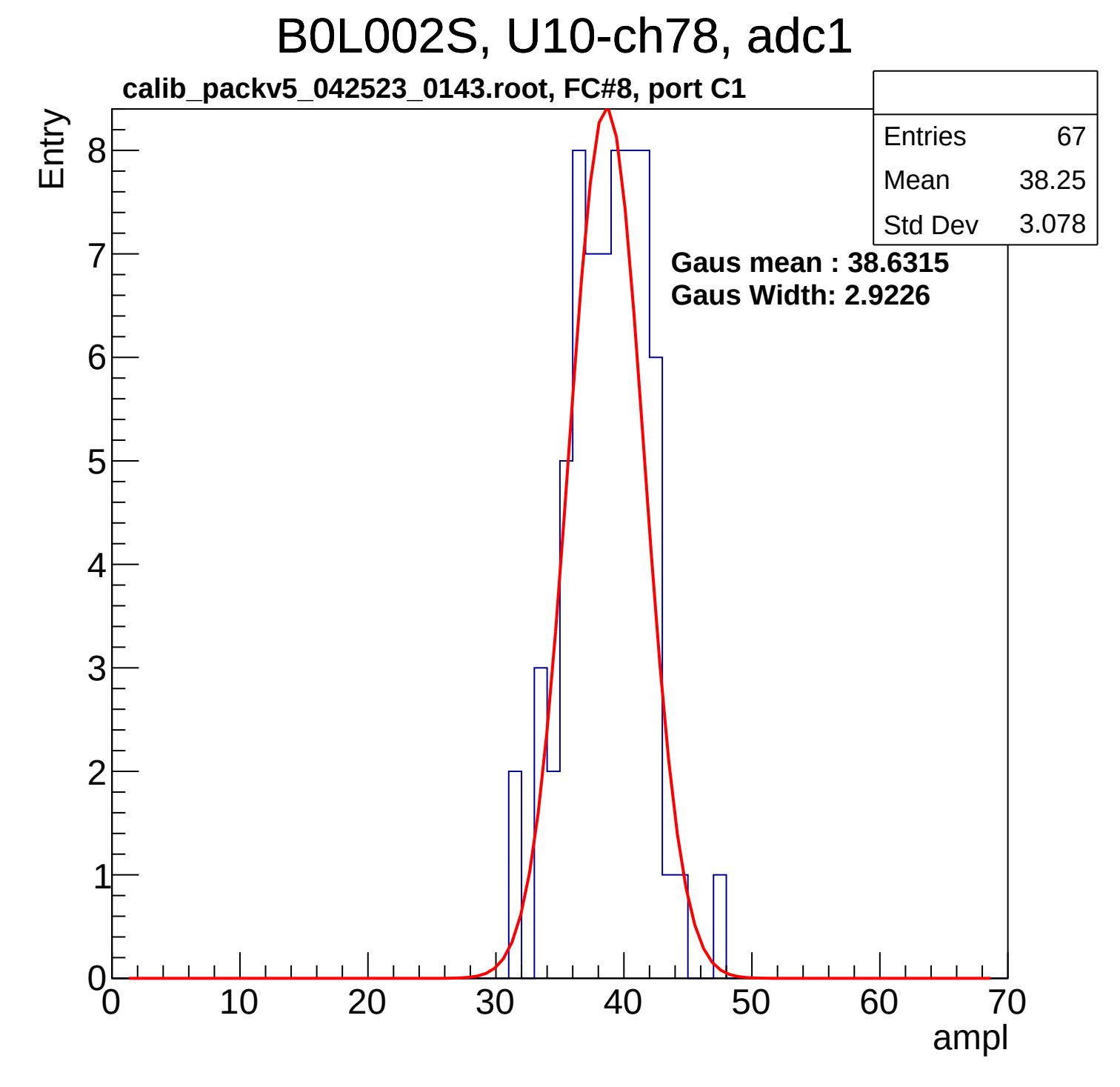

B0L002S, U10-ch77, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 57.59
Std Dev 7.043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

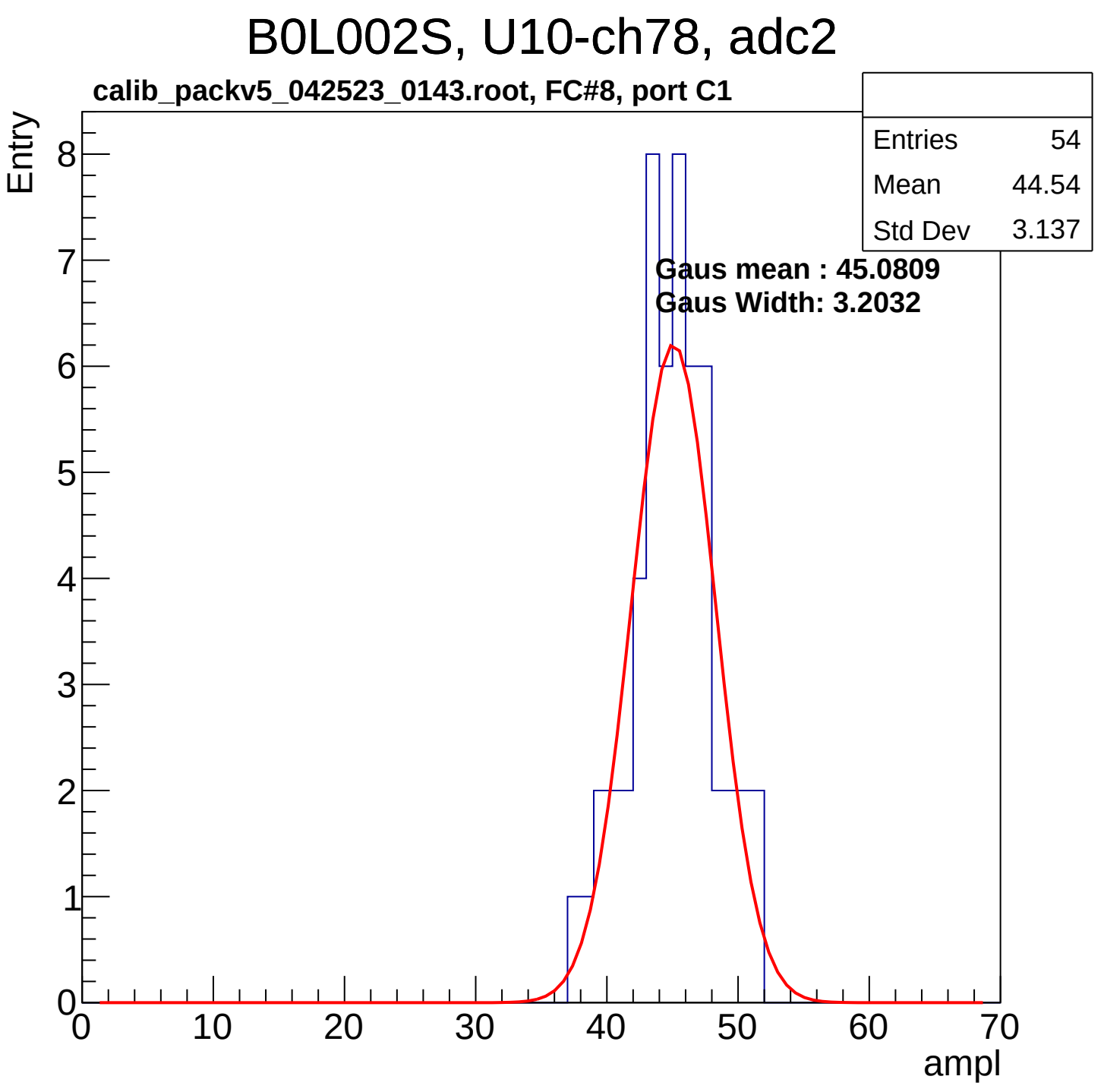

B0L002S, U10-ch77, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 10
Mean 61.5
Std Dev 1.628
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

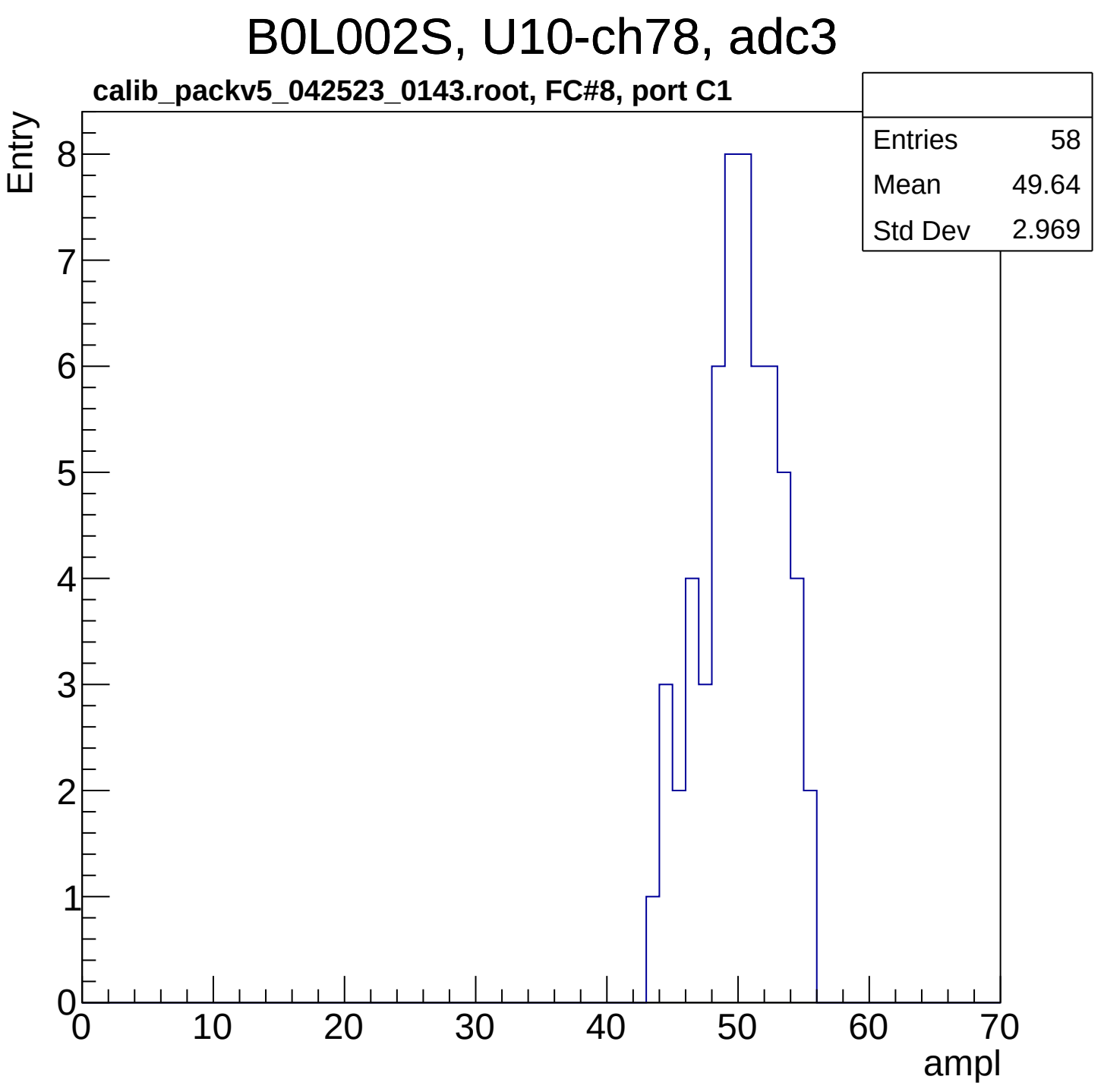

B0L002S, U10-ch77, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

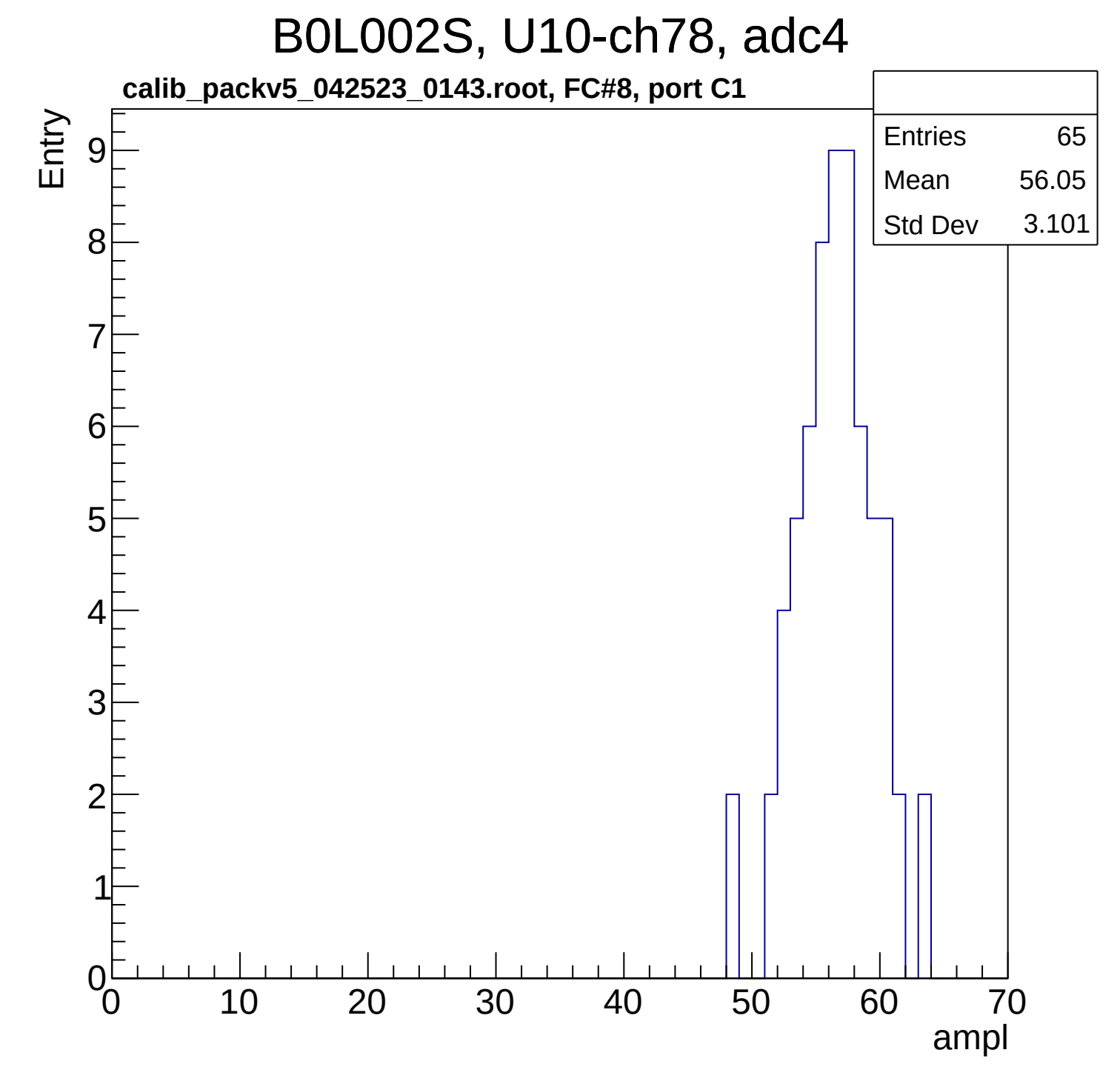

B0L002S, U10-ch77, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

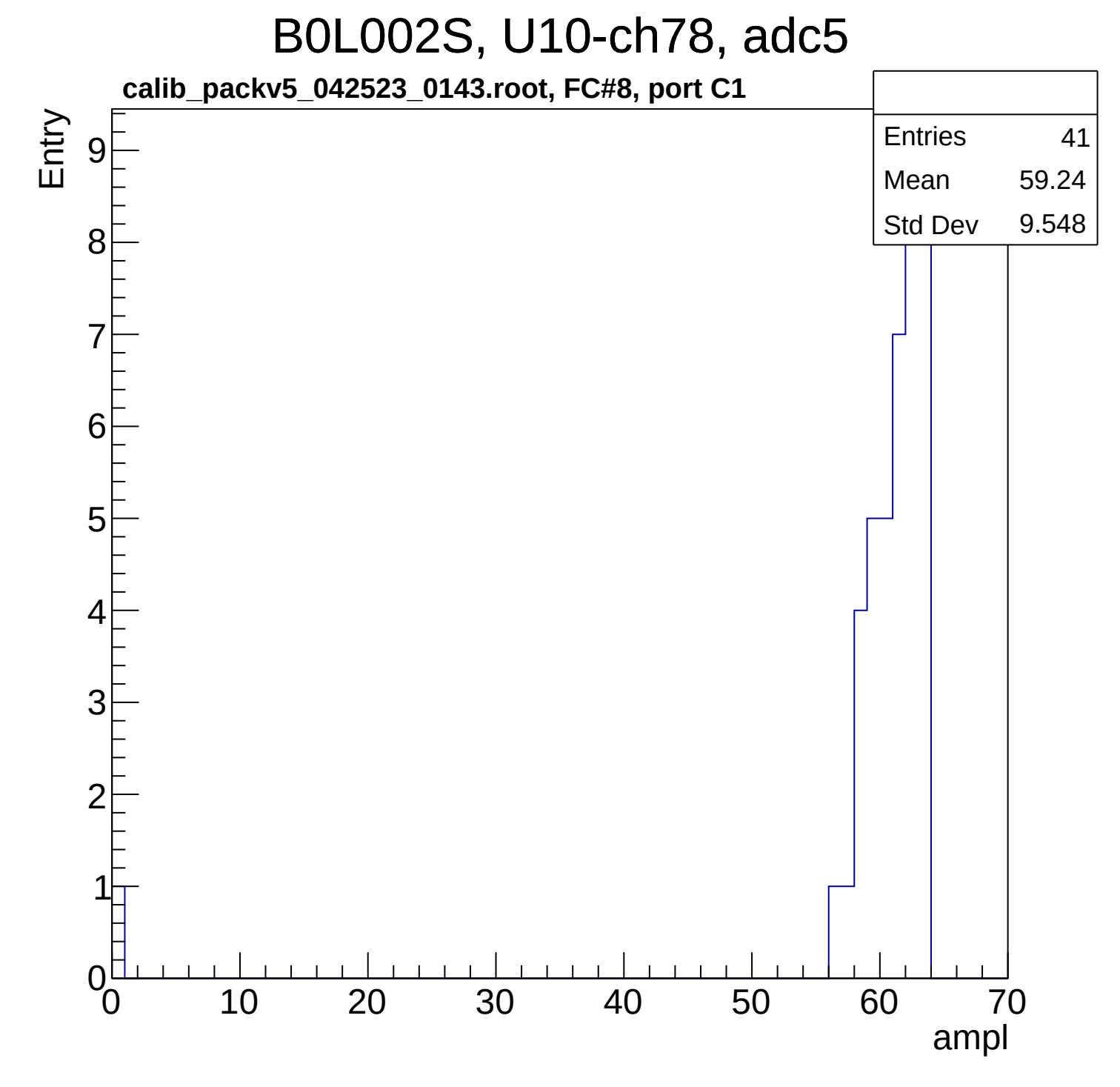

B0L002S, U10-ch78, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.97
Std Dev 2.948
Gaus mean : 32.2475
Gaus Width: 3.4502
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

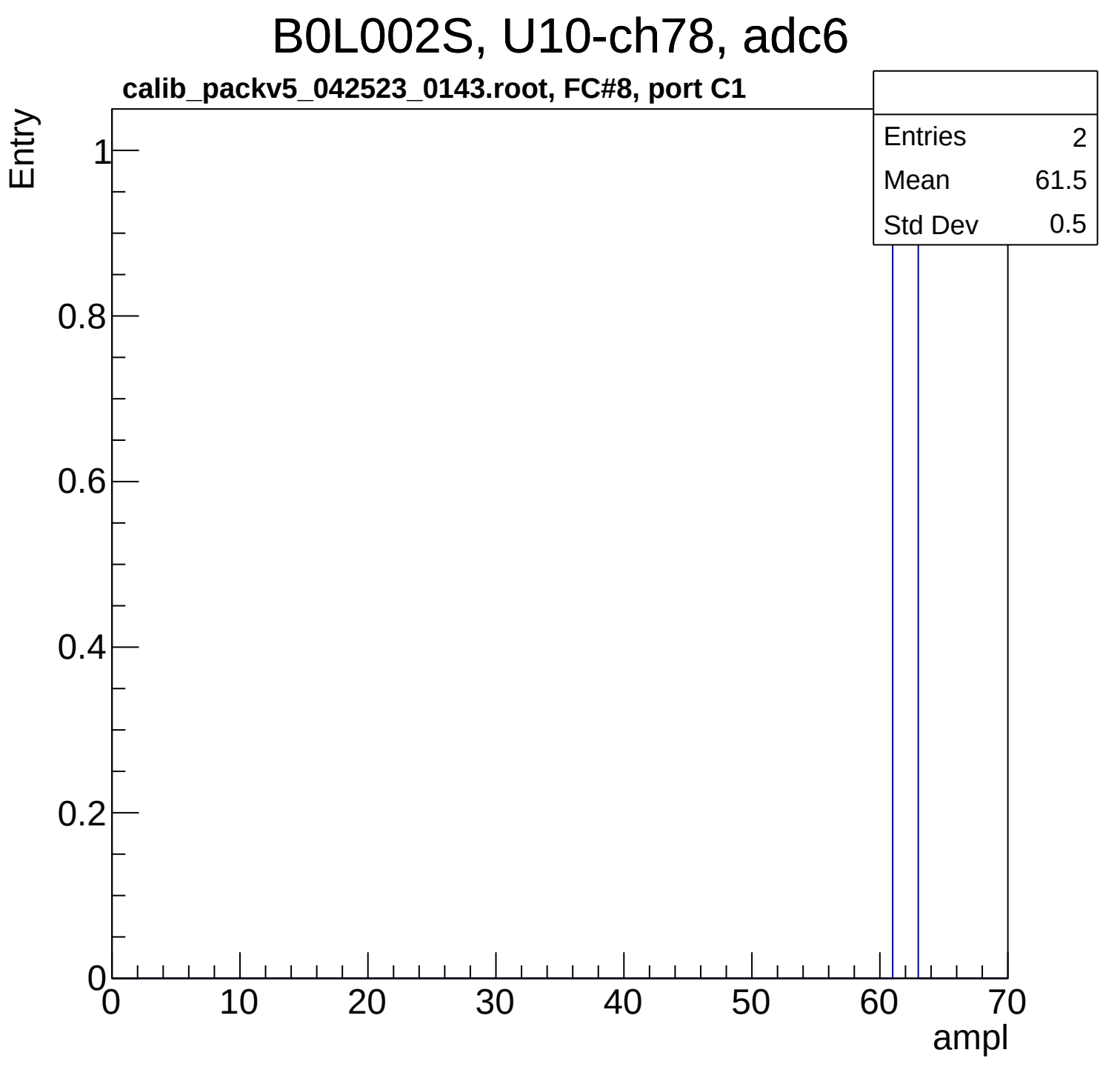

B0L002S, U10-ch78, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 38.25
Std Dev 3.078
Gaus mean : 38.6315
Gaus Width: 2.9226
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

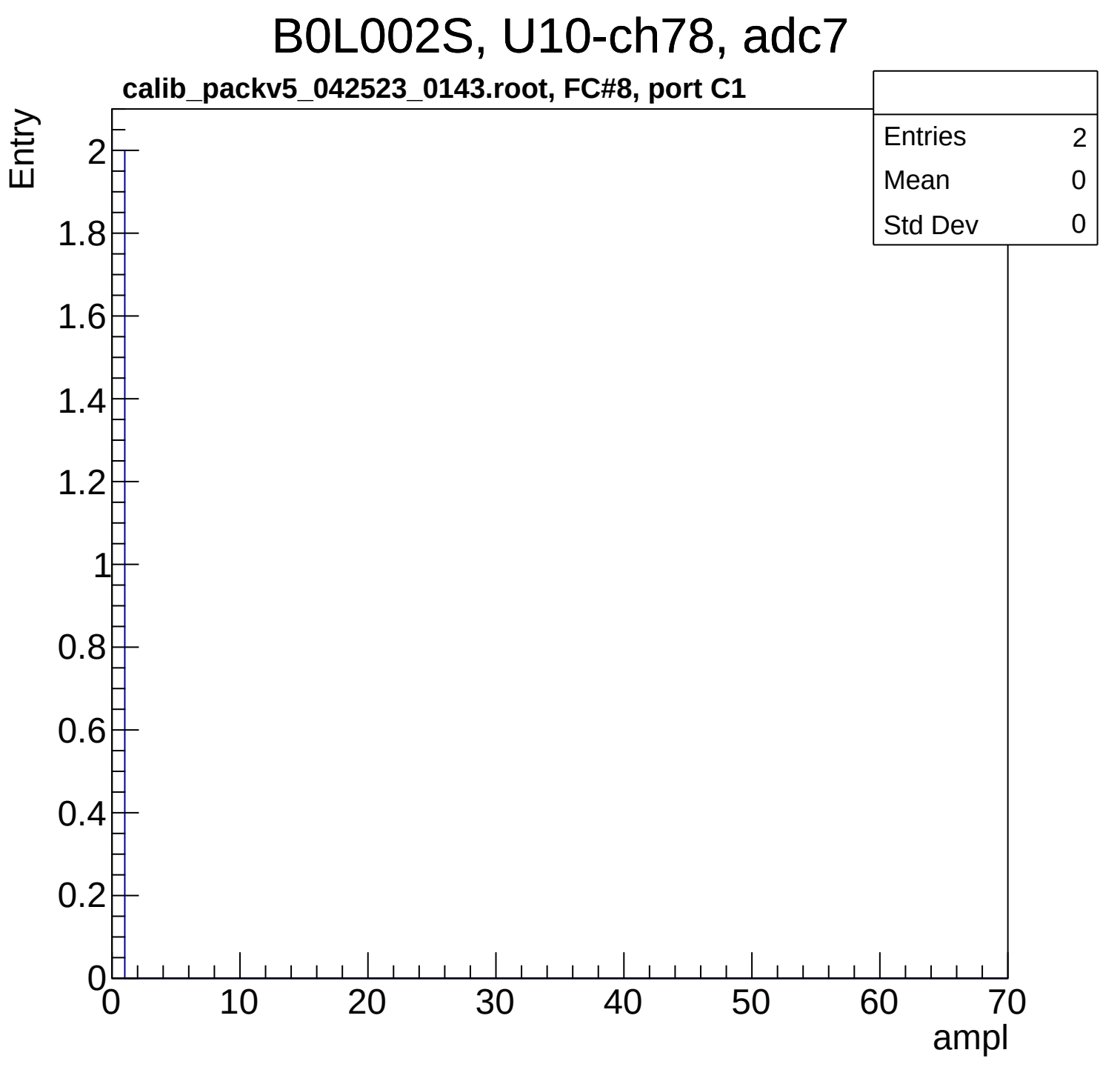

B0L002S, U10-ch78, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 44.54
Std Dev 3.137
Gaus mean : 45.0809
Gaus Width: 3.2032
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

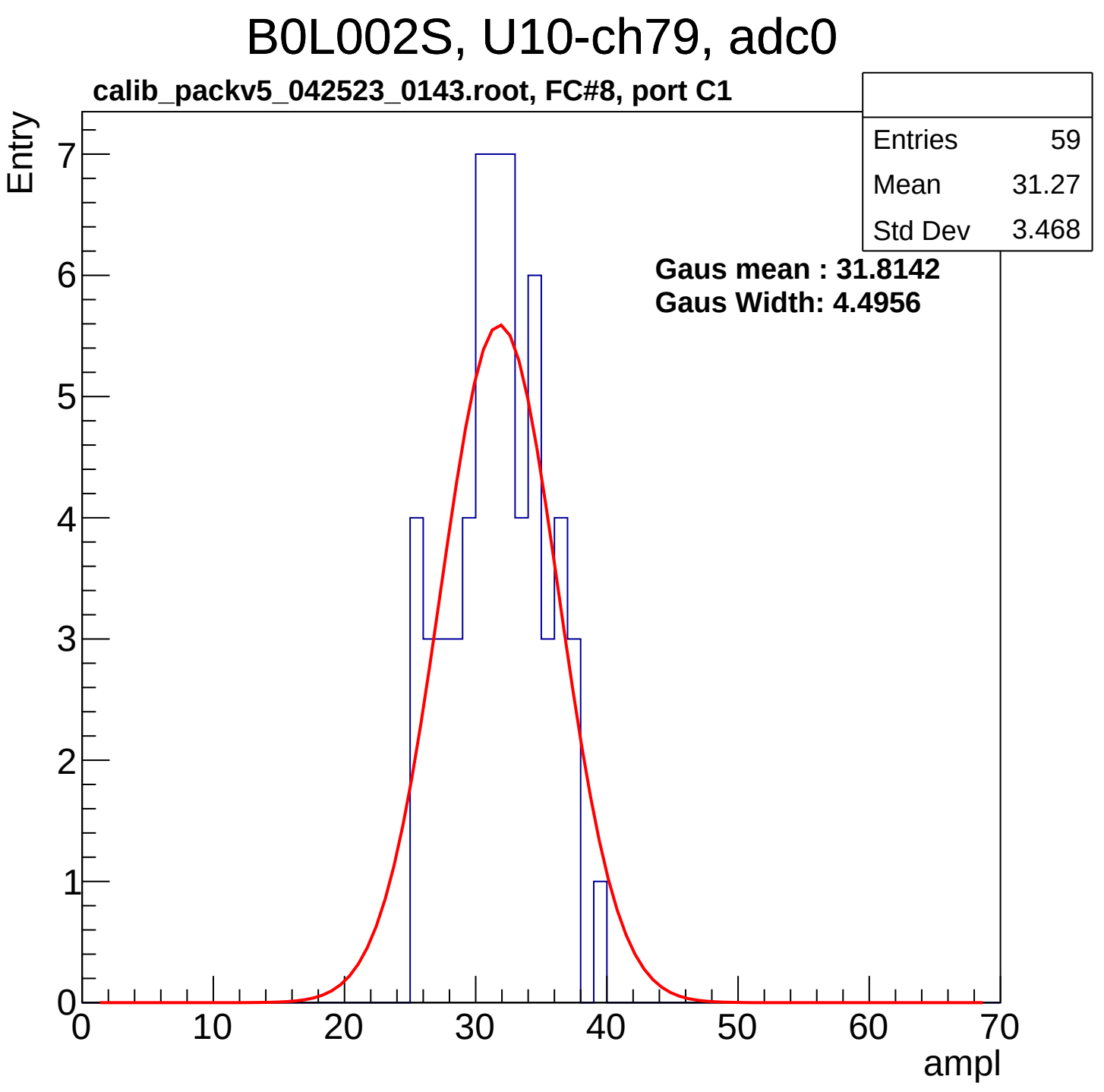

B0L002S, U10-ch78, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 49.64
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

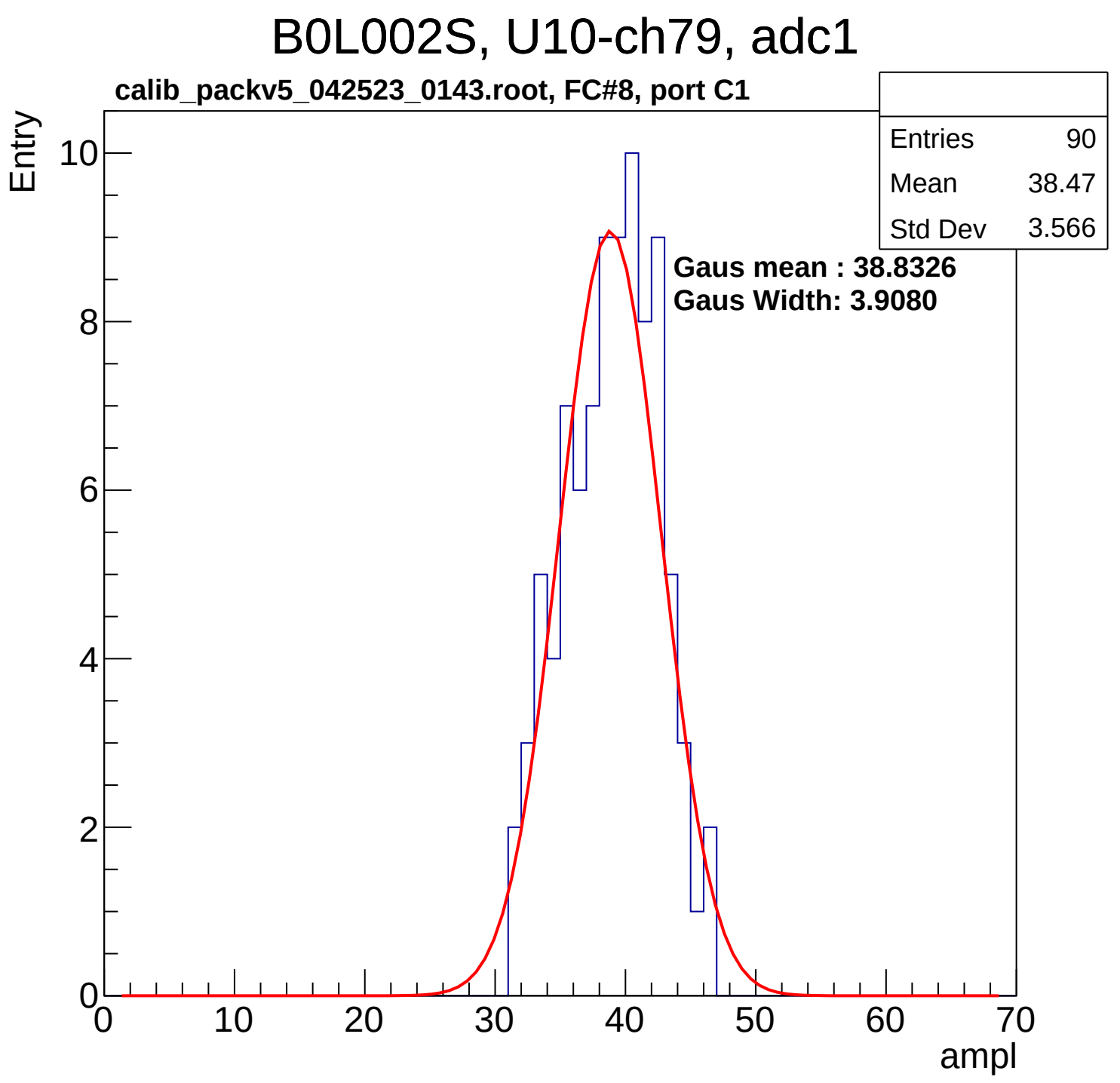

B0L002S, U10-ch78, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.05
Std Dev 3.101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

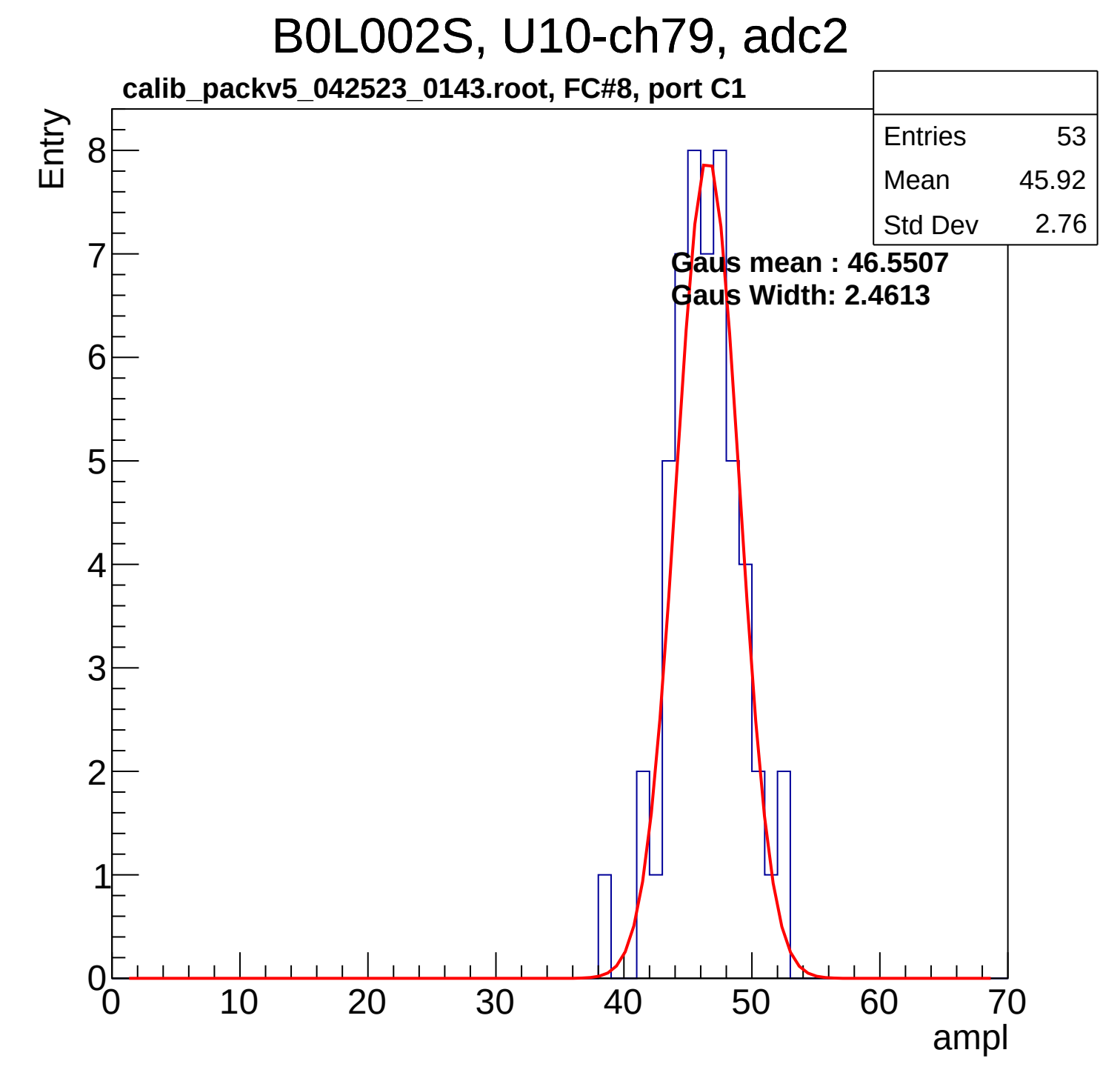

B0L002S, U10-ch78, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59.24
Std Dev 9.548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

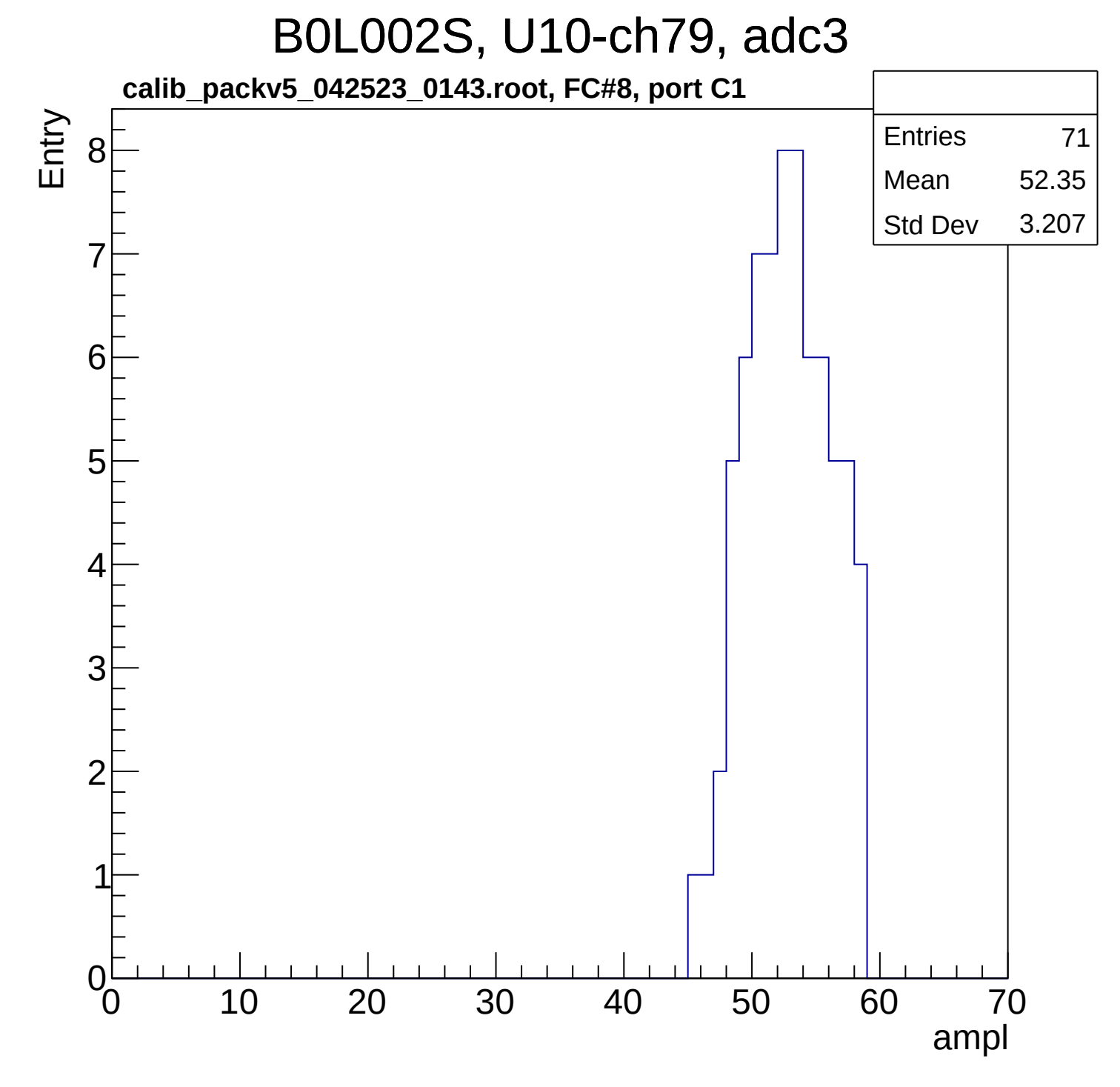

B0L002S, U10-ch78, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

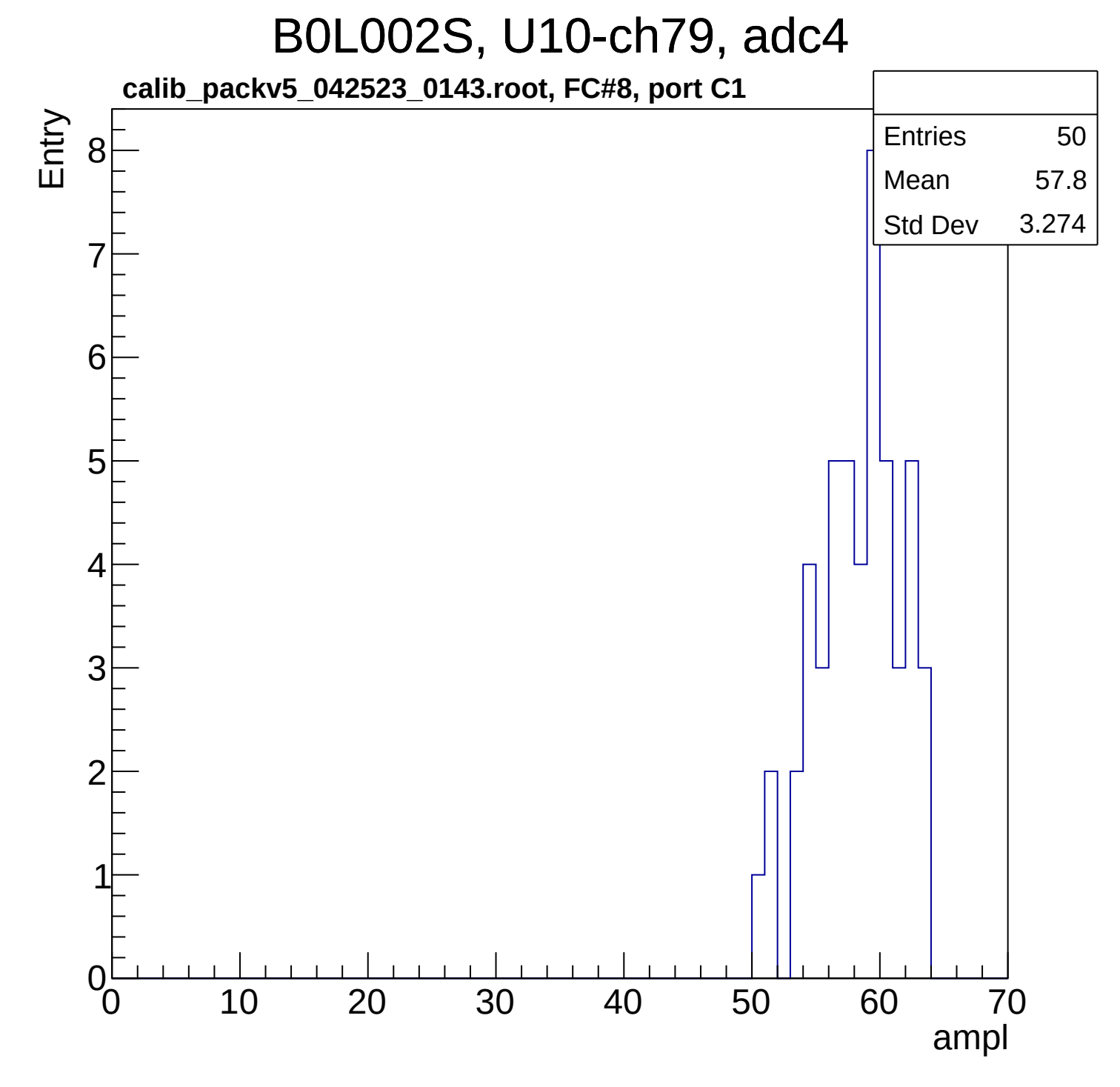

B0L002S, U10-ch78, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

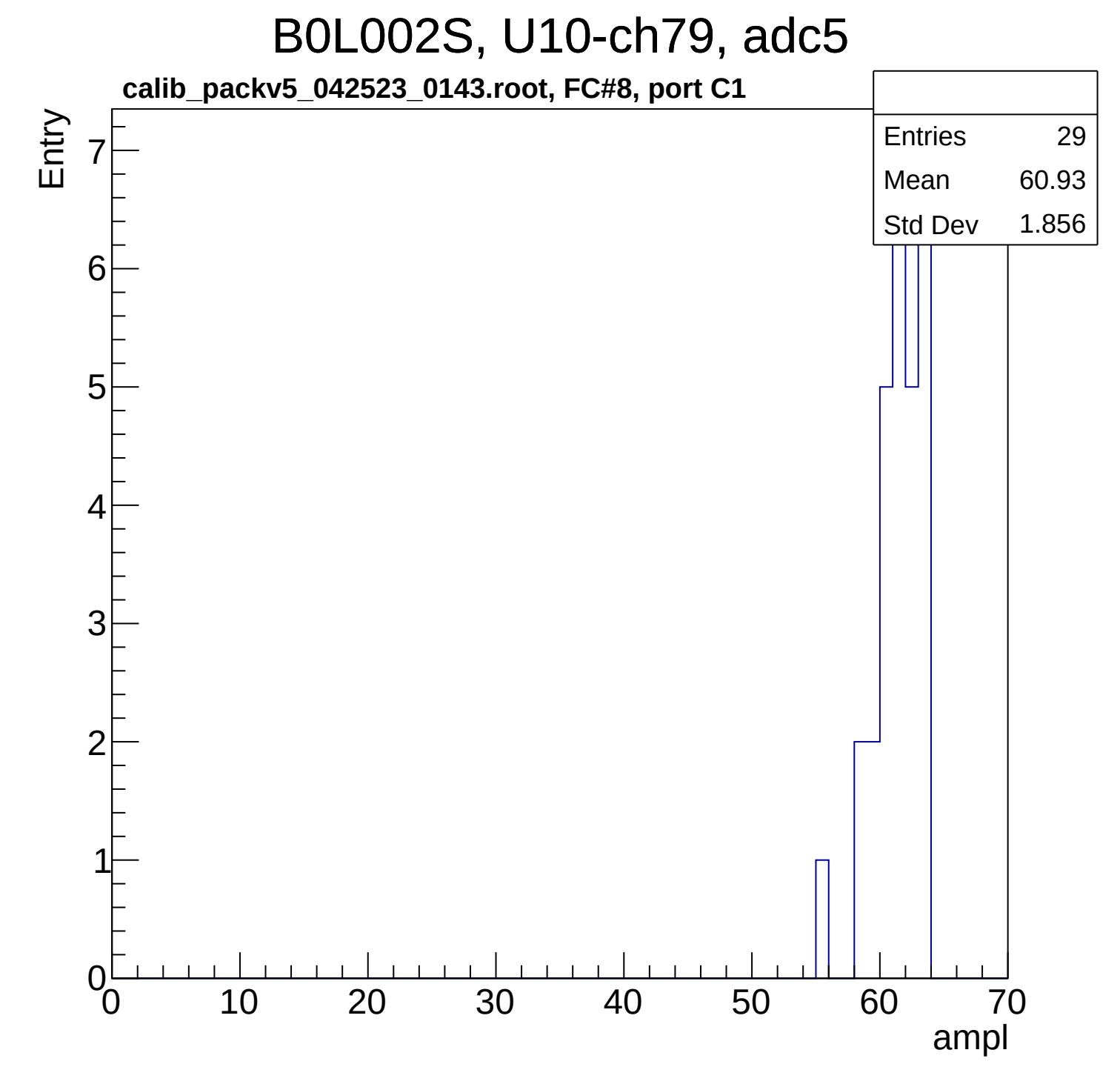

B0L002S, U10-ch79, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 31.27
Std Dev 3.468
Gaus mean : 31.8142
Gaus Width: 4.4956
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

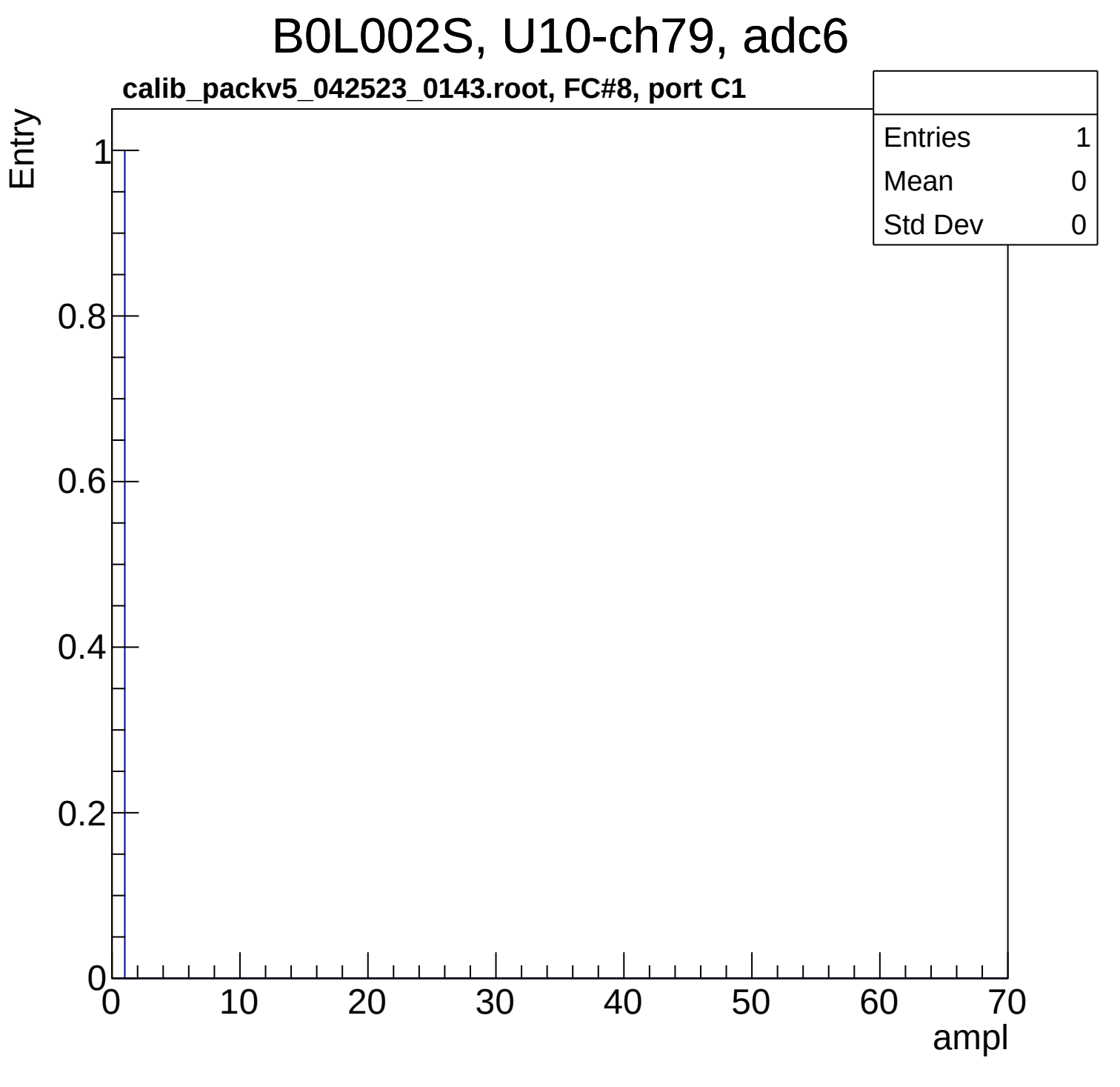

B0L002S, U10-ch79, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 90
Mean 38.47
Std Dev 3.566
Gaus mean : 38.8326
Gaus Width: 3.9080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

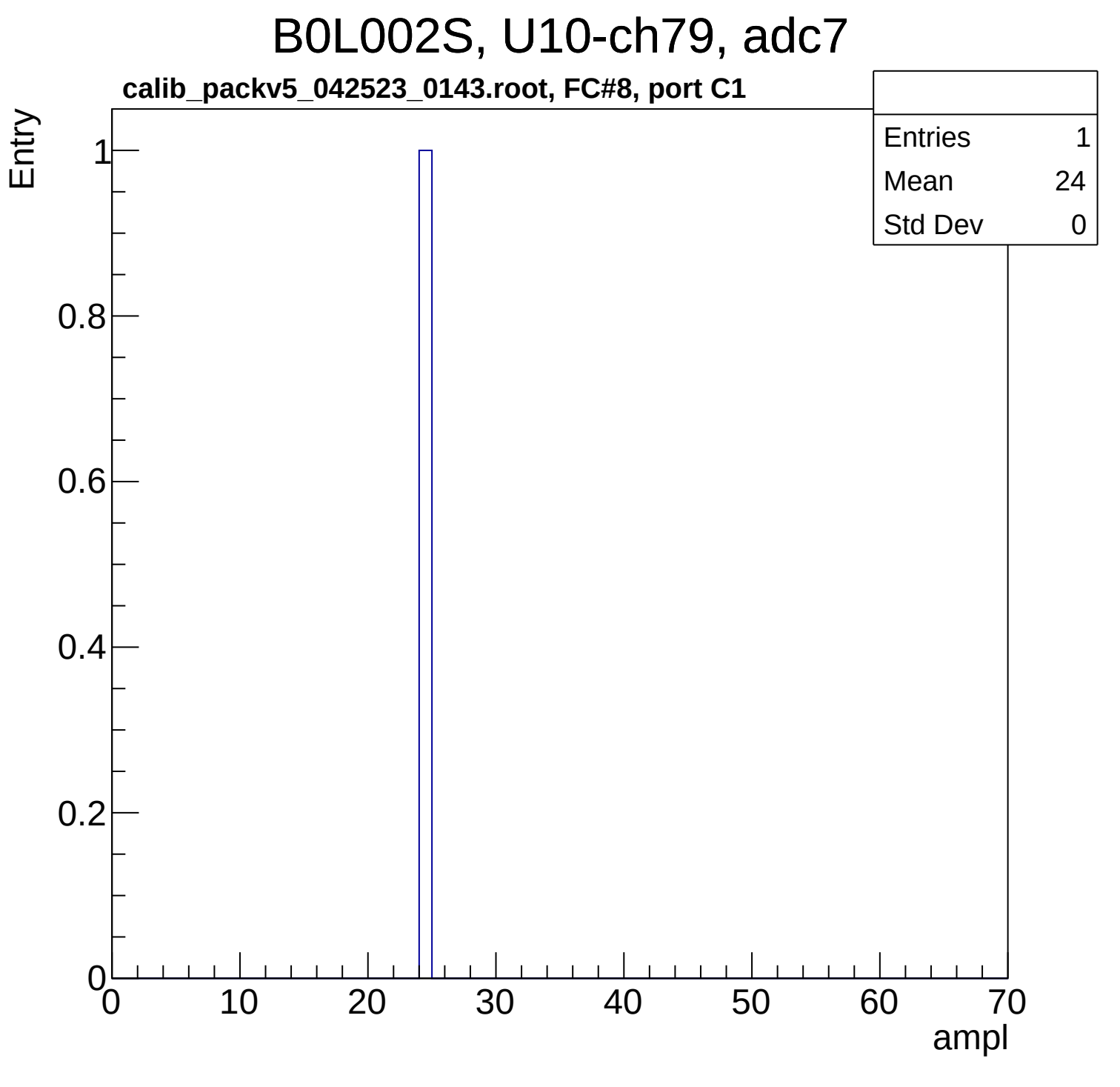

B0L002S, U10-ch79, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 45.92
Std Dev 2.76
Gaus mean : 46.5507
Gaus Width: 2.4613
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

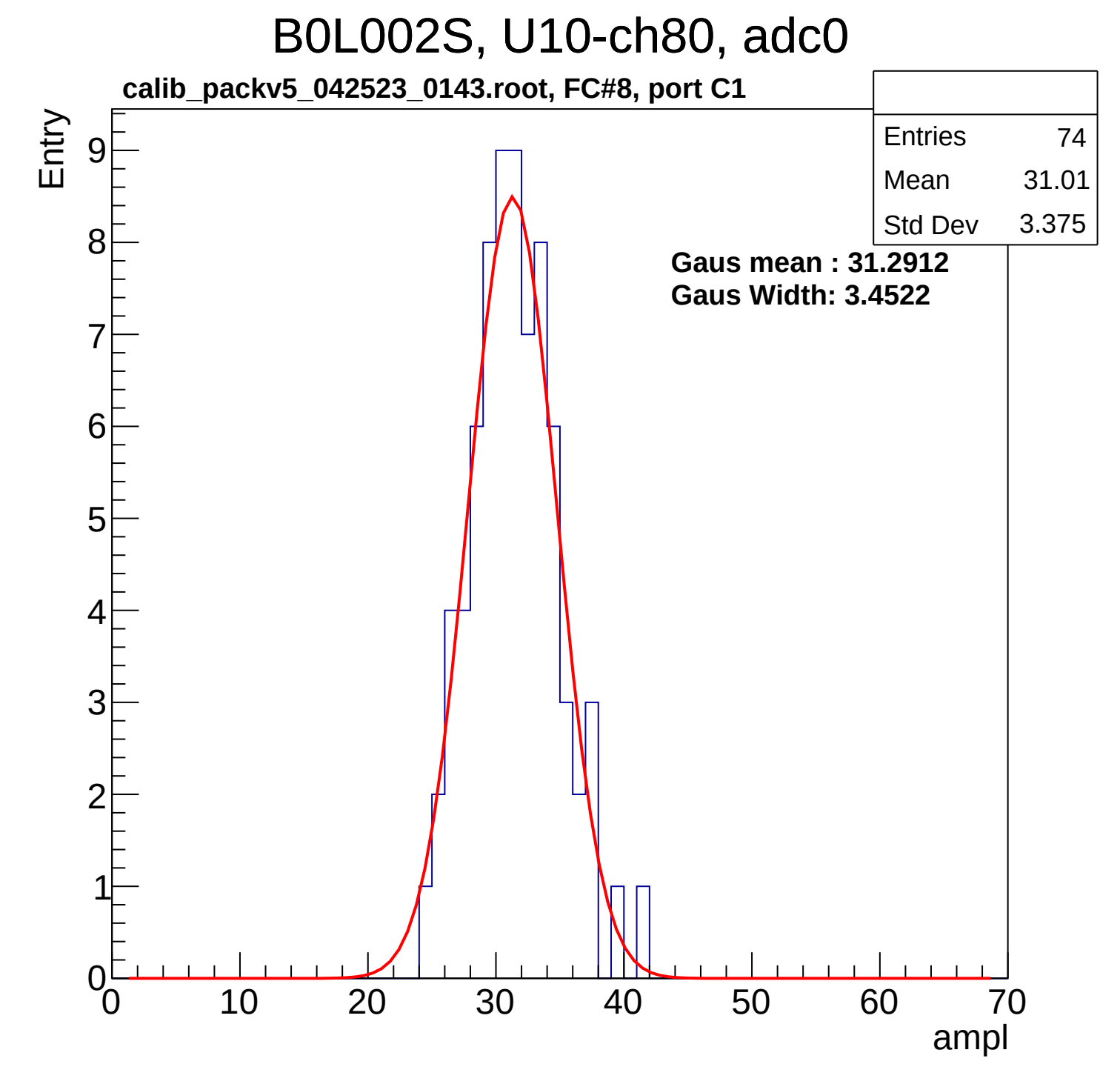

B0L002S, U10-ch79, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.35
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

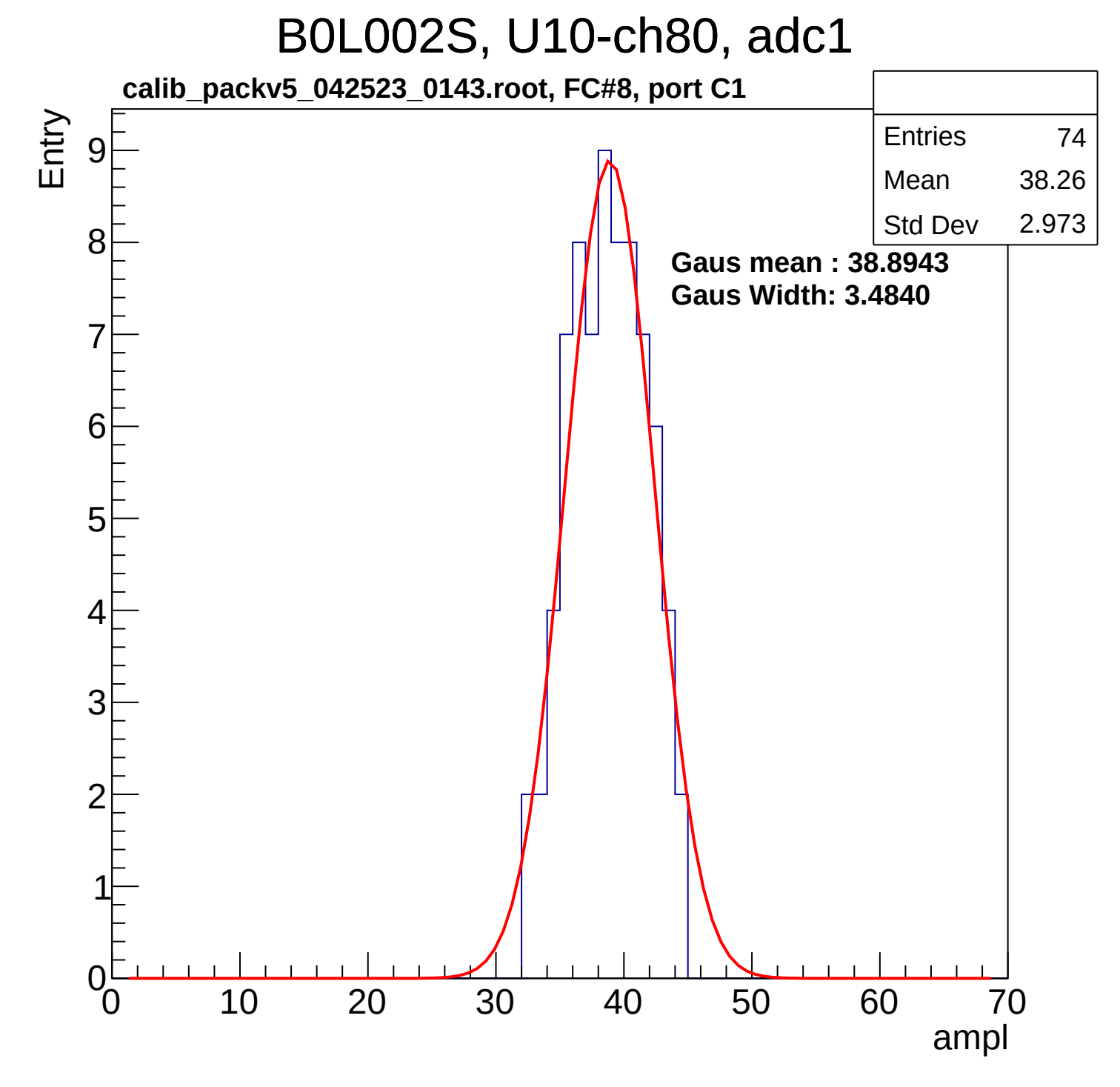

B0L002S, U10-ch79, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 57.8
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

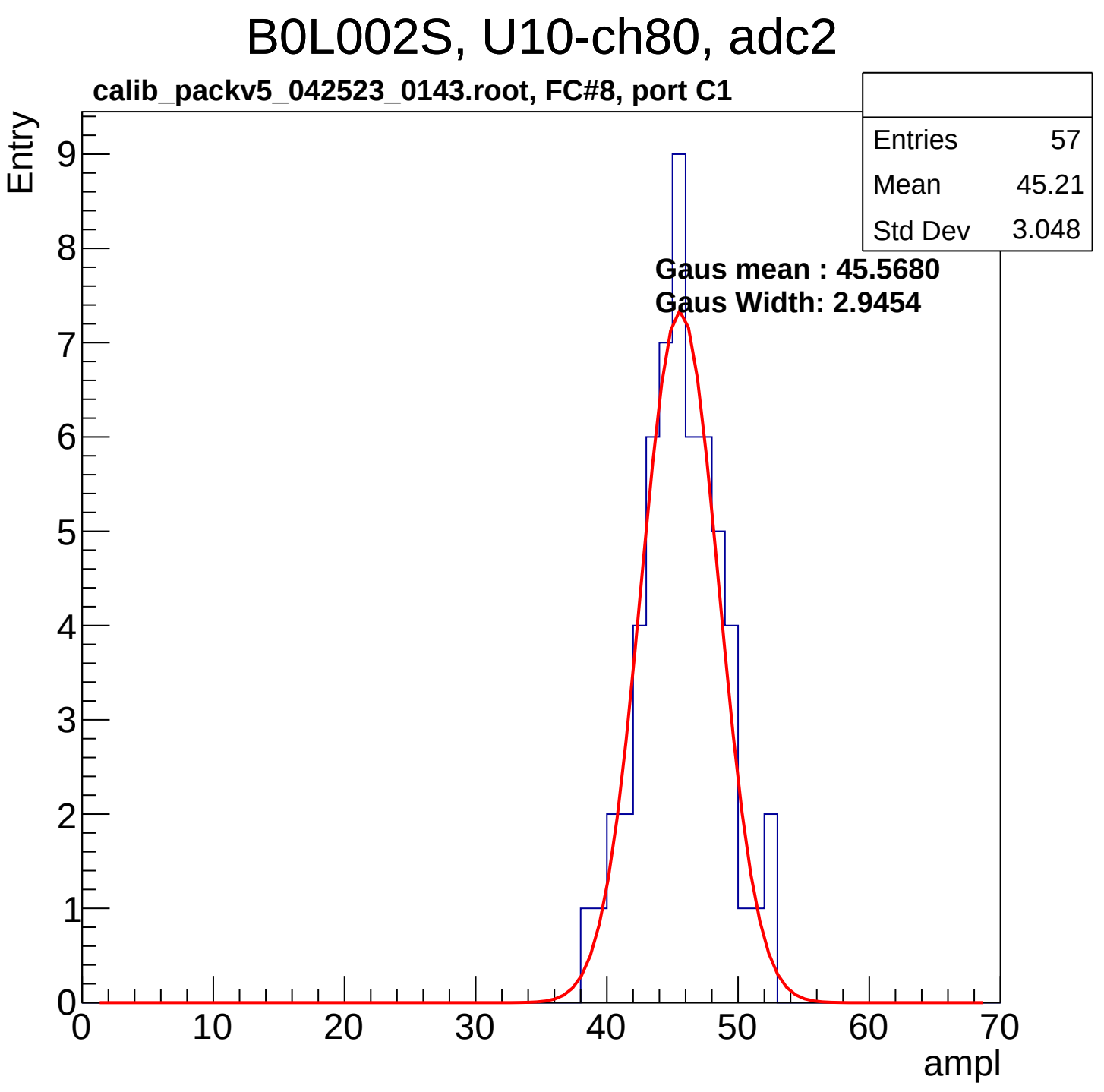

B0L002S, U10-ch79, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 60.93
Std Dev 1.856
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

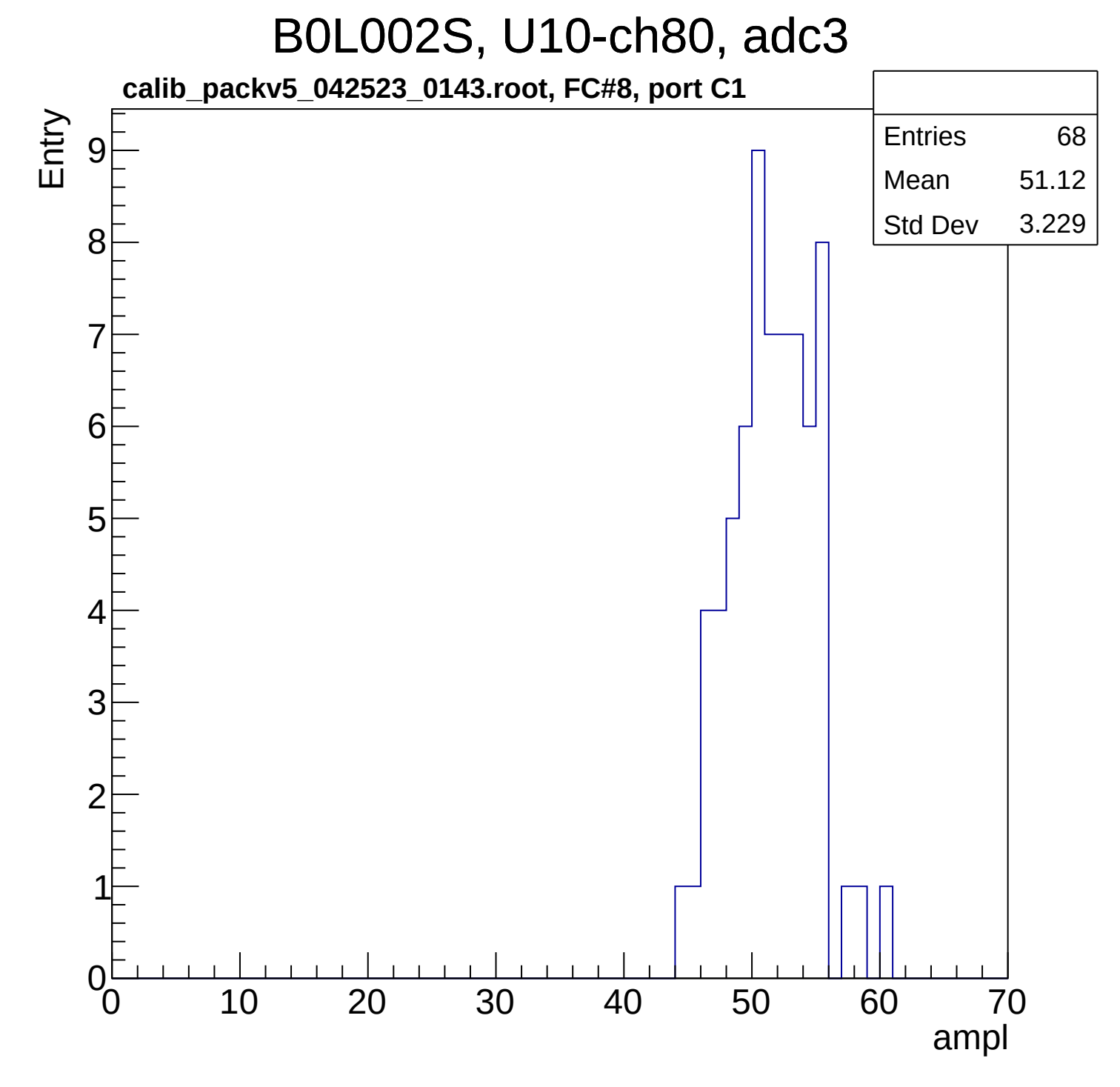

B0L002S, U10-ch79, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

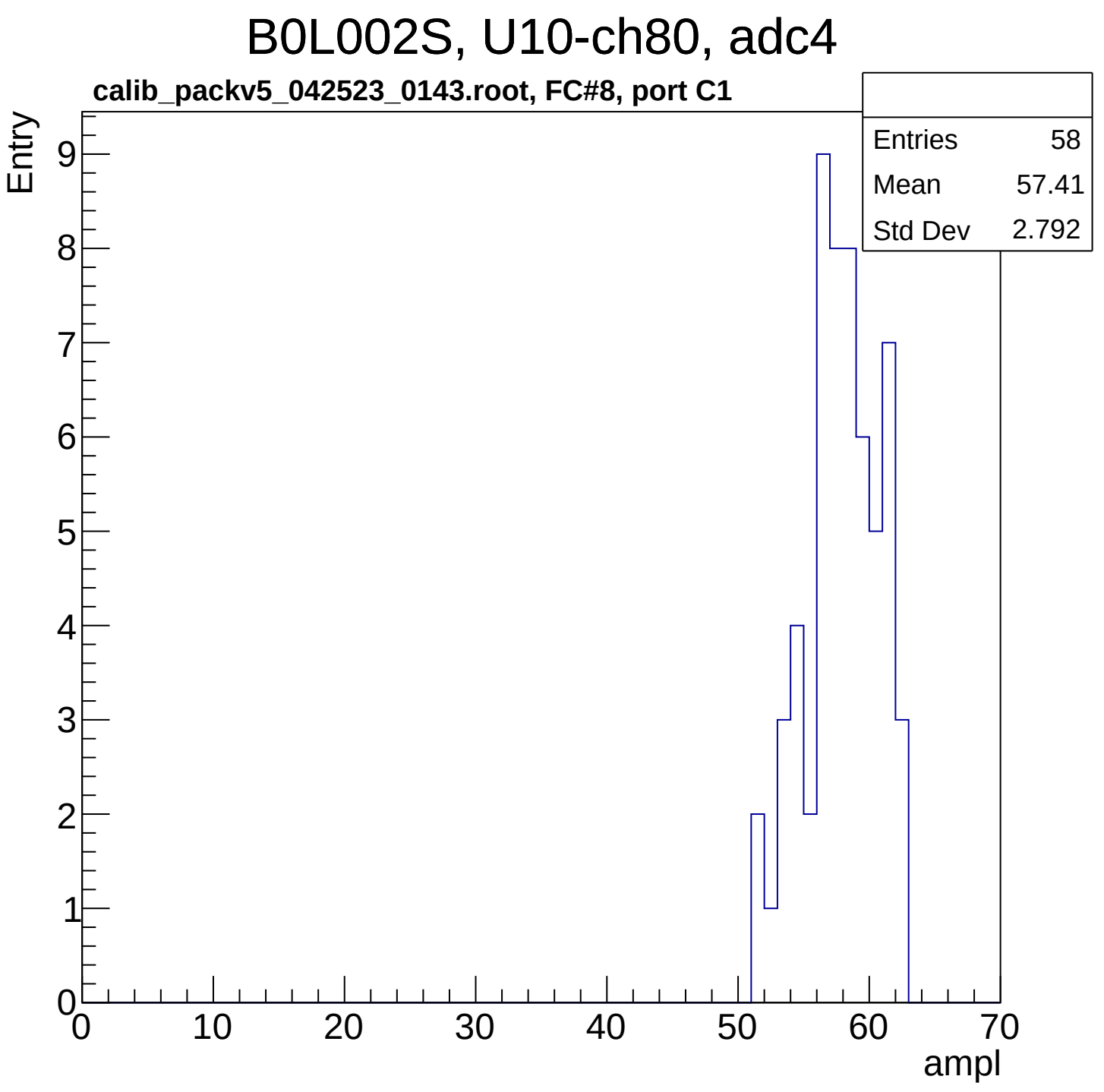

B0L002S, U10-ch79, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

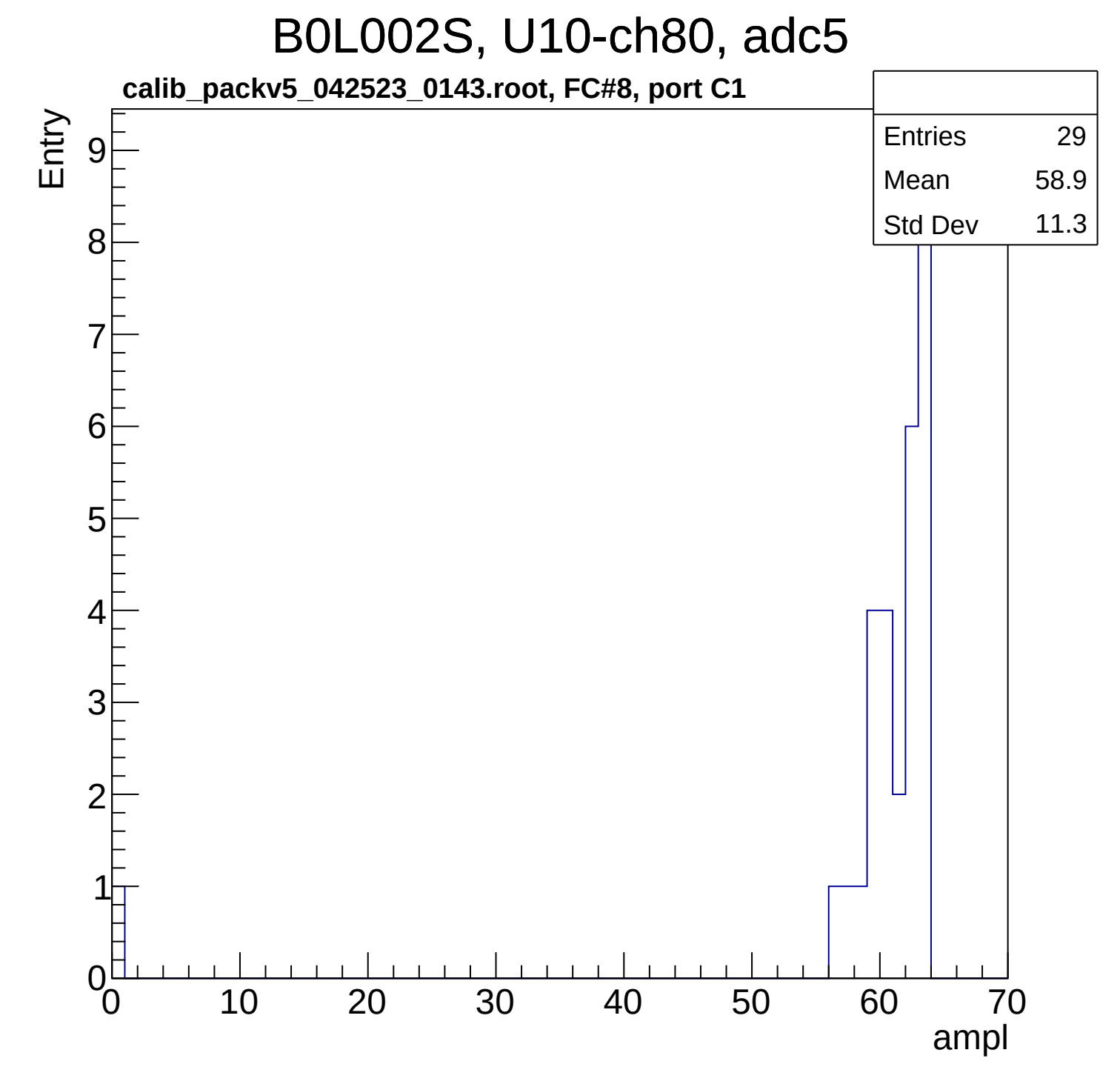

B0L002S, U10-ch80, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 31.01
Std Dev 3.375
Gaus mean : 31.2912
Gaus Width: 3.4522
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

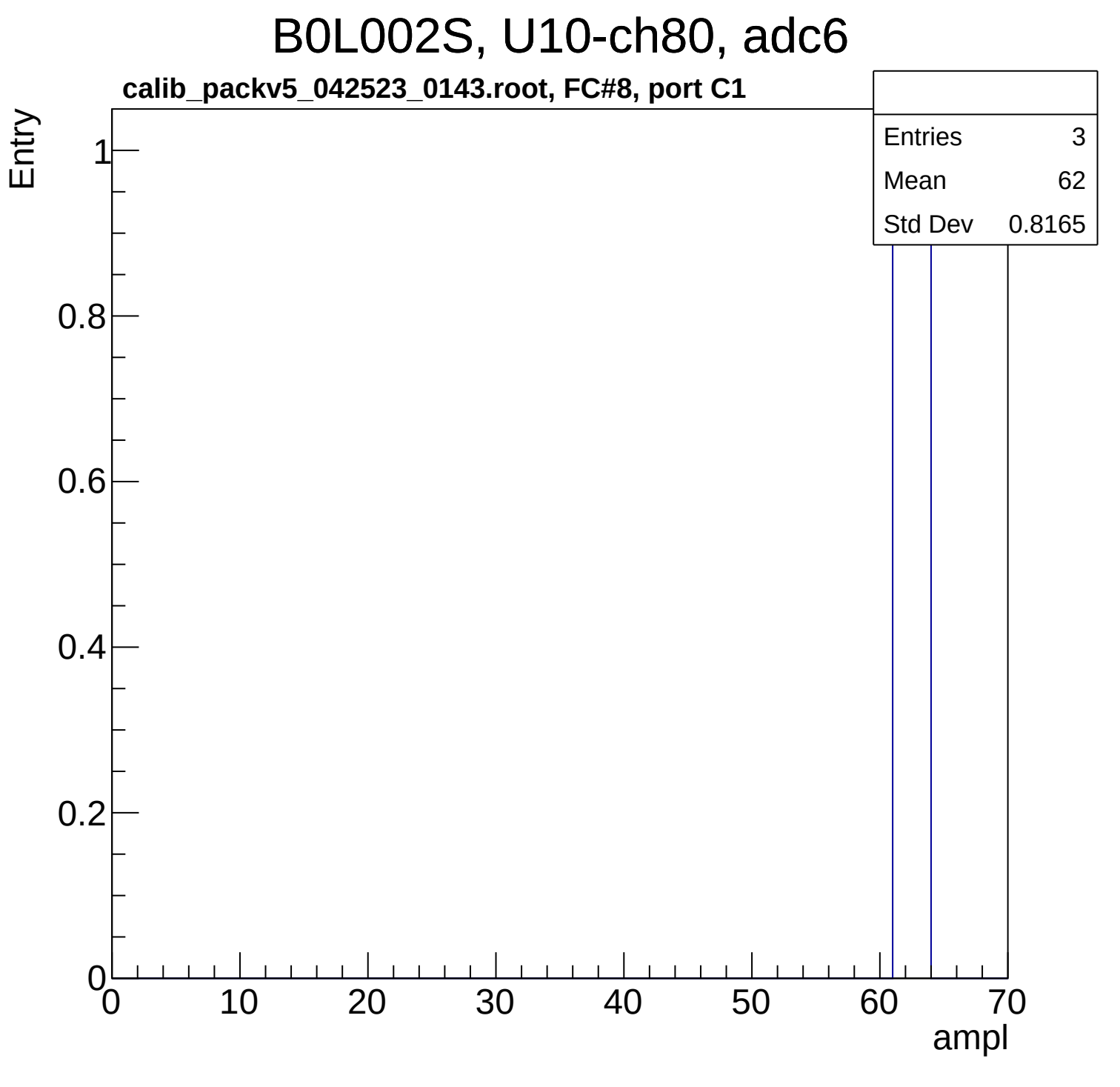

B0L002S, U10-ch80, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.26
Std Dev 2.973
Gaus mean : 38.8943
Gaus Width: 3.4840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

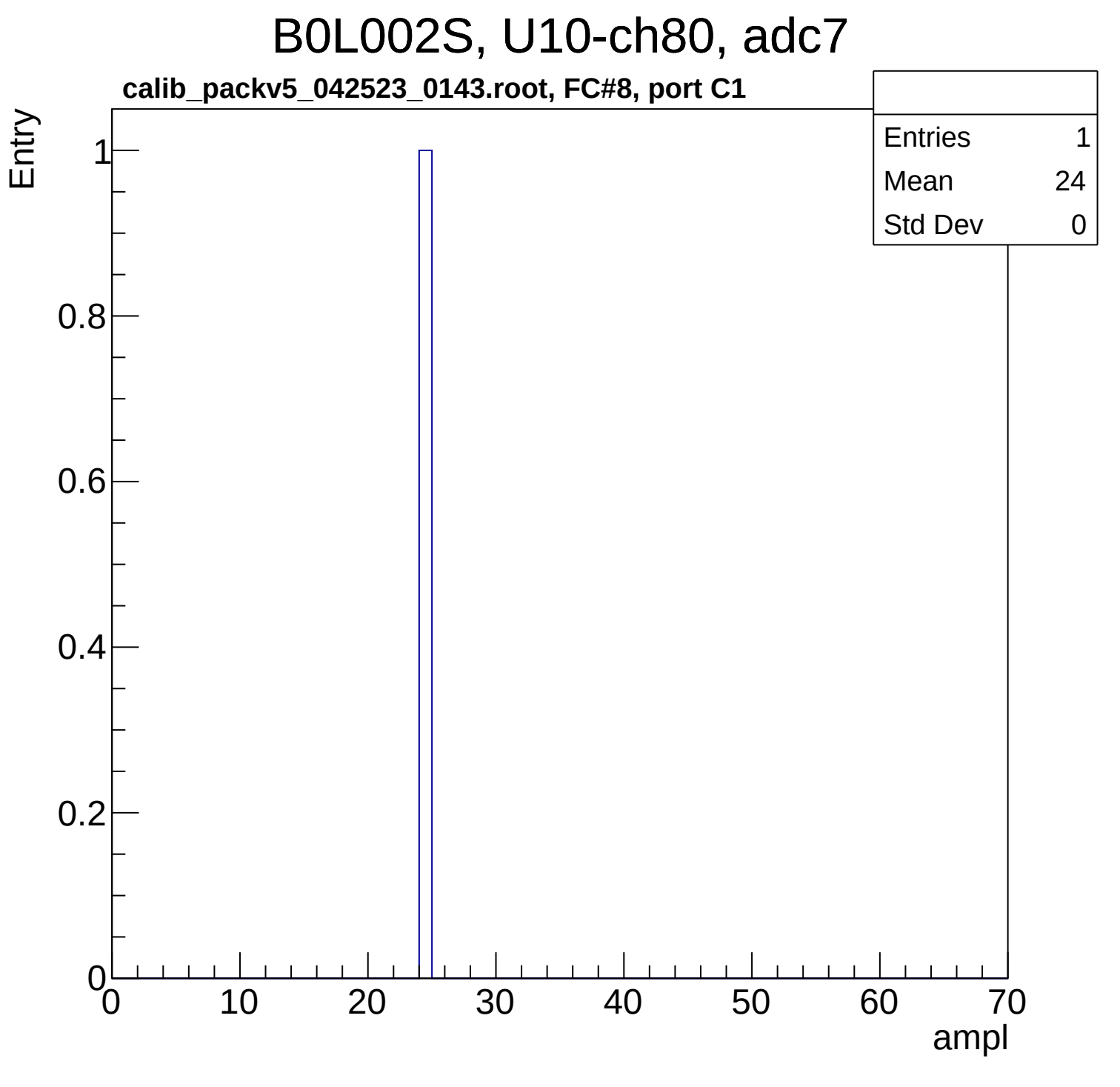

B0L002S, U10-ch80, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 45.21
Std Dev 3.048
Gaus mean : 45.5680
Gaus Width: 2.9454
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

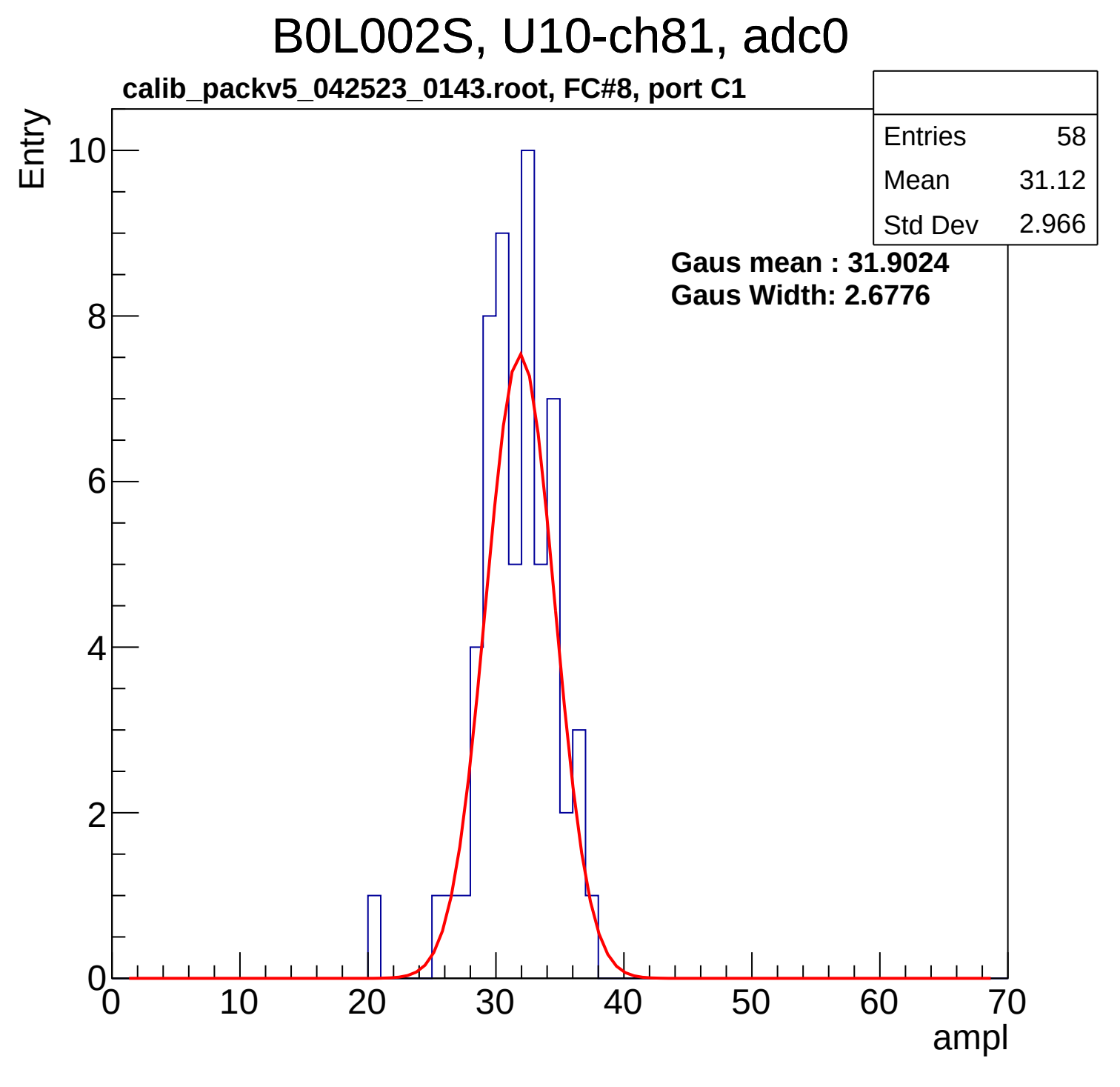

B0L002S, U10-ch80, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.12
Std Dev 3.229
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

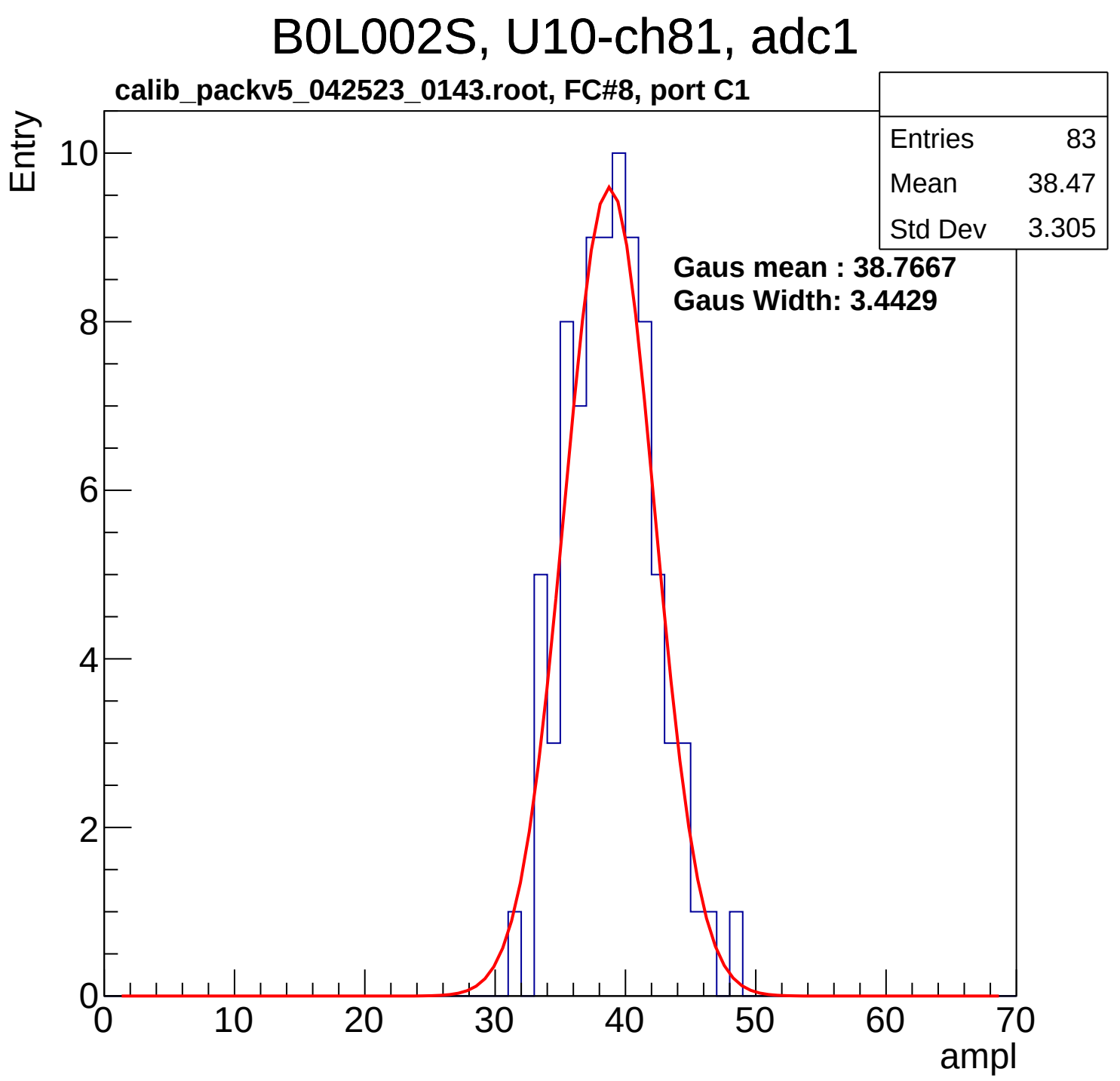

B0L002S, U10-ch80, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.41
Std Dev 2.792
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

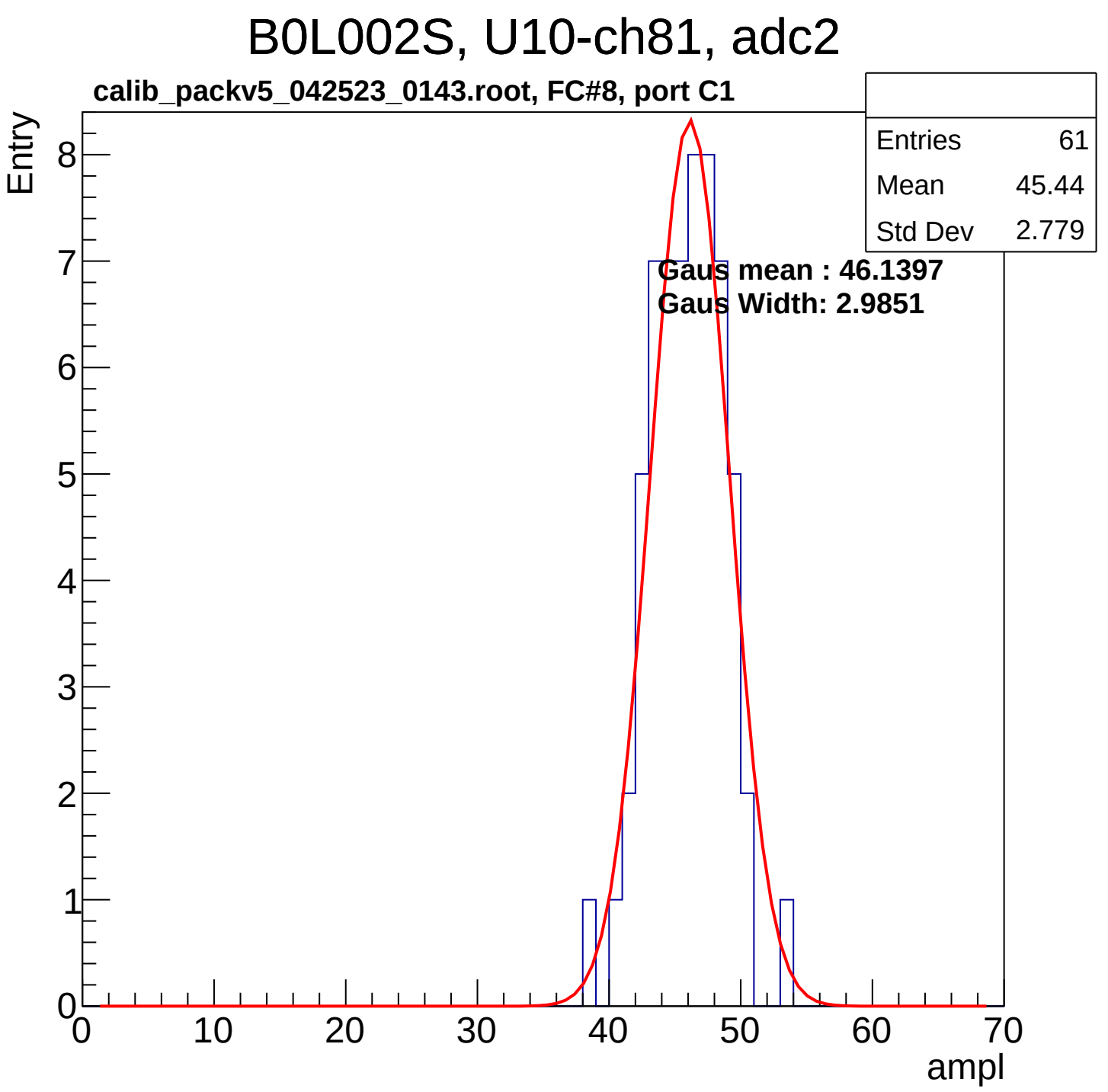

B0L002S, U10-ch80, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 29
Mean 58.9
Std Dev 11.3
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

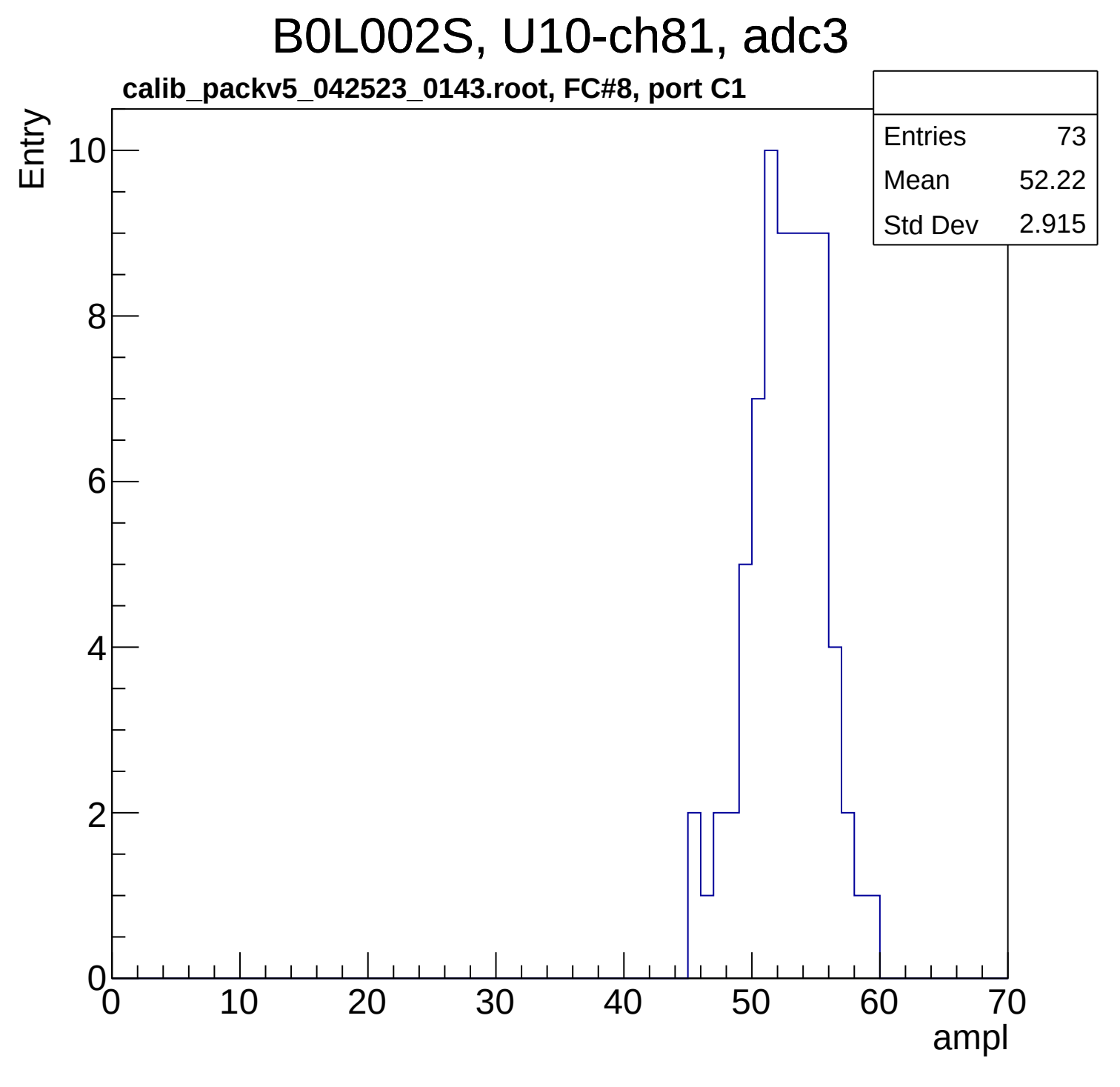

B0L002S, U10-ch80, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

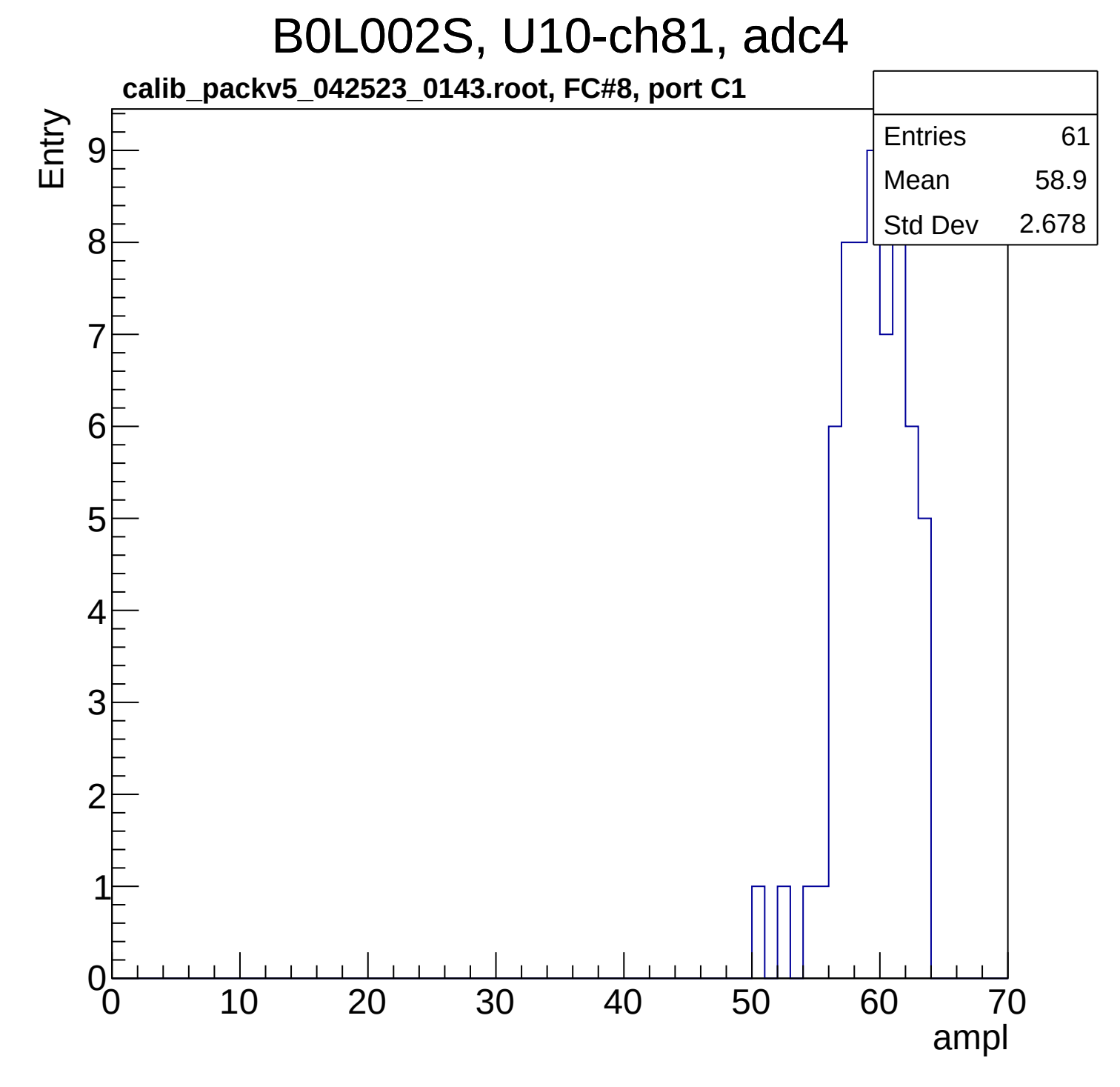

B0L002S, U10-ch80, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

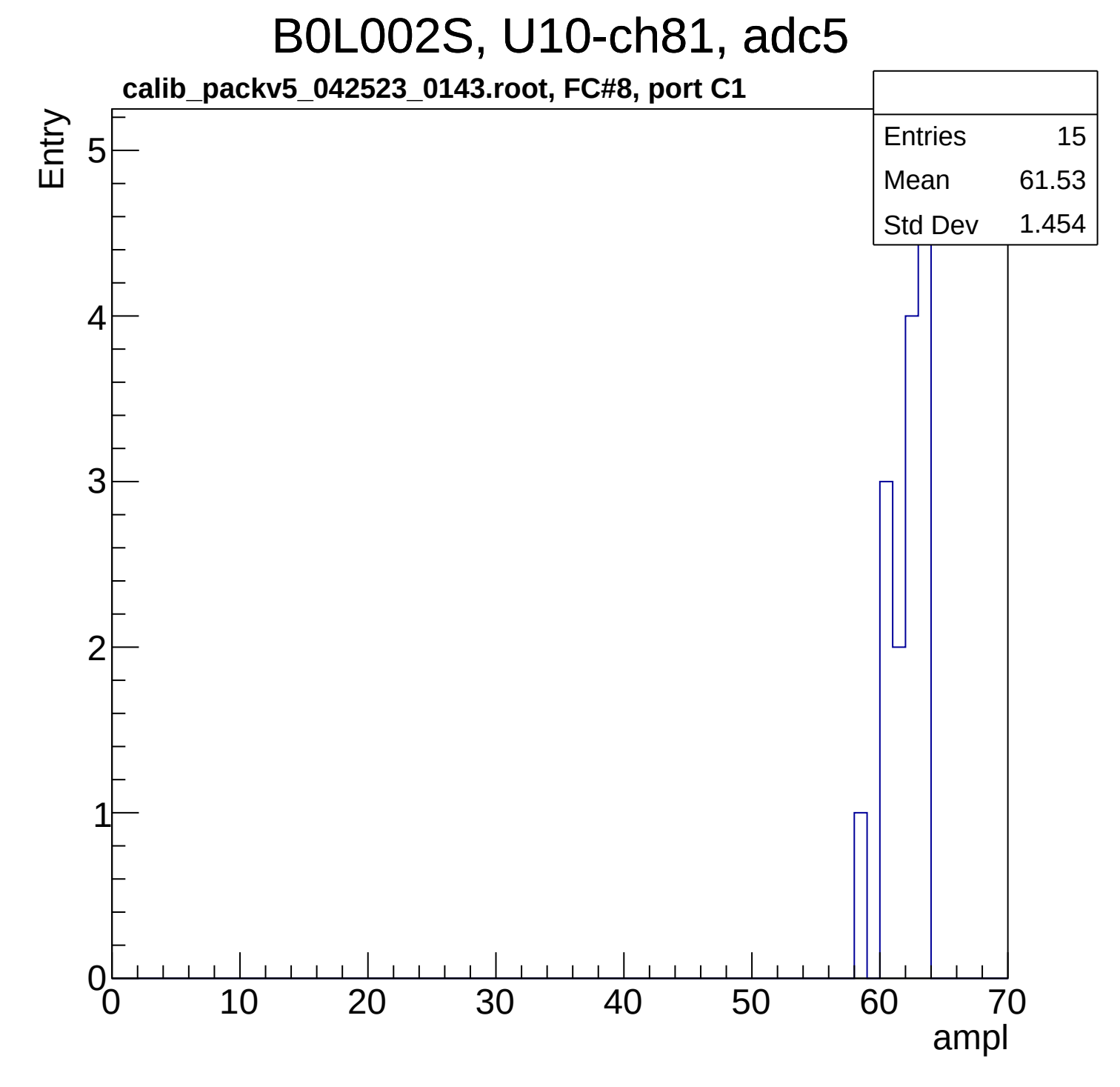

B0L002S, U10-ch81, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 31.12
Std Dev 2.966
Gaus mean : 31.9024
Gaus Width: 2.6776
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

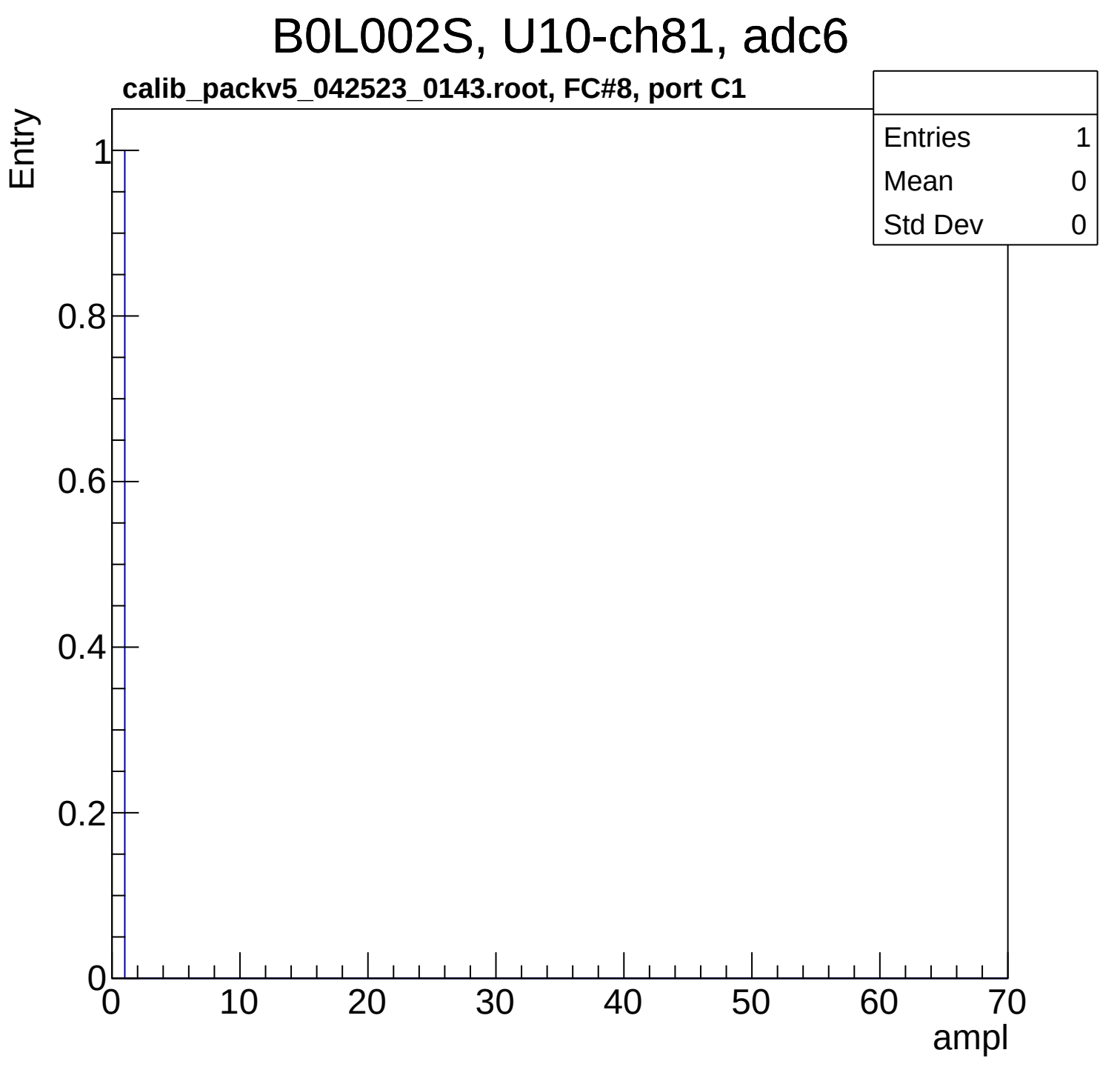

B0L002S, U10-ch81, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 38.47
Std Dev 3.305
Gaus mean : 38.7667
Gaus Width: 3.4429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

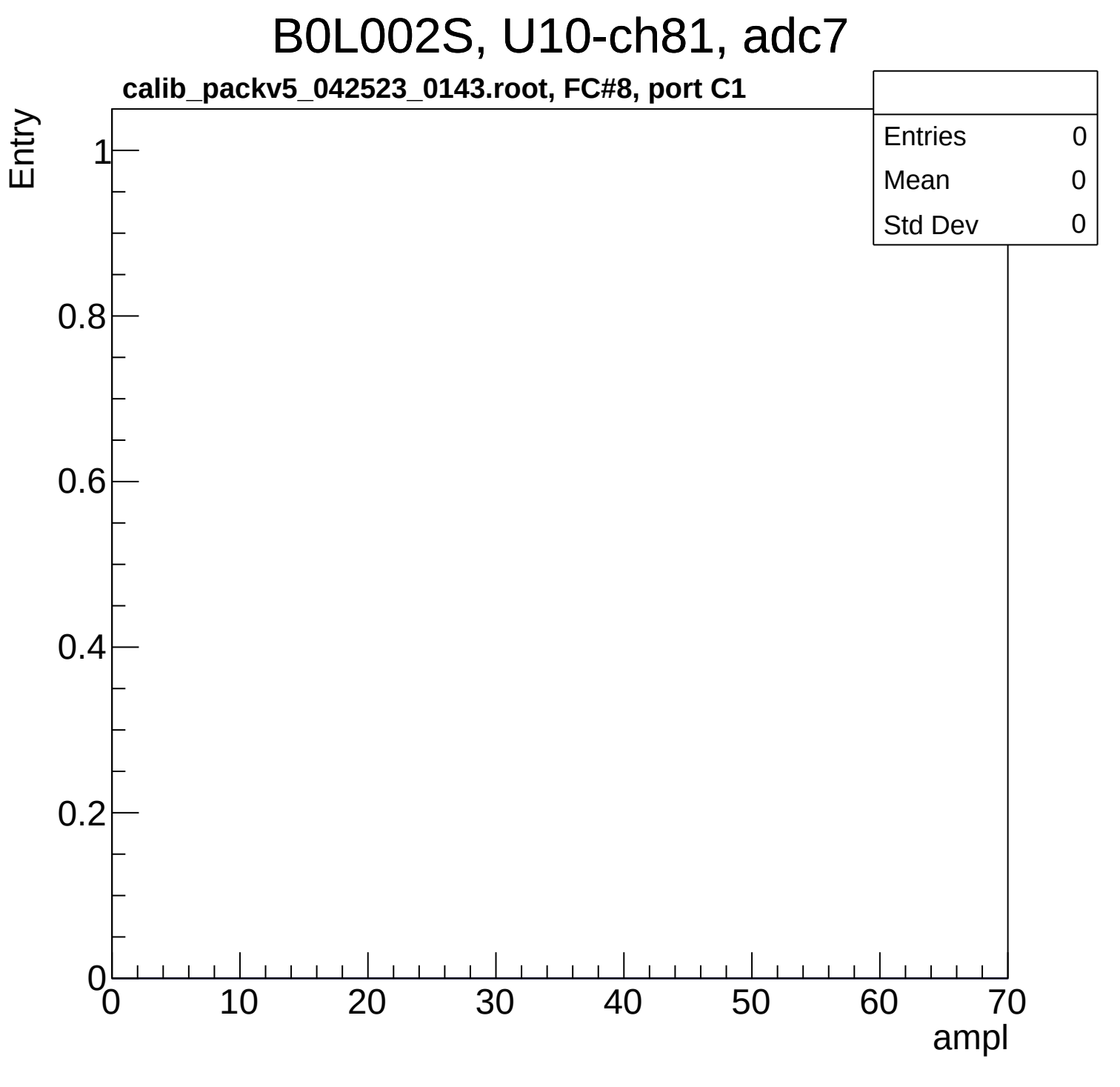

B0L002S, U10-ch81, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 45.44
Std Dev 2.779
Gaus mean : 46.1397
Gaus Width: 2.9851
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

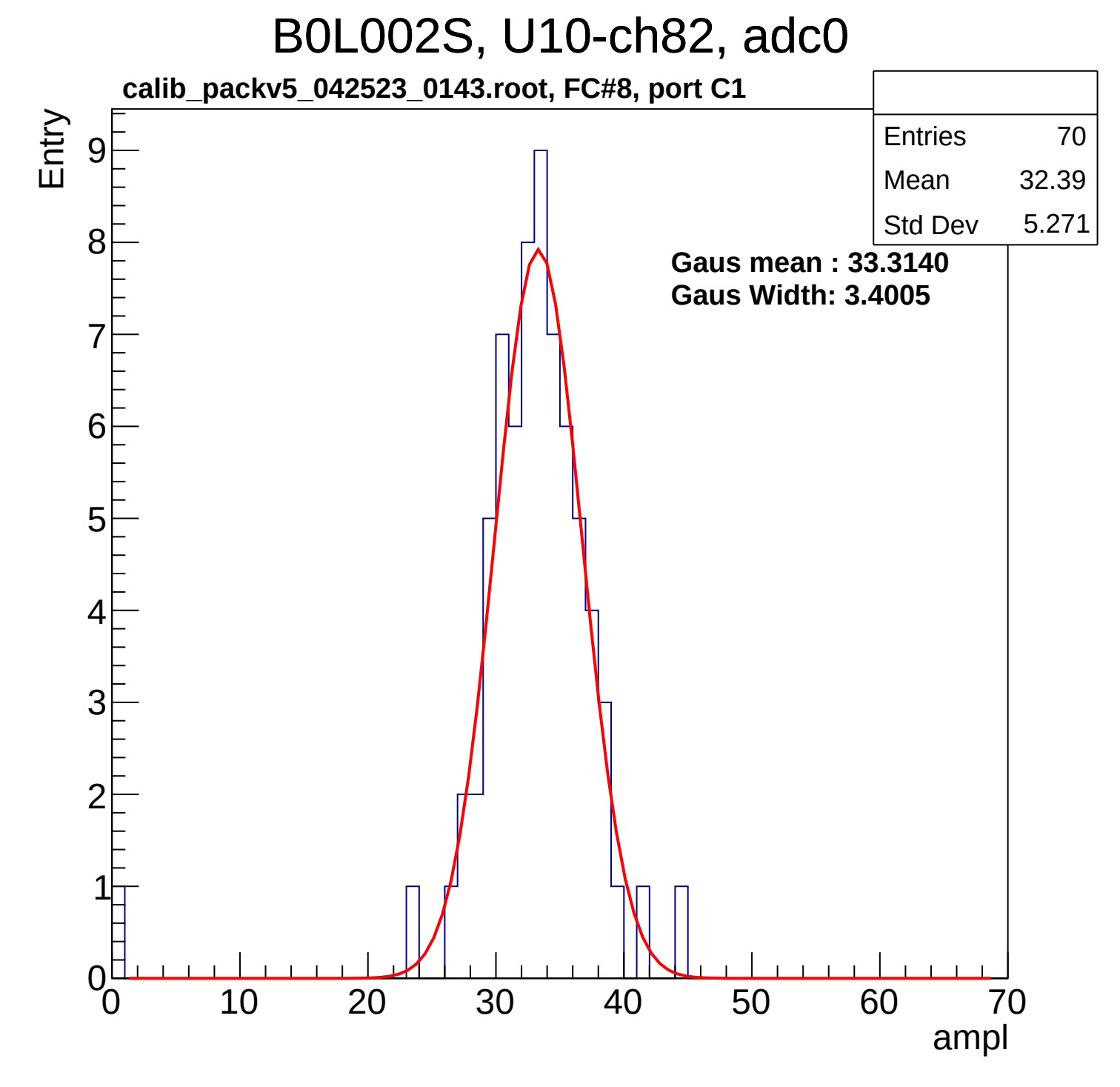

B0L002S, U10-ch81, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 52.22
Std Dev 2.915
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

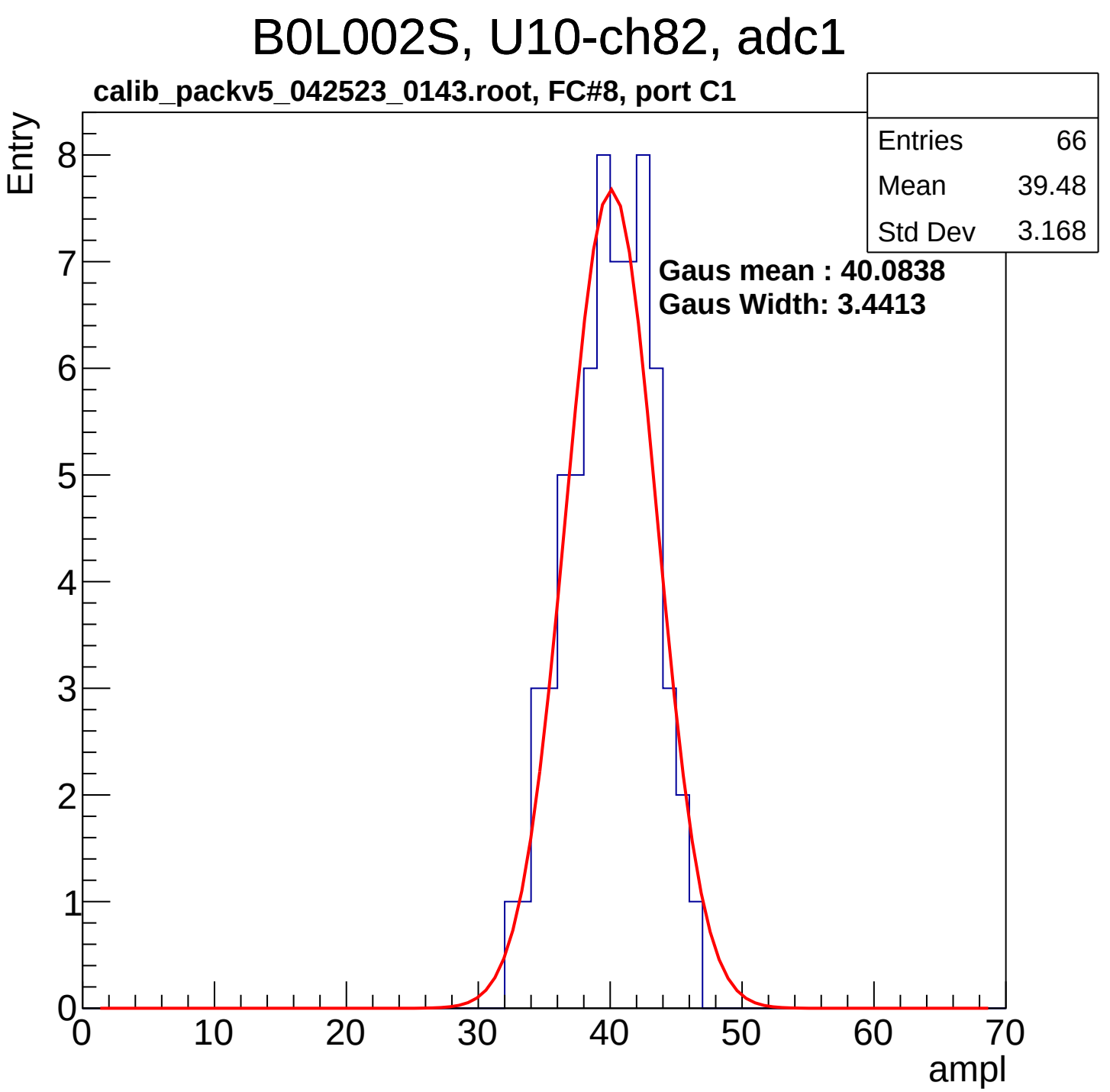

B0L002S, U10-ch81, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 58.9
Std Dev 2.678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

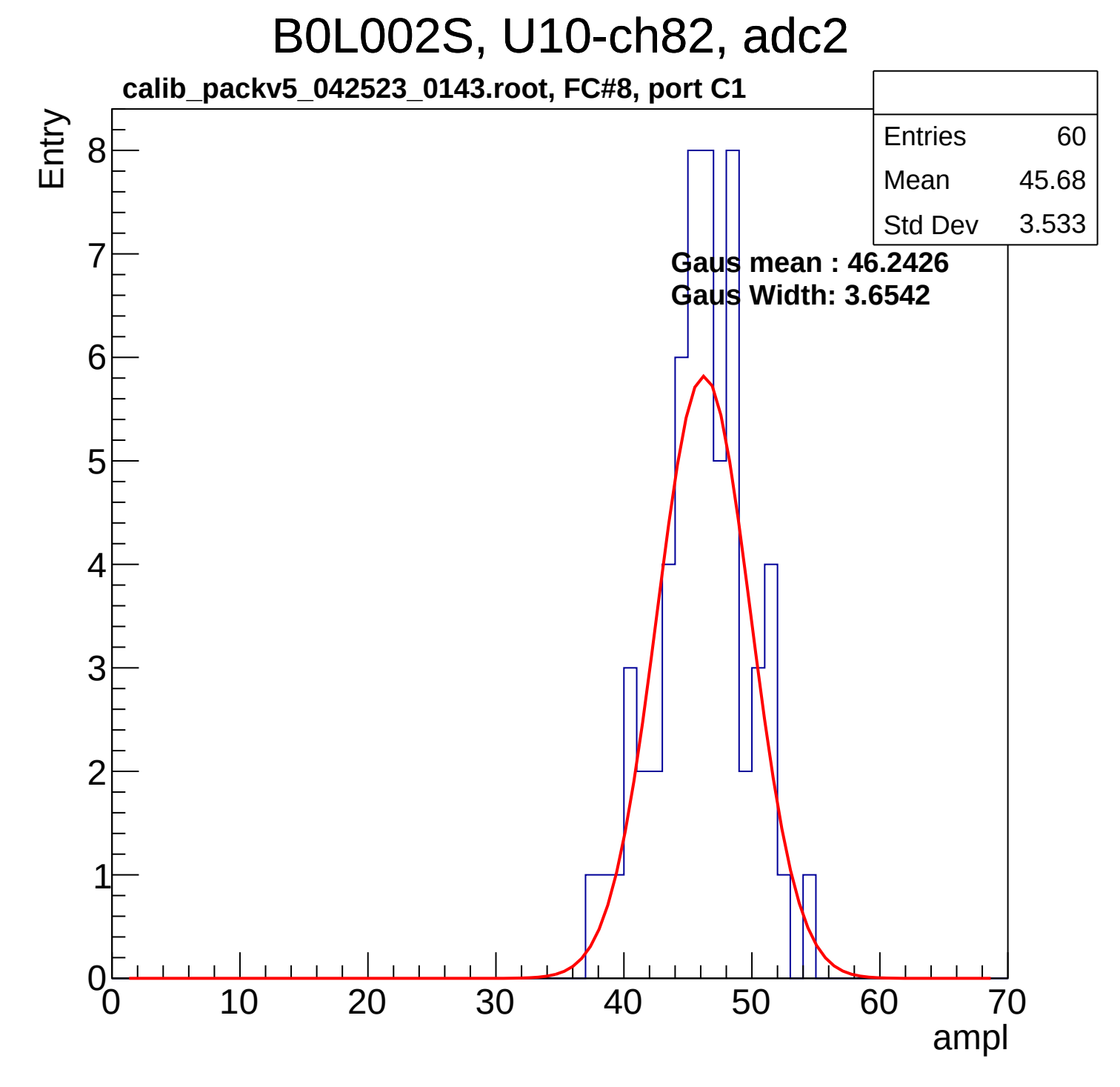

B0L002S, U10-ch81, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 15
Mean 61.53
Std Dev 1.454
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

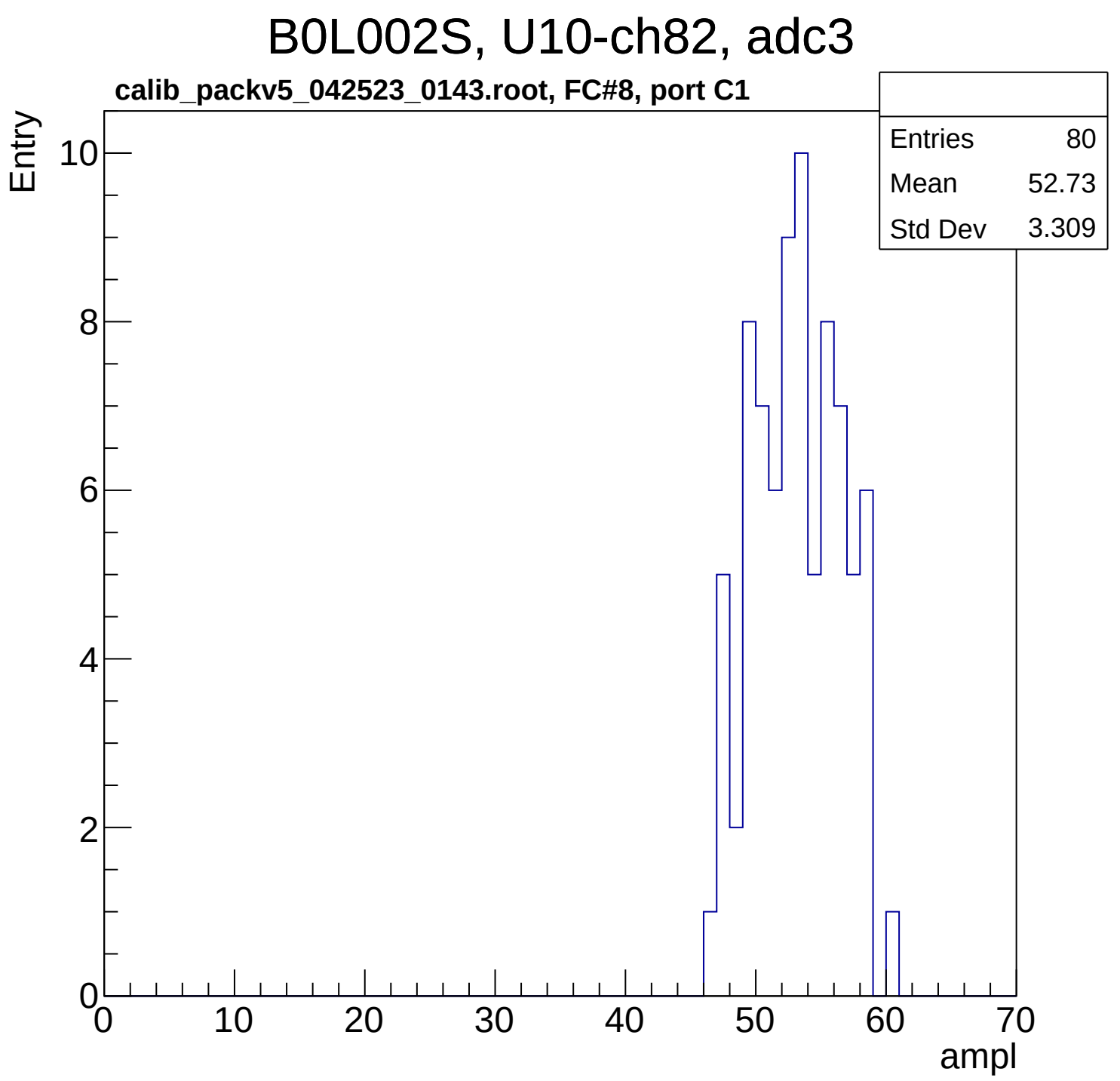

B0L002S, U10-ch81, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

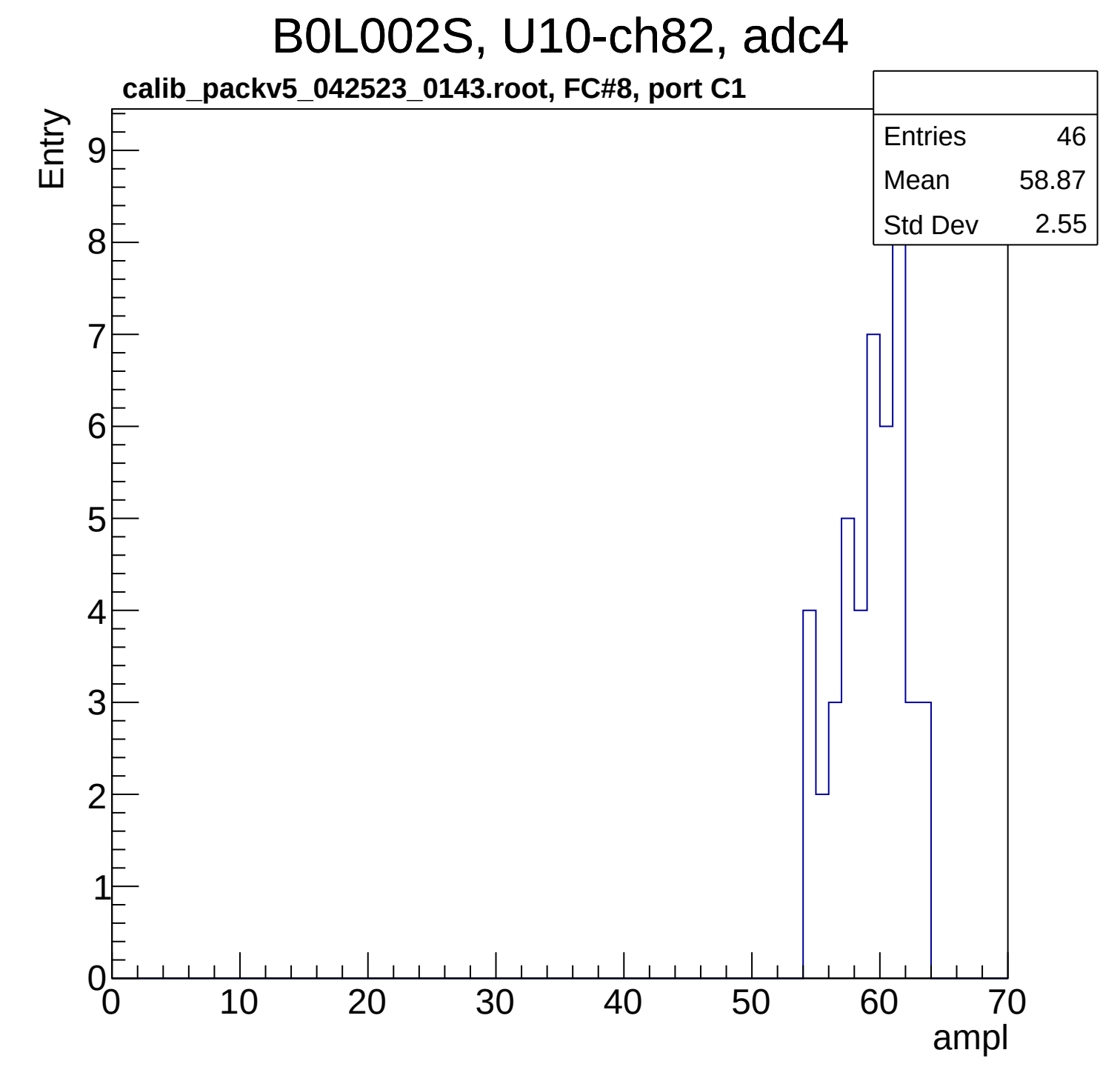

B0L002S, U10-ch81, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

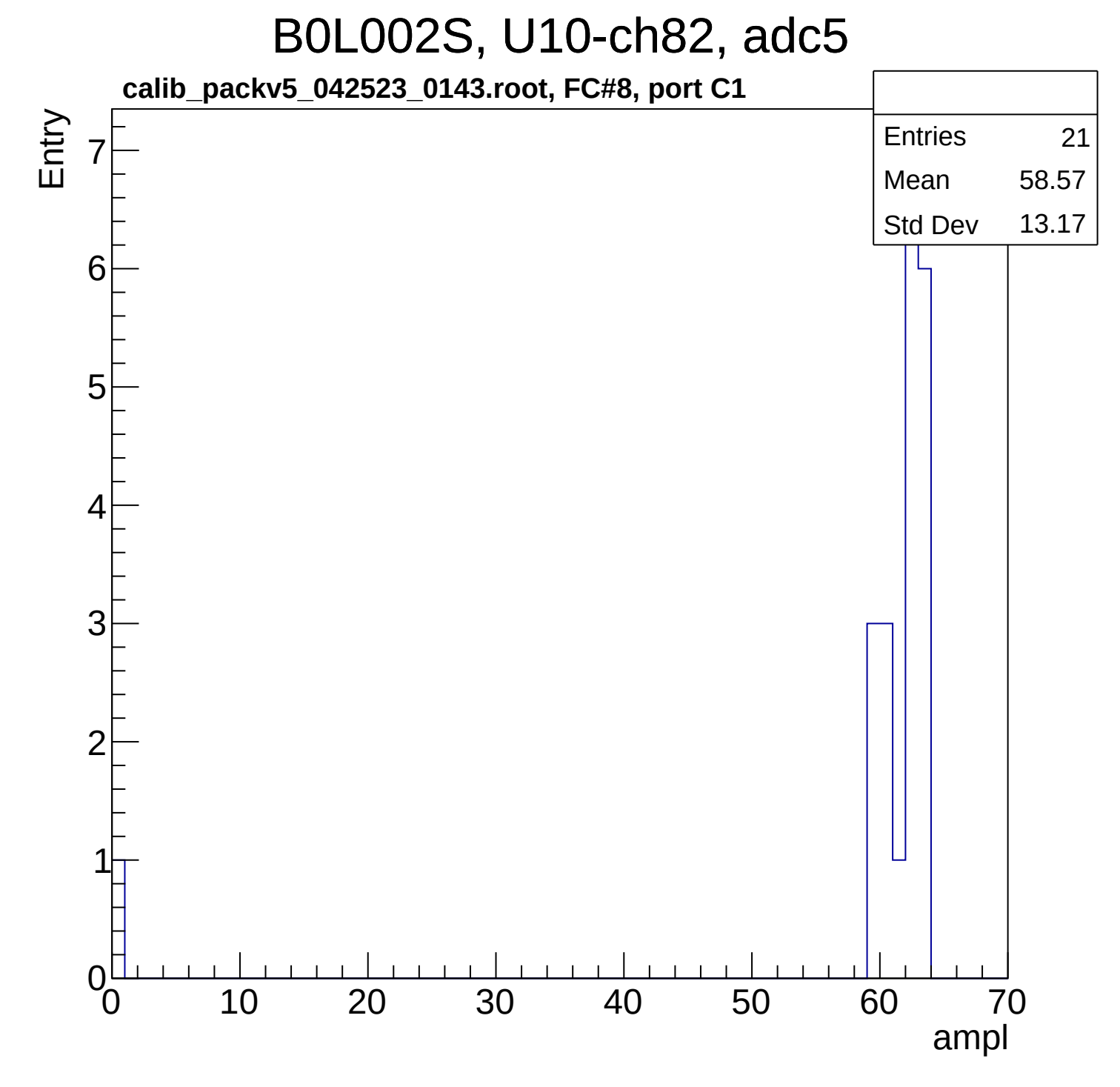

B0L002S, U10-ch82, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 32.39
Std Dev 5.271
Gaus mean : 33.3140
Gaus Width: 3.4005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

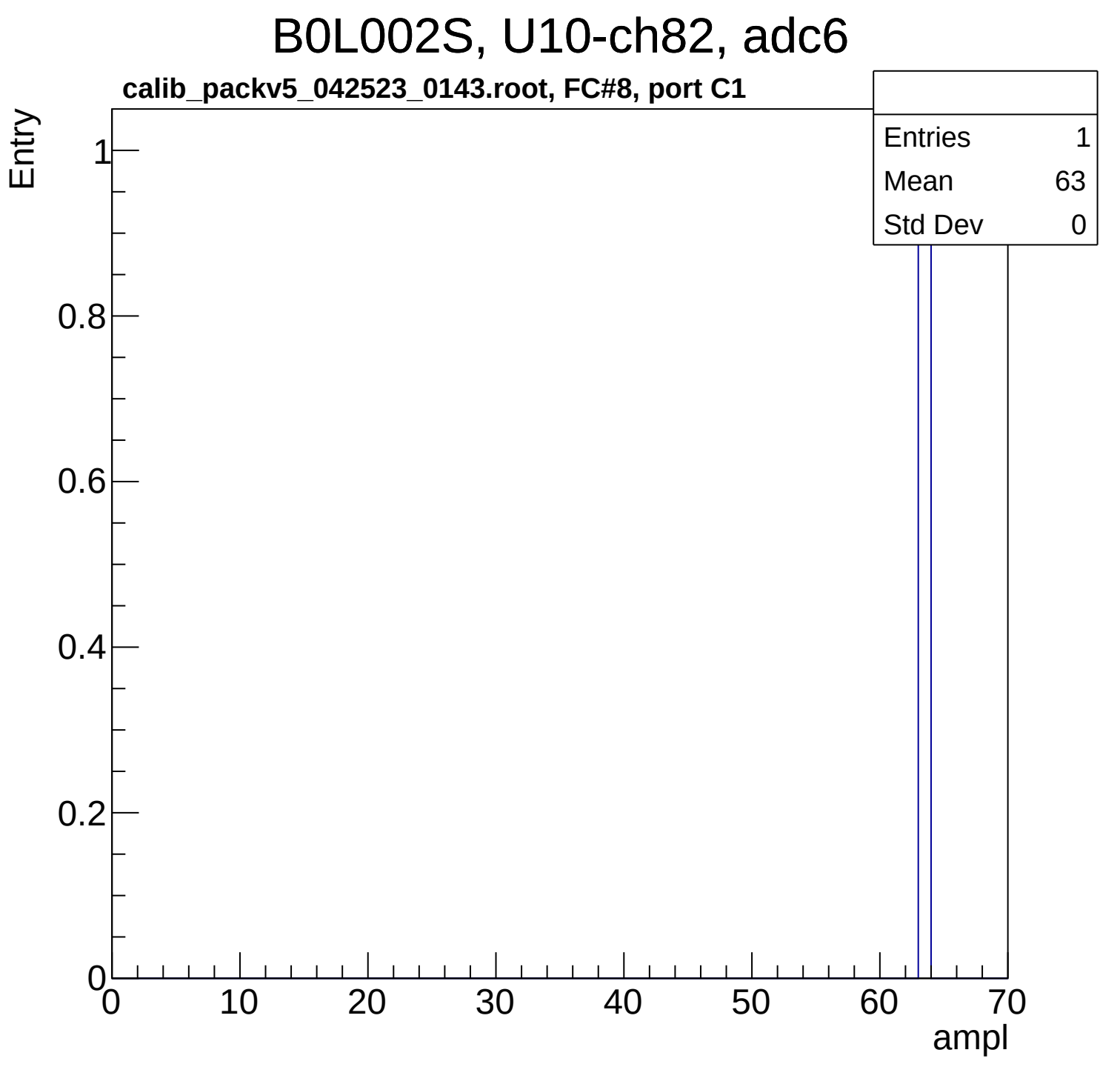

B0L002S, U10-ch82, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 39.48
Std Dev 3.168
Gaus mean : 40.0838
Gaus Width: 3.4413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

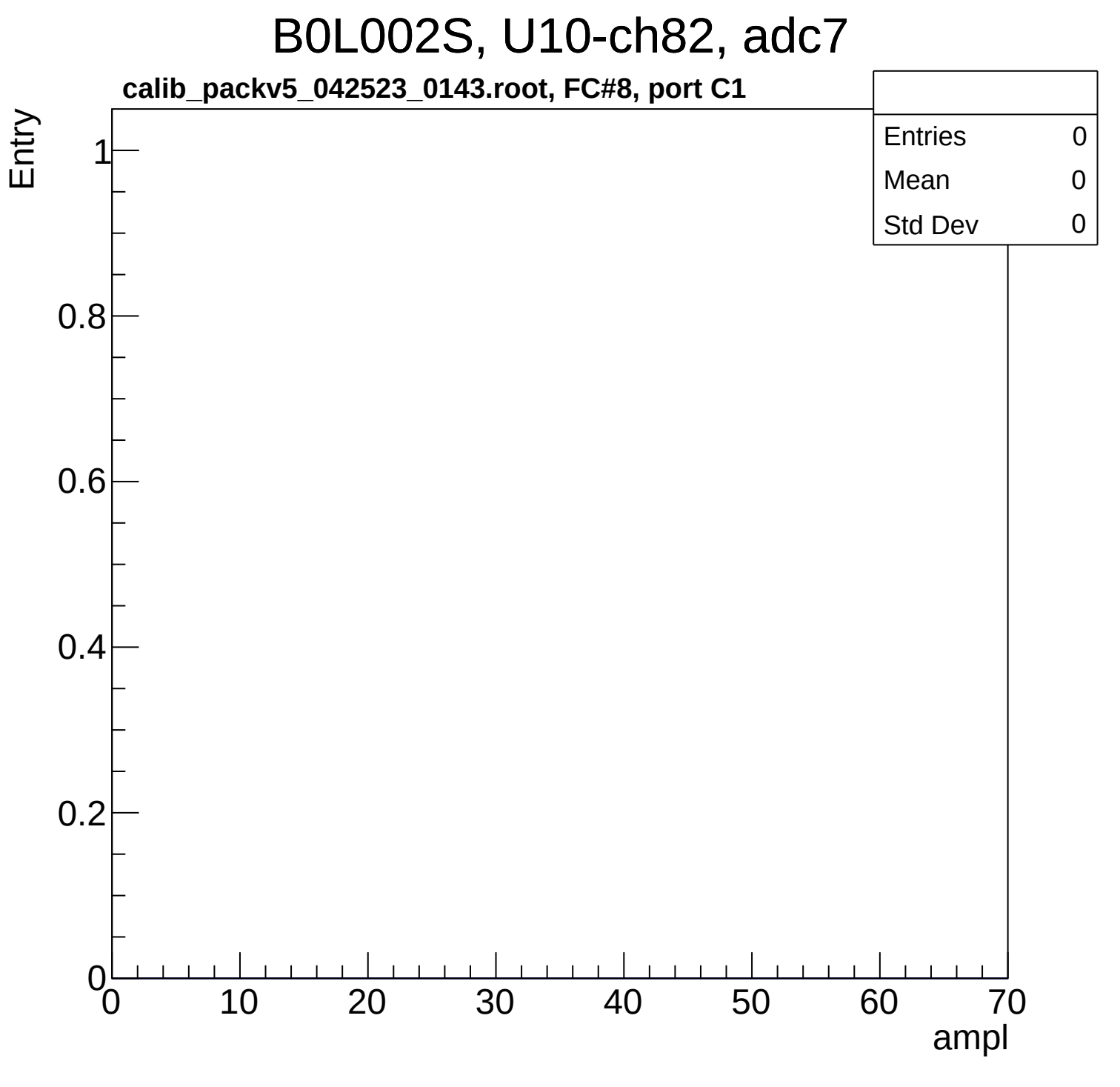

B0L002S, U10-ch82, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 45.68
Std Dev 3.533
Gaus mean : 46.2426
Gaus Width: 3.6542
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

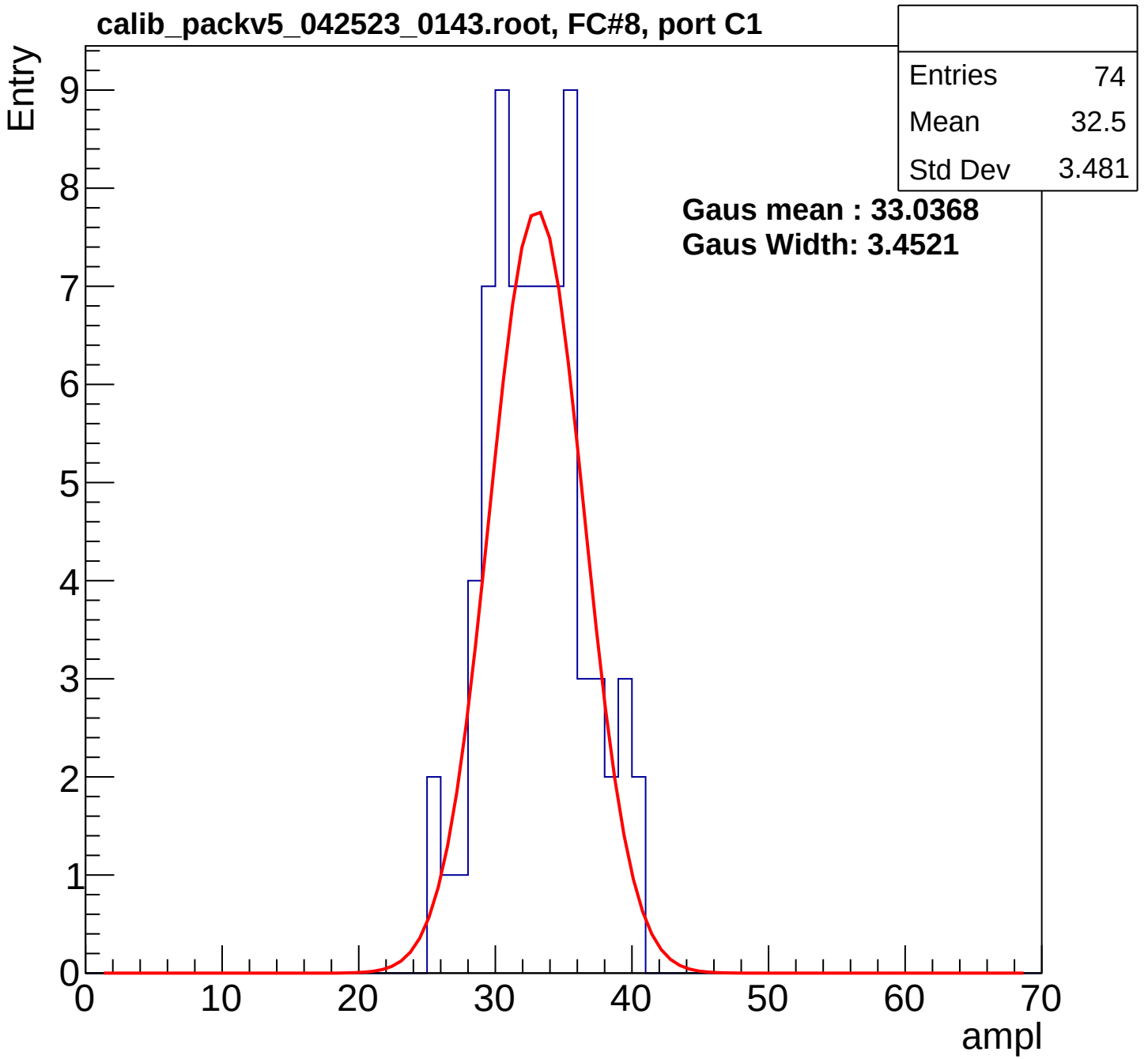

B0L002S, U10-ch82, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 52.73
Std Dev 3.309
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

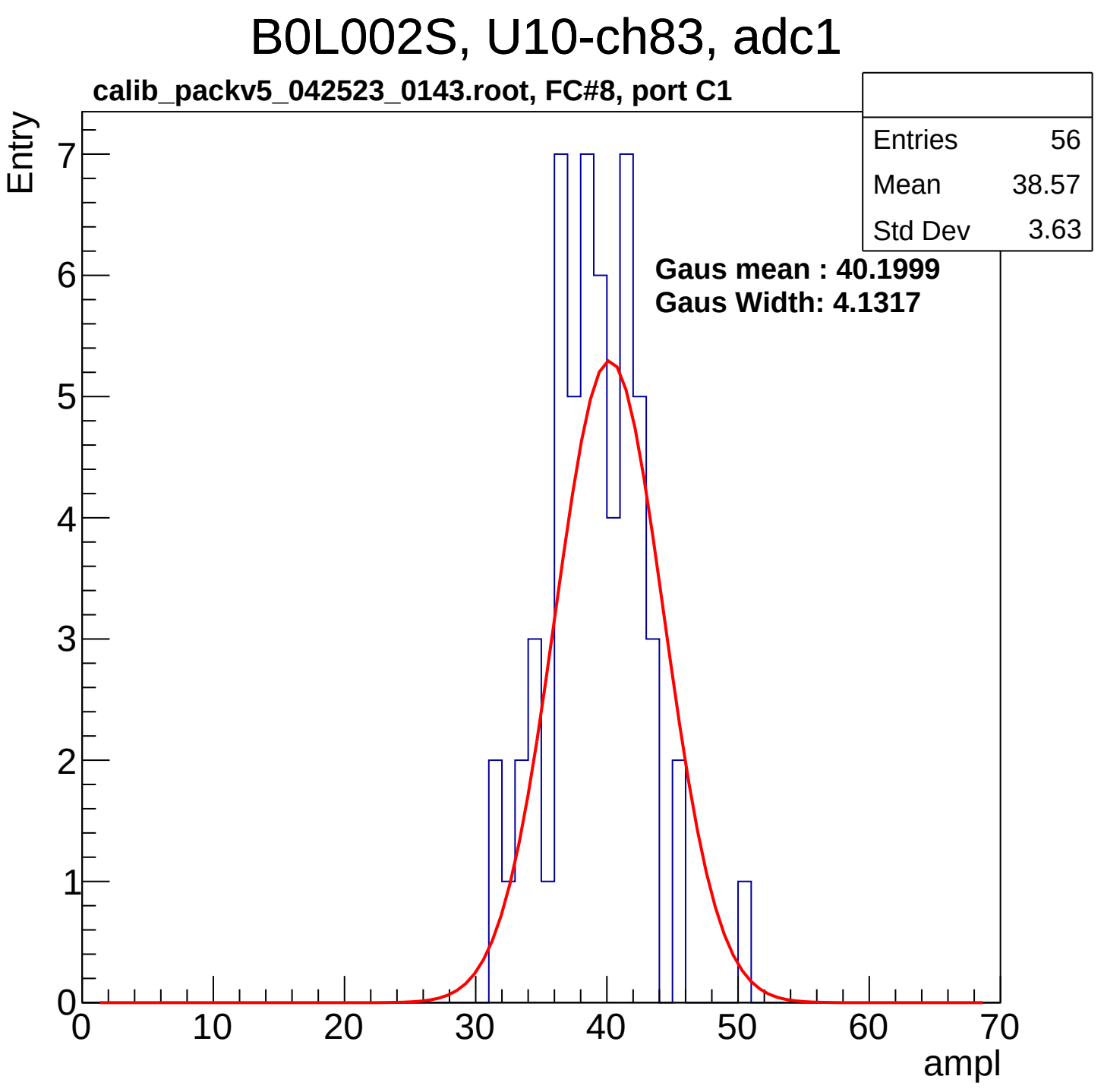

B0L002S, U10-ch82, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 58.87
Std Dev 2.55
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

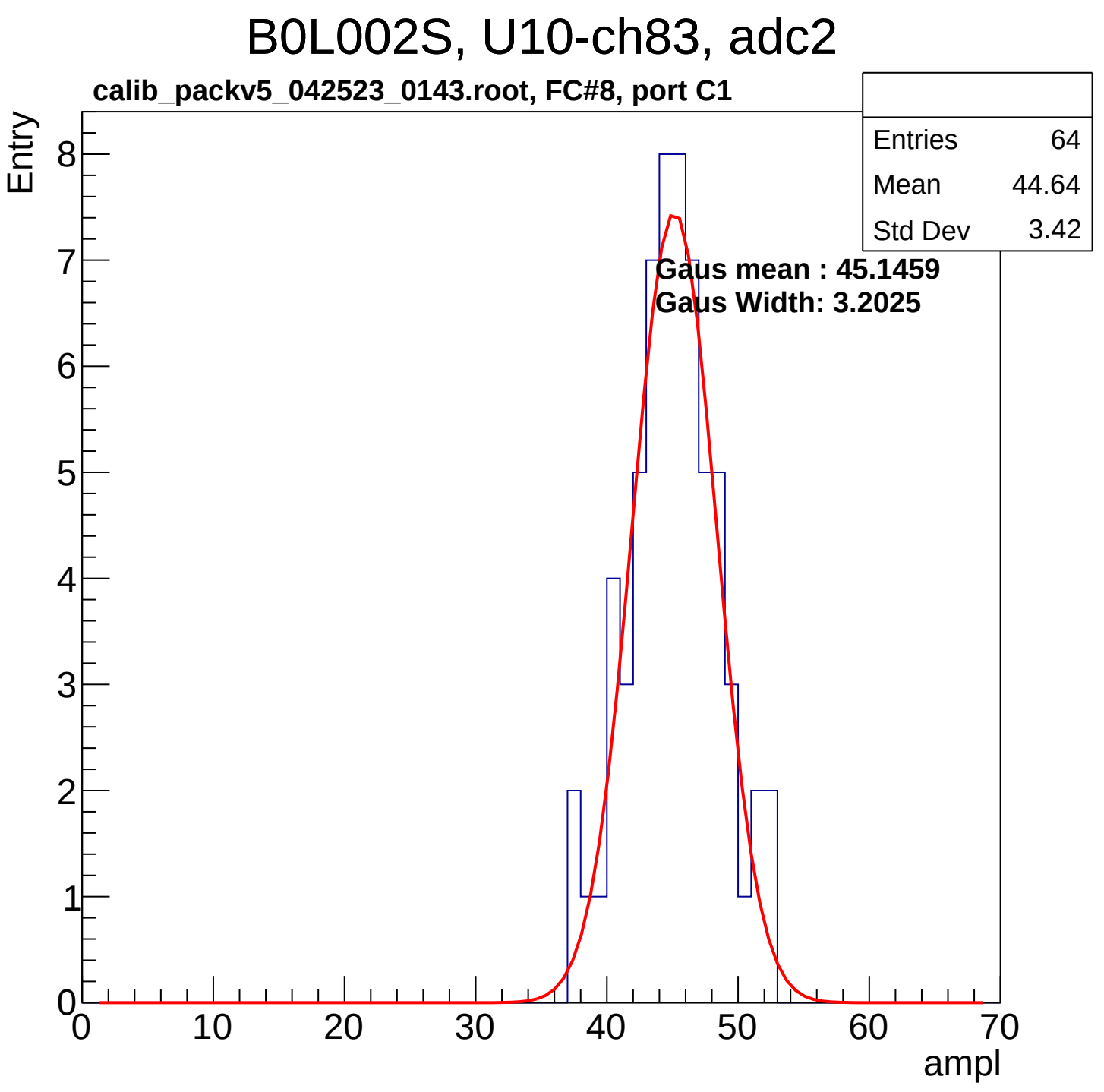

B0L002S, U10-ch82, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.57
Std Dev 13.17
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

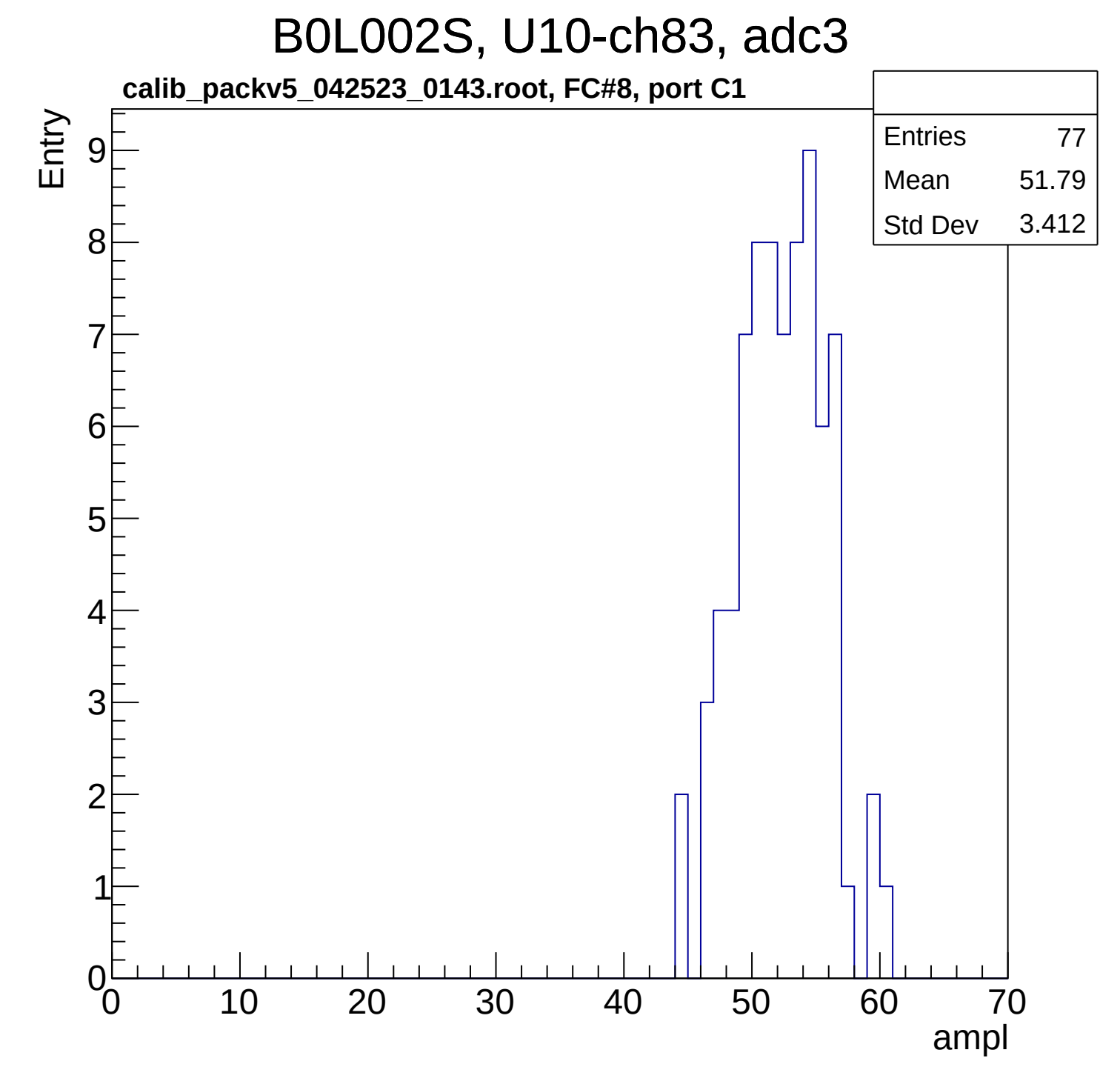

B0L002S, U10-ch82, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

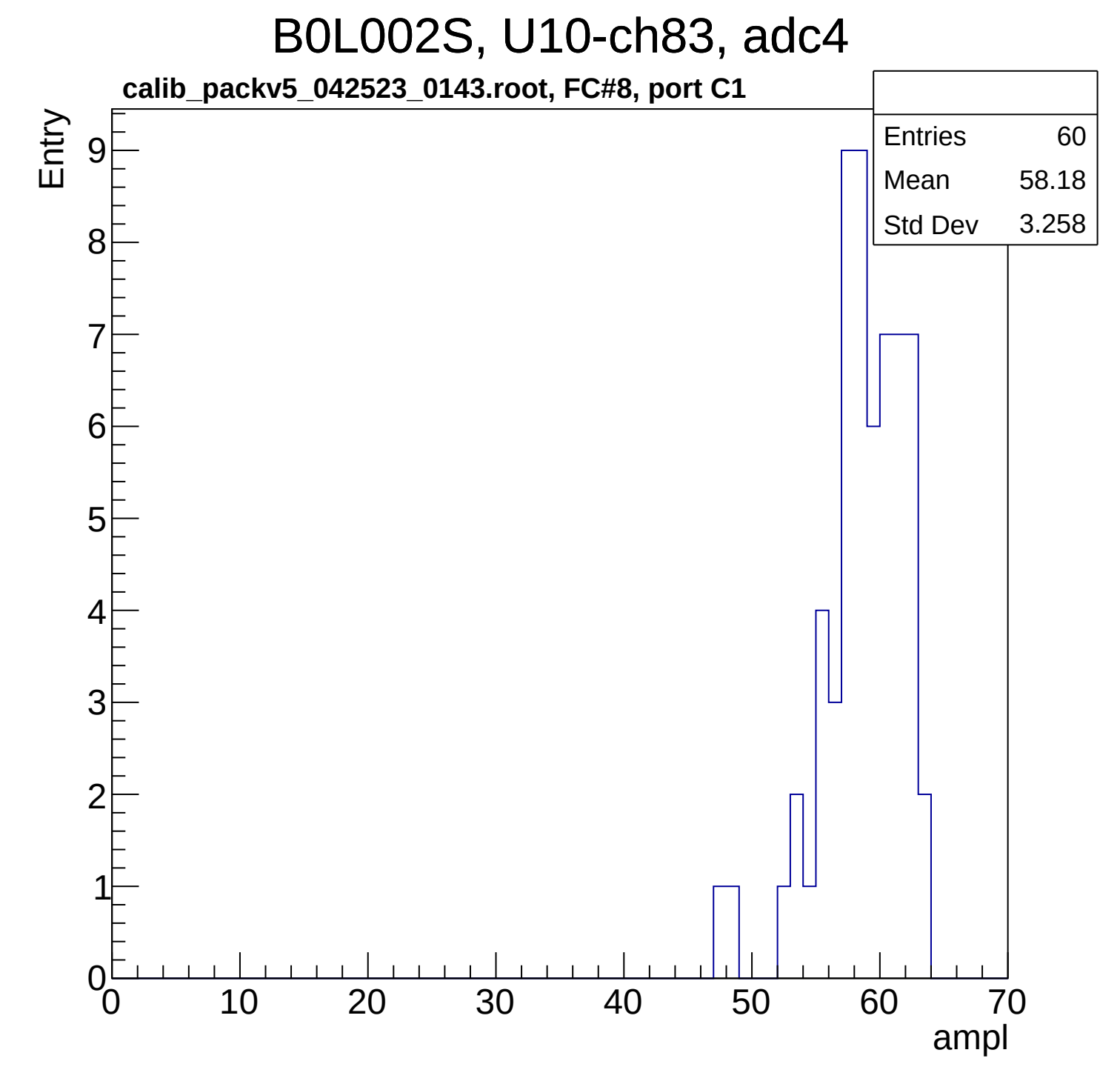

B0L002S, U10-ch82, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

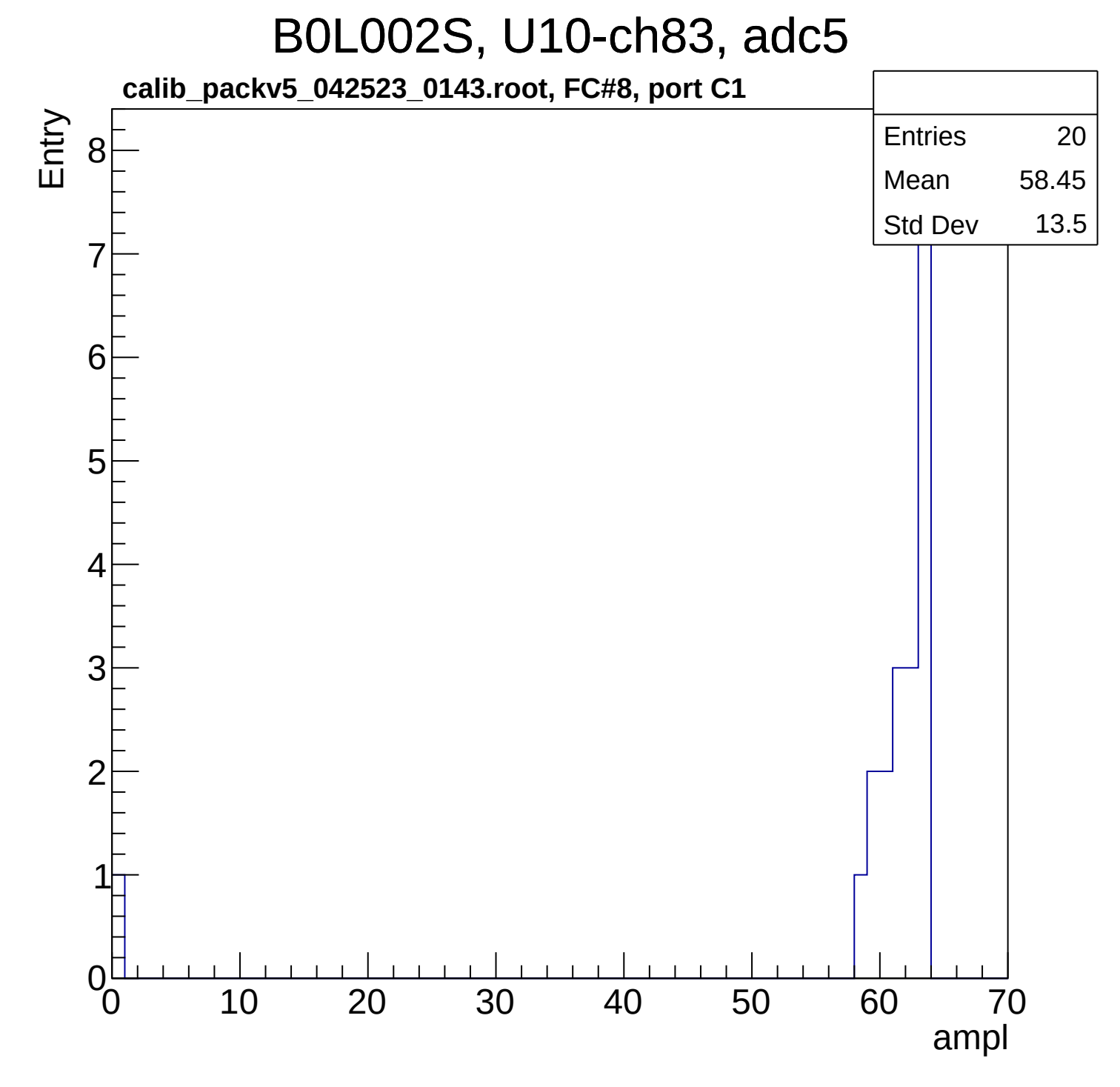

B0L002S, U10-ch83, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 32.5
Std Dev 3.481
Gaus mean : 33.0368
Gaus Width: 3.4521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

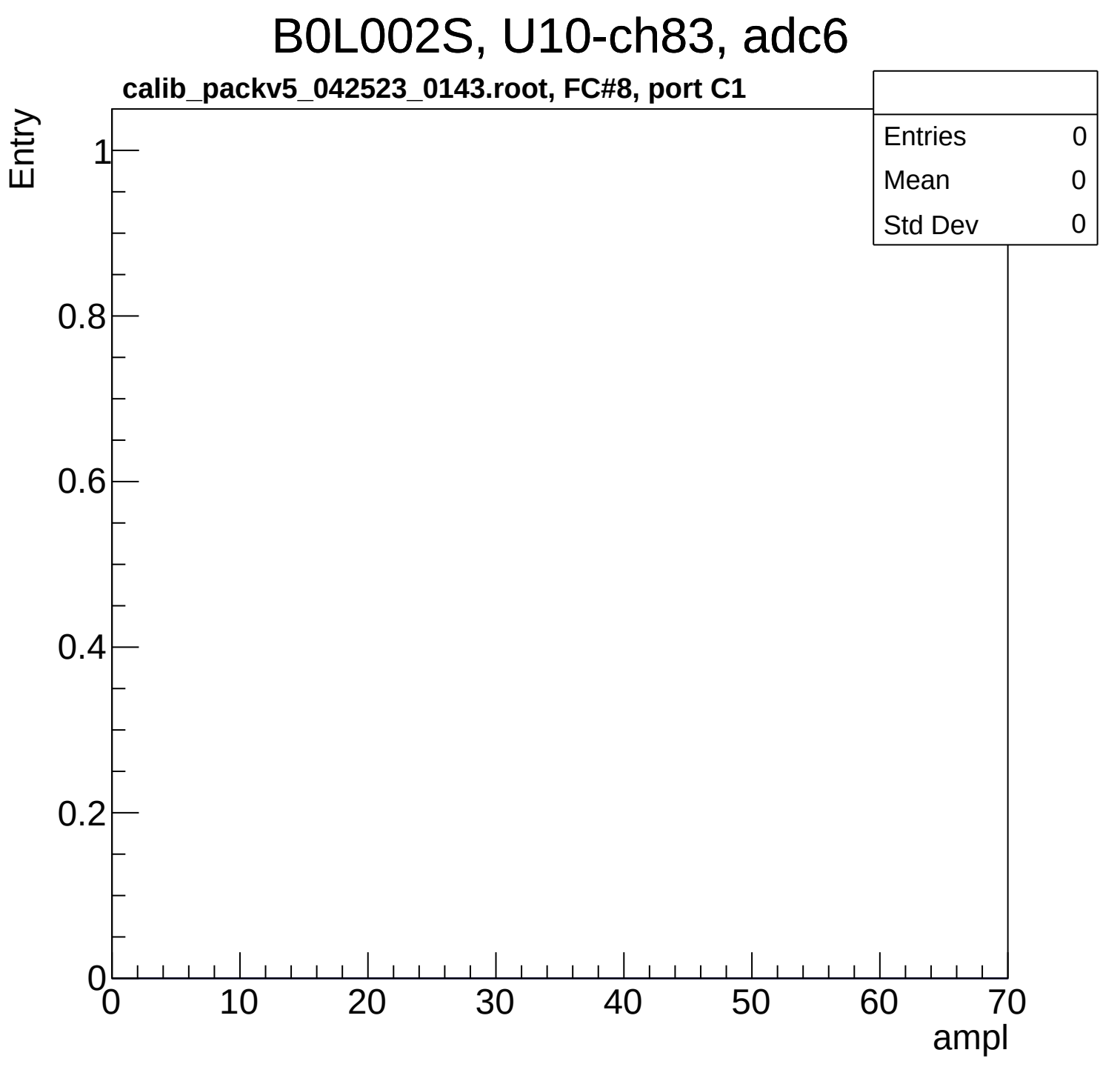

B0L002S, U10-ch83, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 38.57
Std Dev 3.63
Gaus mean : 40.1999
Gaus Width: 4.1317
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

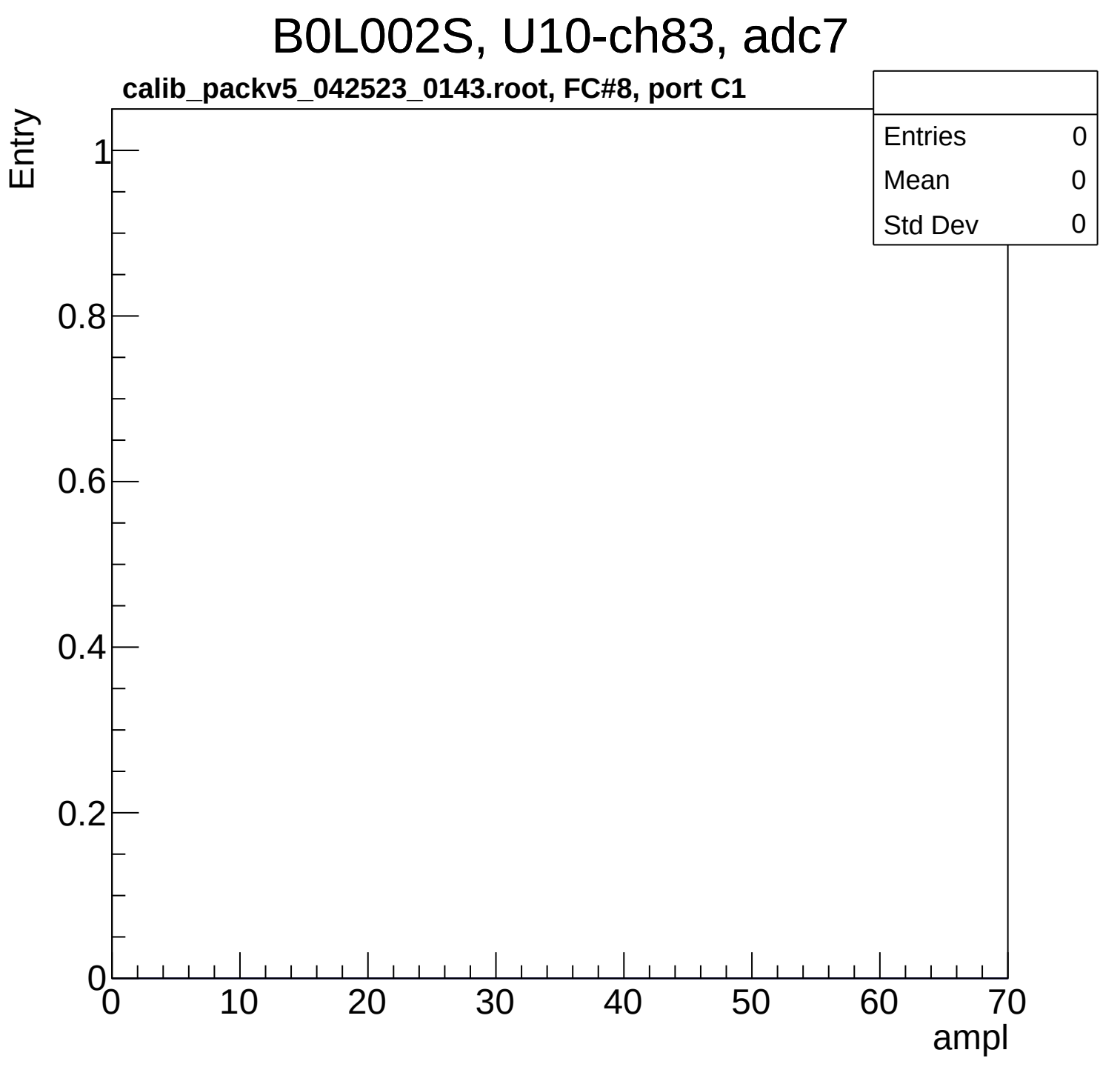

B0L002S, U10-ch83, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 44.64
Std Dev 3.42
Gaus mean : 45.1459
Gaus Width: 3.2025
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

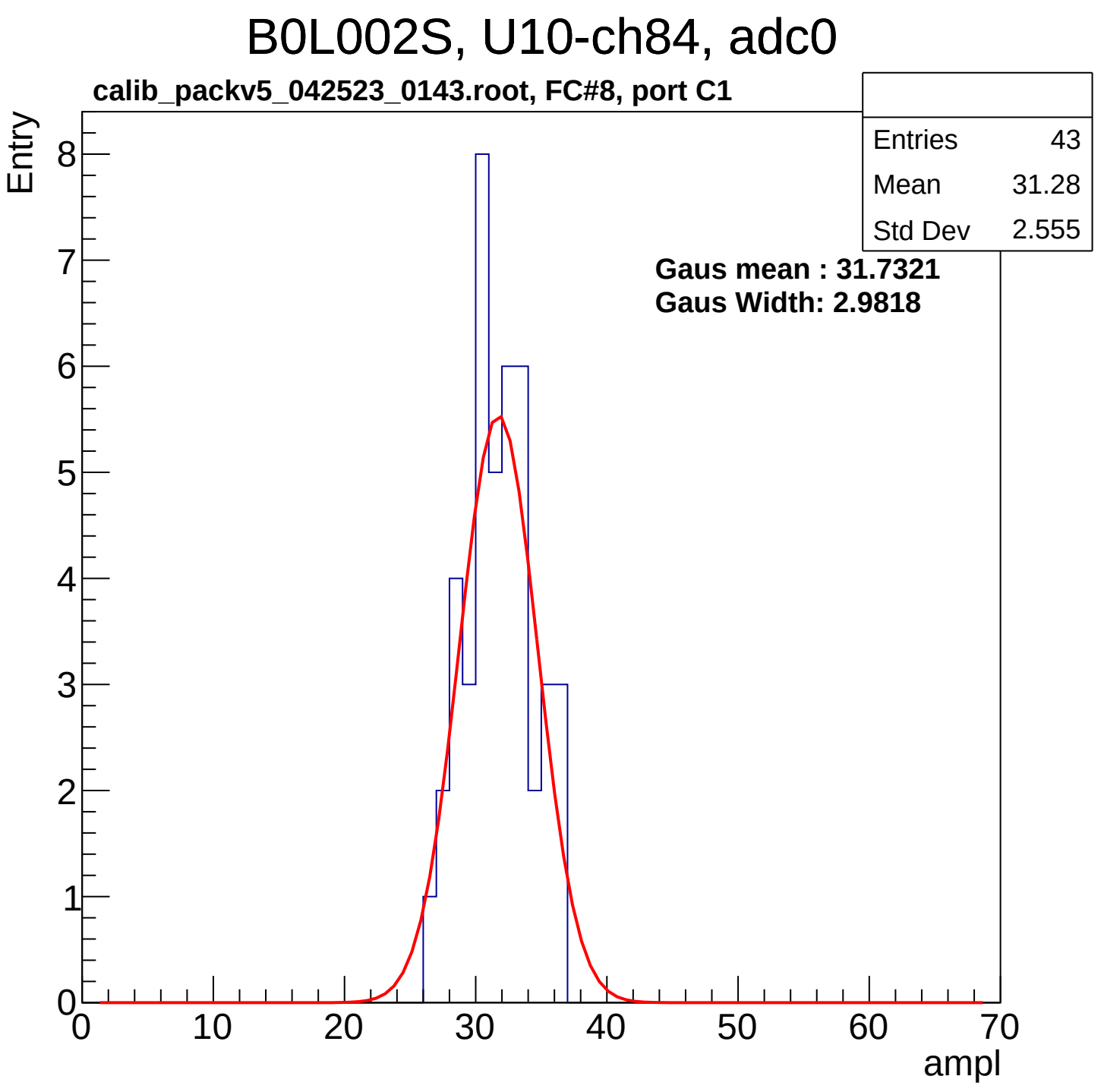

B0L002S, U10-ch83, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 51.79
Std Dev 3.412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

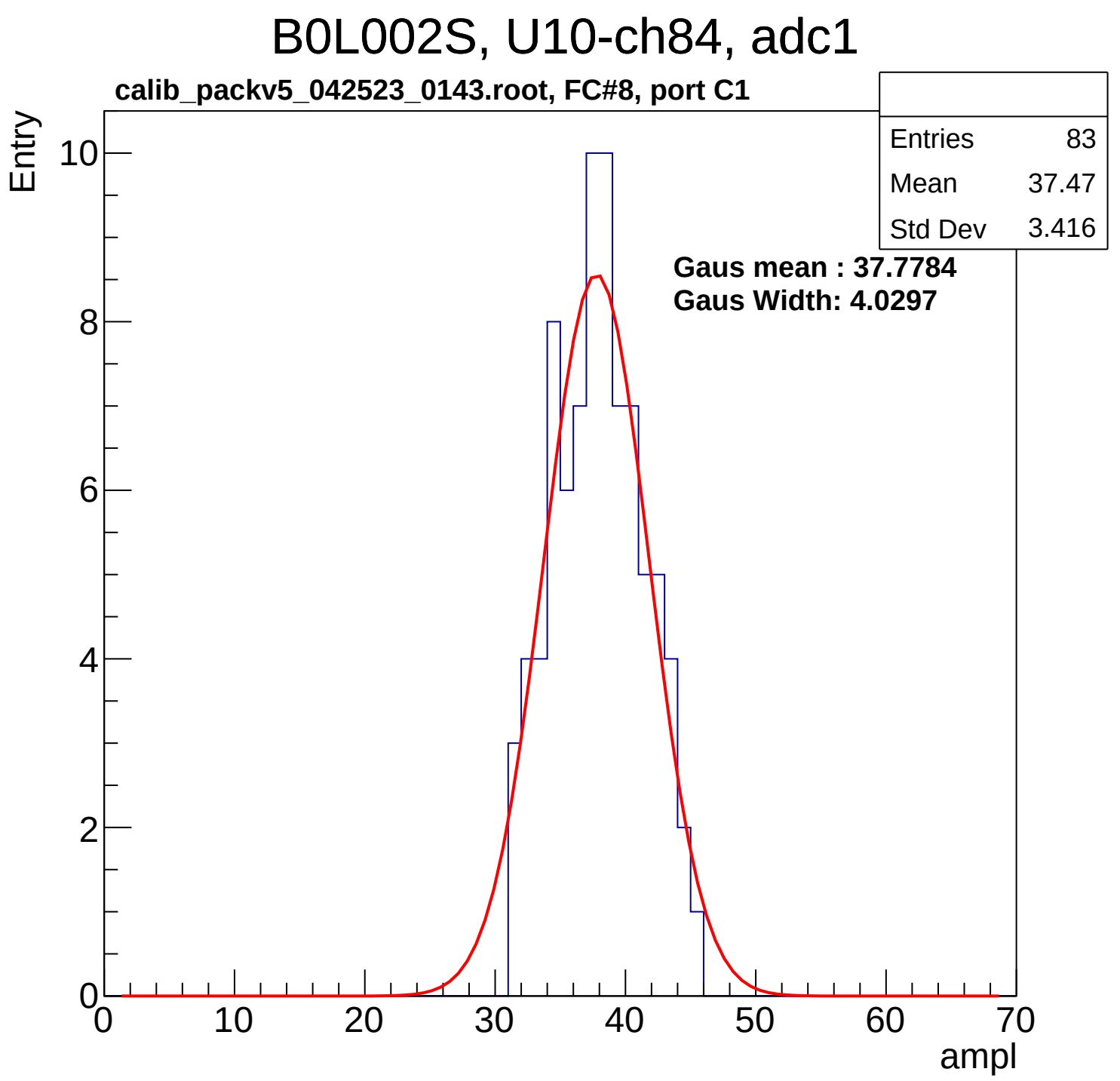

B0L002S, U10-ch83, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 58.18
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

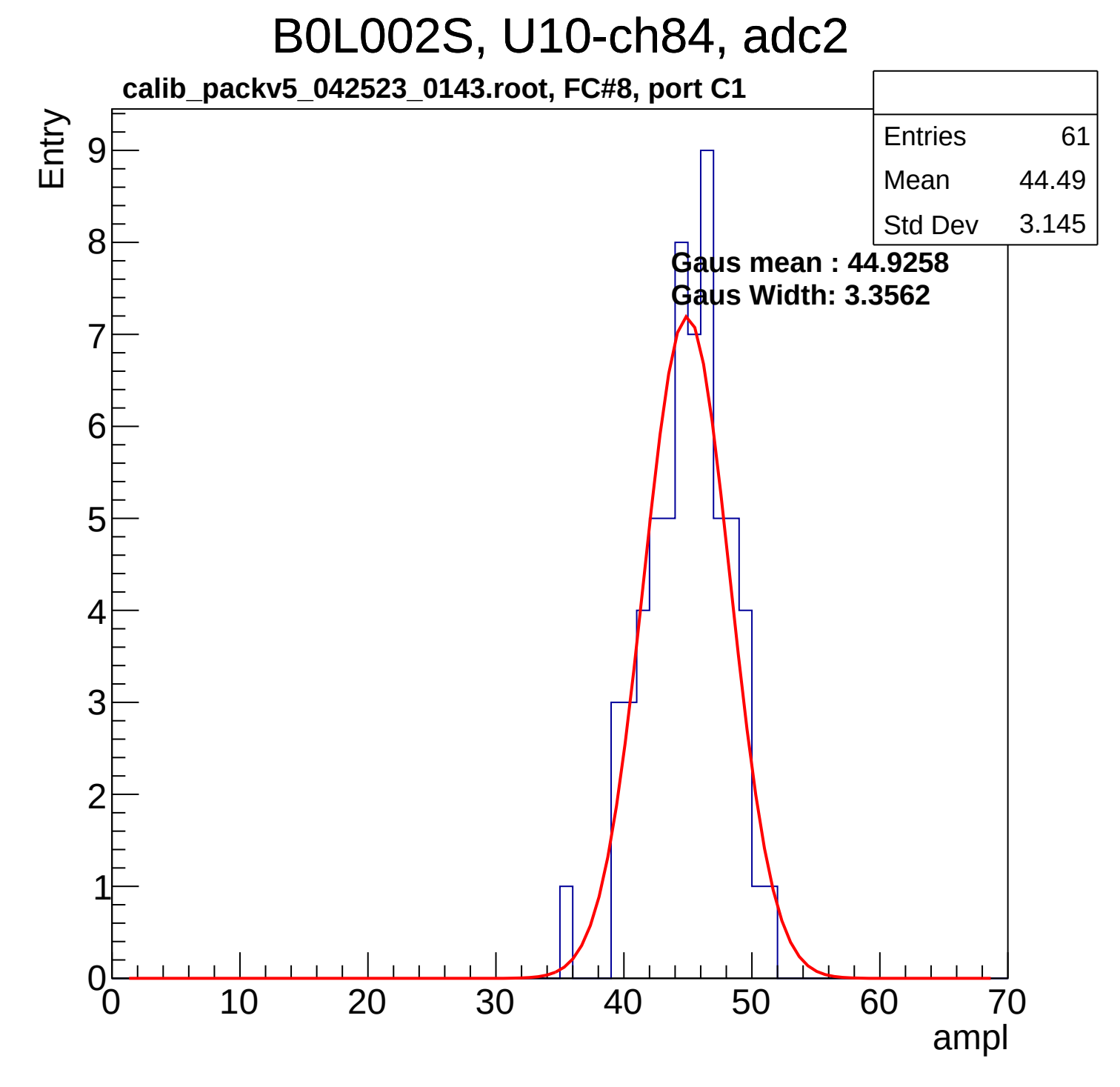

B0L002S, U10-ch83, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 20
Mean 58.45
Std Dev 13.5
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

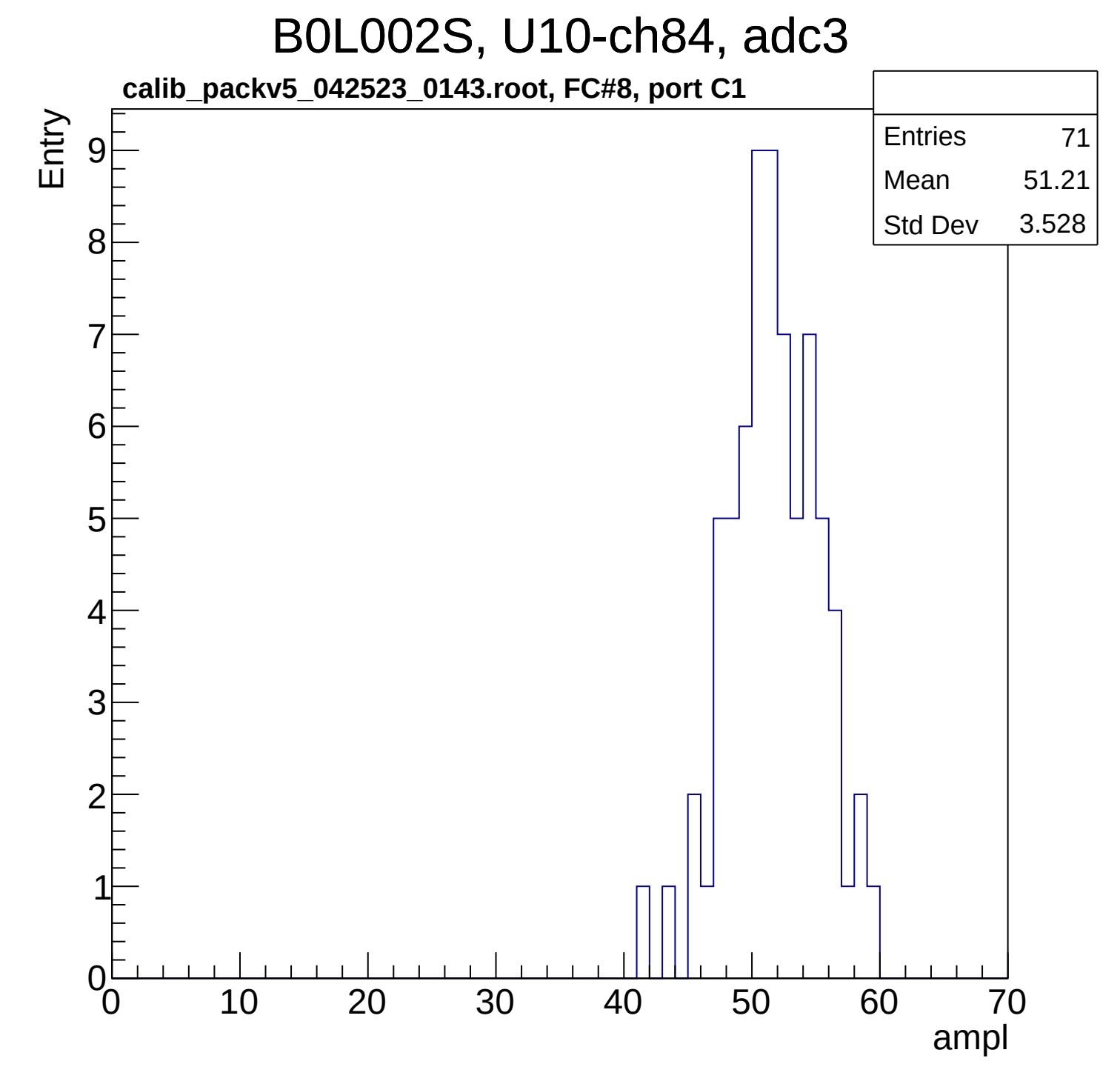

B0L002S, U10-ch83, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

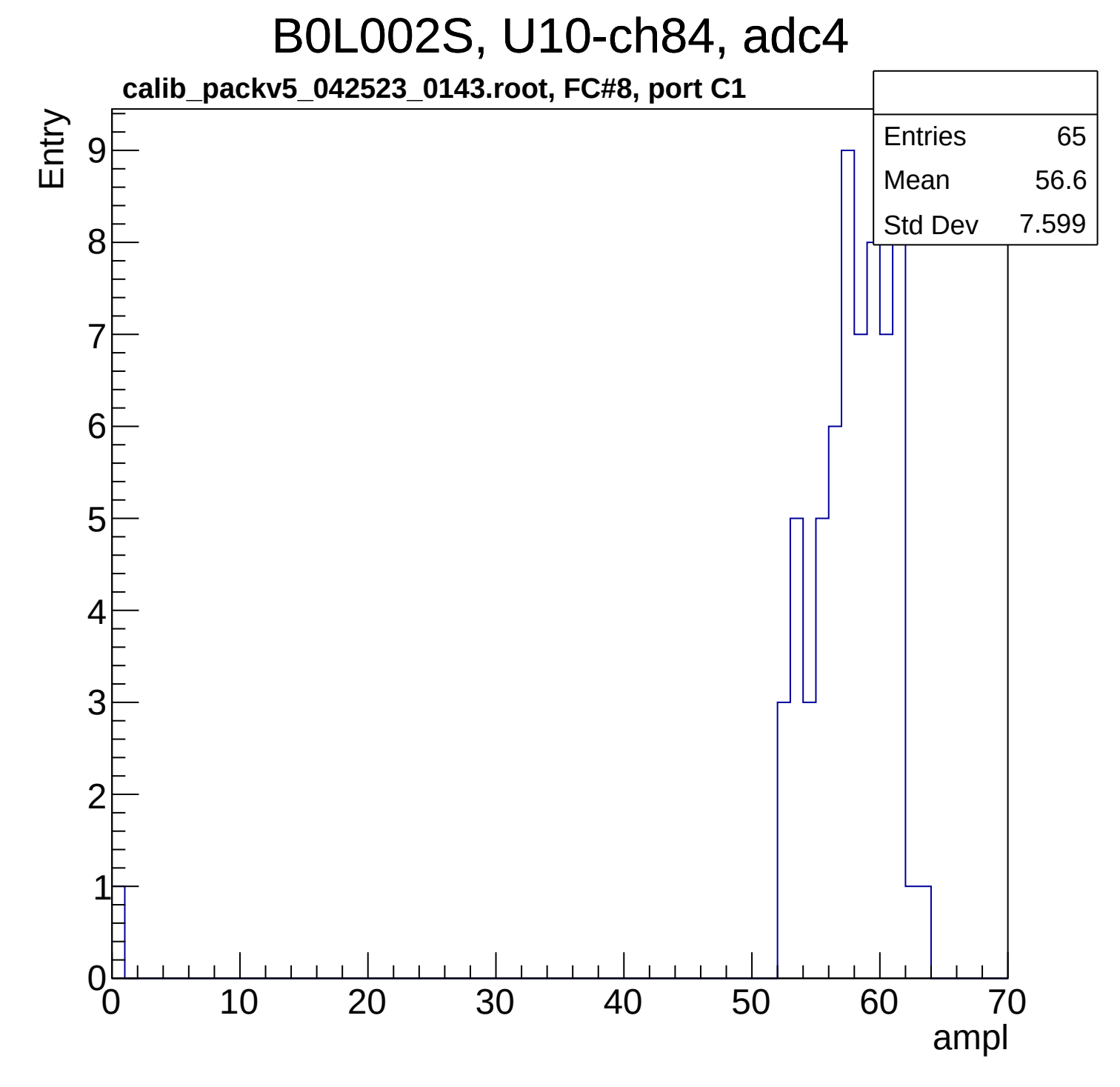

B0L002S, U10-ch83, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

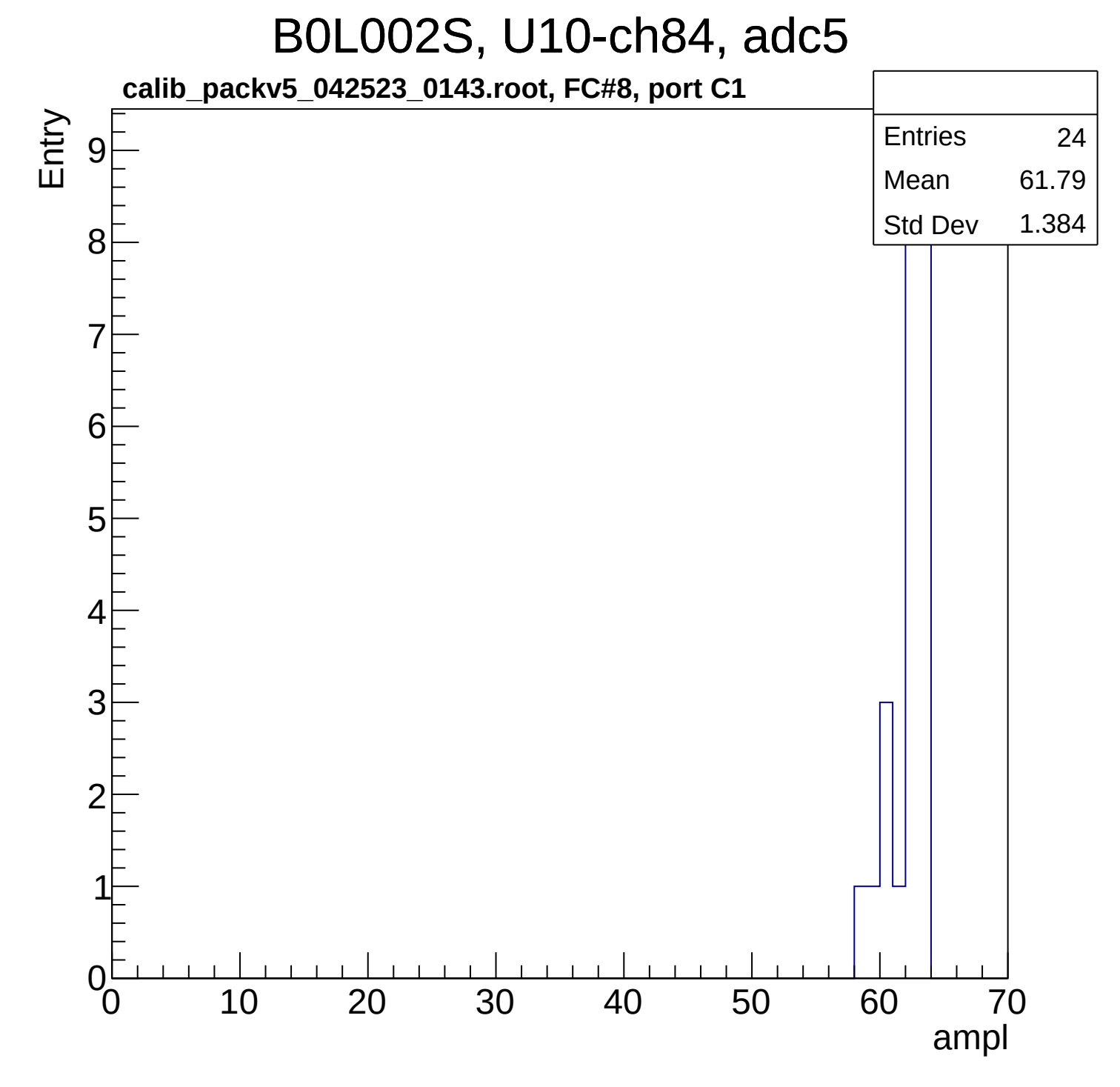

B0L002S, U10-ch84, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 31.28
Std Dev 2.555
Gaus mean : 31.7321
Gaus Width: 2.9818
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

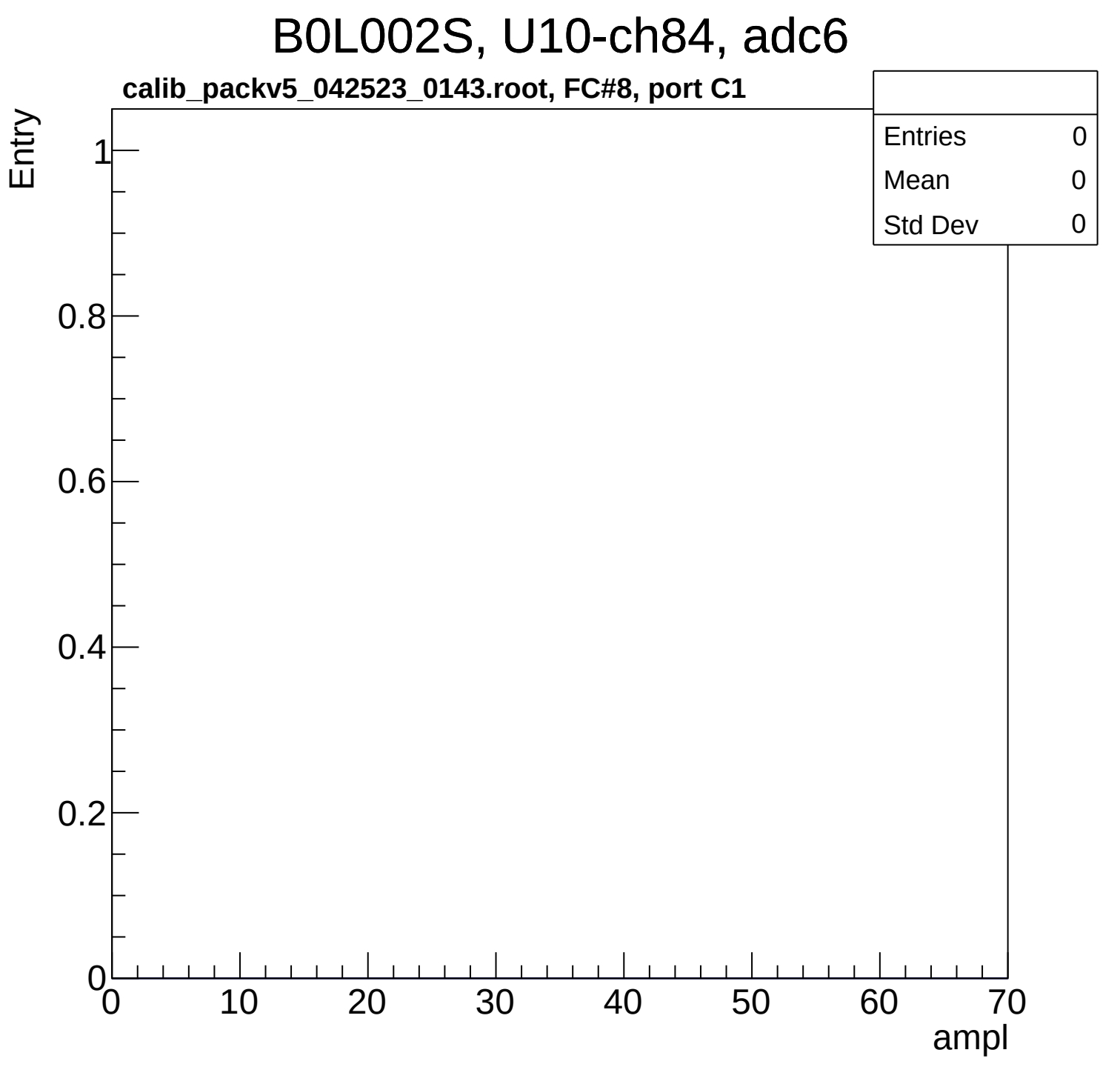

B0L002S, U10-ch84, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 37.47
Std Dev 3.416
Gaus mean : 37.7784
Gaus Width: 4.0297
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

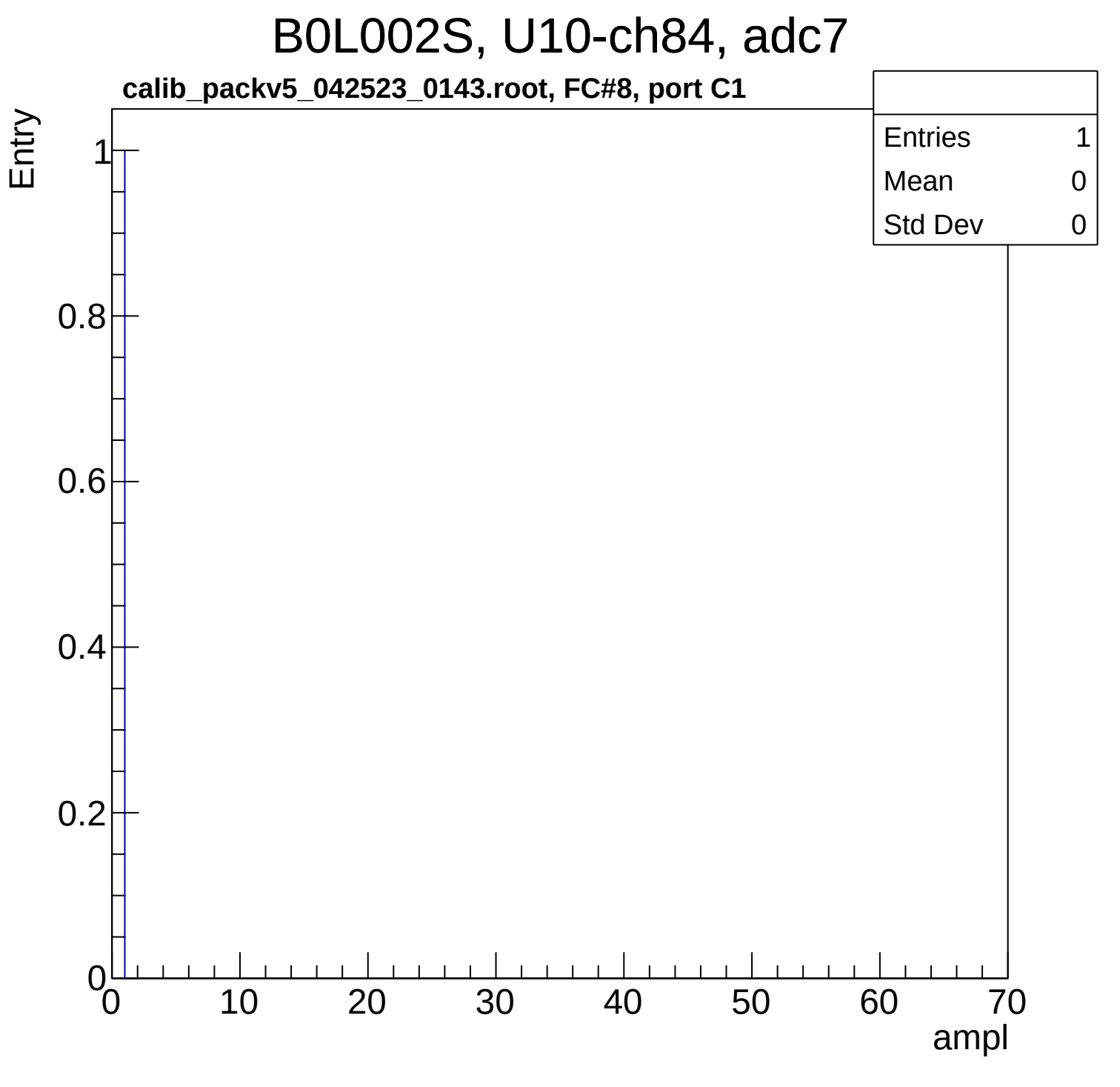

B0L002S, U10-ch84, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.49
Std Dev 3.145
Gaus mean : 44.9258
Gaus Width: 3.3562
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

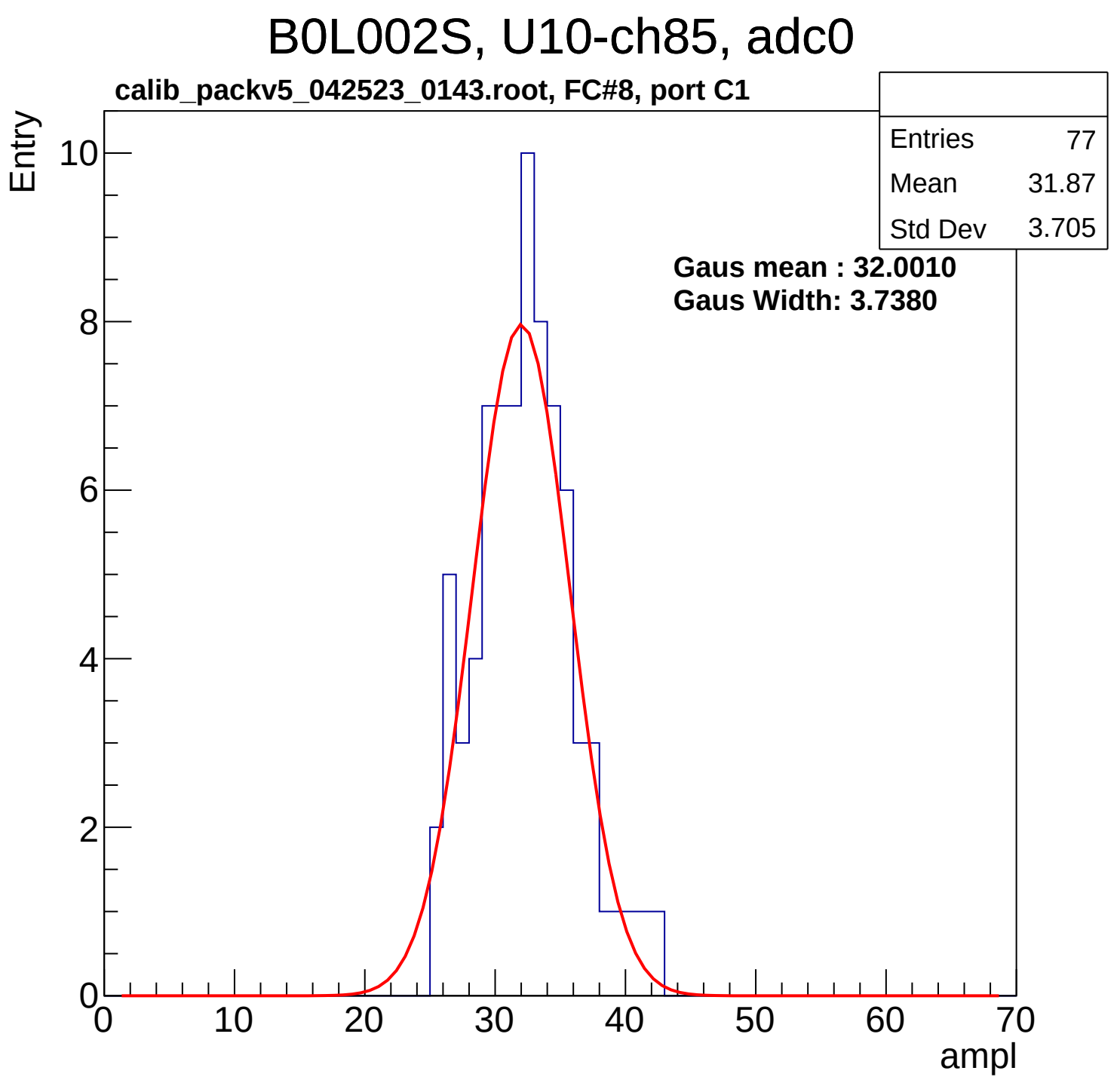

B0L002S, U10-ch84, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 51.21
Std Dev 3.528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

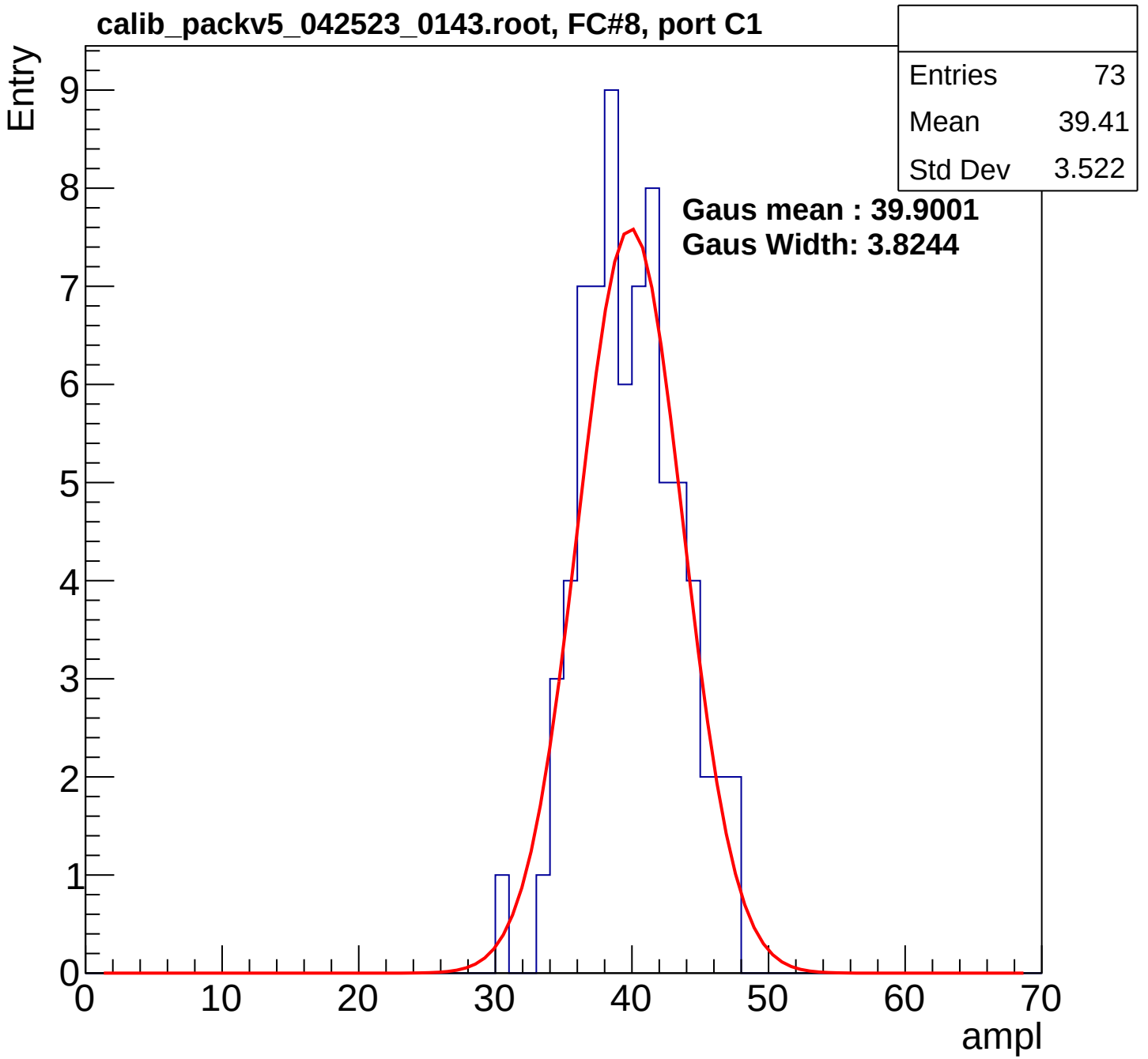

B0L002S, U10-ch84, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 56.6
Std Dev 7.599
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

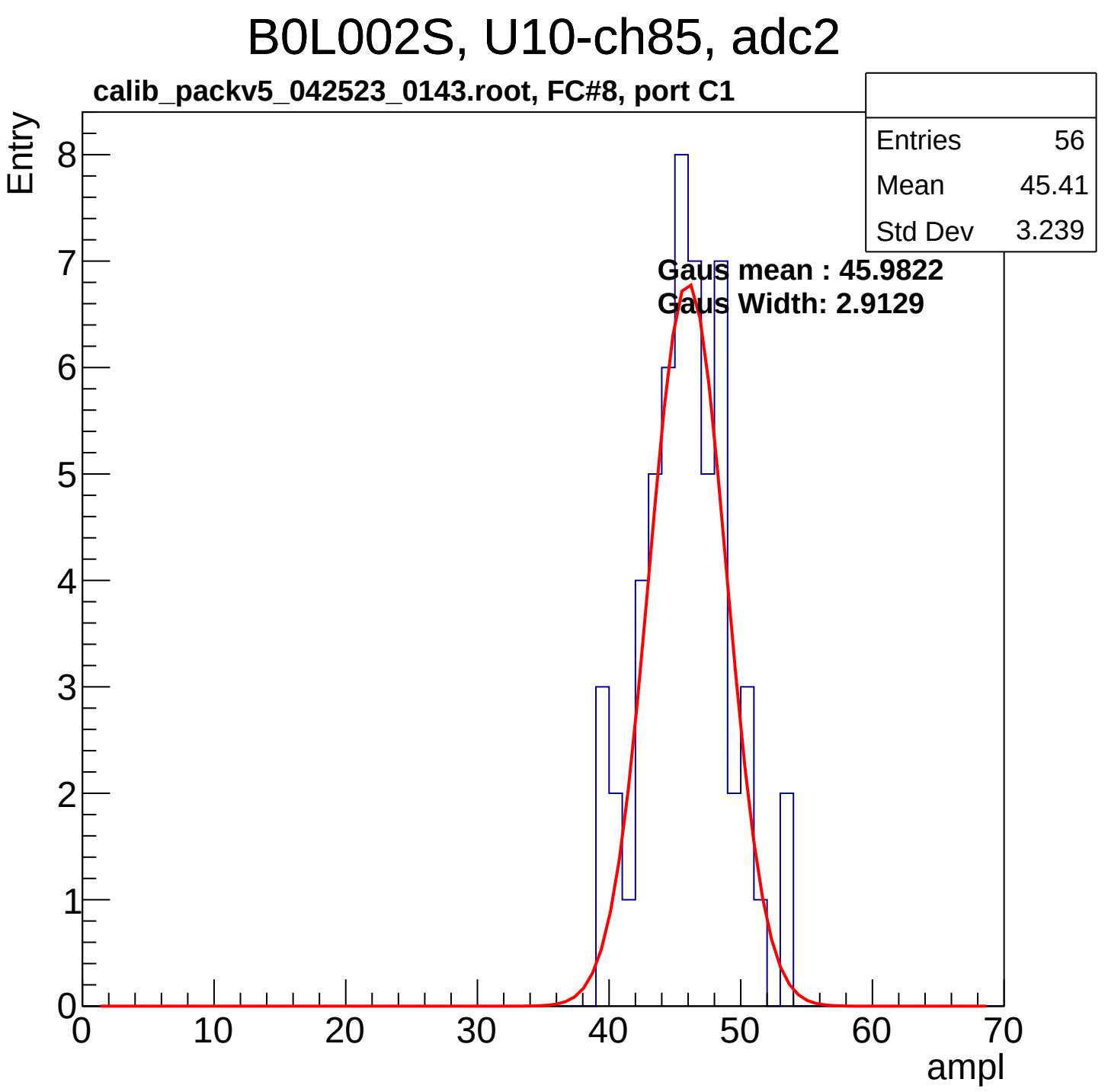

B0L002S, U10-ch84, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.79
Std Dev 1.384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

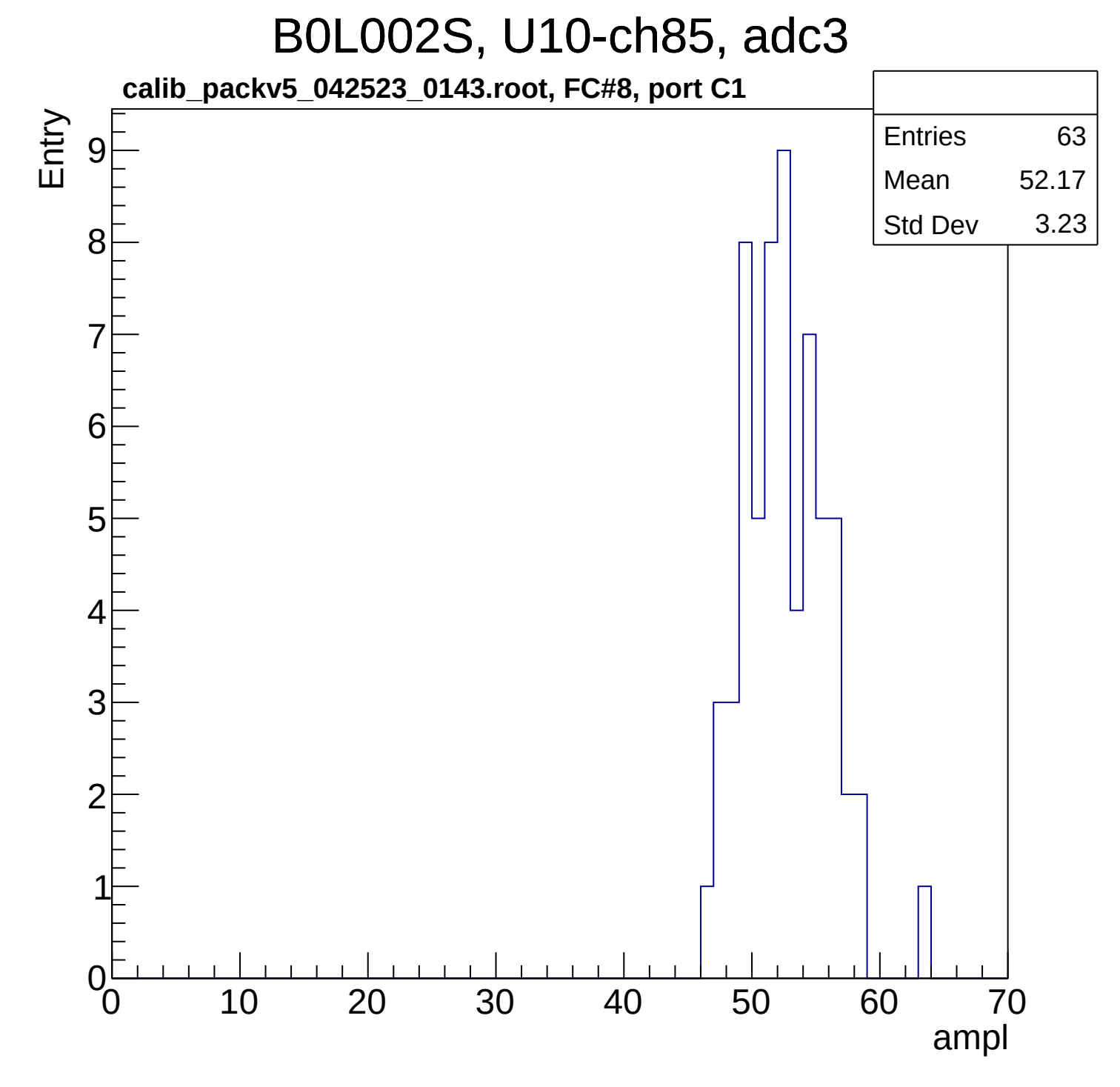

B0L002S, U10-ch84, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

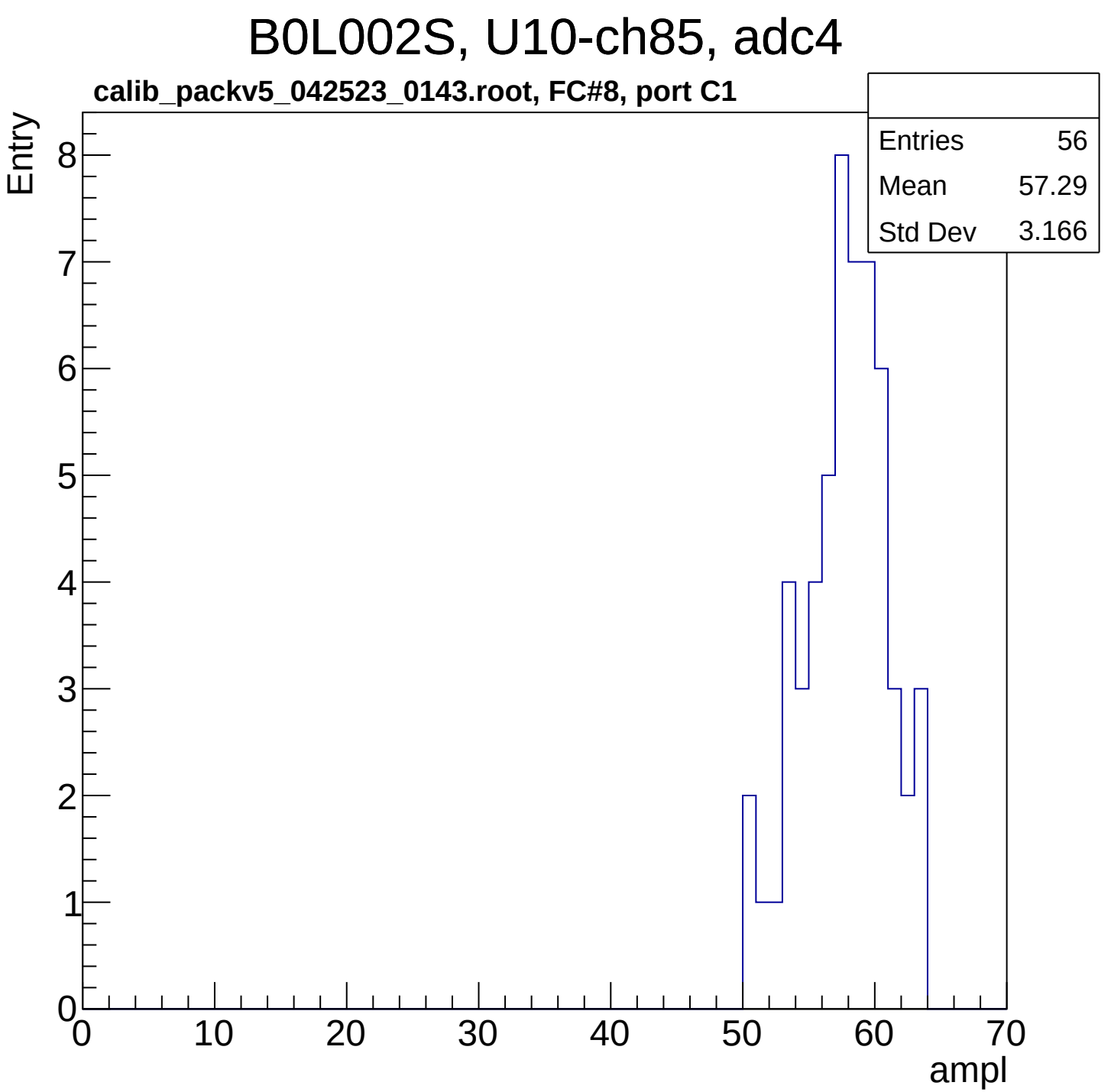

B0L002S, U10-ch84, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

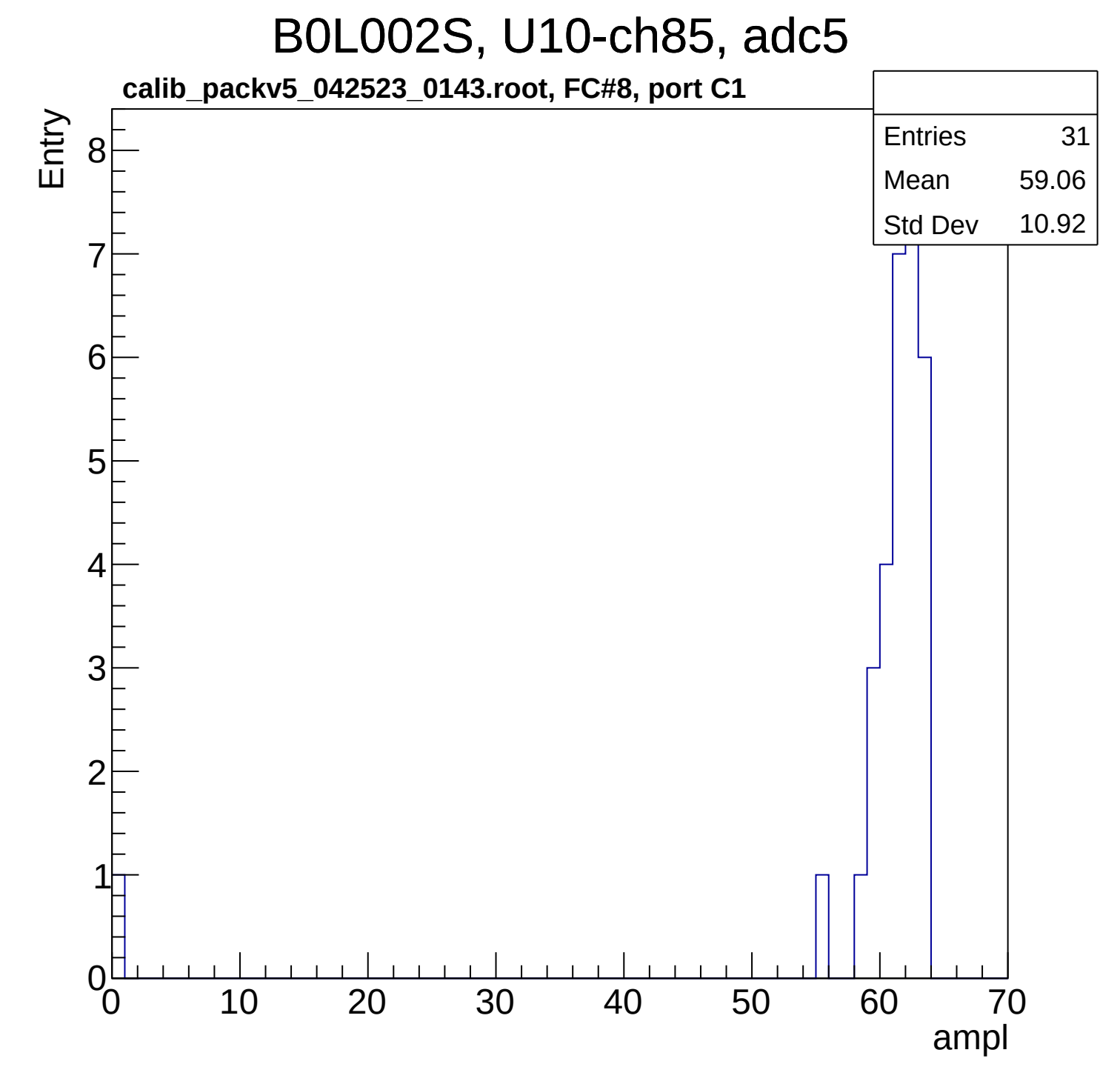

B0L002S, U10-ch85, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 31.87
Std Dev 3.705
Gaus mean : 32.0010
Gaus Width: 3.7380
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

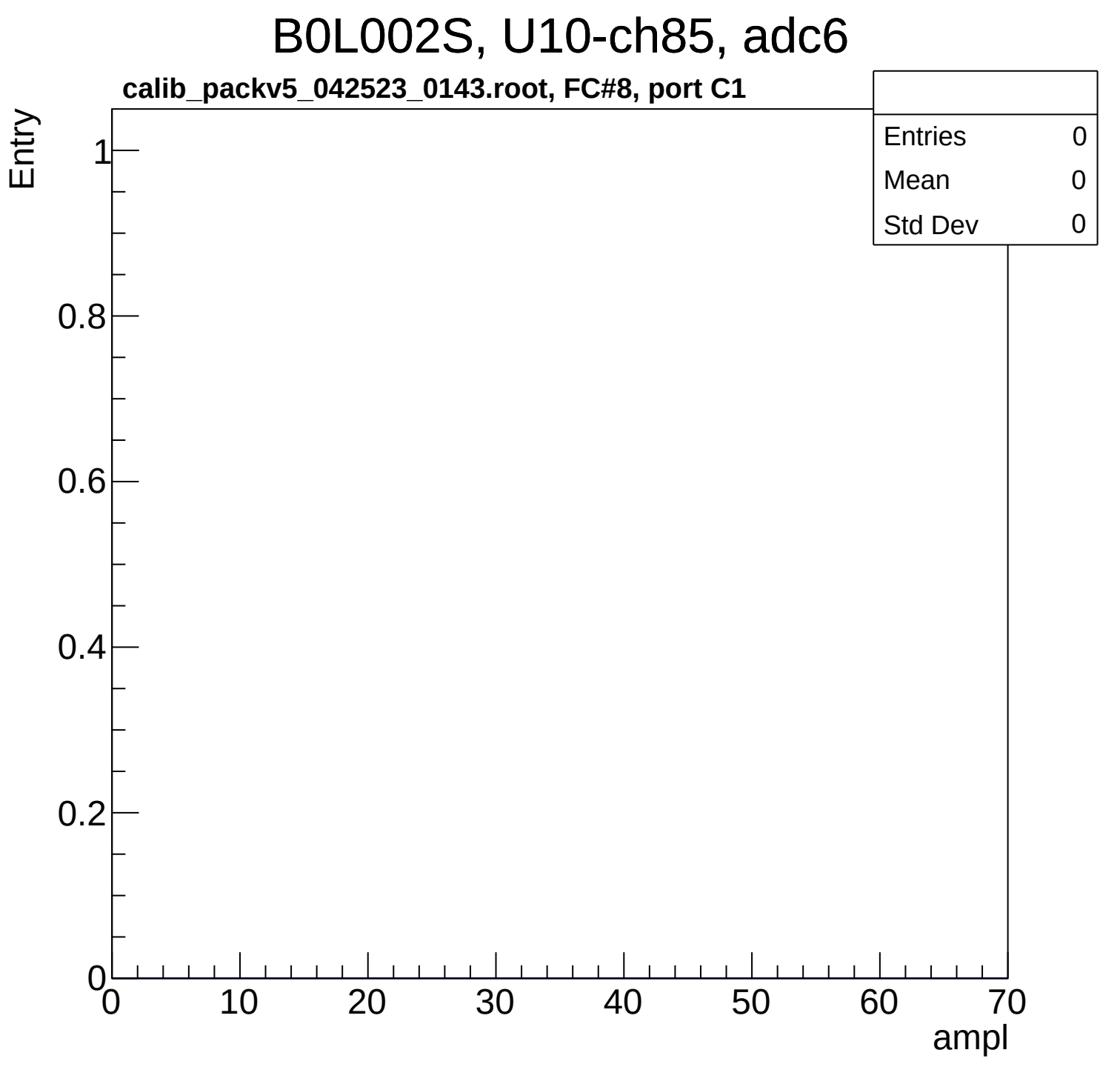

B0L002S, U10-ch85, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 39.41
Std Dev 3.522
Gaus mean : 39.9001
Gaus Width: 3.8244
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

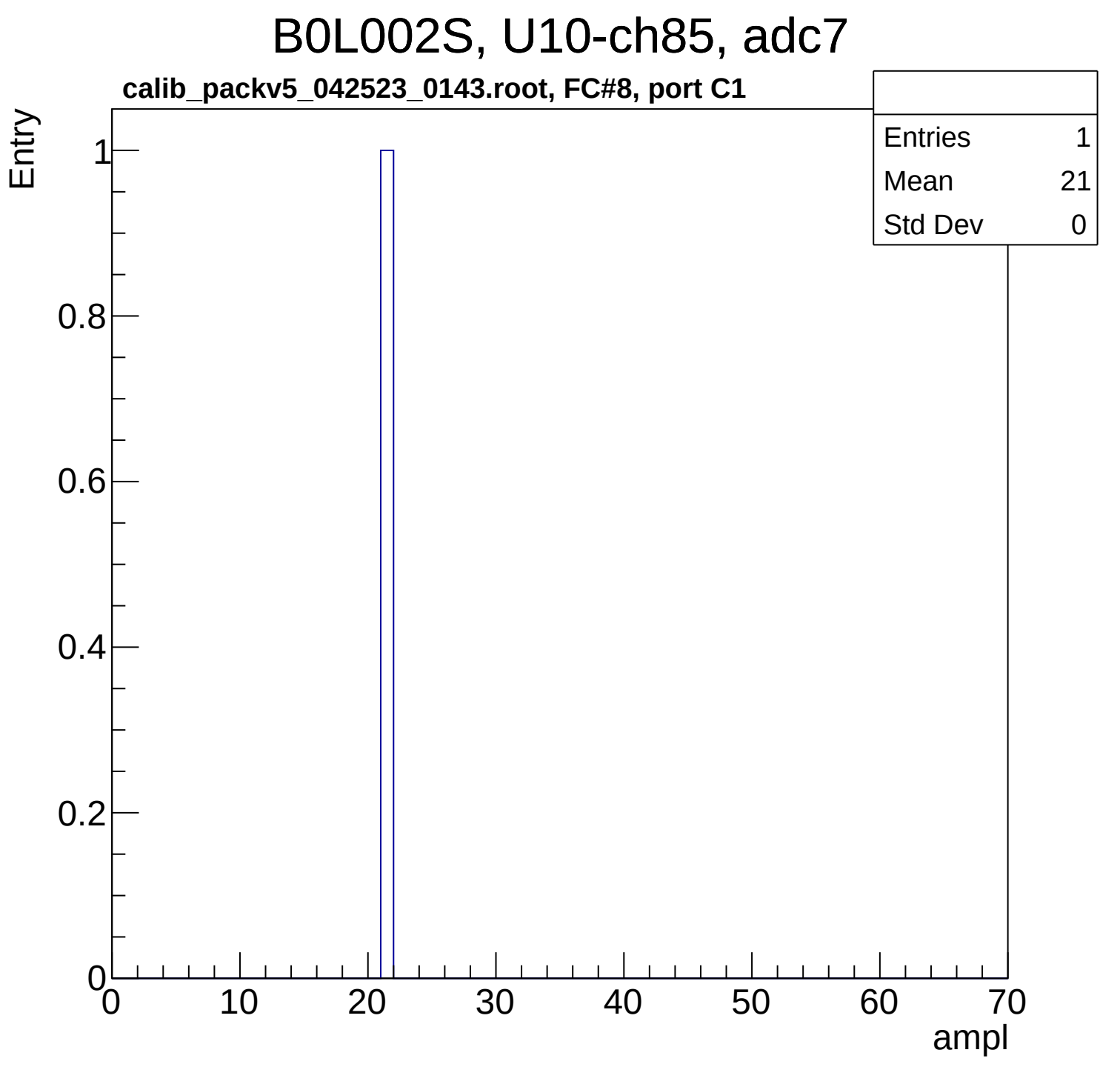

B0L002S, U10-ch85, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 45.41
Std Dev 3.239
Gaus mean : 45.9822
Gaus Width: 2.9129
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

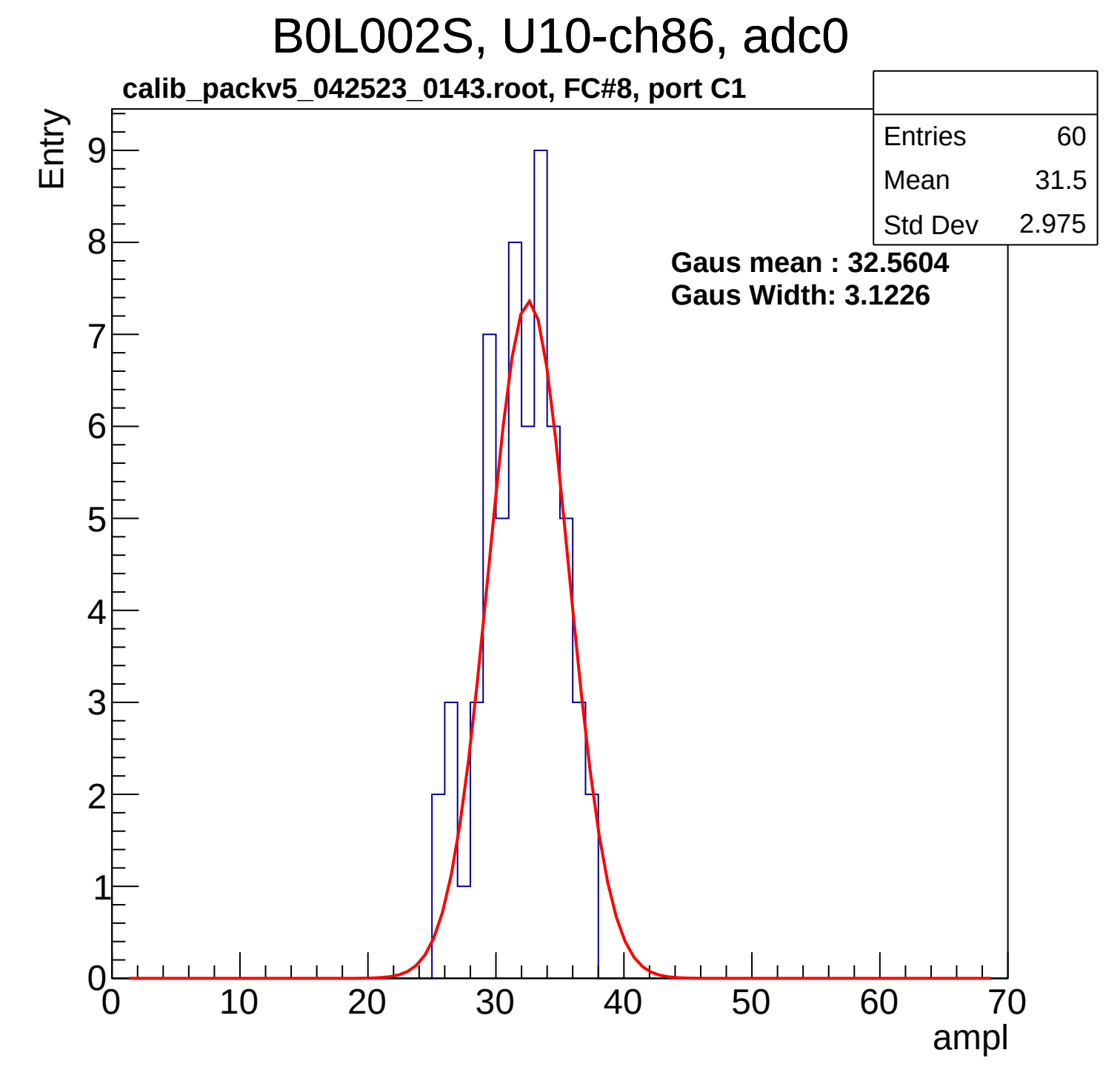

B0L002S, U10-ch85, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52.17
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

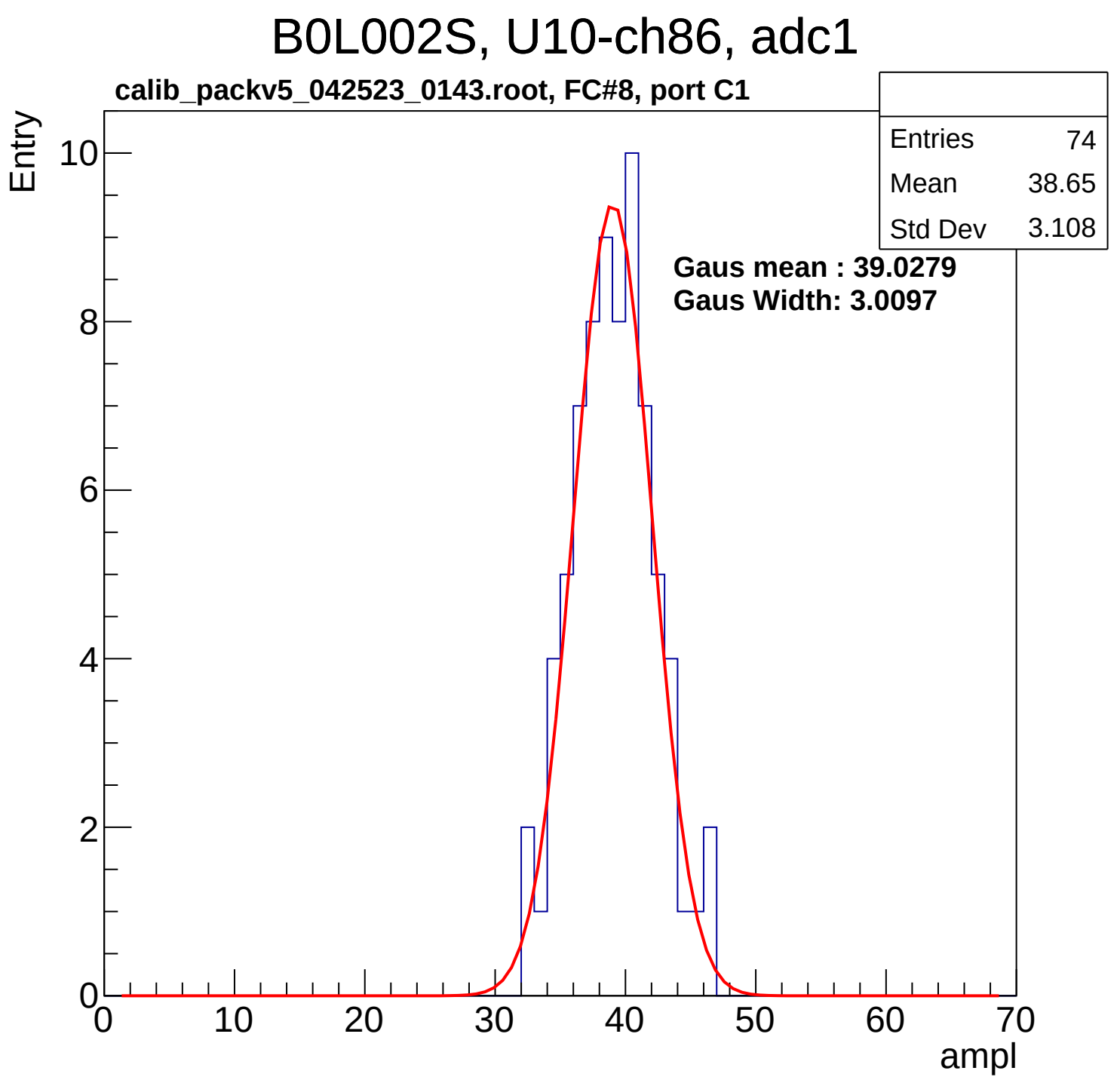

B0L002S, U10-ch85, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.29
Std Dev 3.166
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

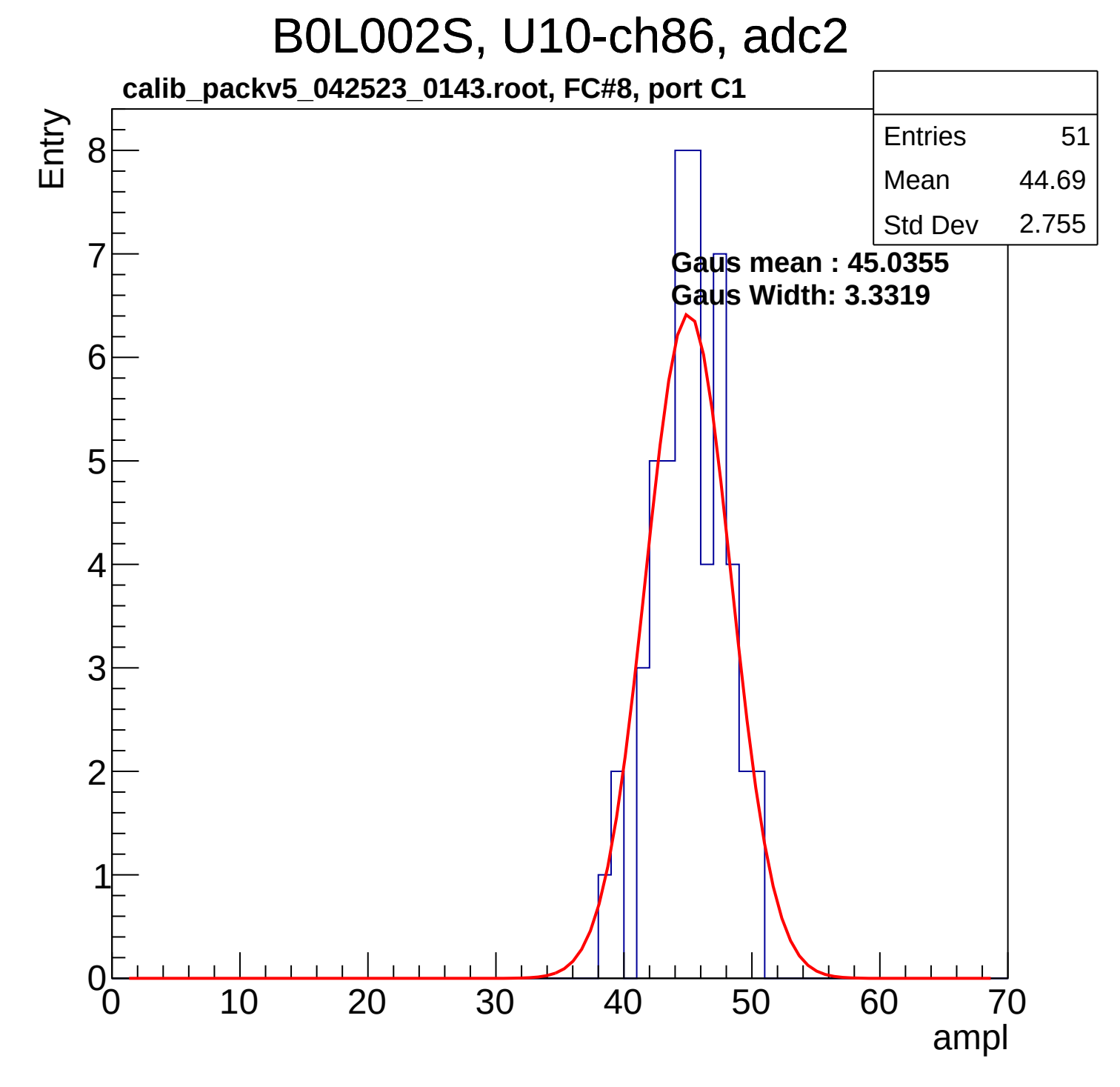

B0L002S, U10-ch85, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.06
Std Dev 10.92
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

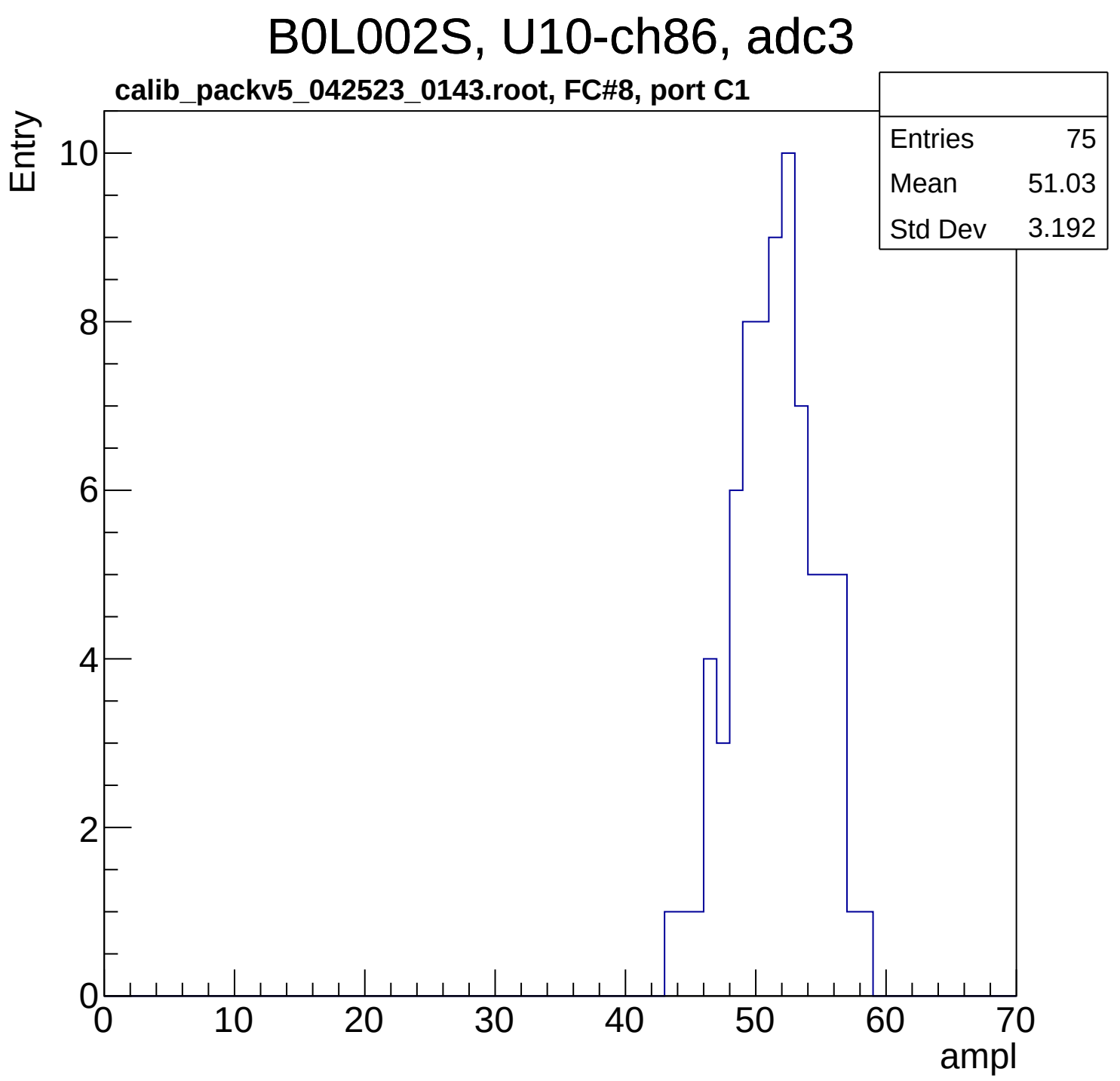

B0L002S, U10-ch85, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

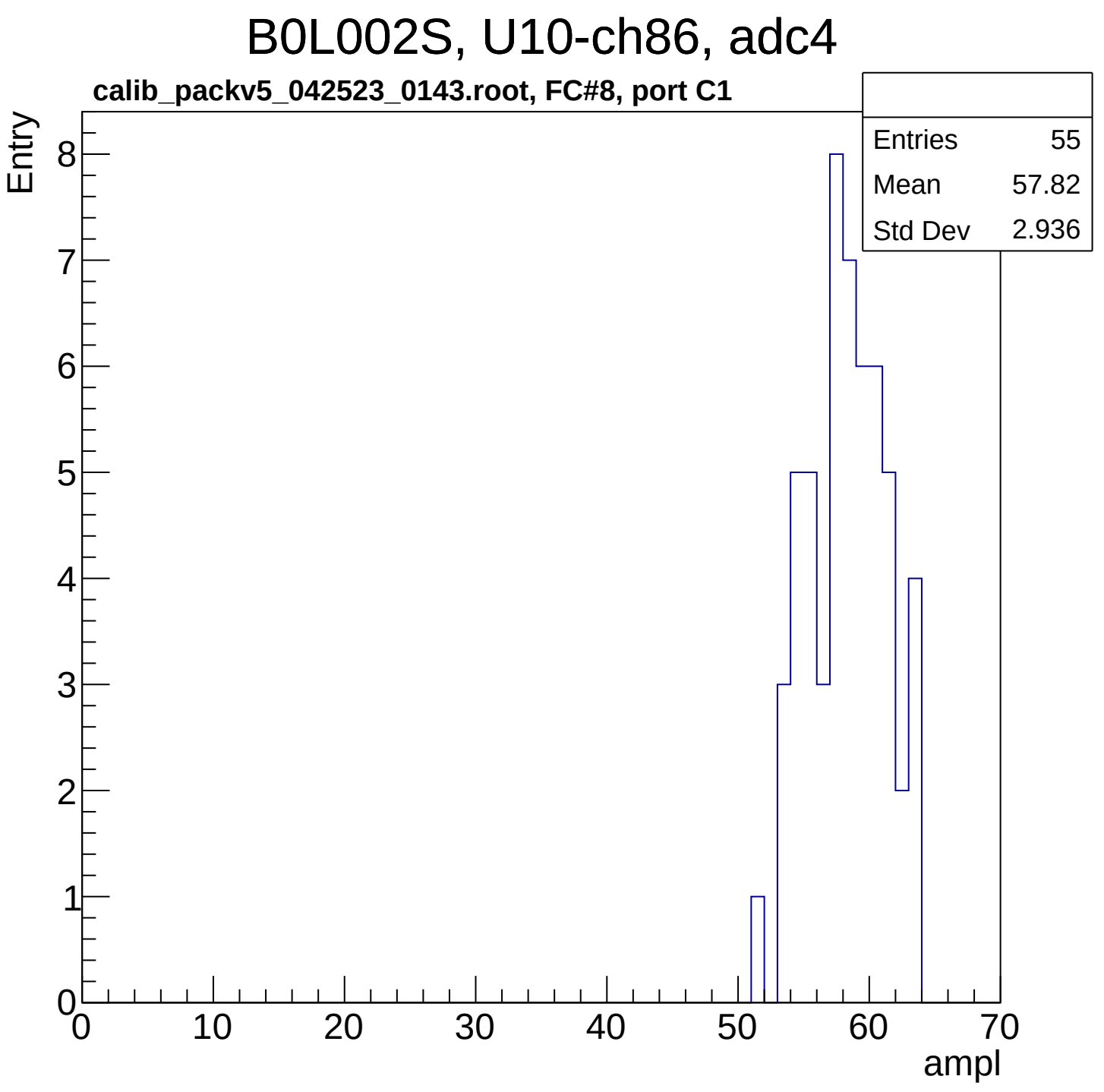

B0L002S, U10-ch85, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

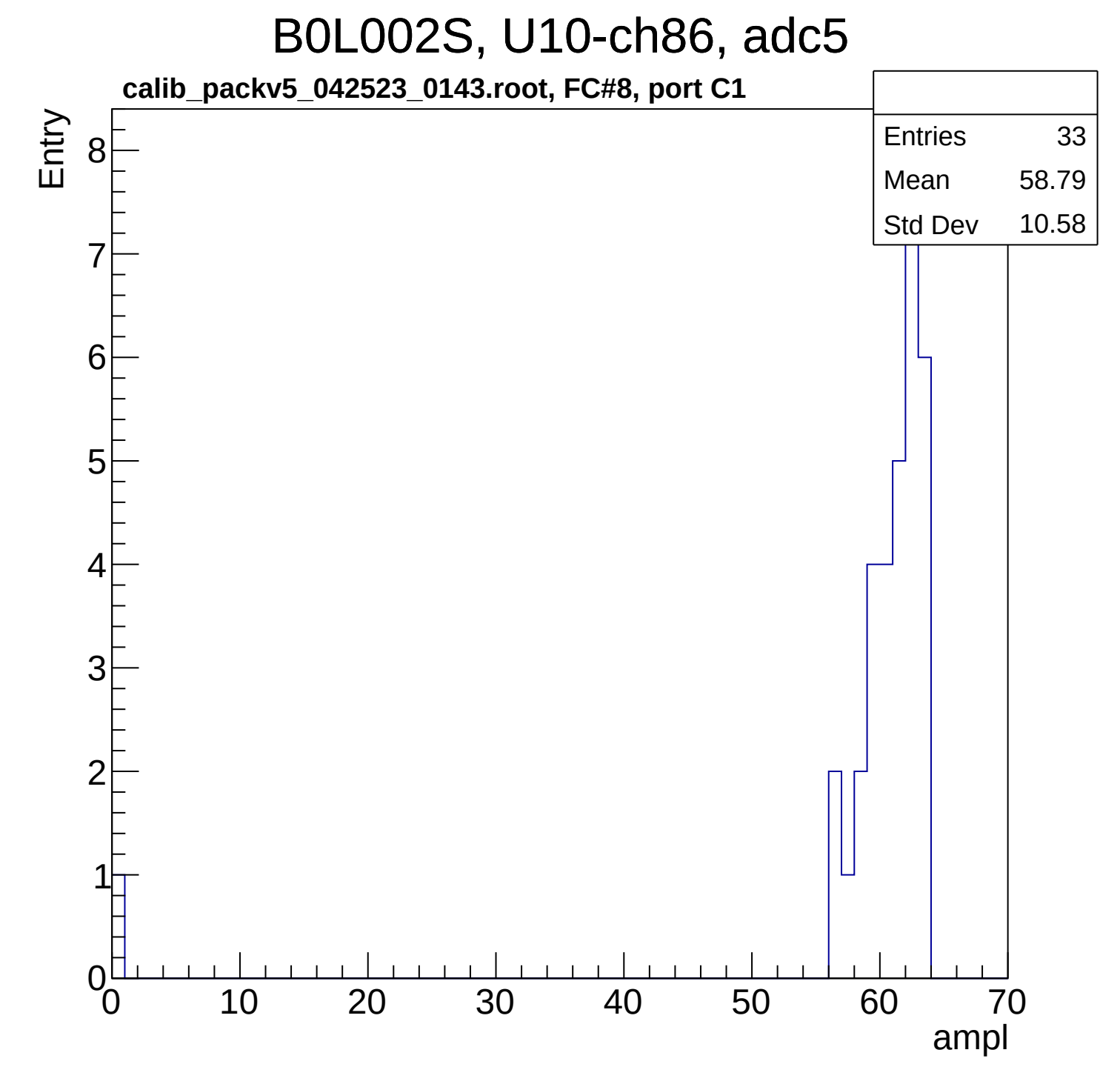

B0L002S, U10-ch86, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 31.5
Std Dev 2.975
Gaus mean : 32.5604
Gaus Width: 3.1226
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

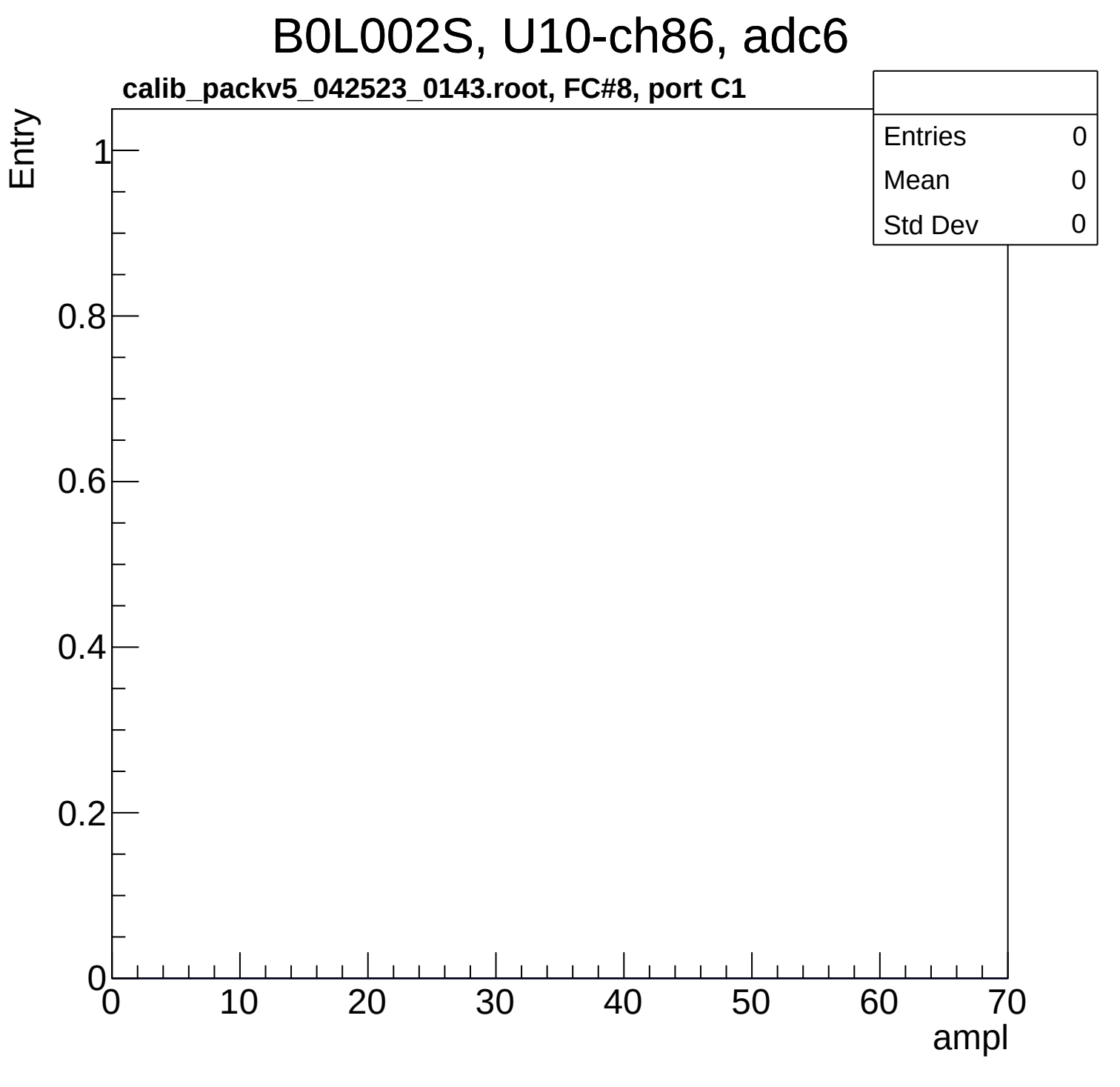

B0L002S, U10-ch86, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.65
Std Dev 3.108
Gaus mean : 39.0279
Gaus Width: 3.0097
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

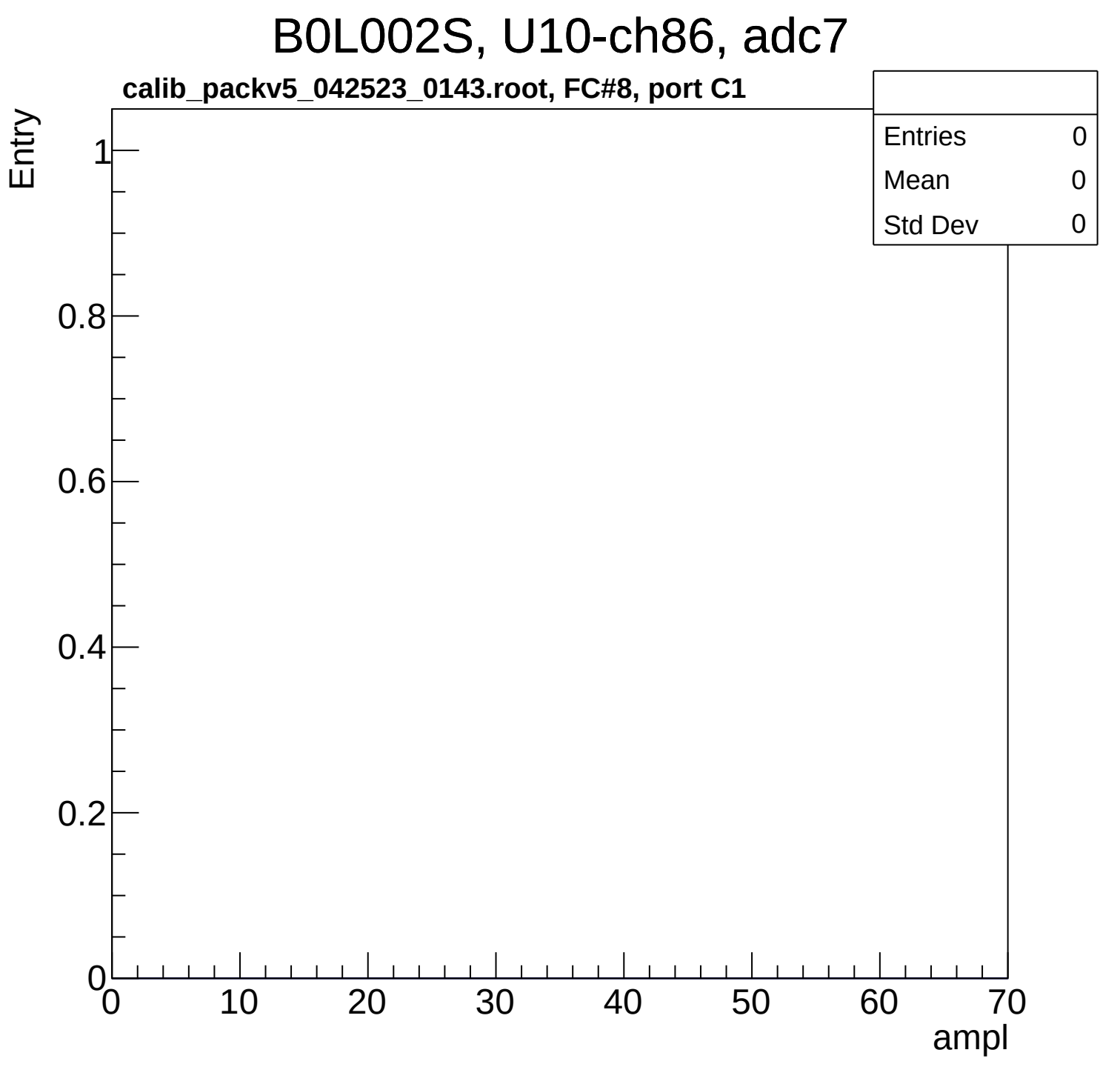

B0L002S, U10-ch86, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 44.69
Std Dev 2.755
Gaus mean : 45.0355
Gaus Width: 3.3319
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

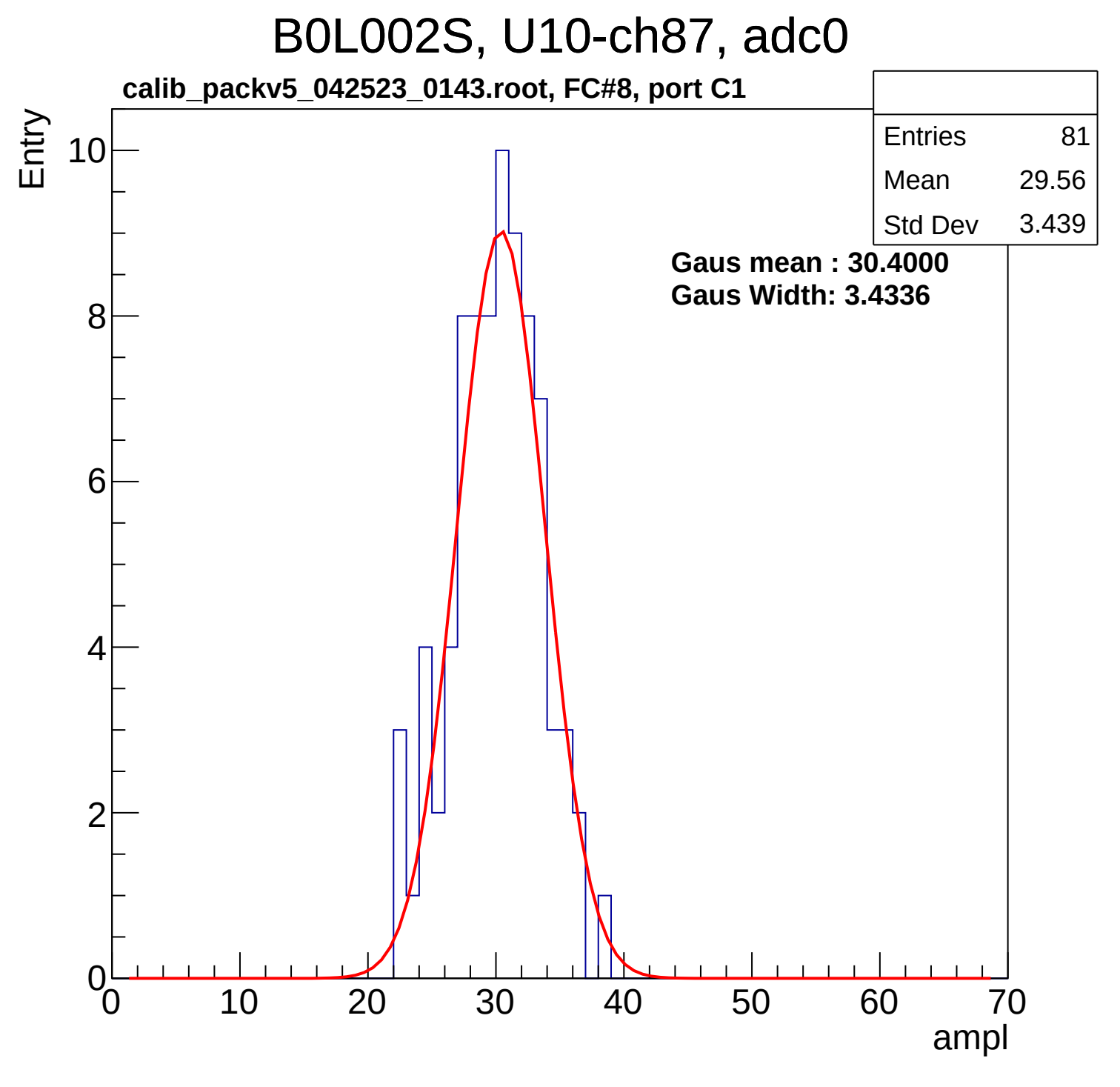

B0L002S, U10-ch86, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.03
Std Dev 3.192
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

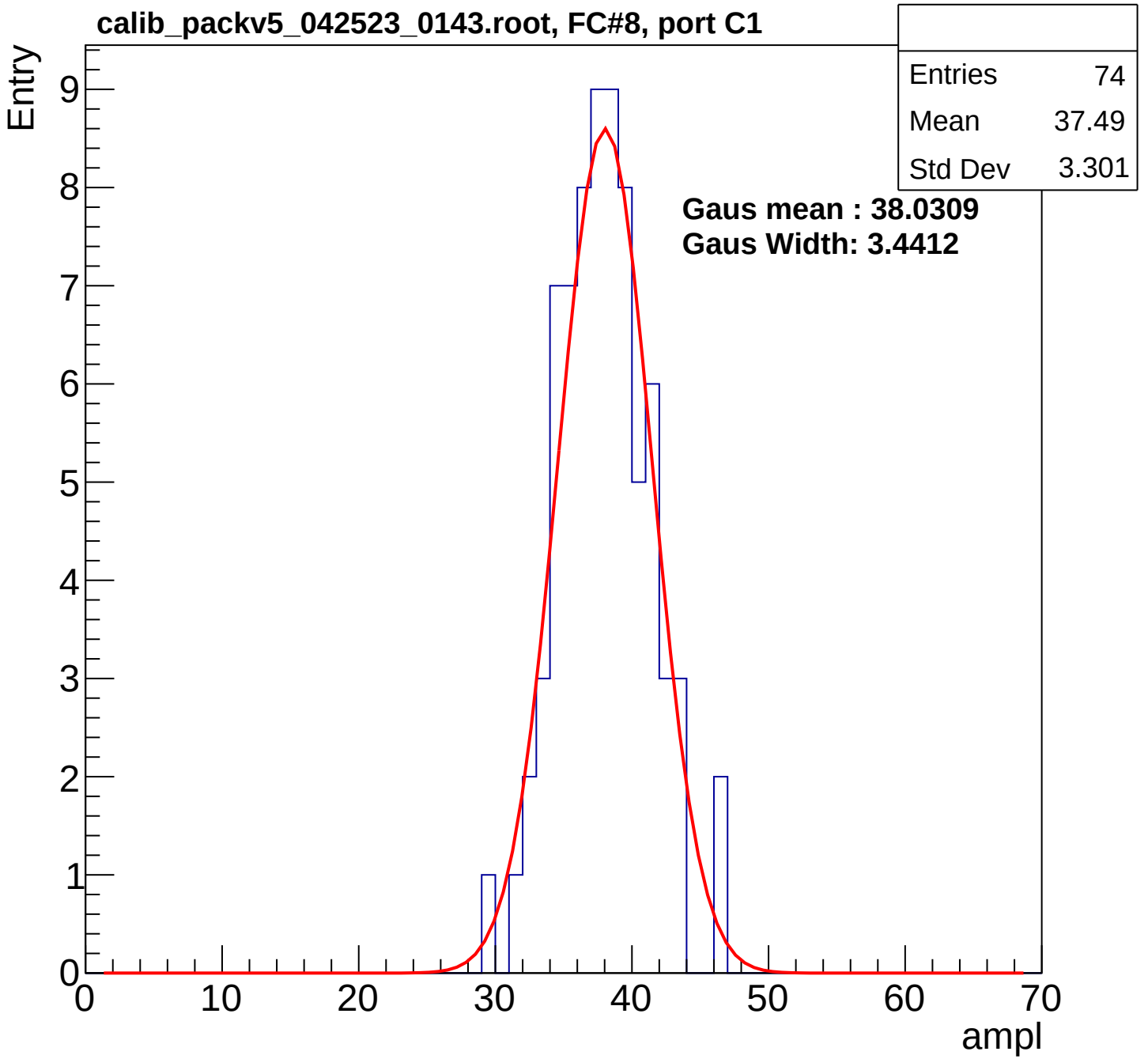

B0L002S, U10-ch86, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 57.82
Std Dev 2.936
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

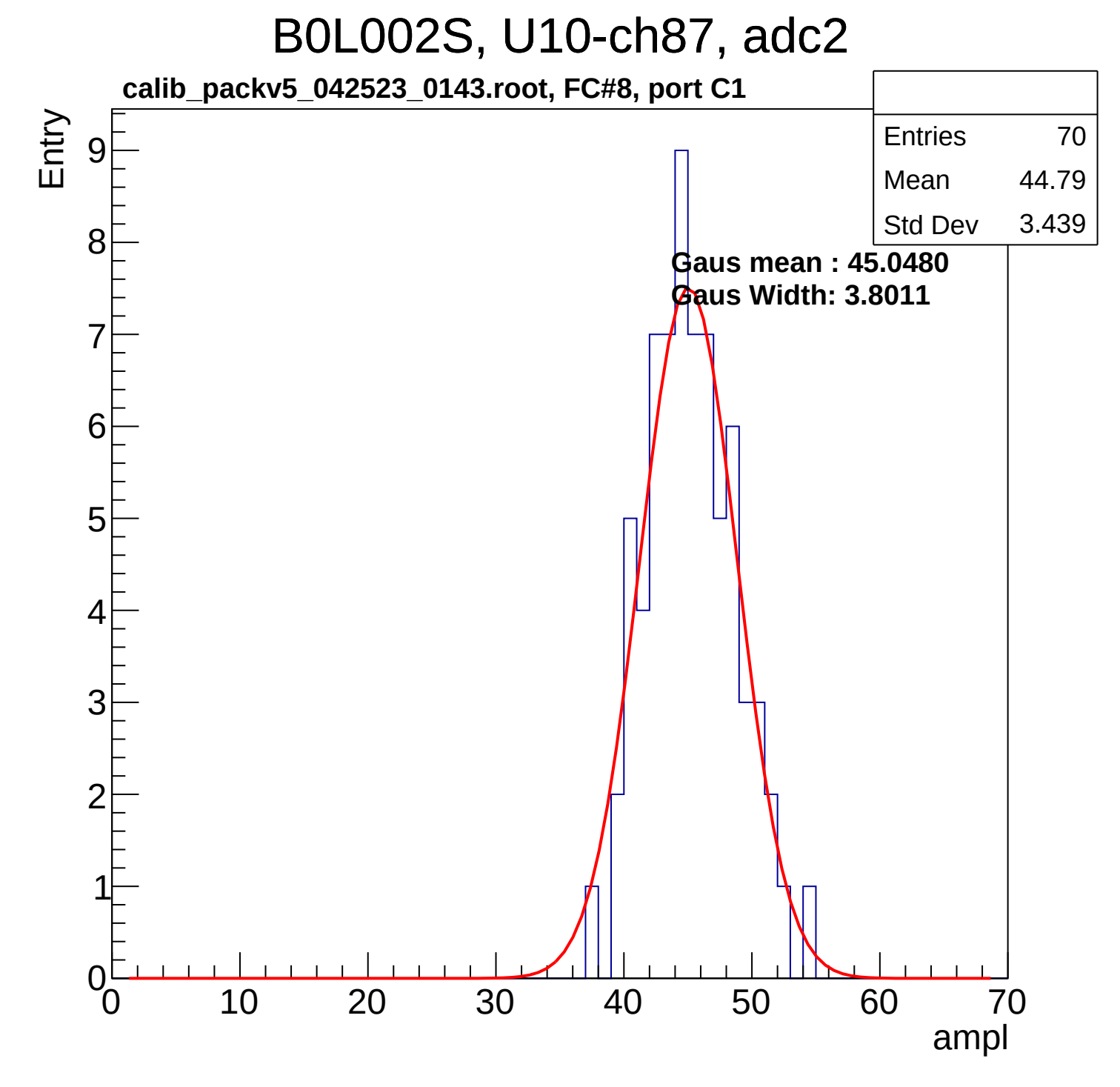

B0L002S, U10-ch86, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.79
Std Dev 10.58
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

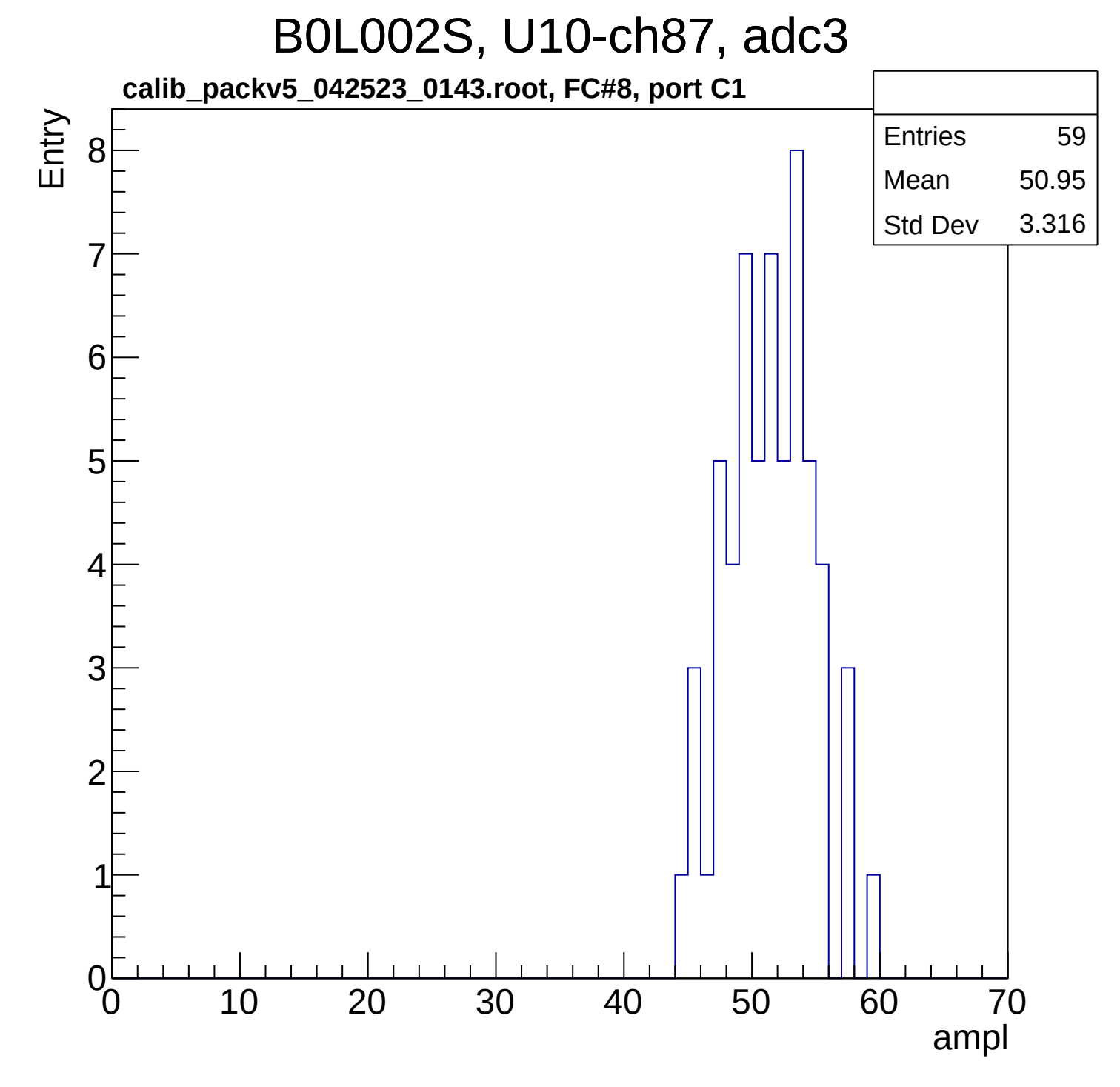

B0L002S, U10-ch86, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

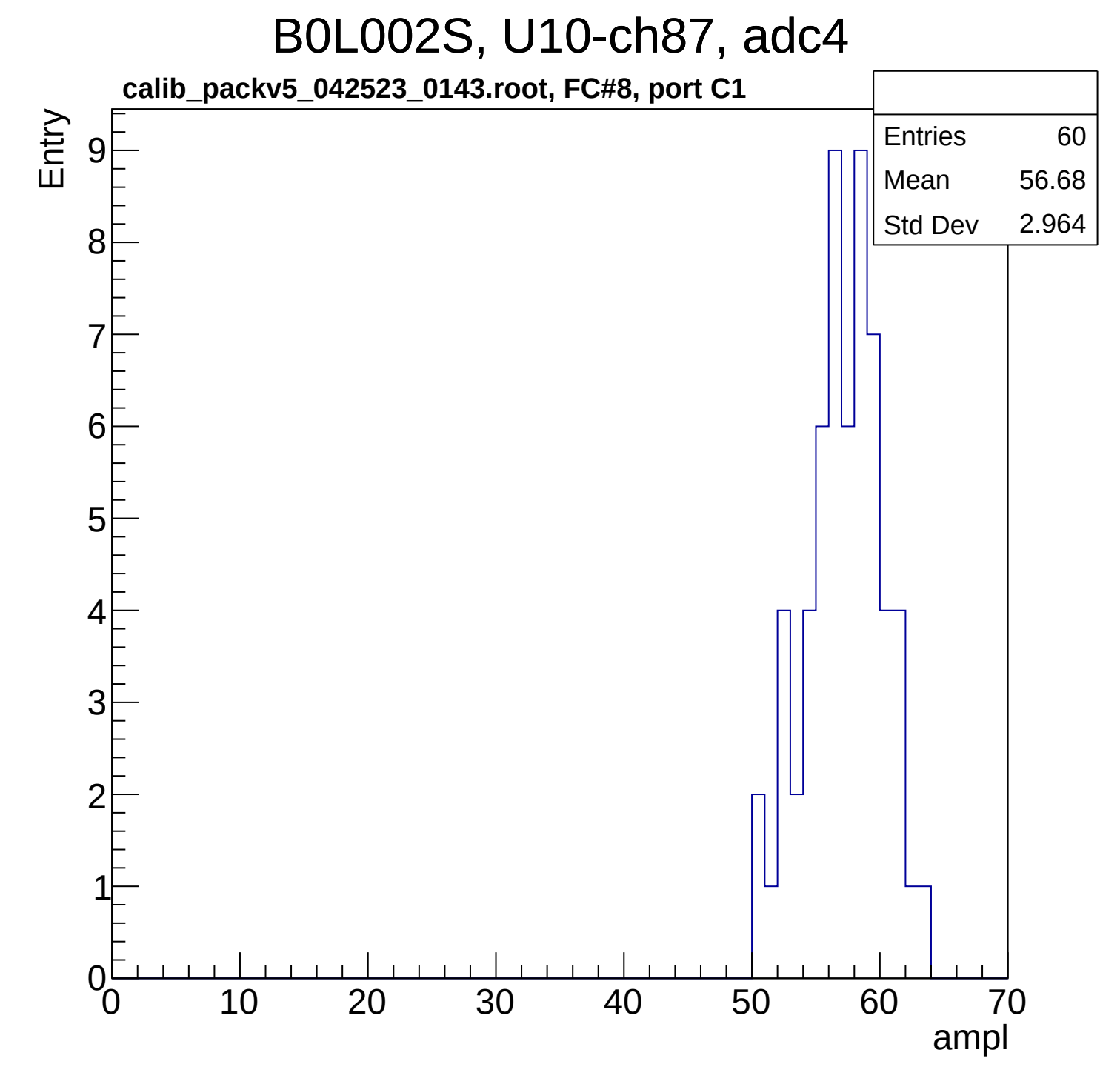

B0L002S, U10-ch86, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

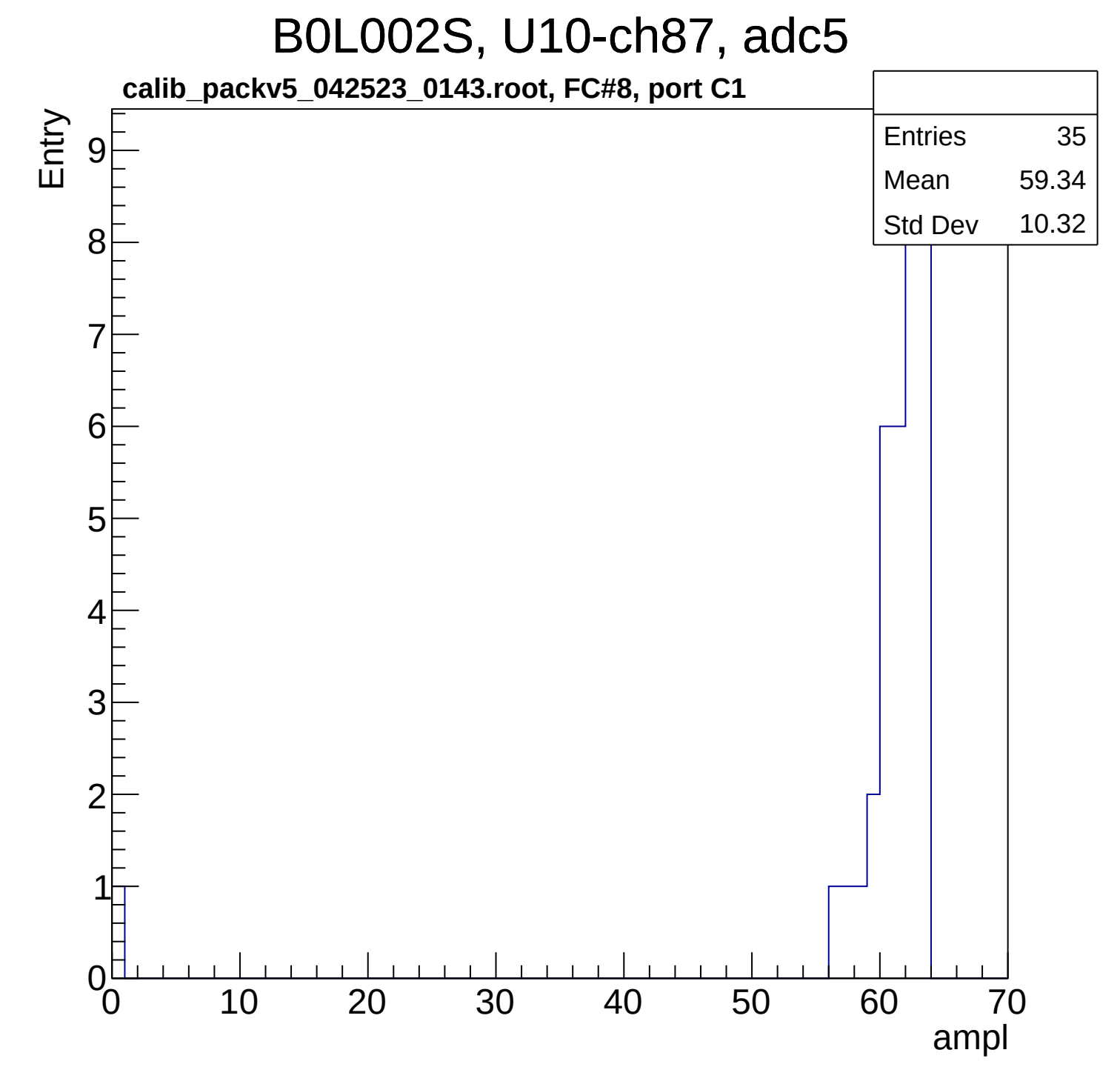

B0L002S, U10-ch87, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 29.56
Std Dev 3.439
Gaus mean : 30.4000
Gaus Width: 3.4336
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

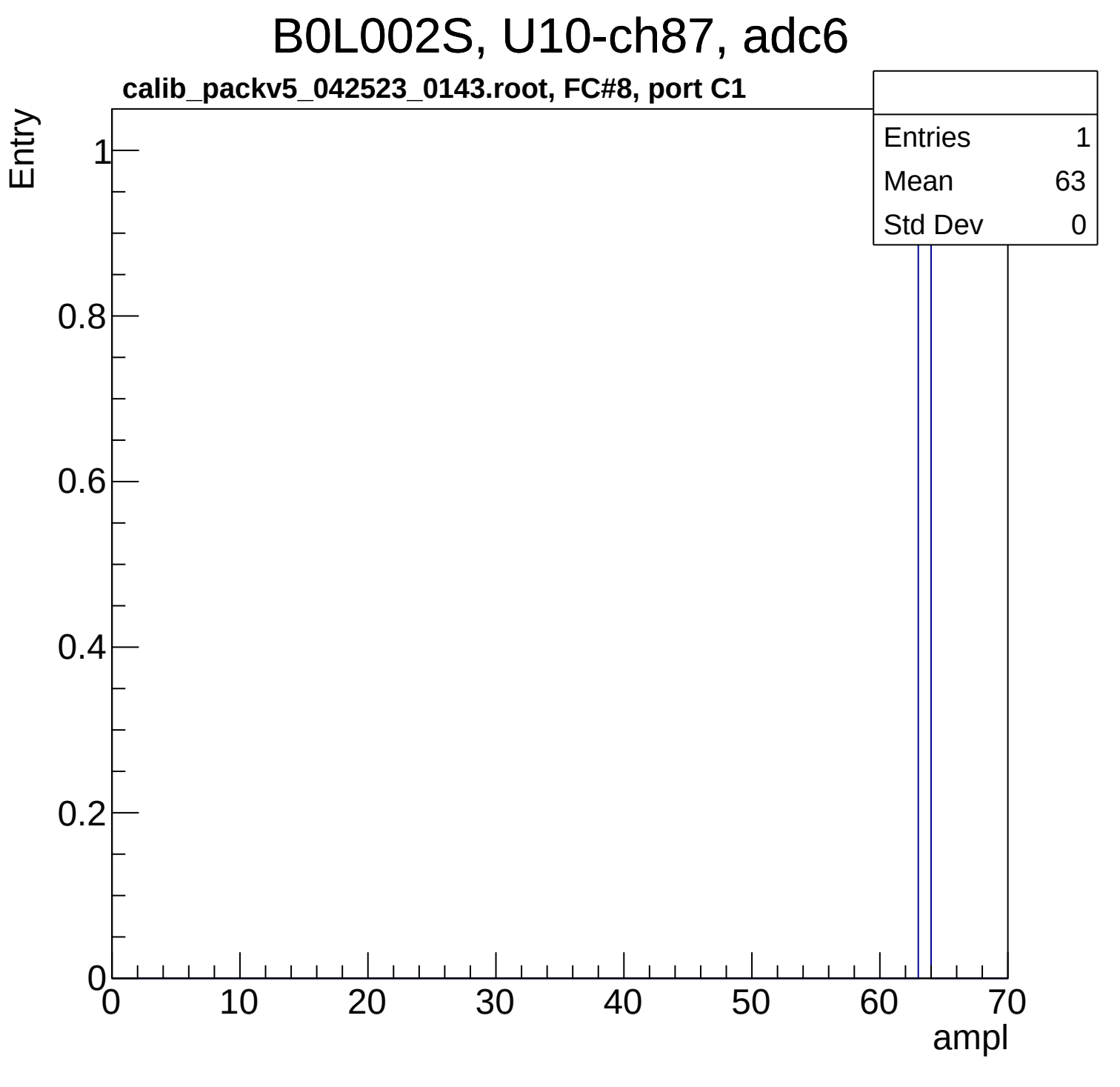

B0L002S, U10-ch87, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 37.49
Std Dev 3.301
Gaus mean : 38.0309
Gaus Width: 3.4412
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

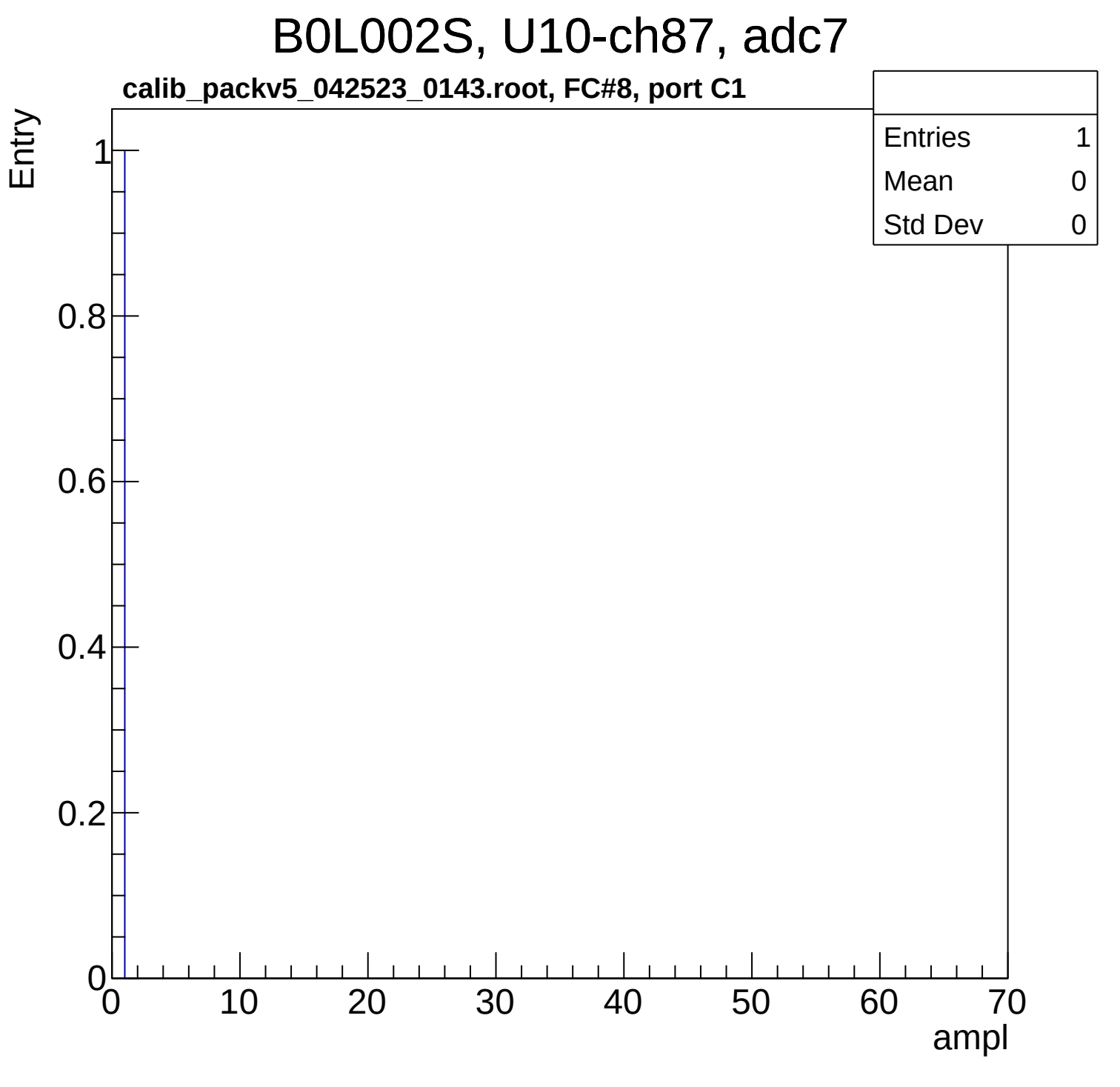

B0L002S, U10-ch87, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 44.79
Std Dev 3.439
Gaus mean : 45.0480
Gaus Width: 3.8011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

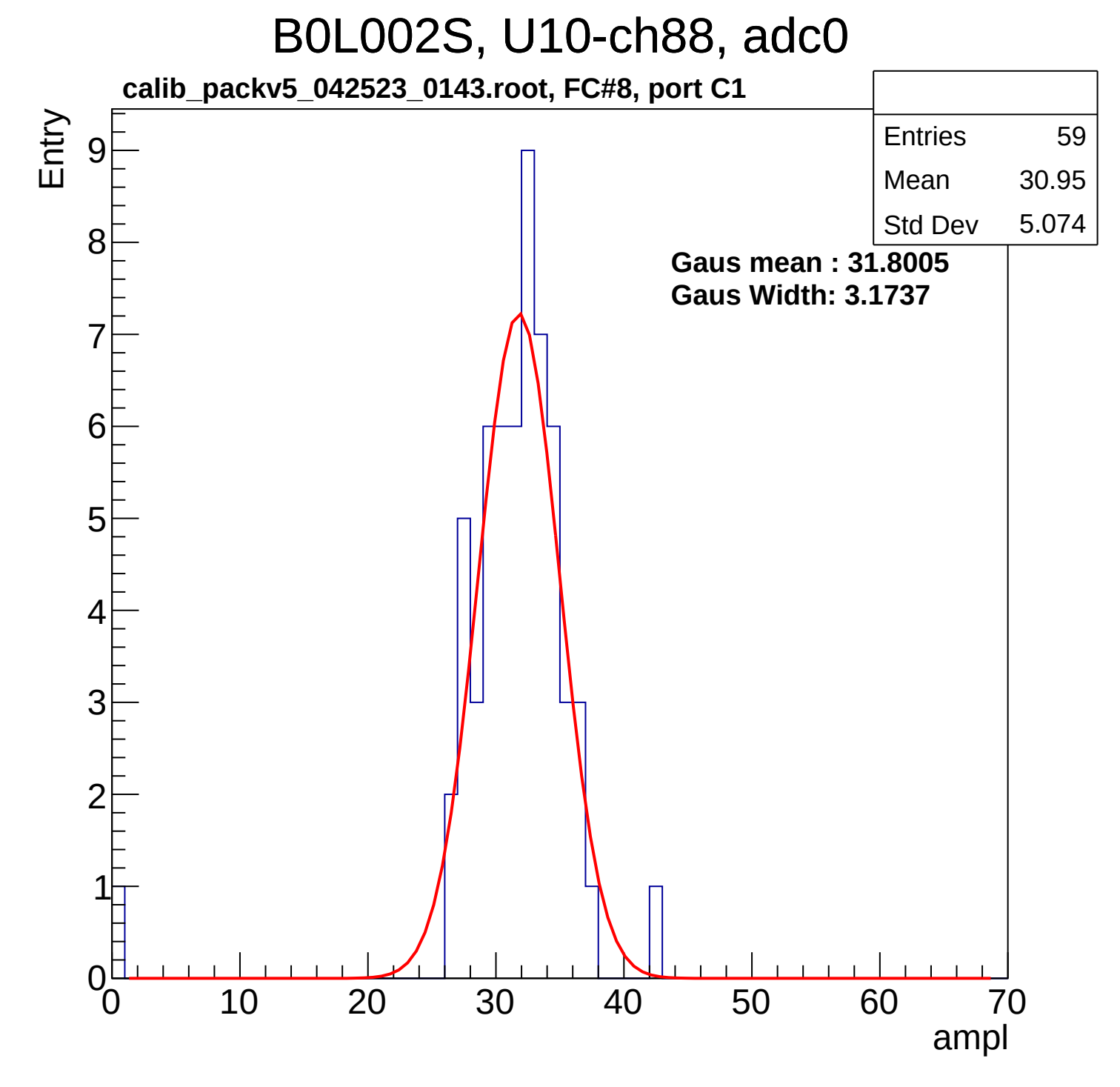

B0L002S, U10-ch87, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 50.95
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

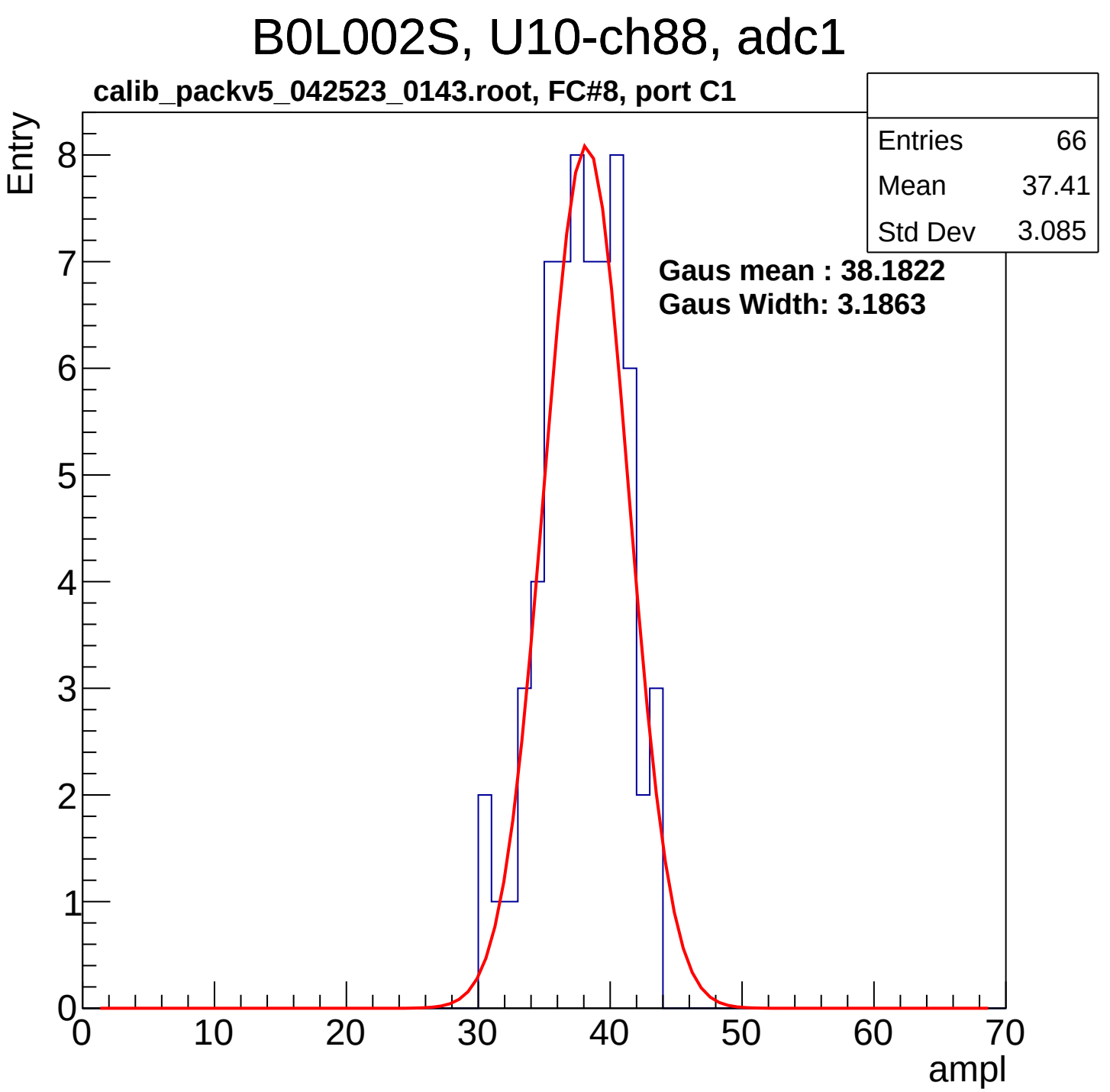

B0L002S, U10-ch87, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.68
Std Dev 2.964
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

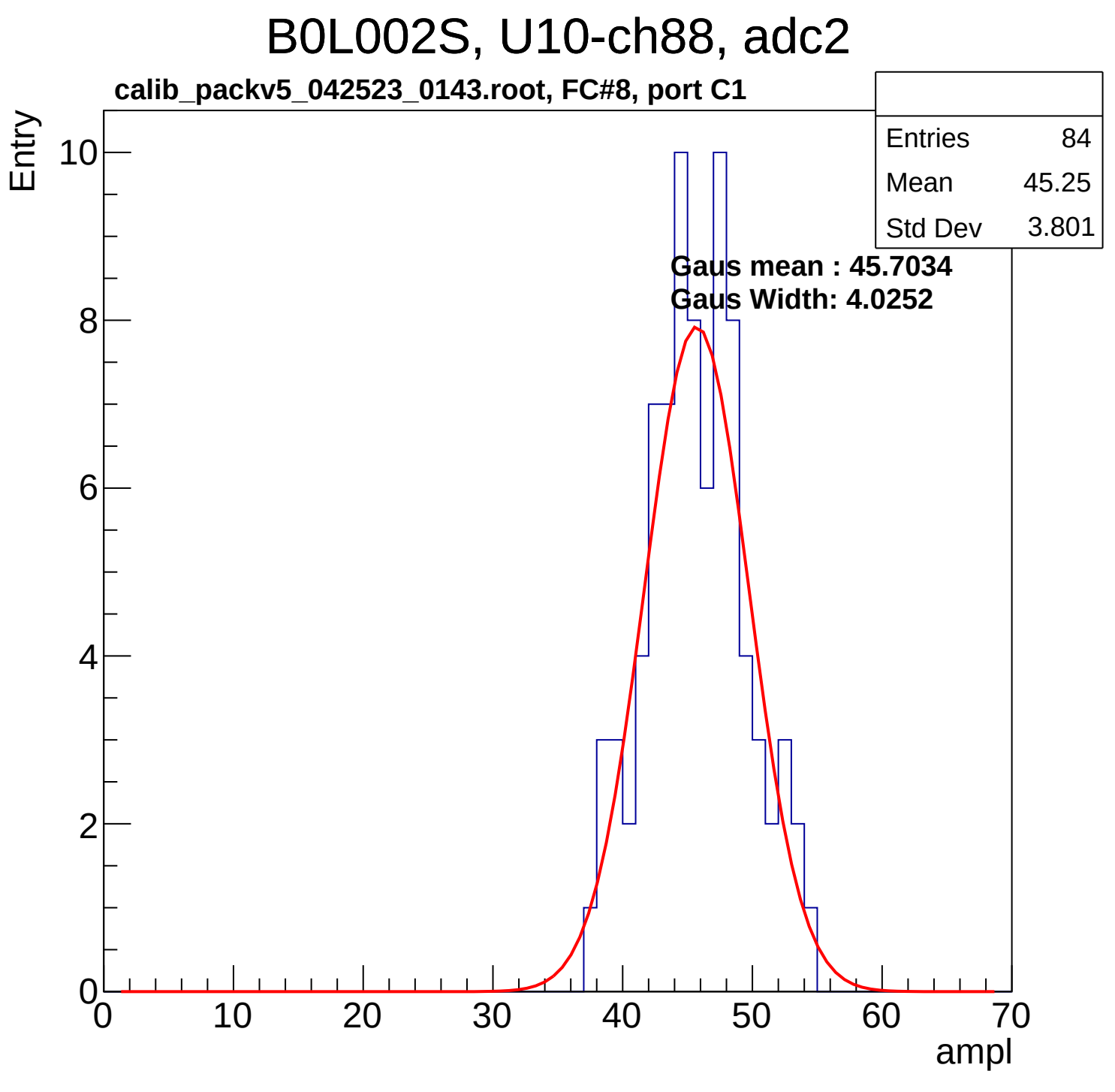

B0L002S, U10-ch87, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.34
Std Dev 10.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

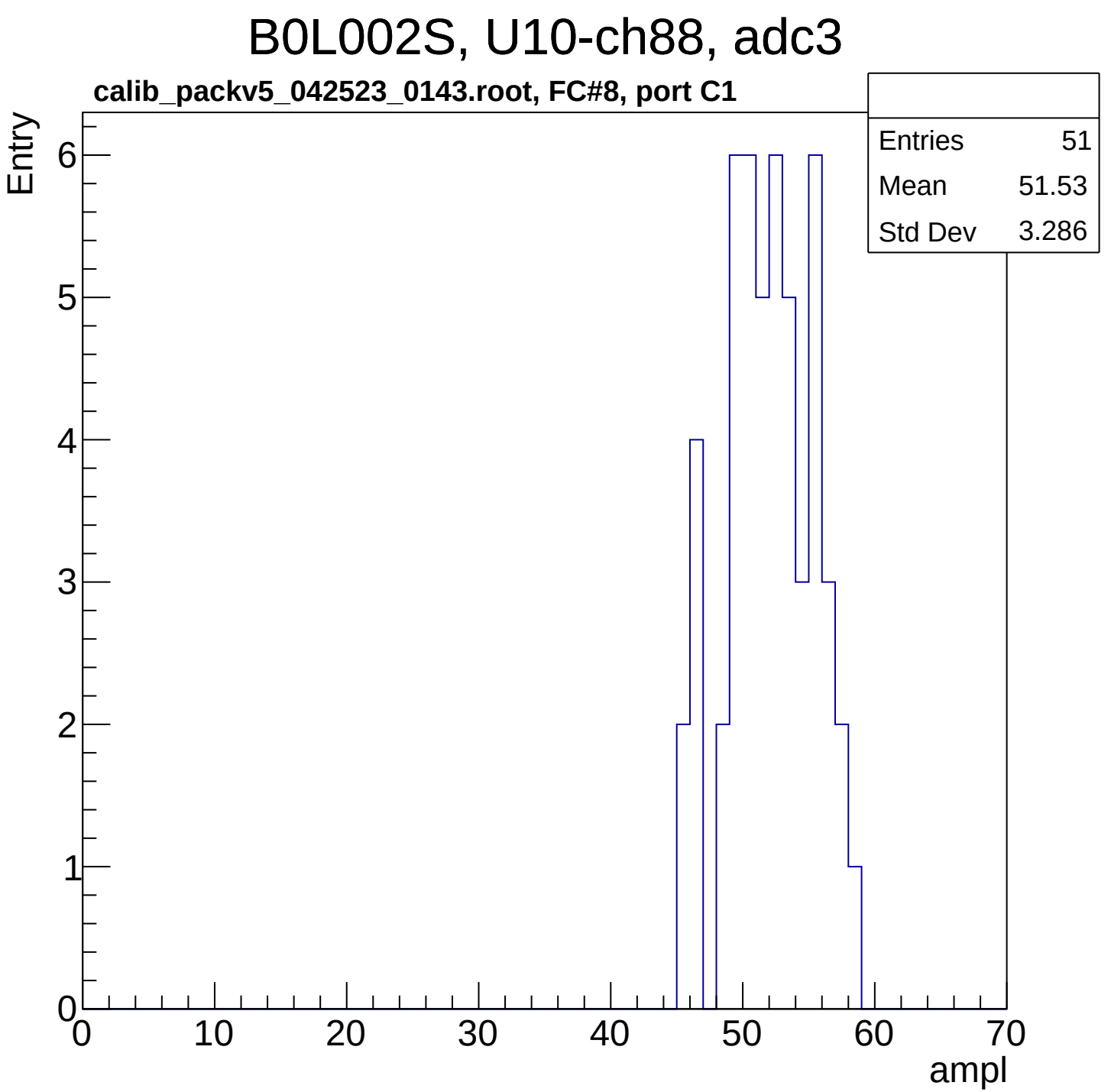

B0L002S, U10-ch87, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

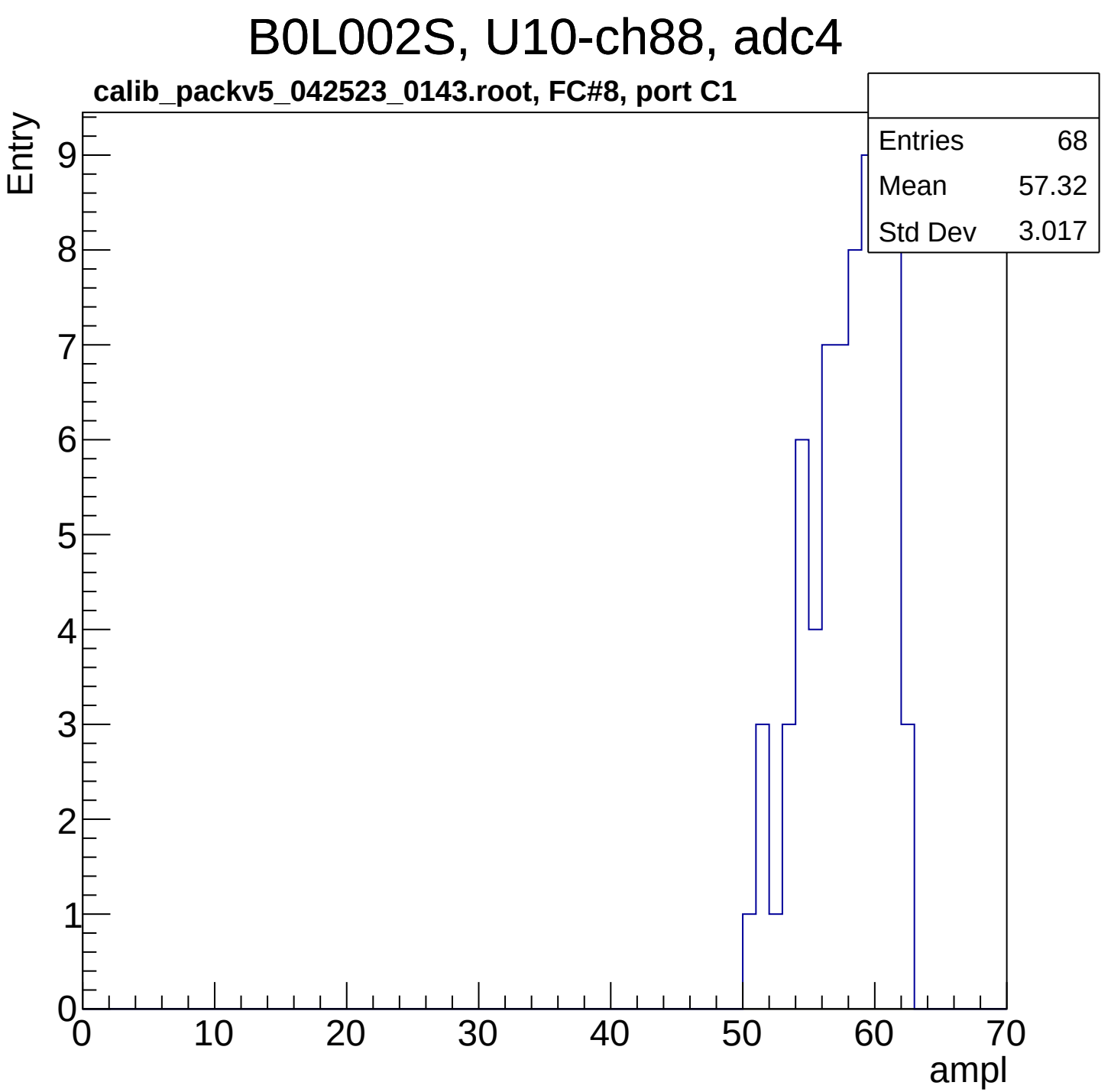

B0L002S, U10-ch87, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

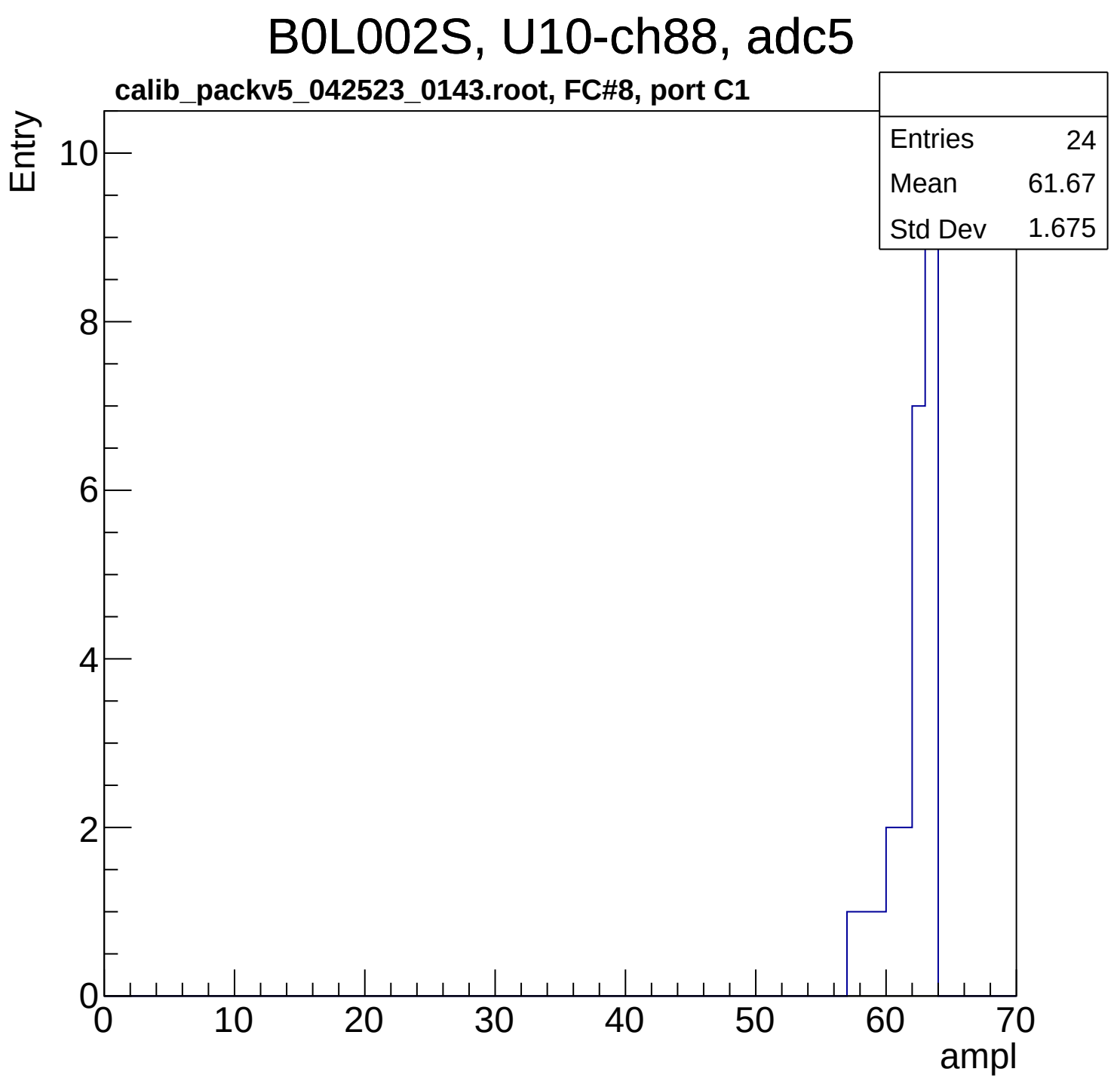

B0L002S, U10-ch88, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 30.95
Std Dev 5.074
Gaus mean : 31.8005
Gaus Width: 3.1737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

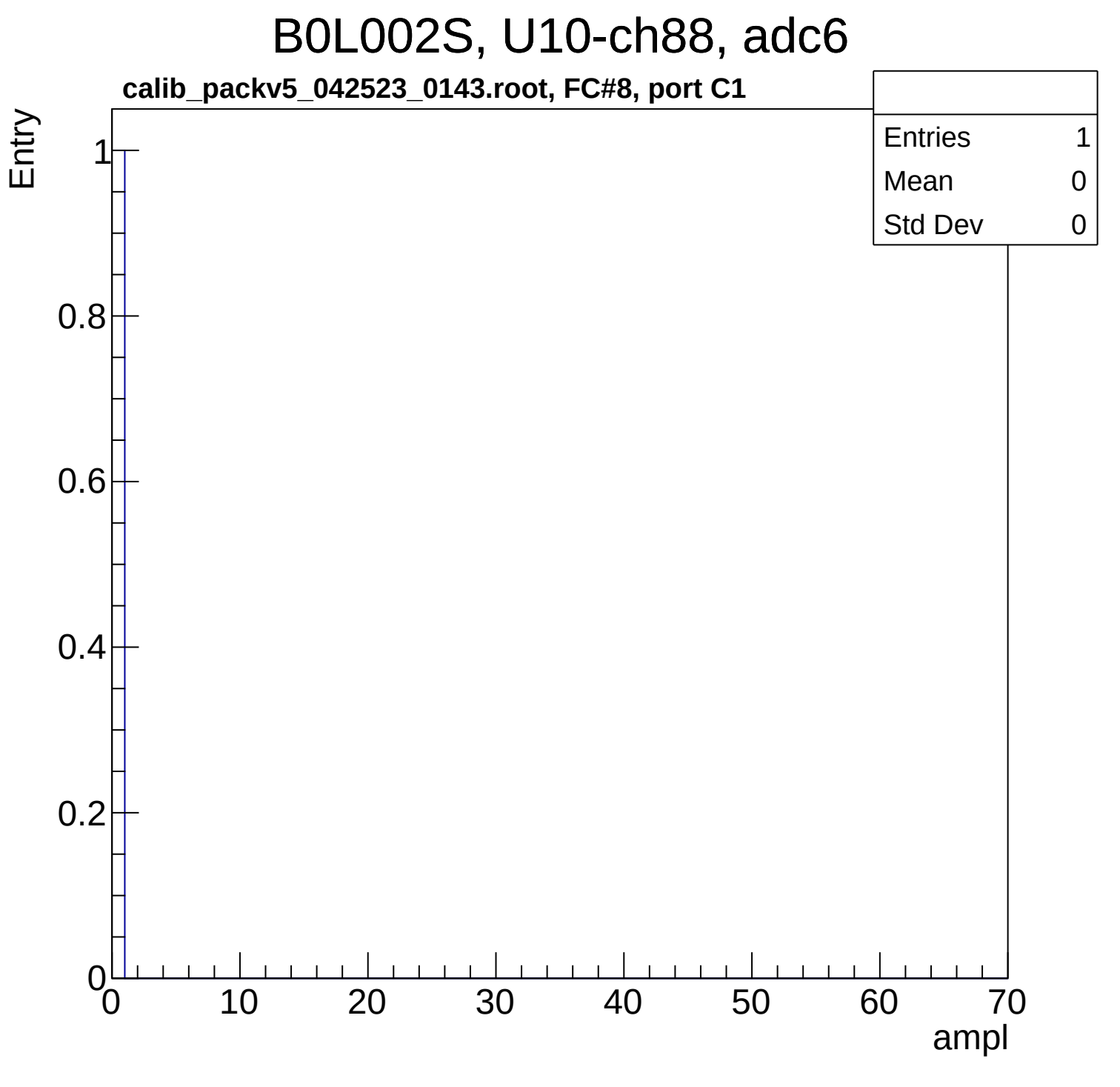

B0L002S, U10-ch88, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.41
Std Dev 3.085
Gaus mean : 38.1822
Gaus Width: 3.1863
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

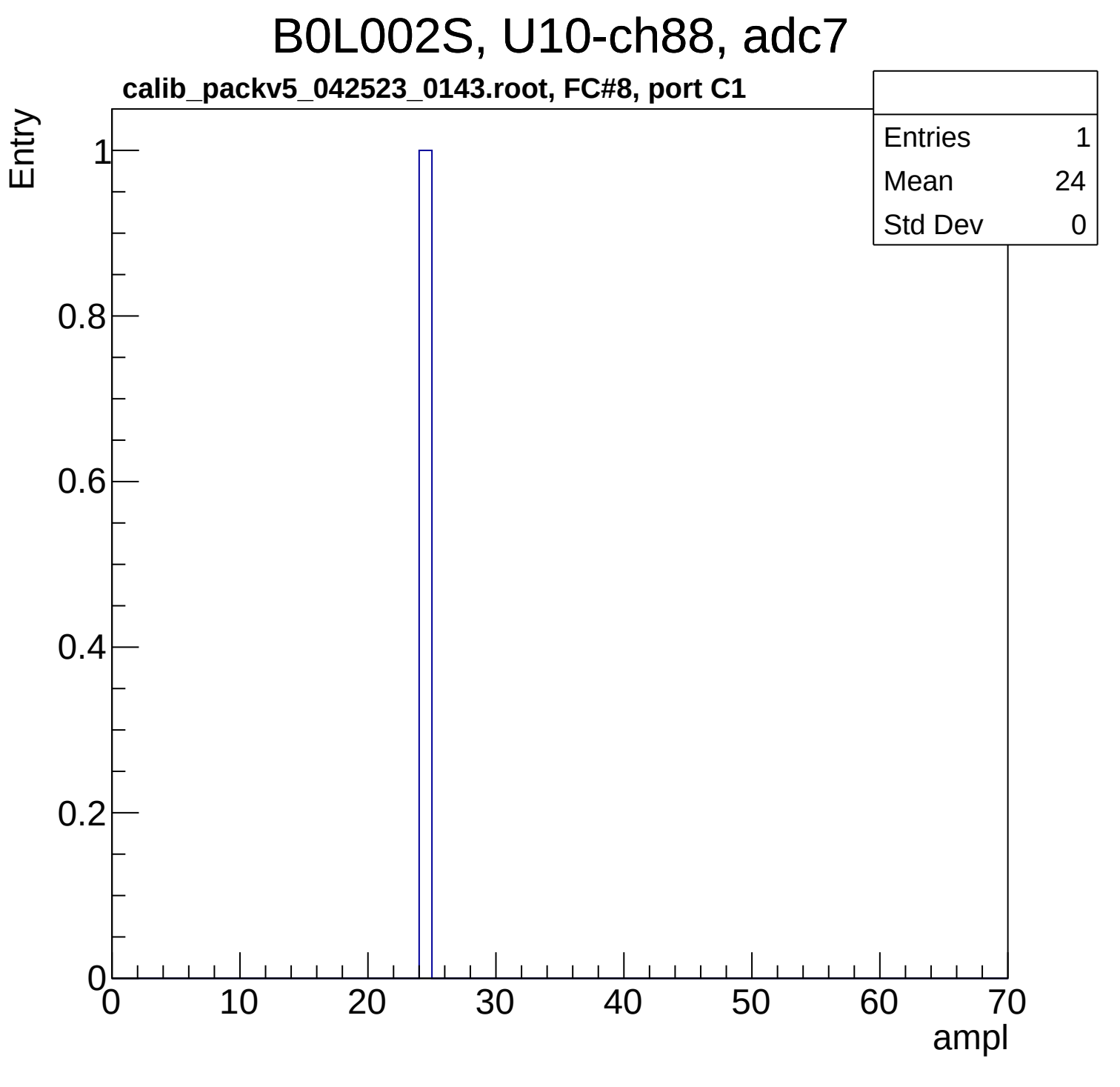

B0L002S, U10-ch88, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 45.25
Std Dev 3.801
Gaus mean : 45.7034
Gaus Width: 4.0252
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

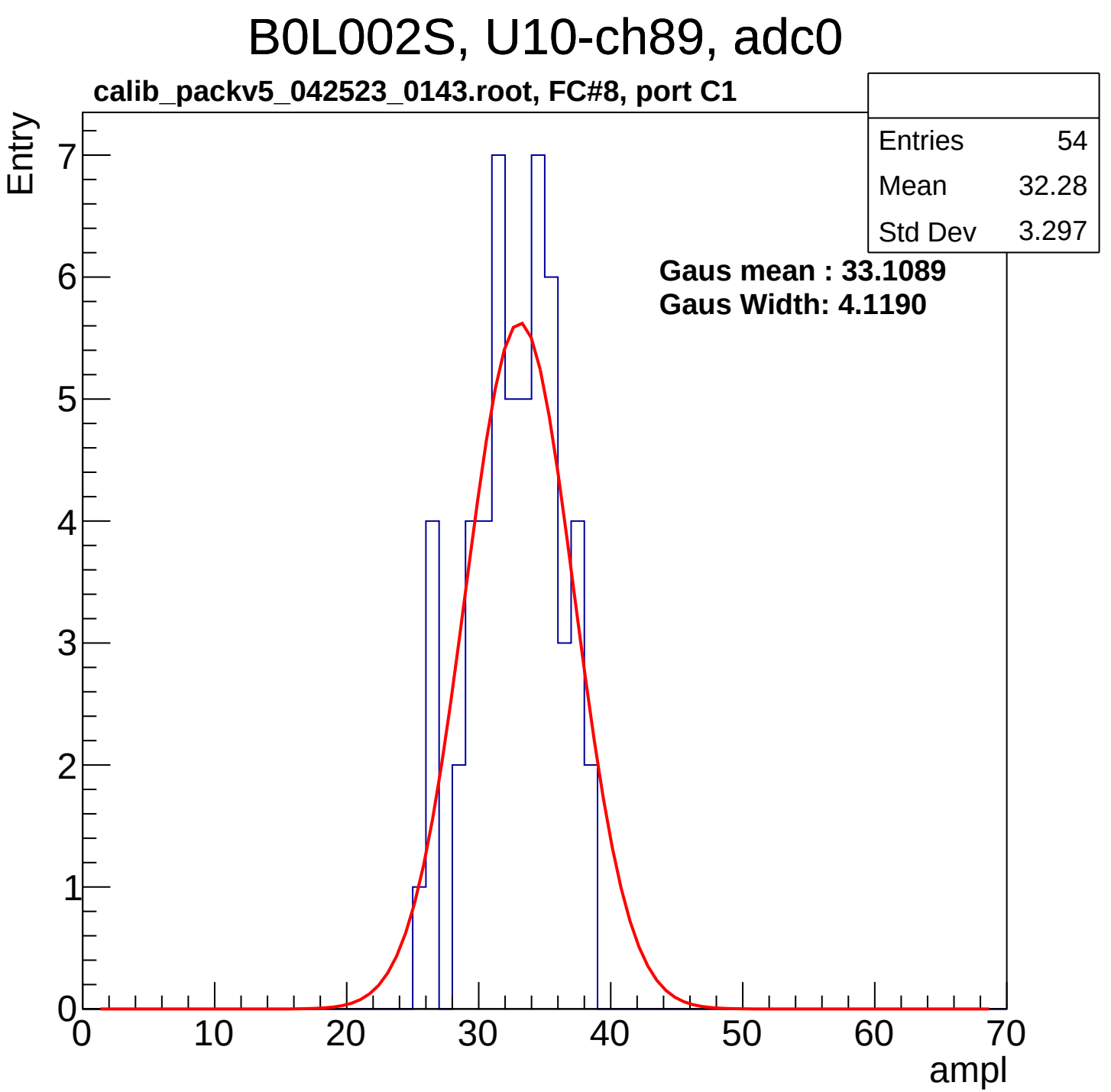

B0L002S, U10-ch88, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 51.53
Std Dev 3.286
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

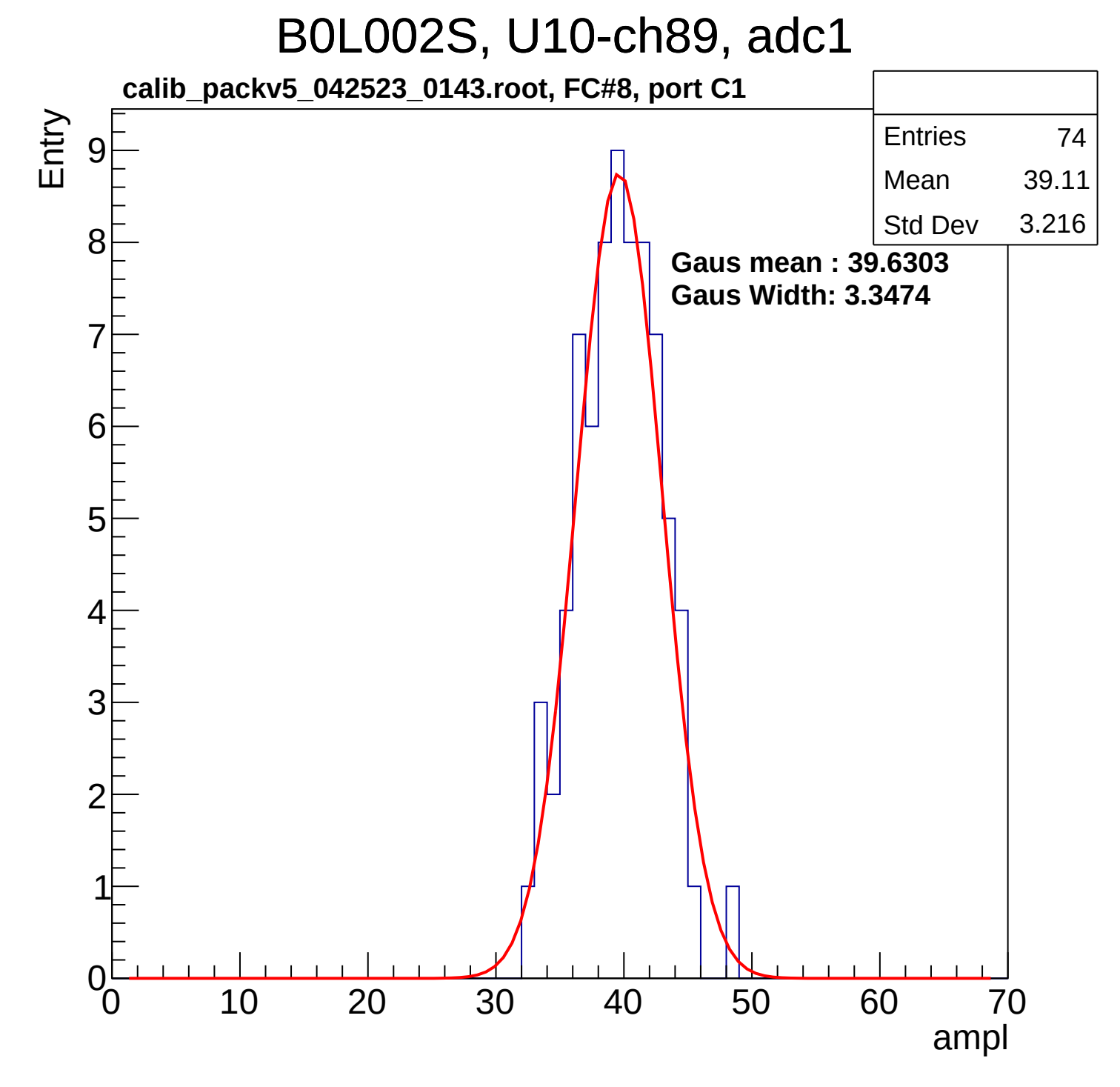

B0L002S, U10-ch88, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 57.32
Std Dev 3.017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

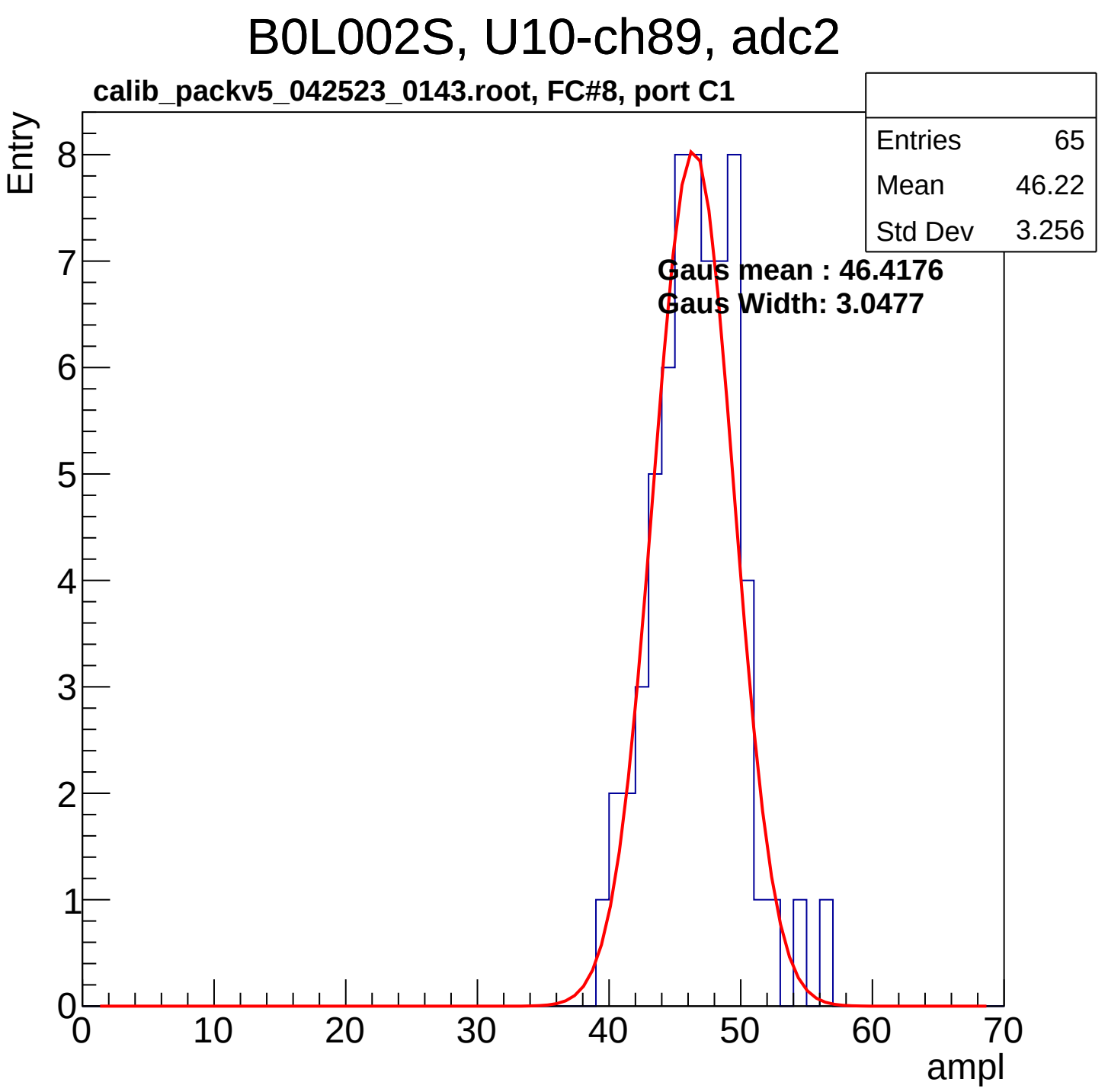

B0L002S, U10-ch88, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.67
Std Dev 1.675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

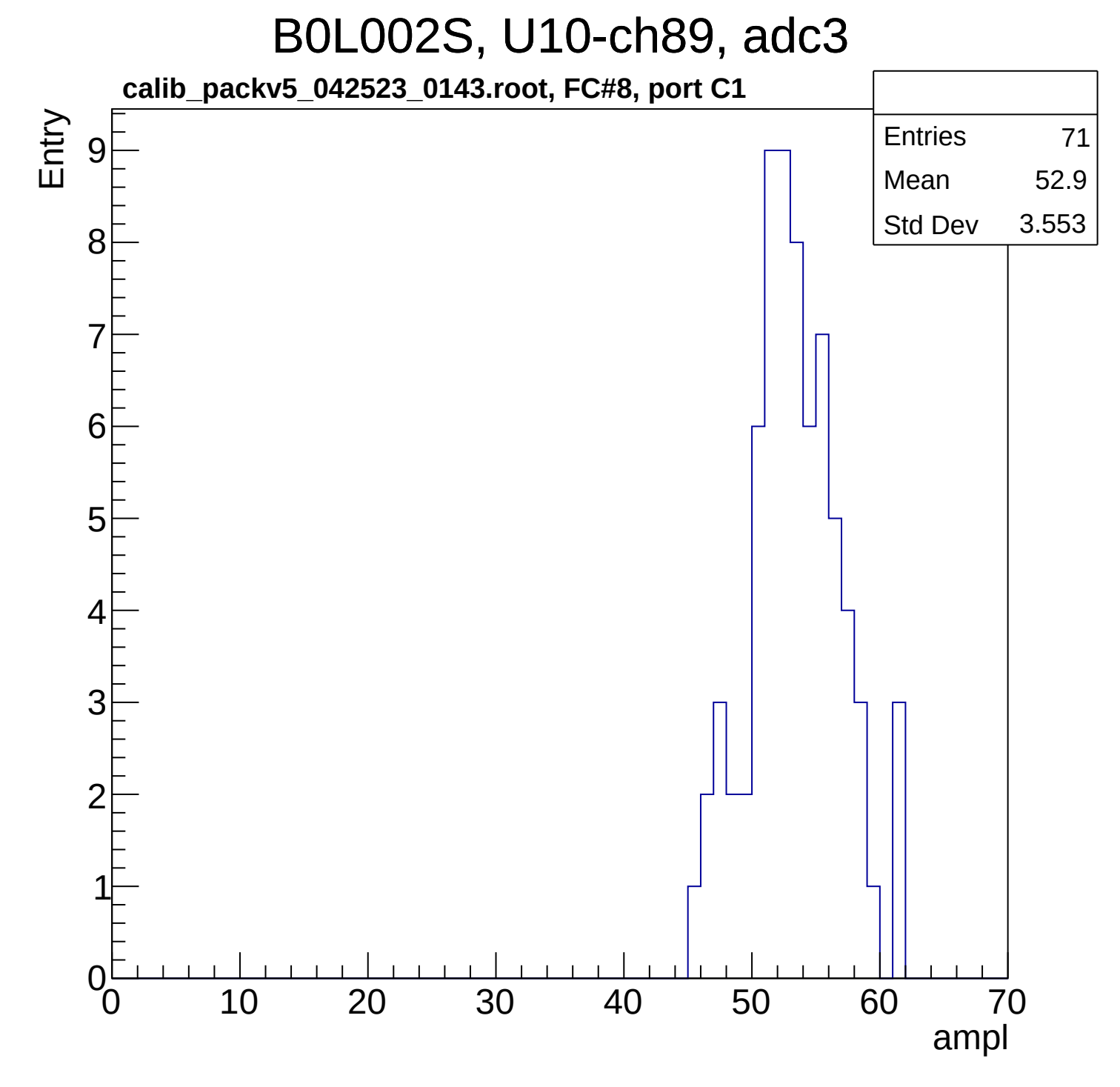

B0L002S, U10-ch88, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

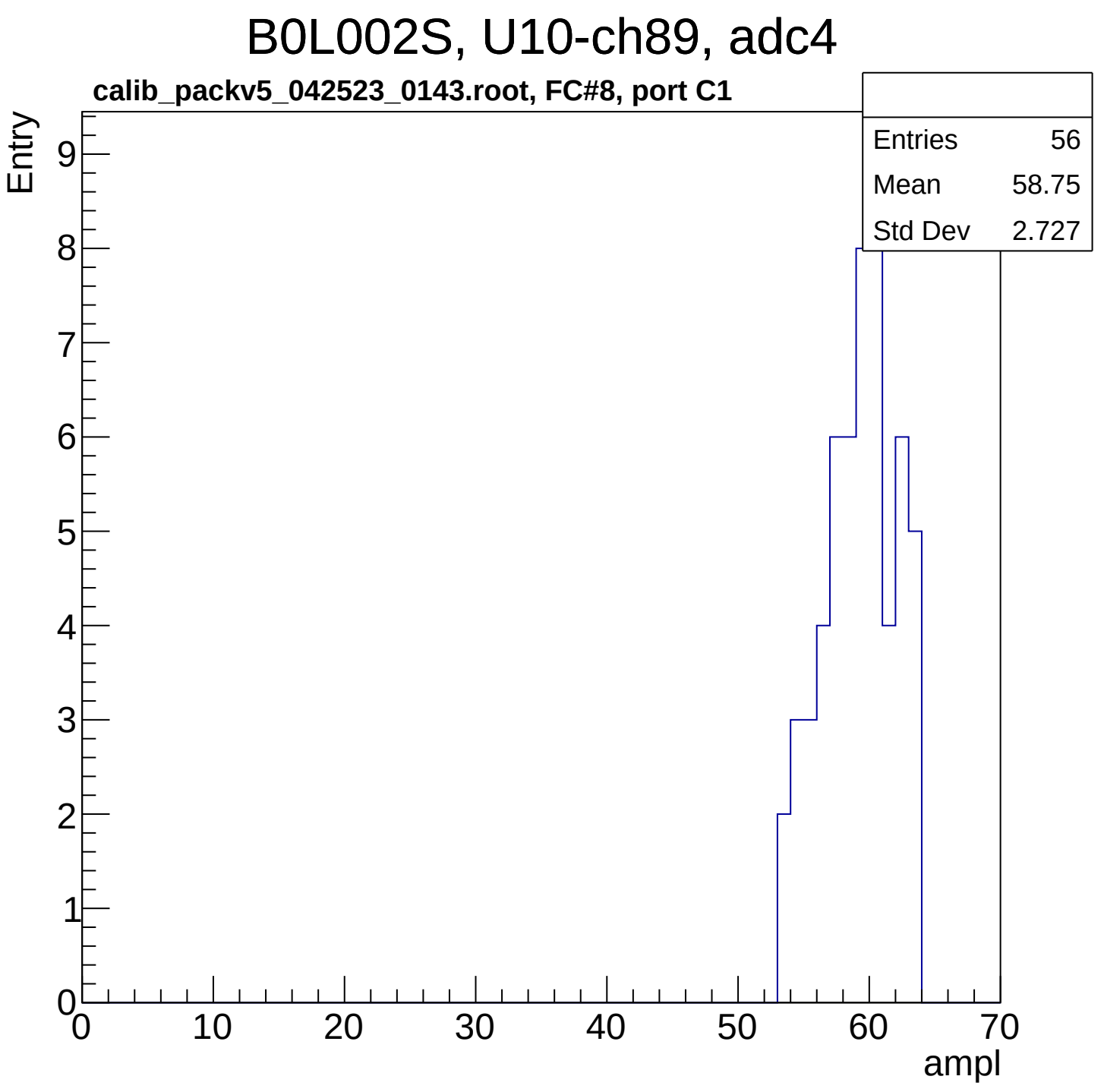

B0L002S, U10-ch88, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

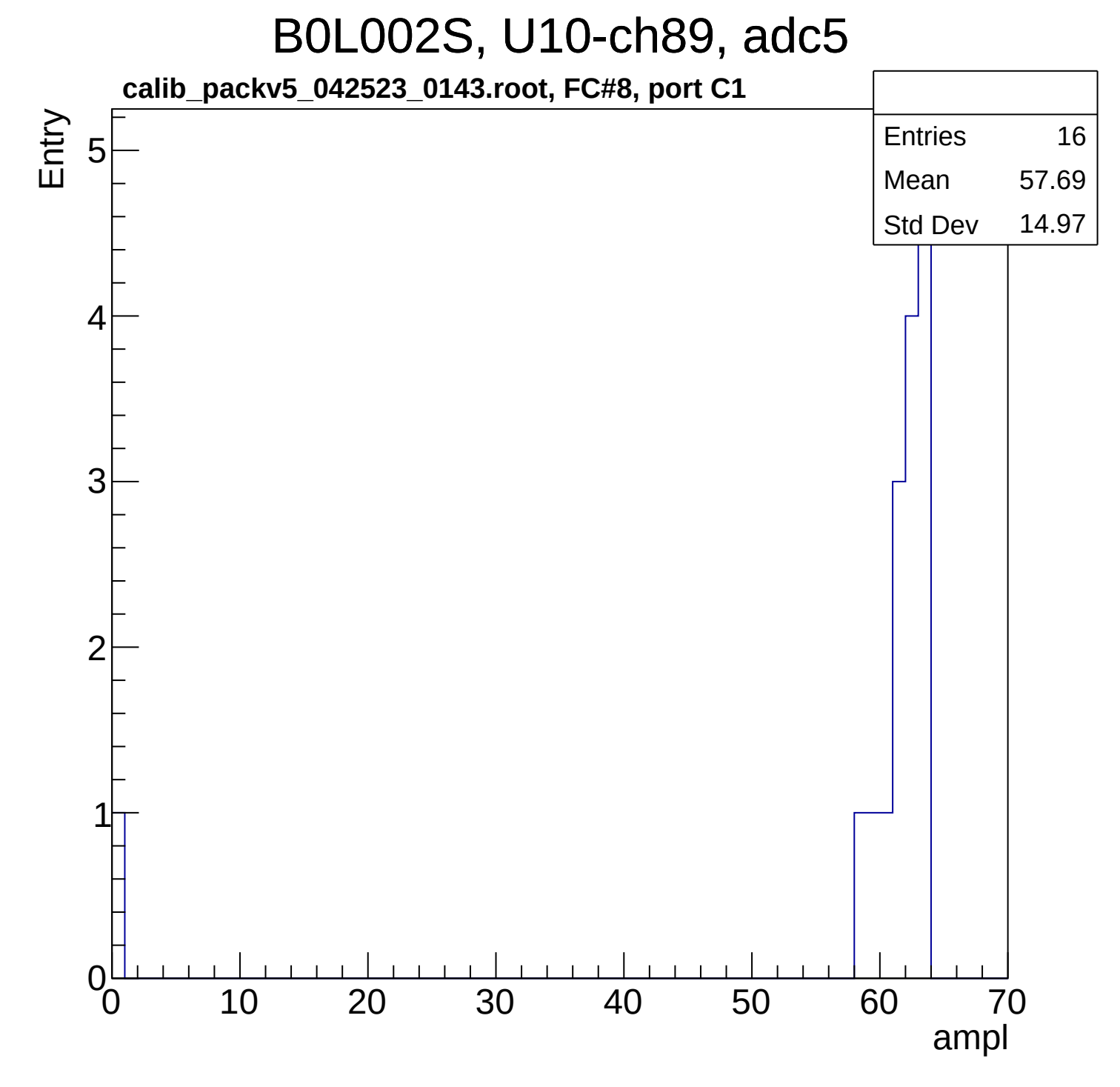

B0L002S, U10-ch89, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 32.28
Std Dev 3.297
Gaus mean : 33.1089
Gaus Width: 4.1190
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

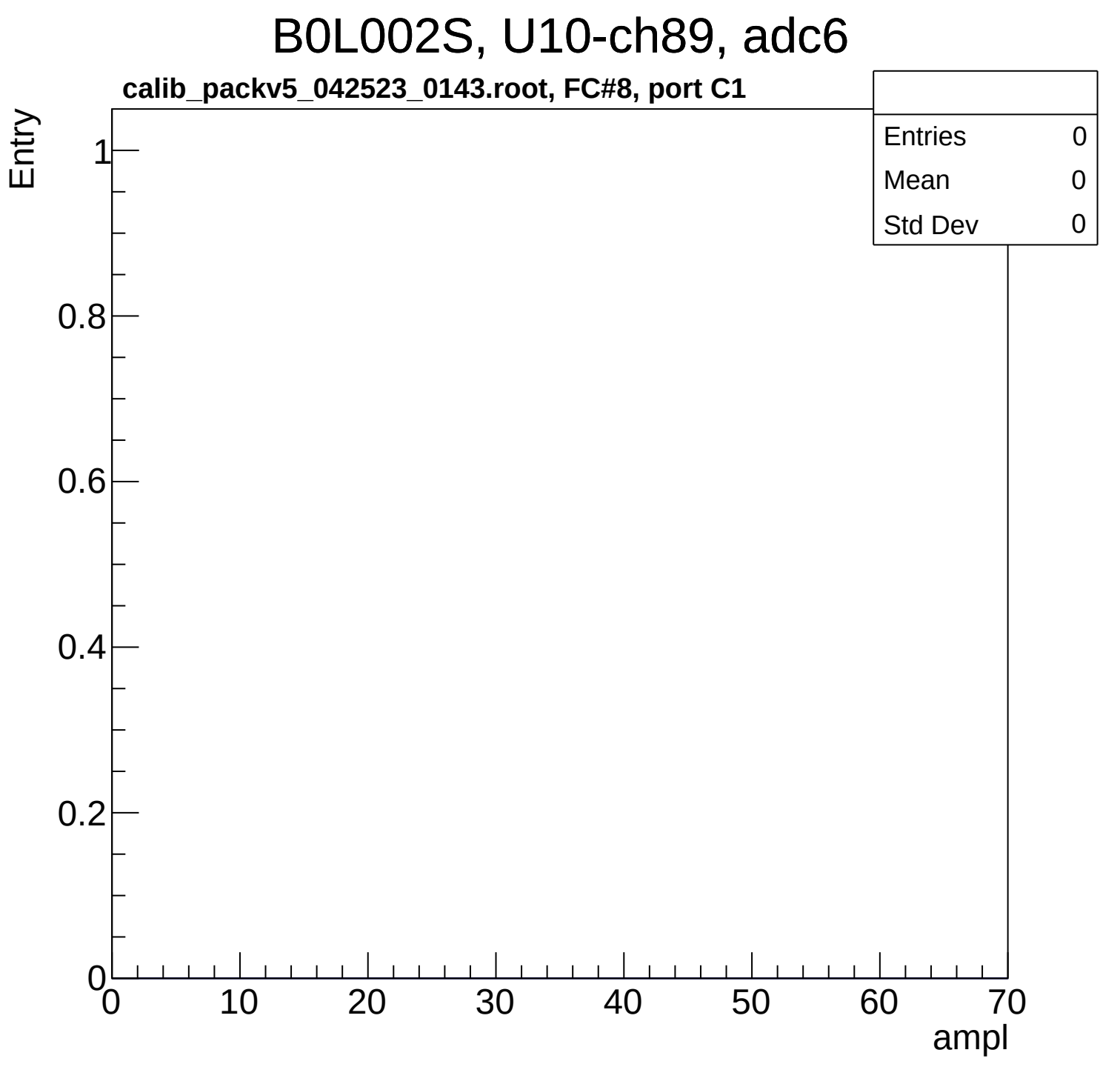

B0L002S, U10-ch89, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 39.11
Std Dev 3.216
Gaus mean : 39.6303
Gaus Width: 3.3474
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

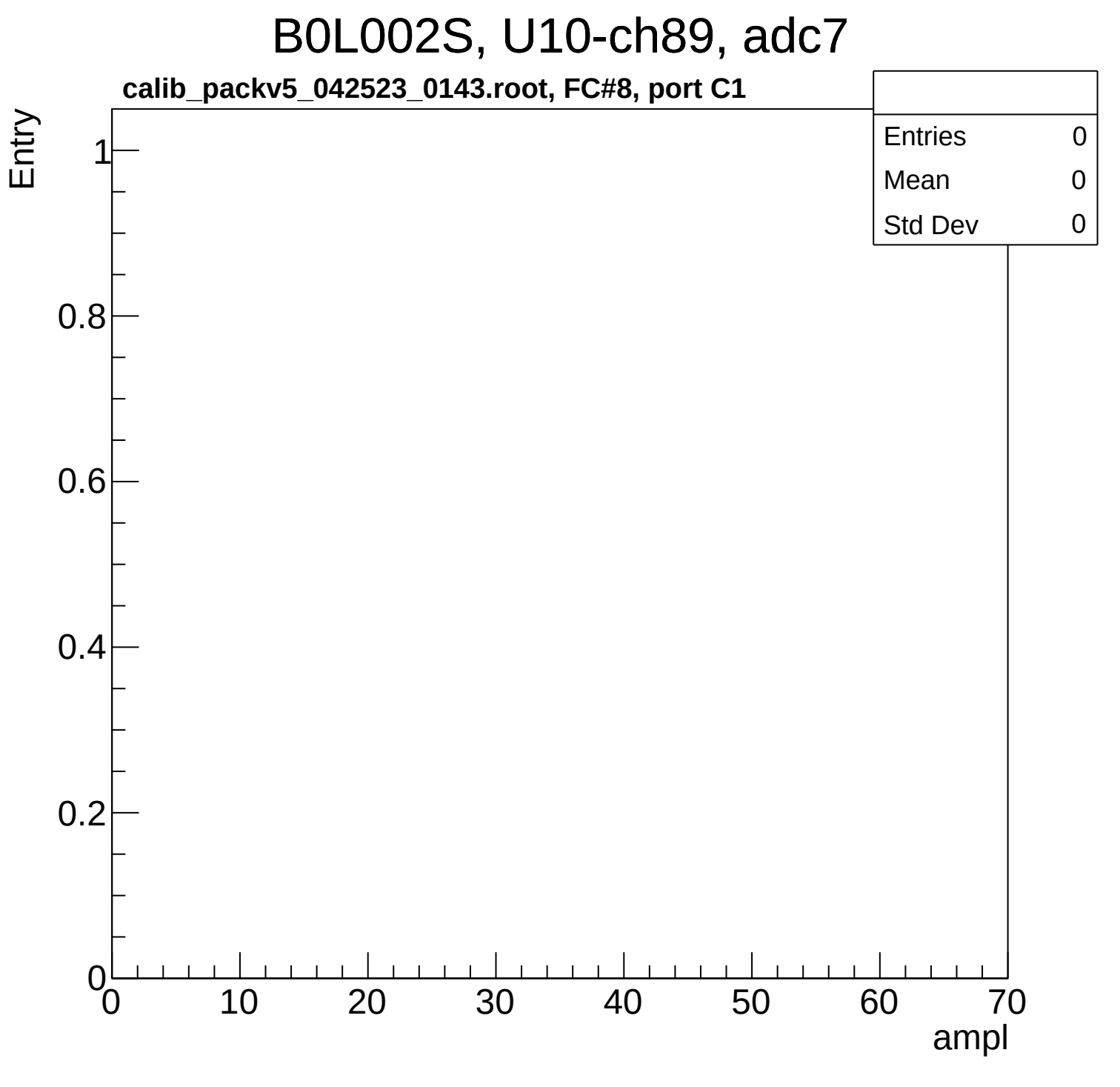

B0L002S, U10-ch89, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 46.22
Std Dev 3.256
Gaus mean : 46.4176
Gaus Width: 3.0477
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

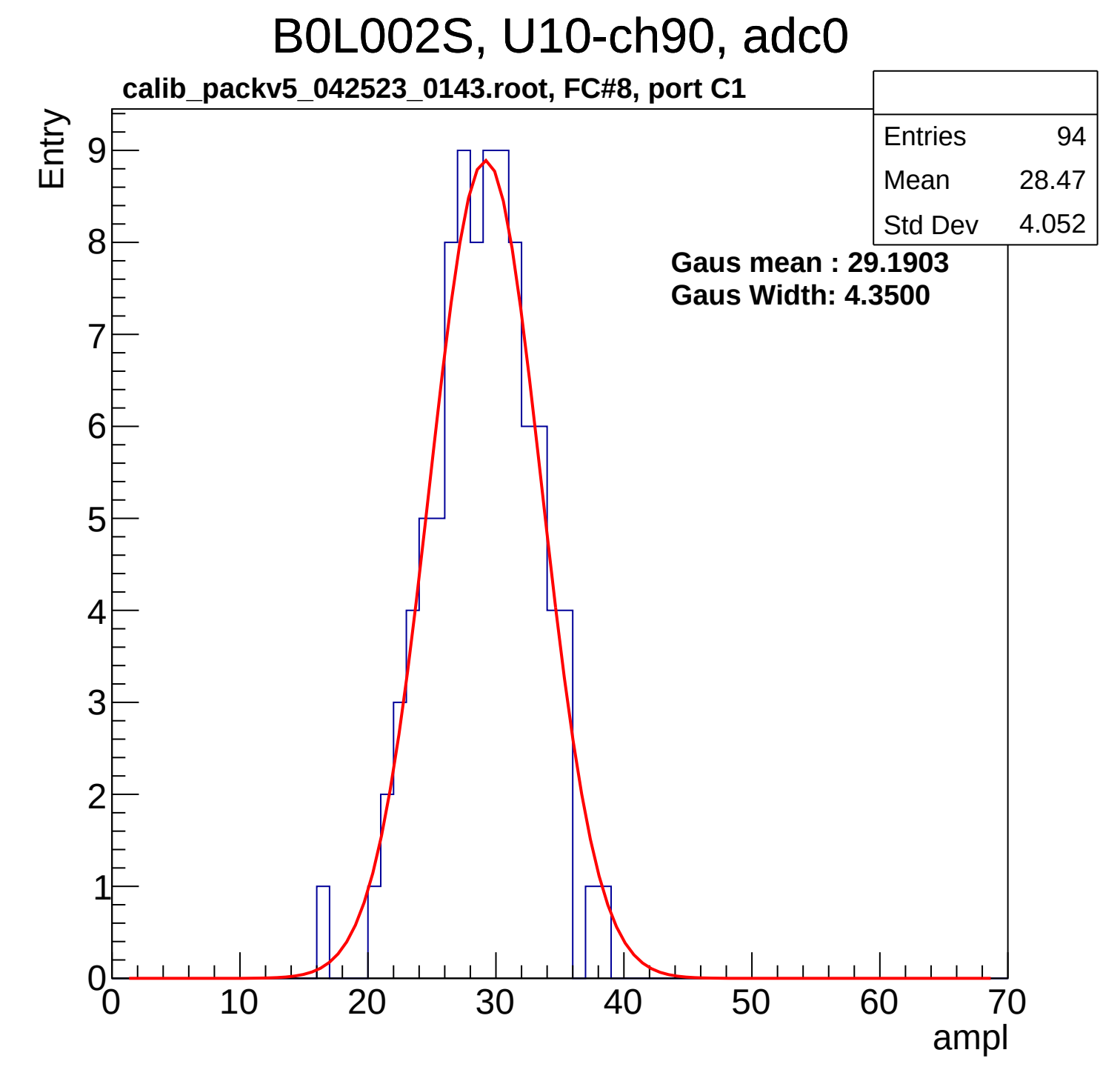

B0L002S, U10-ch89, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 52.9
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

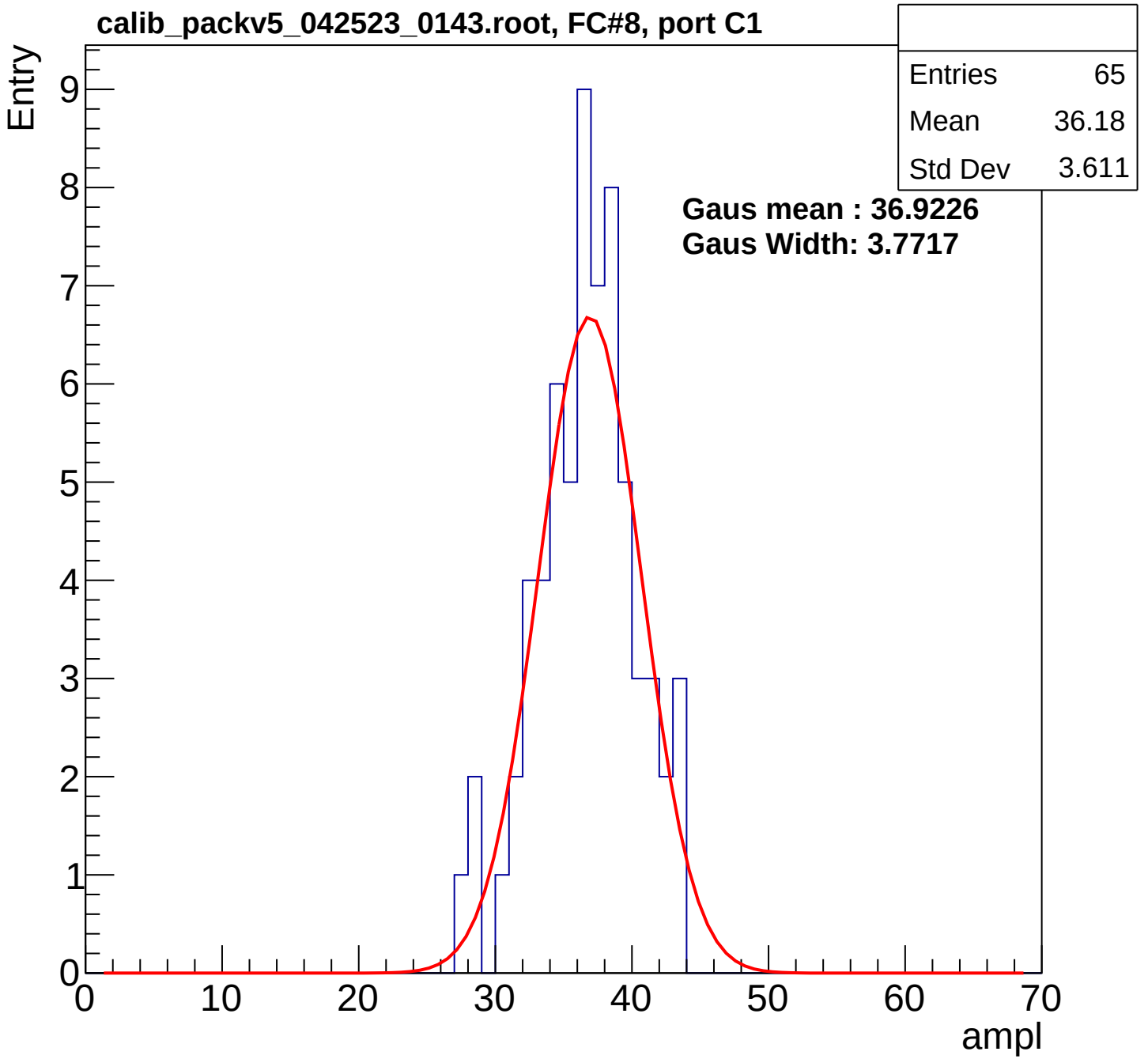

B0L002S, U10-ch89, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 58.75
Std Dev 2.727
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

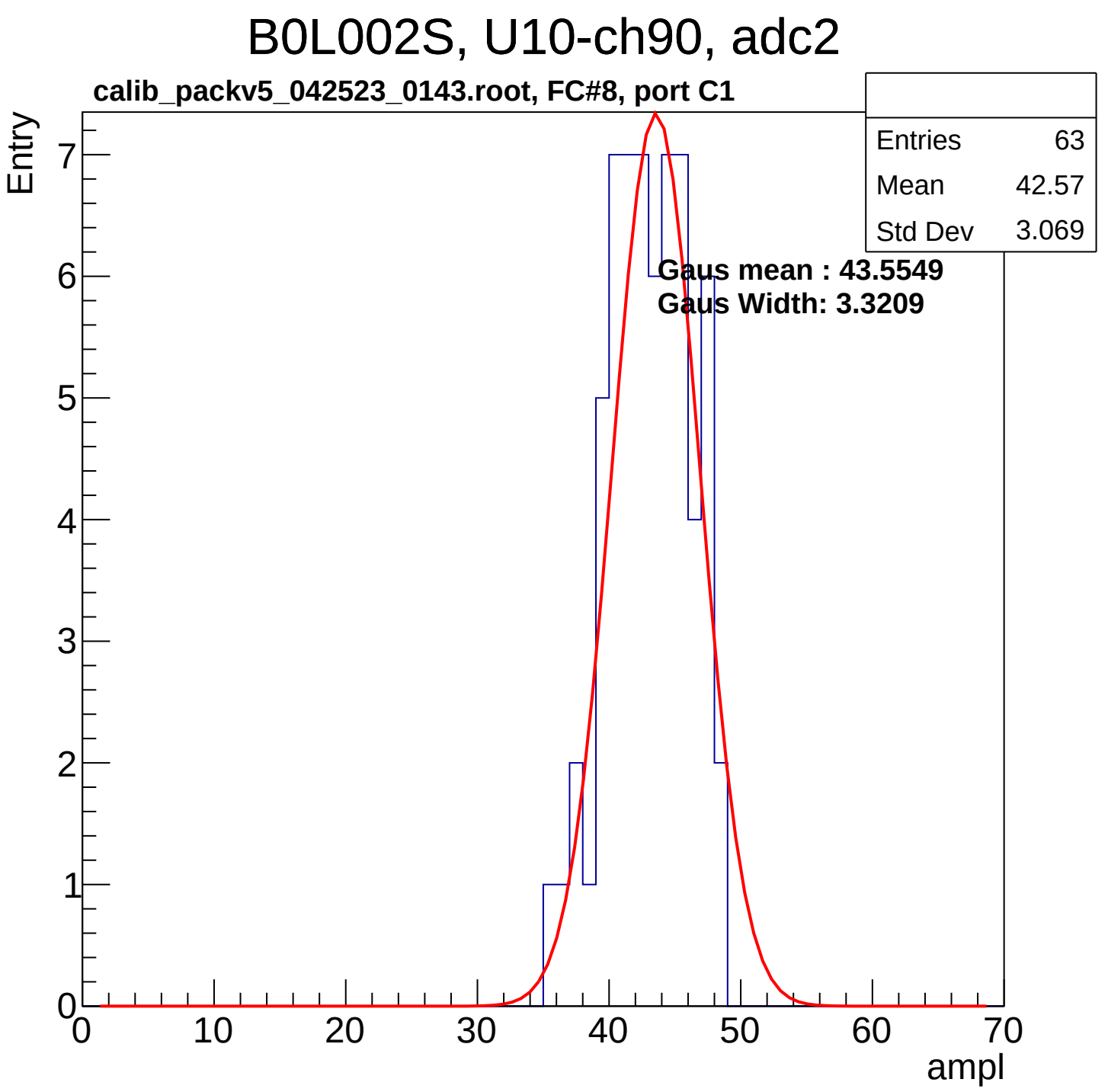

B0L002S, U10-ch89, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 16
Mean 57.69
Std Dev 14.97
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

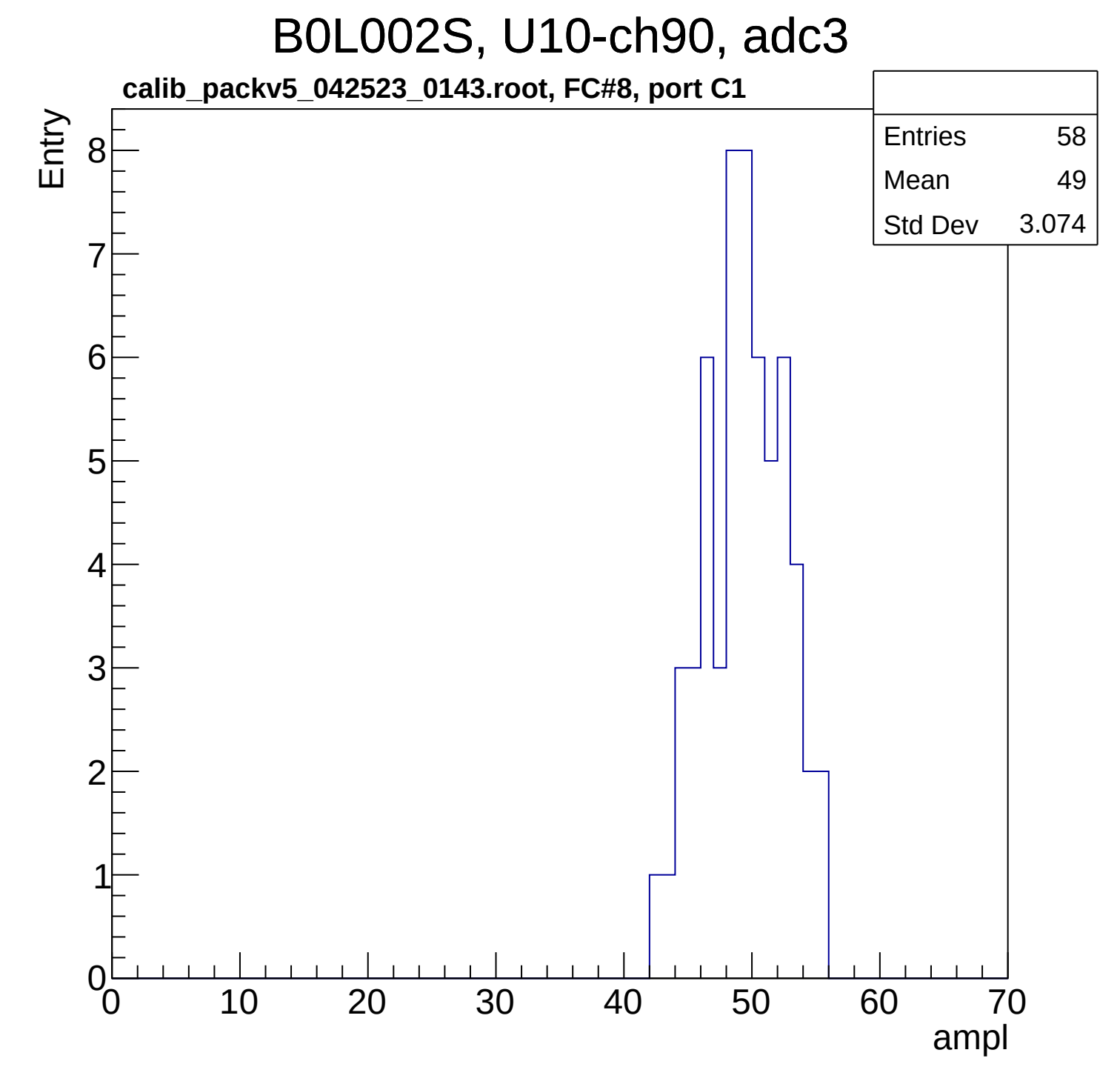

B0L002S, U10-ch89, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

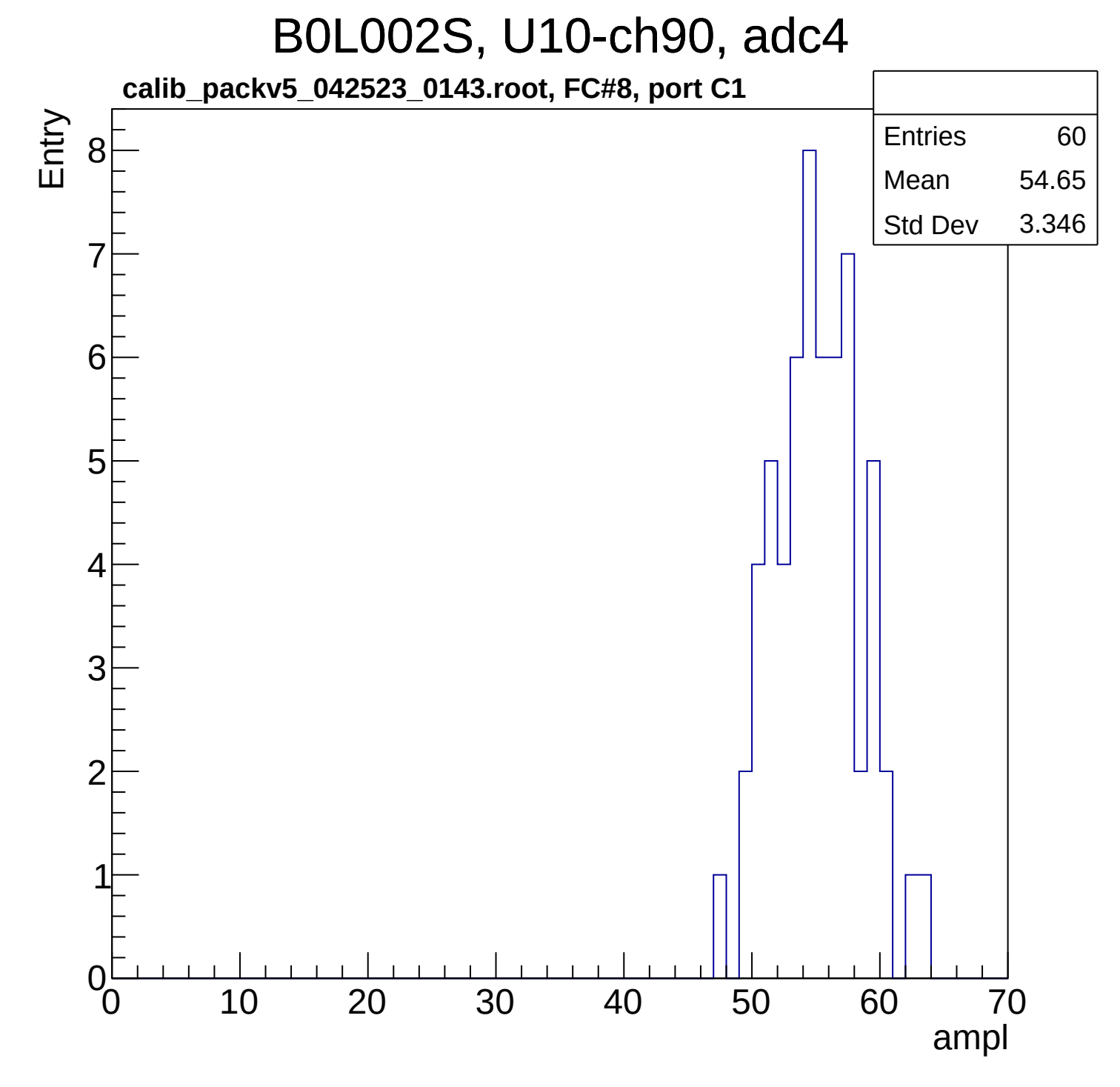

B0L002S, U10-ch89, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

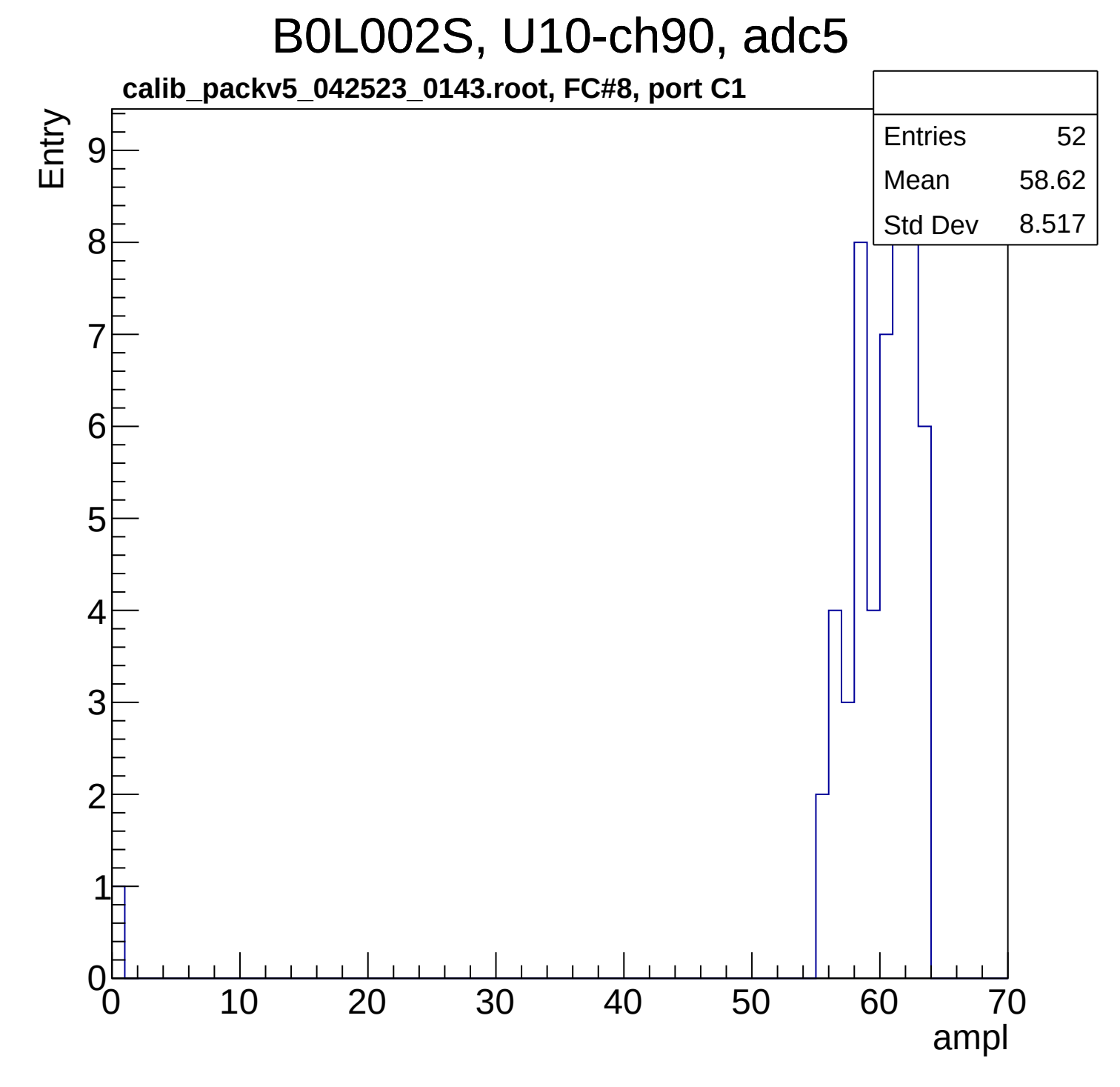

B0L002S, U10-ch90, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 94
Mean 28.47
Std Dev 4.052
Gaus mean : 29.1903
Gaus Width: 4.3500
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

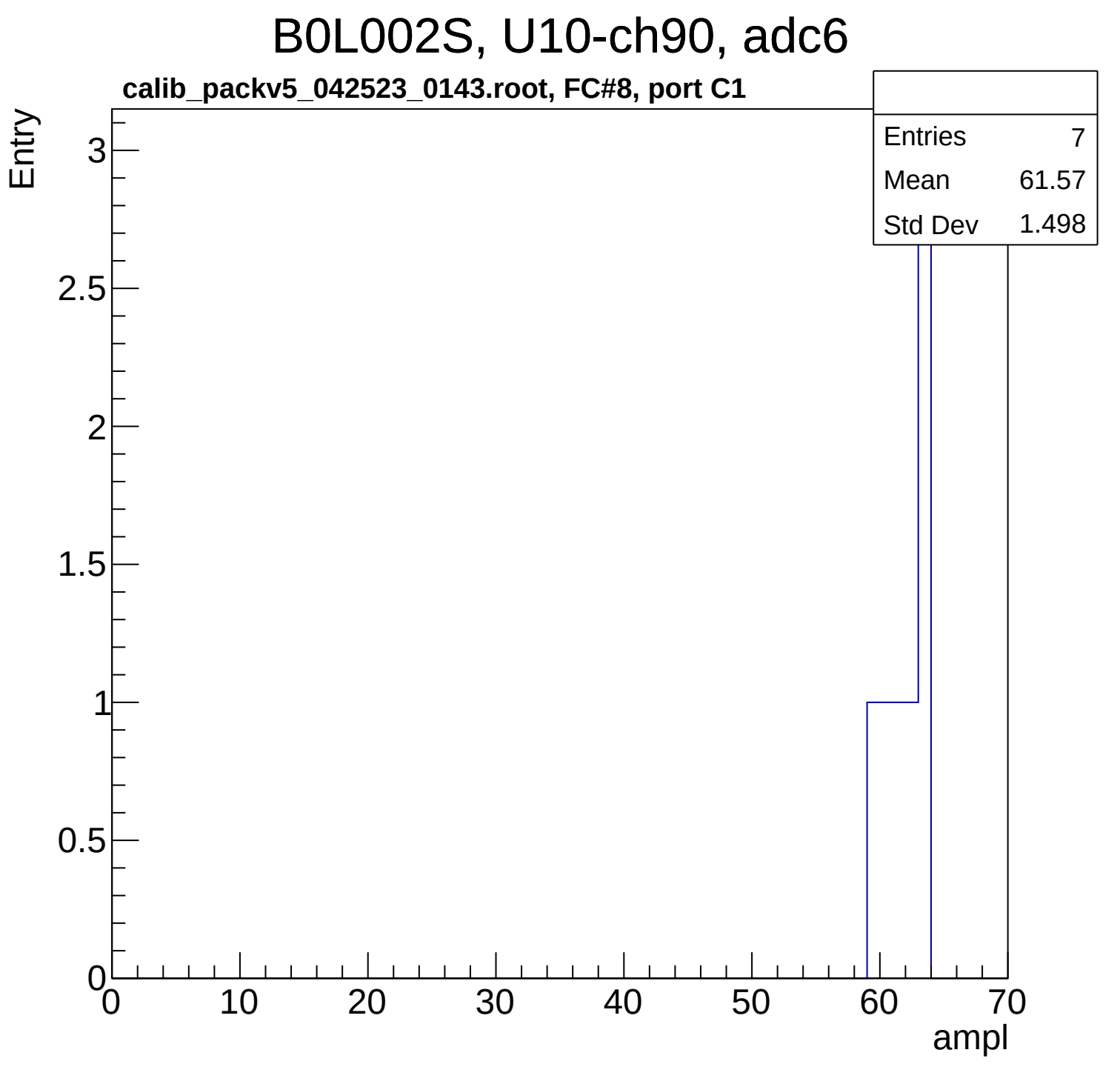

B0L002S, U10-ch90, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 36.18
Std Dev 3.611
Gaus mean : 36.9226
Gaus Width: 3.7717
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

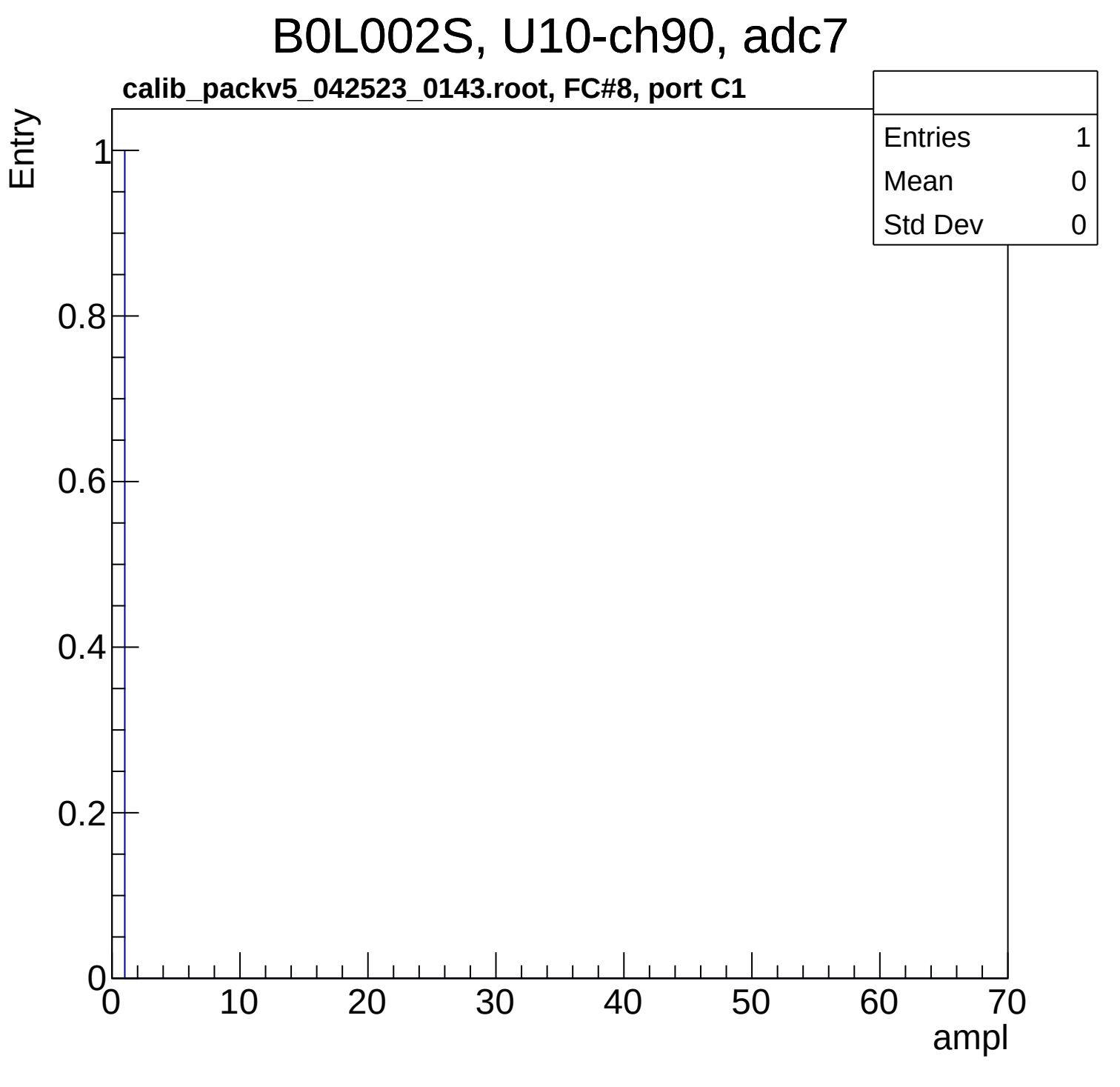

B0L002S, U10-ch90, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 42.57
Std Dev 3.069
Gaus mean : 43.5549
Gaus Width: 3.3209
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

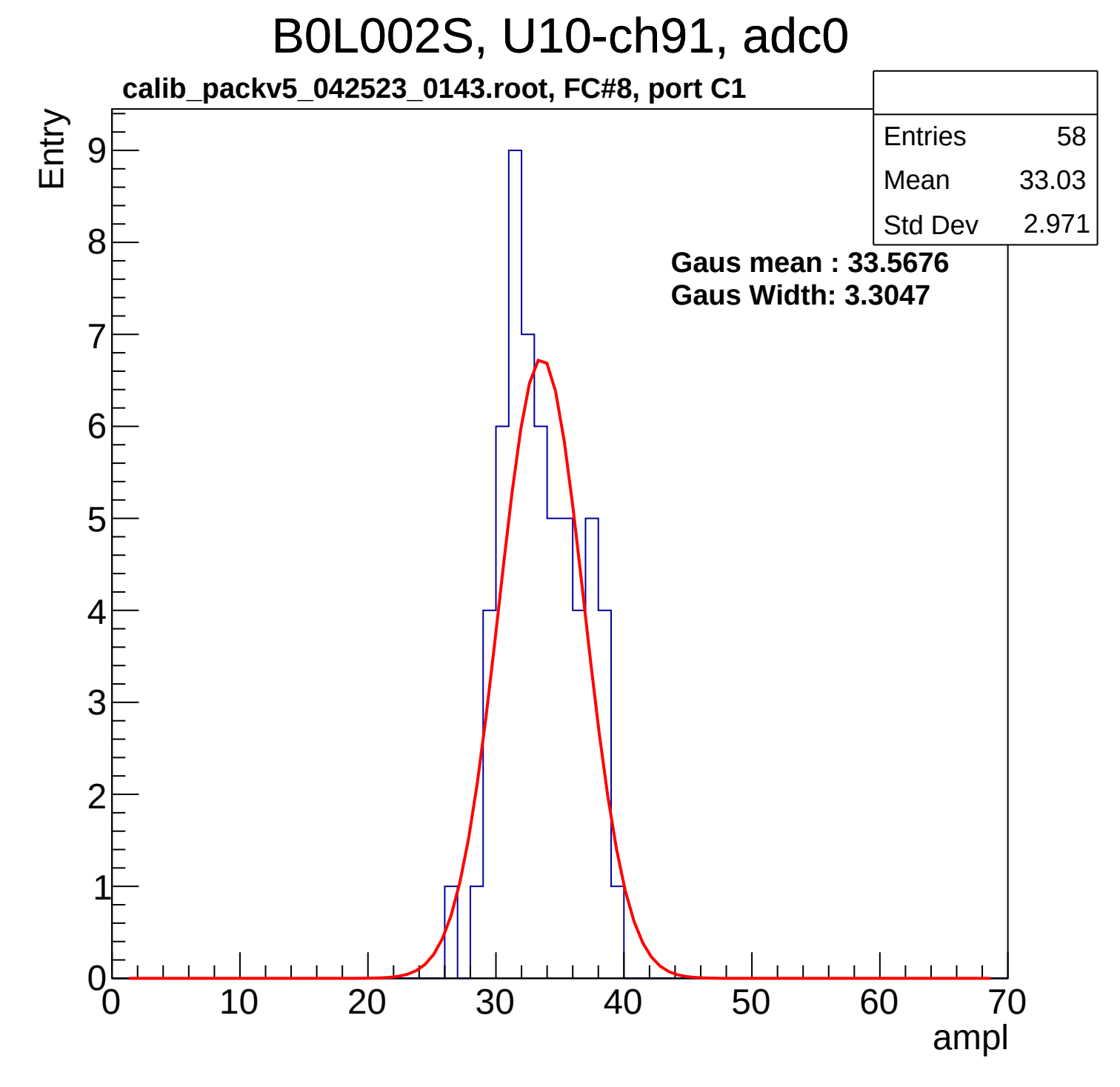

B0L002S, U10-ch90, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 49
Std Dev 3.074
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

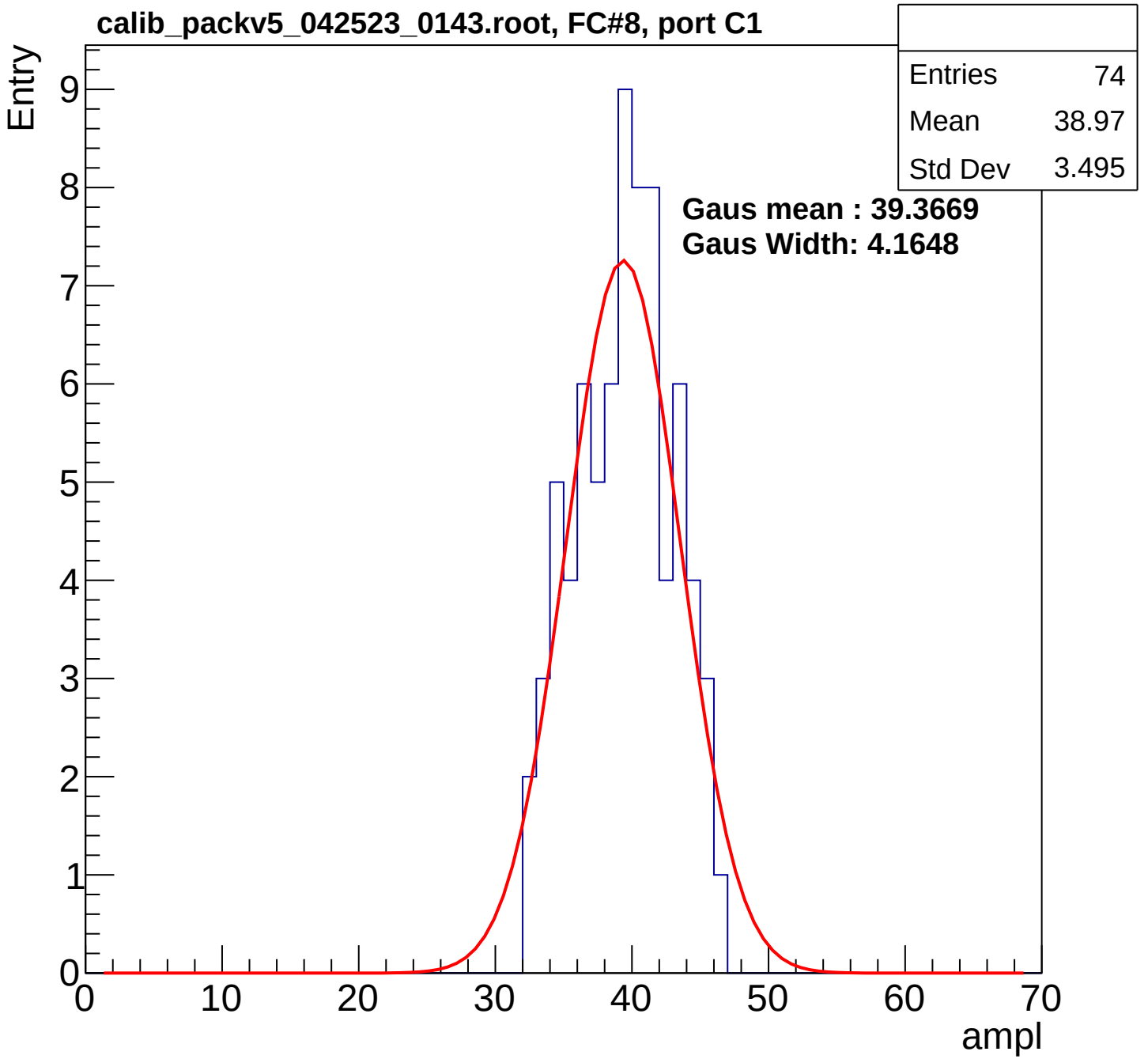

B0L002S, U10-ch90, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 54.65
Std Dev 3.346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

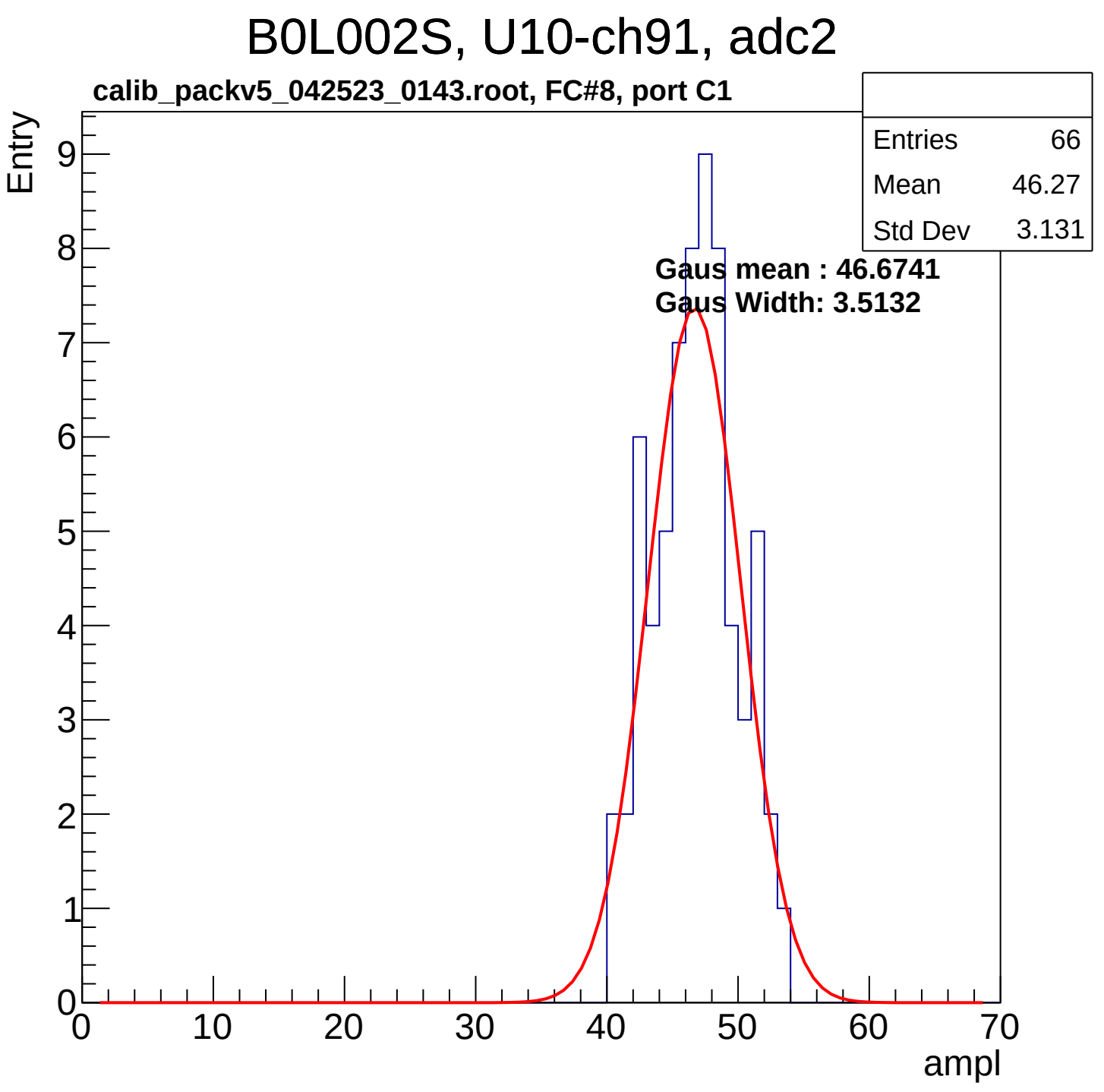

B0L002S, U10-ch90, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.62
Std Dev 8.517
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

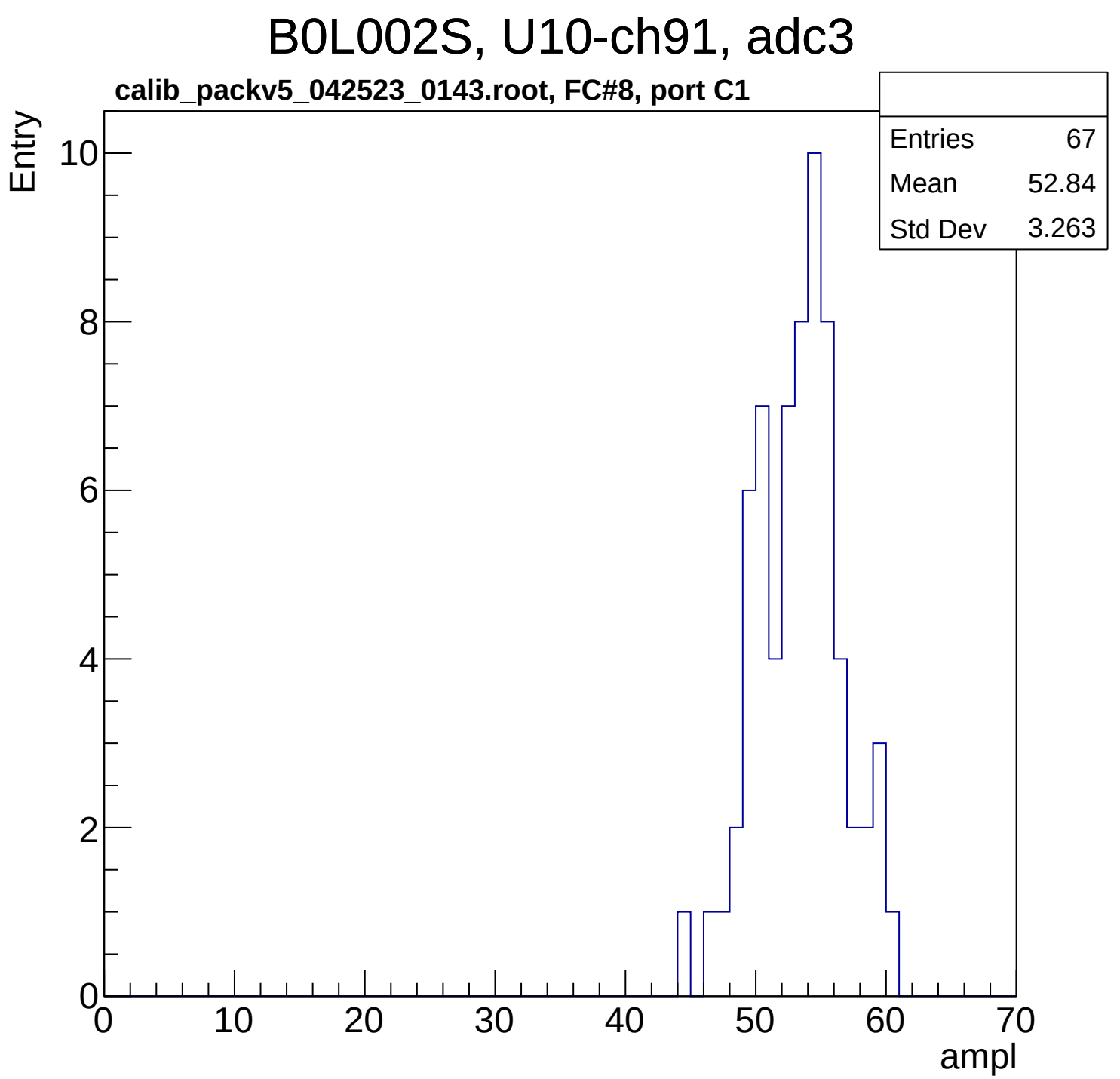

B0L002S, U10-ch90, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 7
Mean 61.57
Std Dev 1.498
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

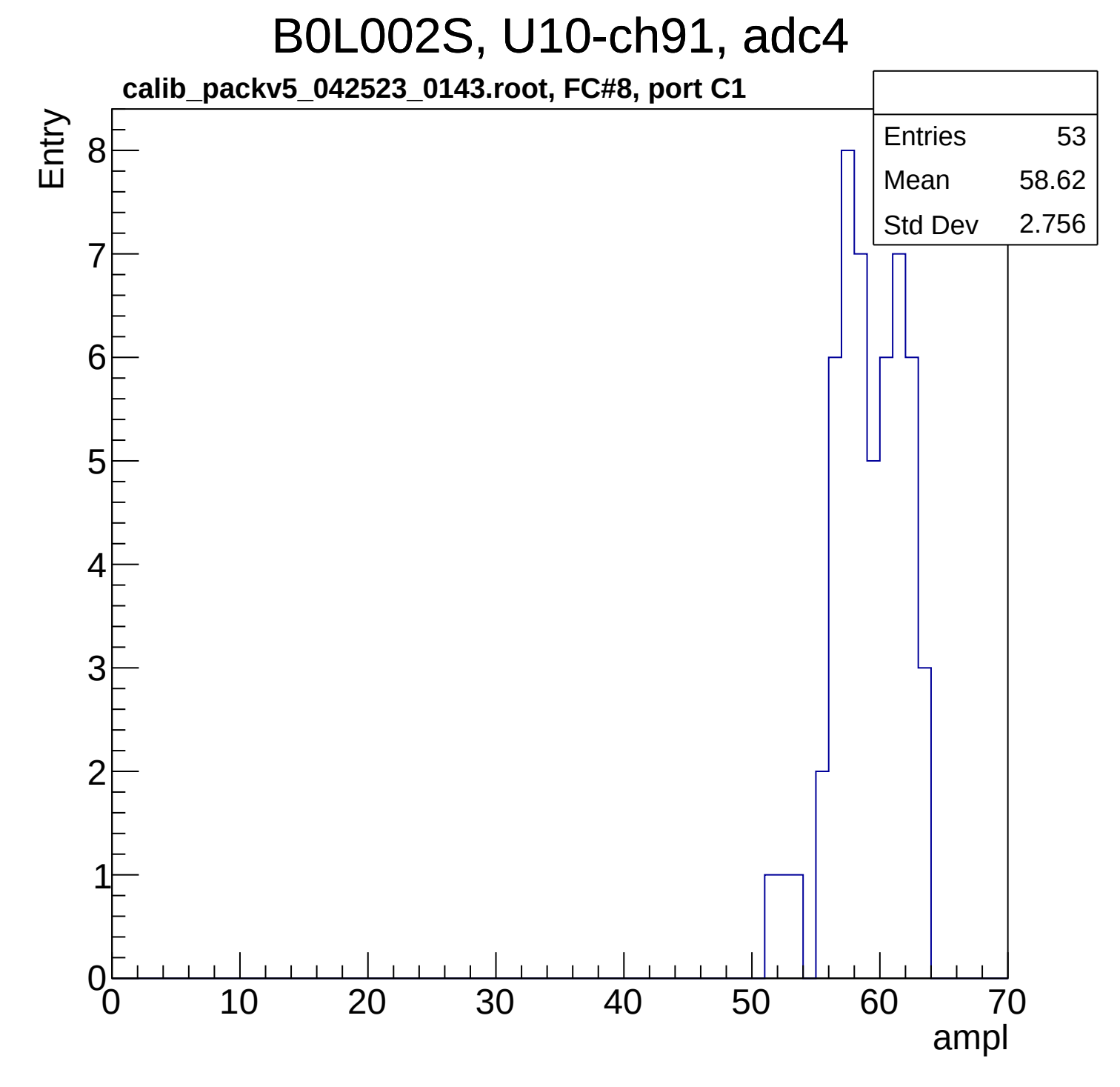

B0L002S, U10-ch90, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

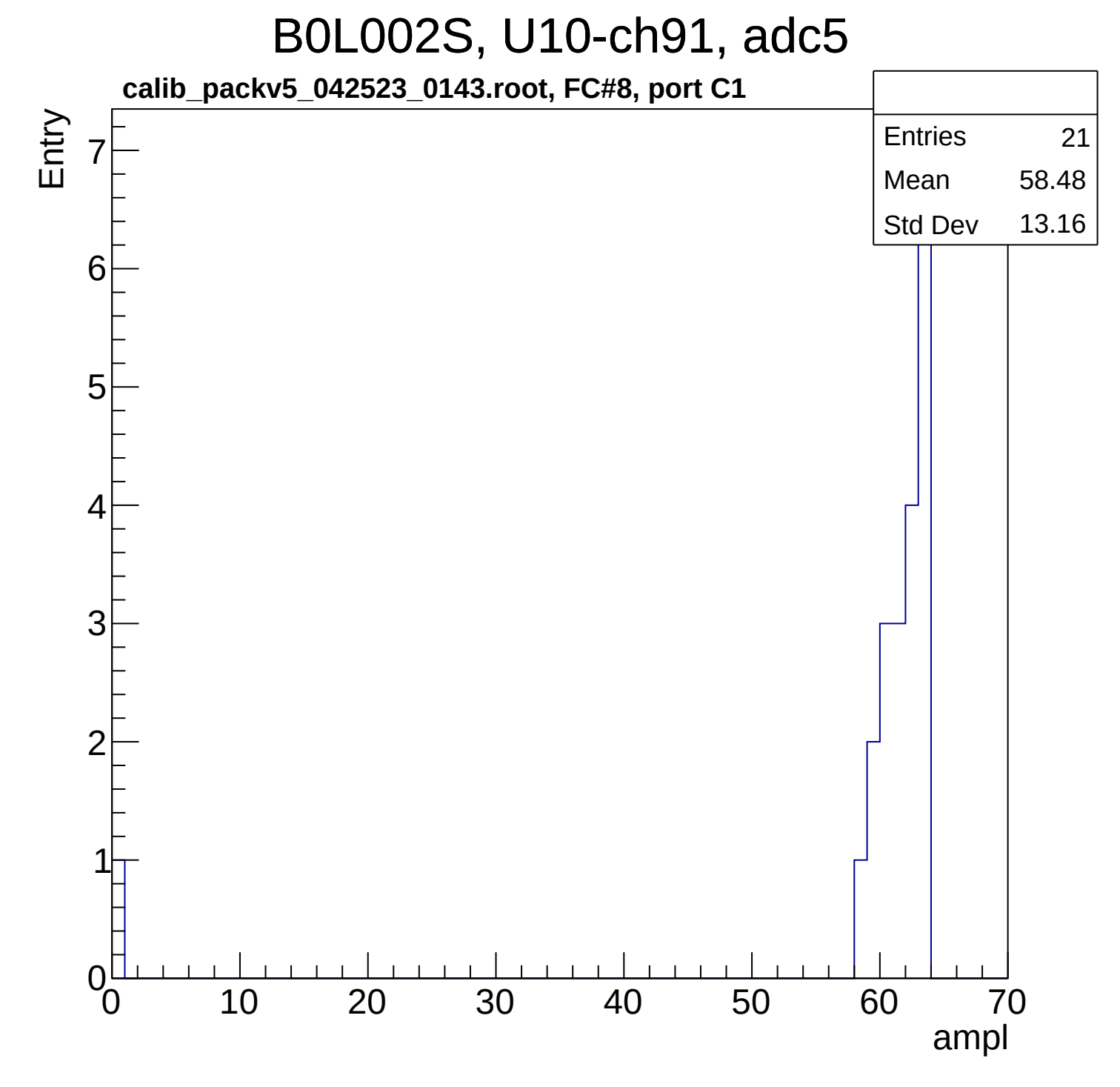

B0L002S, U10-ch91, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 33.03
Std Dev 2.971
Gaus mean : 33.5676
Gaus Width: 3.3047
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

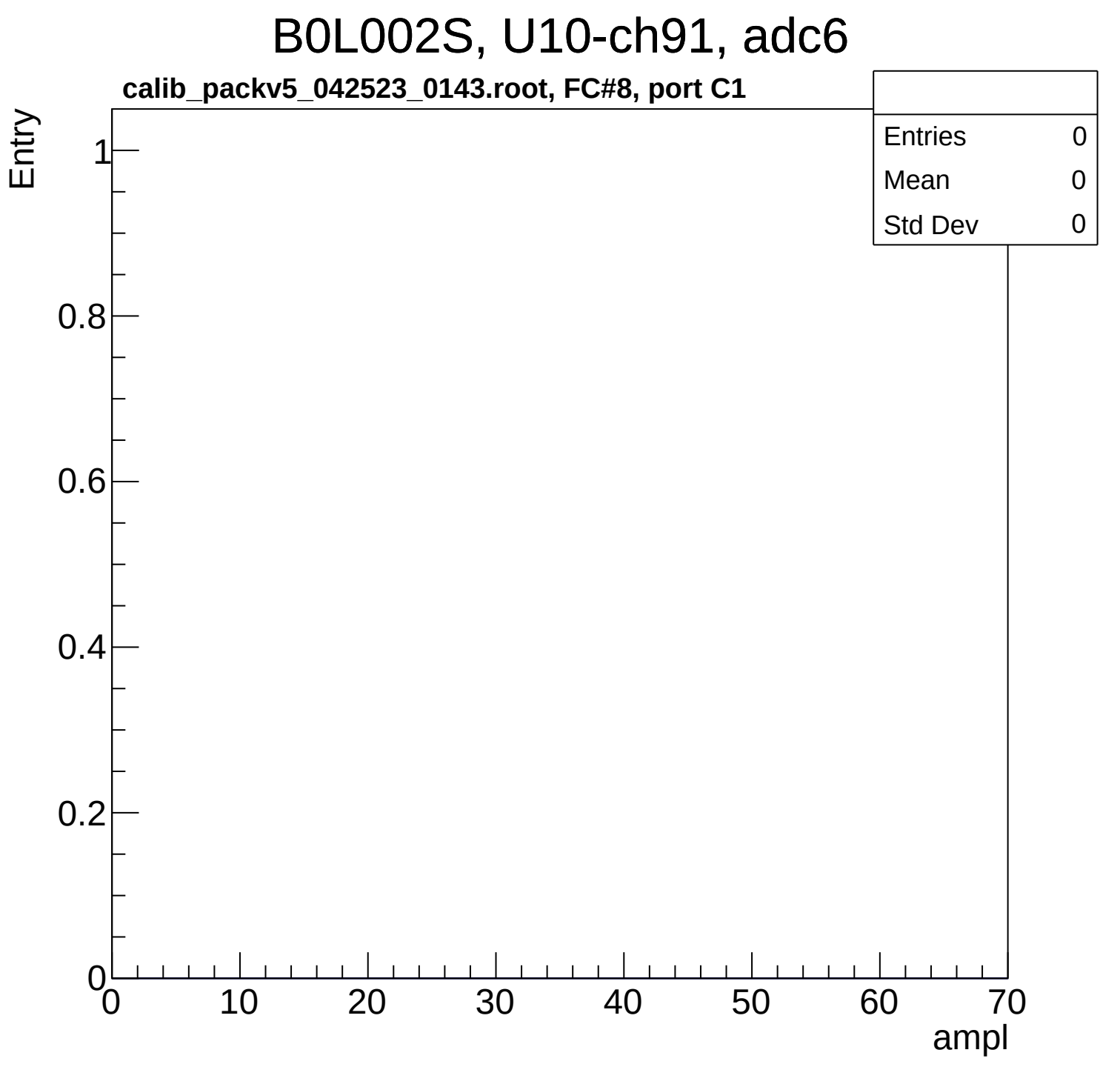

B0L002S, U10-ch91, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 38.97
Std Dev 3.495
Gaus mean : 39.3669
Gaus Width: 4.1648
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

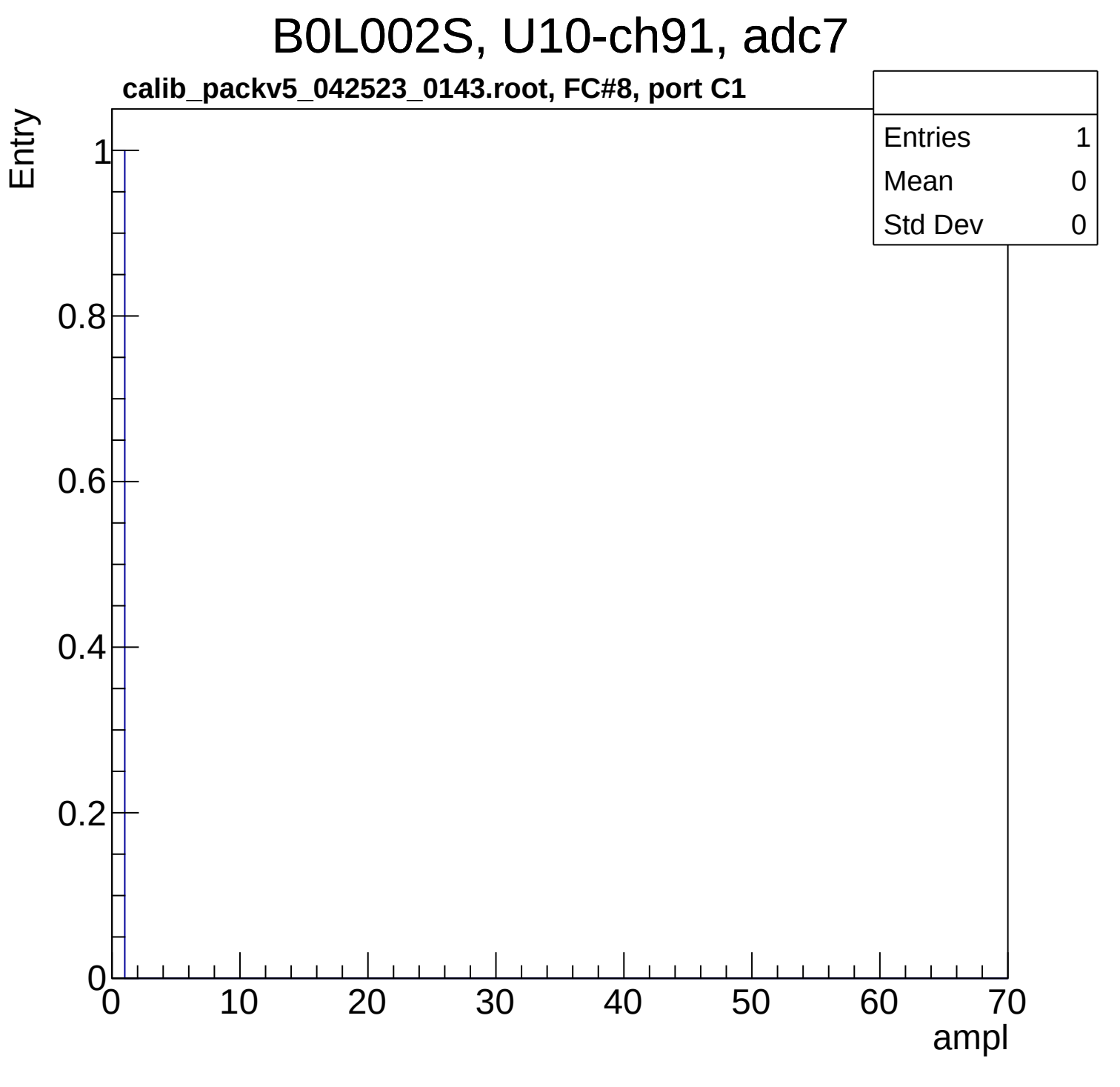

B0L002S, U10-ch91, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 46.27
Std Dev 3.131
Gaus mean : 46.6741
Gaus Width: 3.5132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

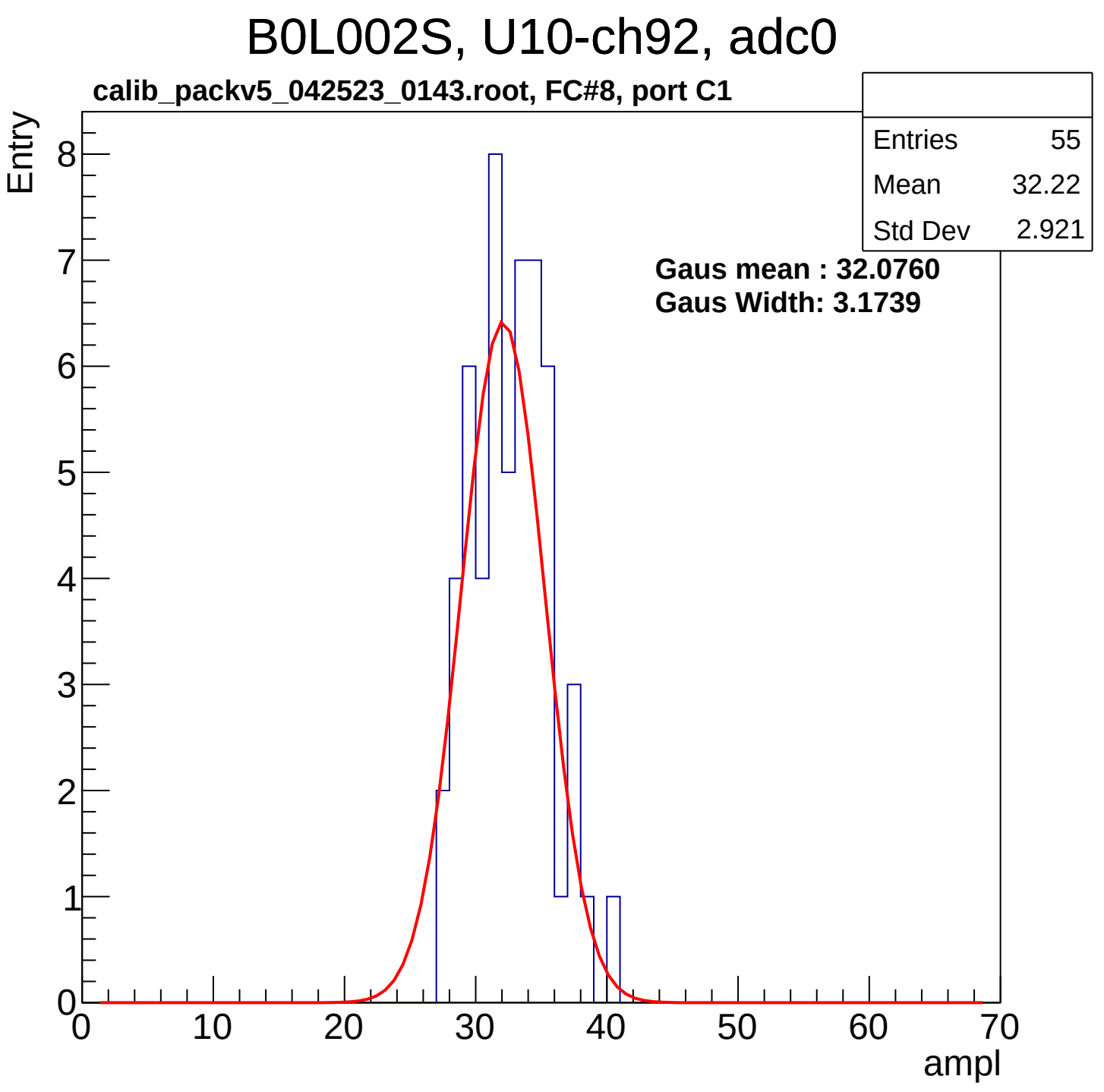

B0L002S, U10-ch91, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 52.84
Std Dev 3.263
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

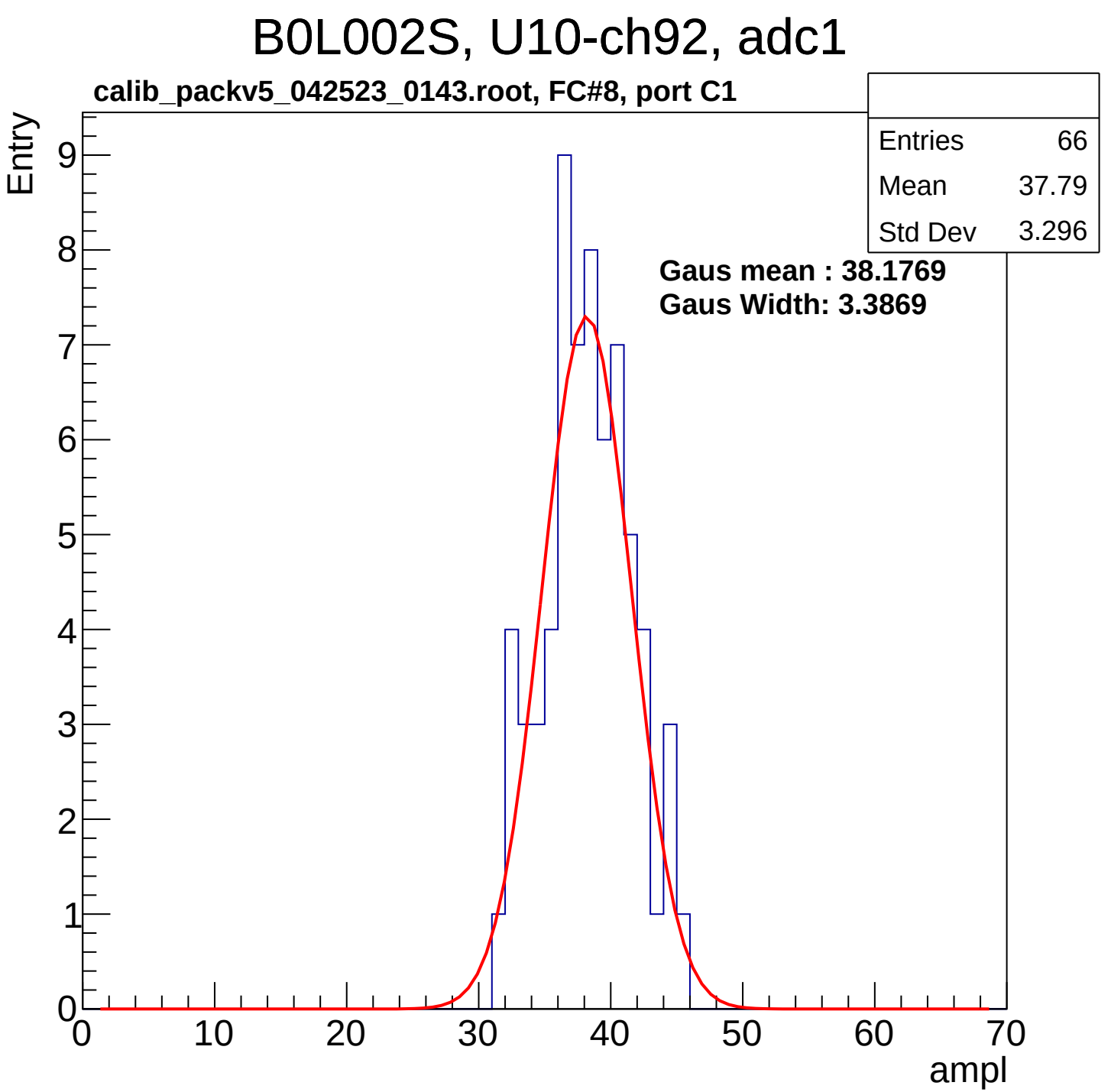

B0L002S, U10-ch91, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 58.62
Std Dev 2.756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

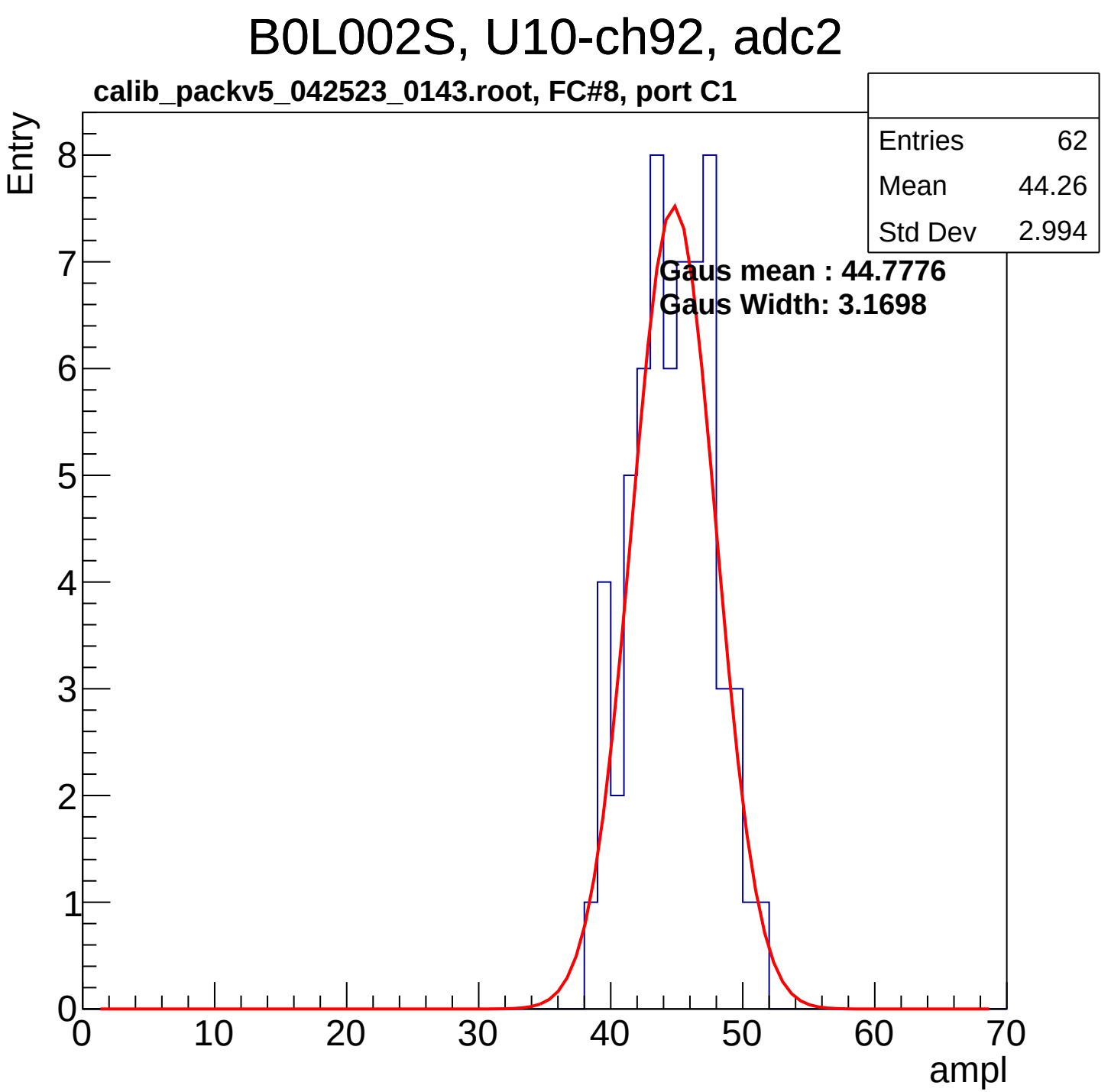

B0L002S, U10-ch91, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.48
Std Dev 13.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

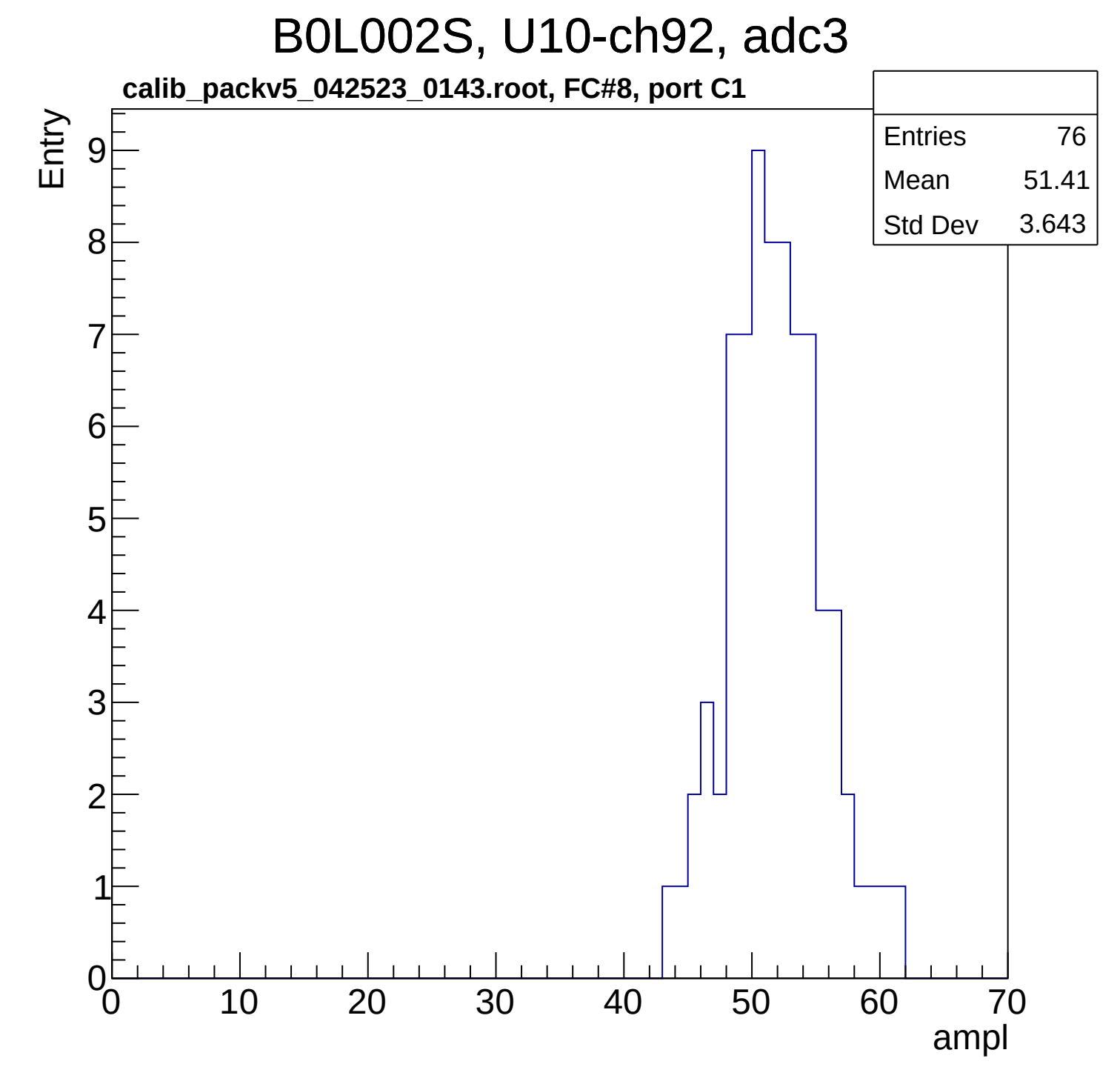

B0L002S, U10-ch91, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

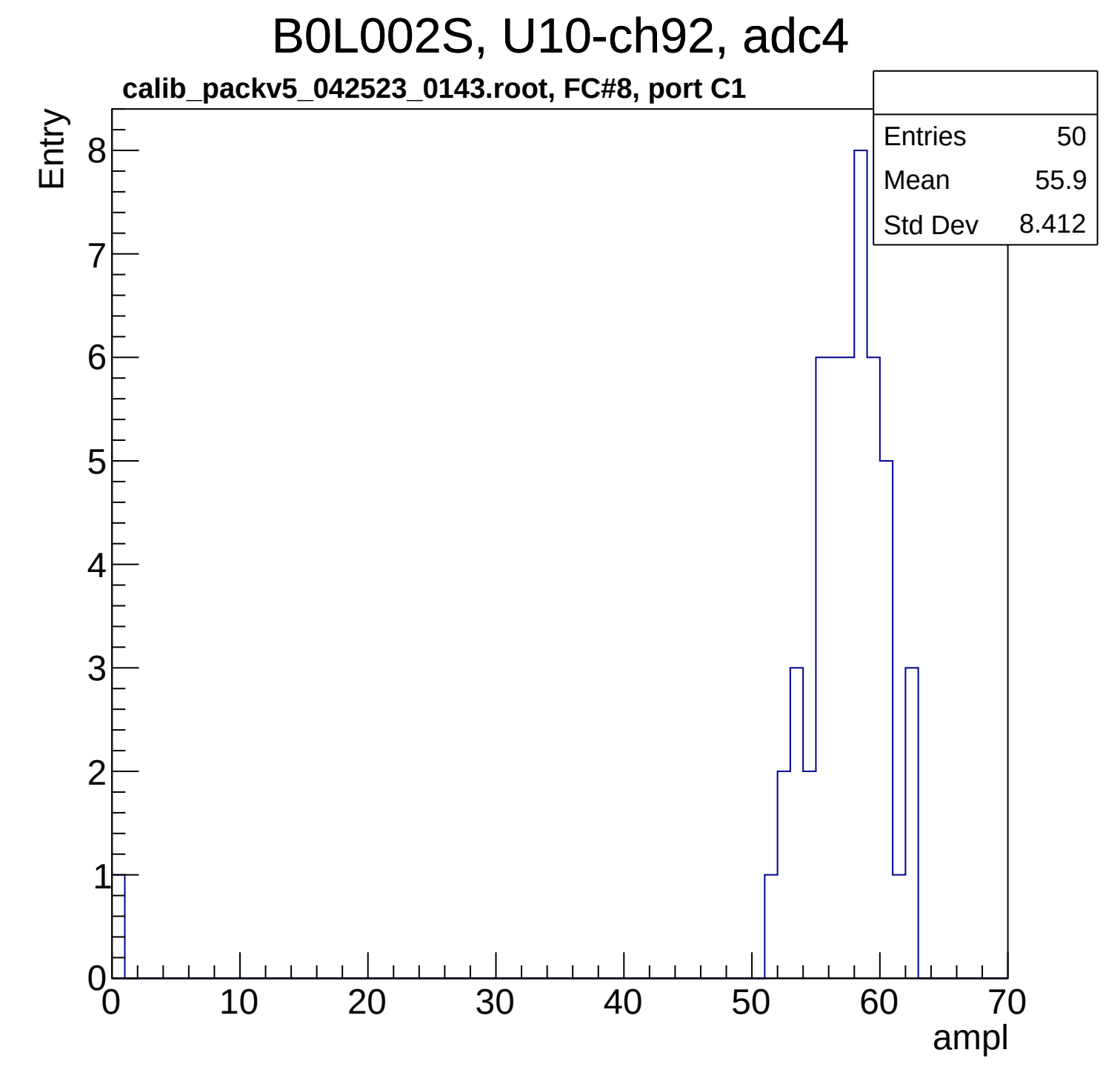

B0L002S, U10-ch91, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

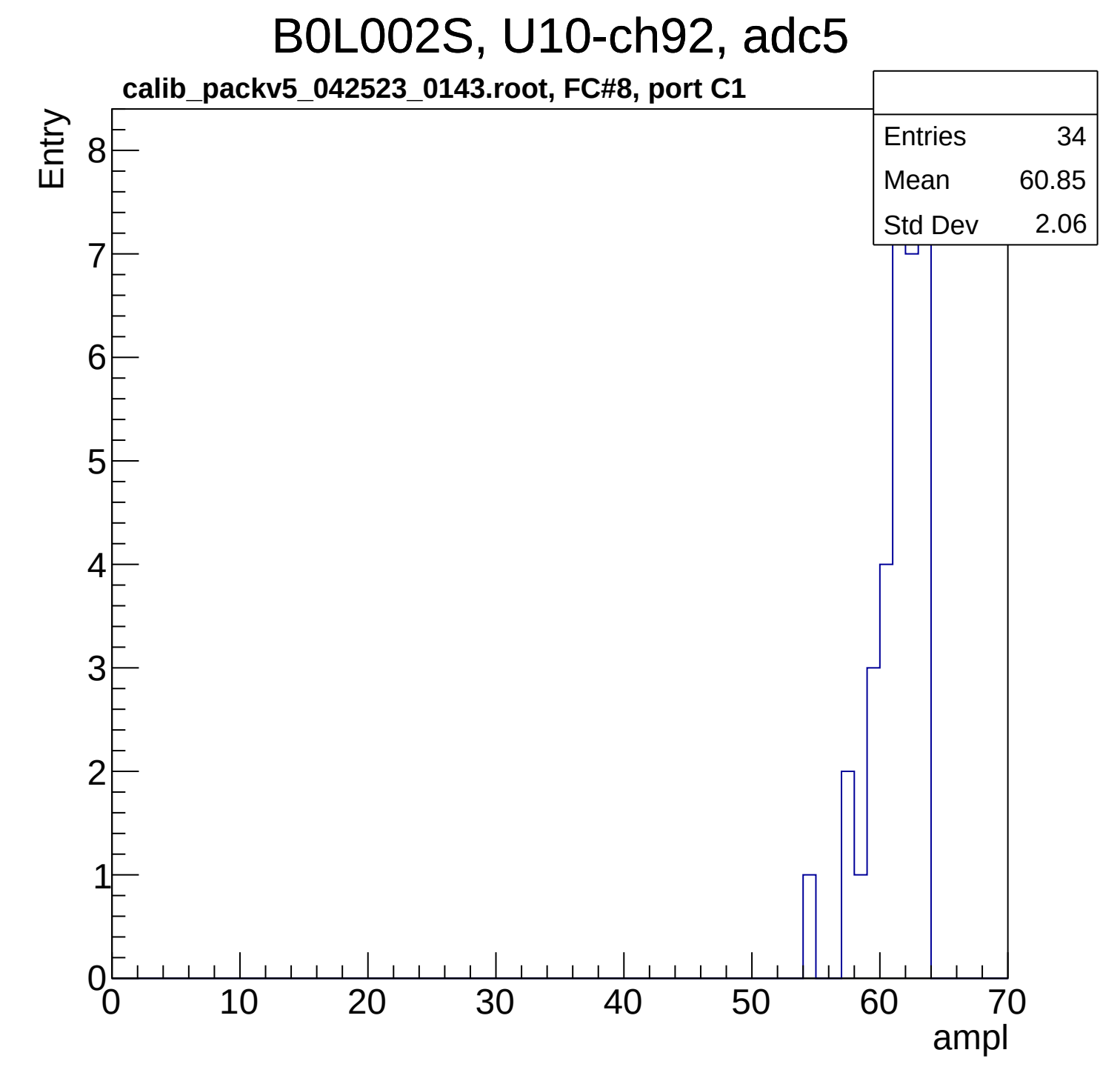

B0L002S, U10-ch92, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 32.22
Std Dev 2.921
Gaus mean : 32.0760
Gaus Width: 3.1739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

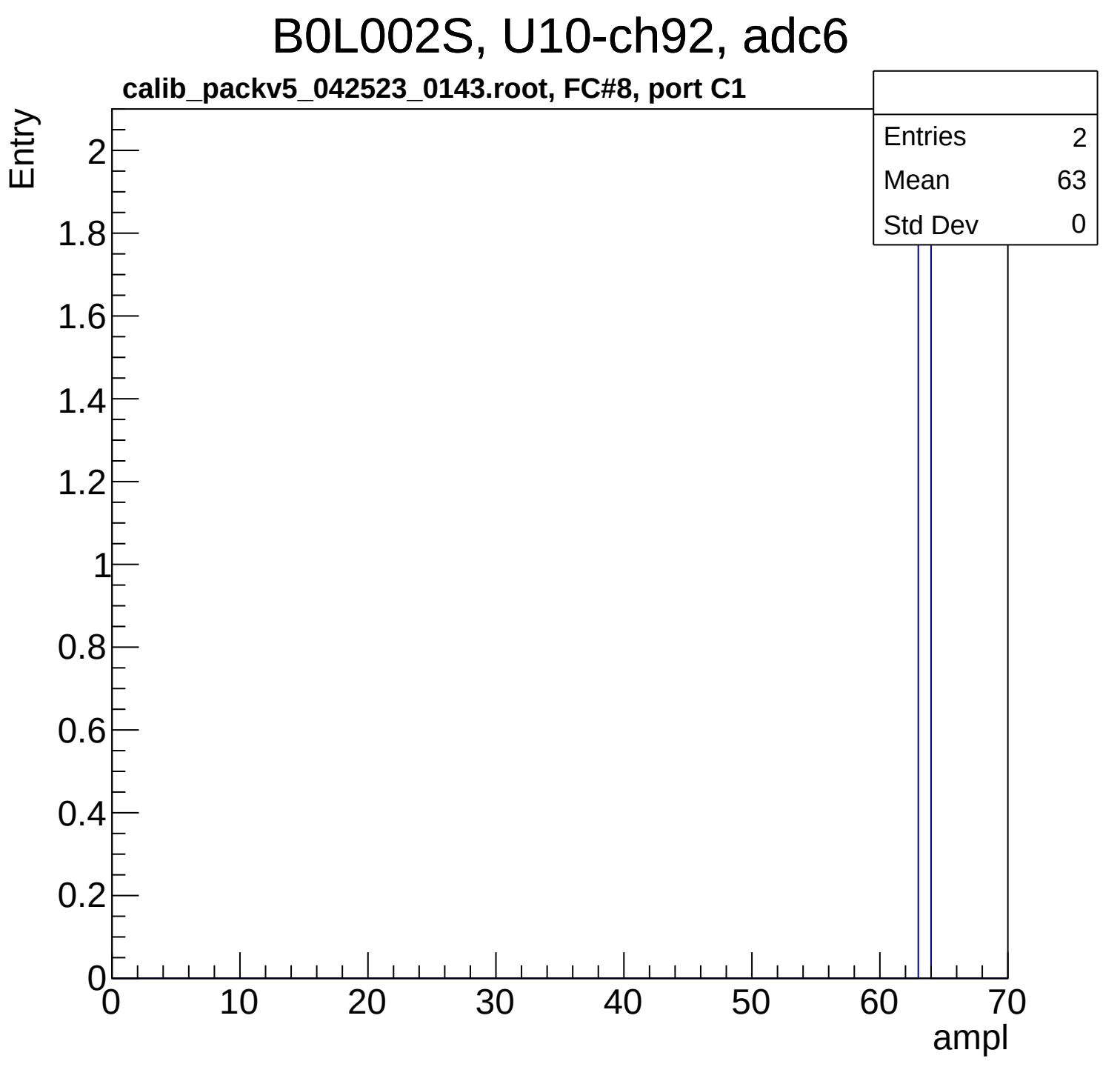

B0L002S, U10-ch92, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 37.79
Std Dev 3.296
Gaus mean : 38.1769
Gaus Width: 3.3869
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

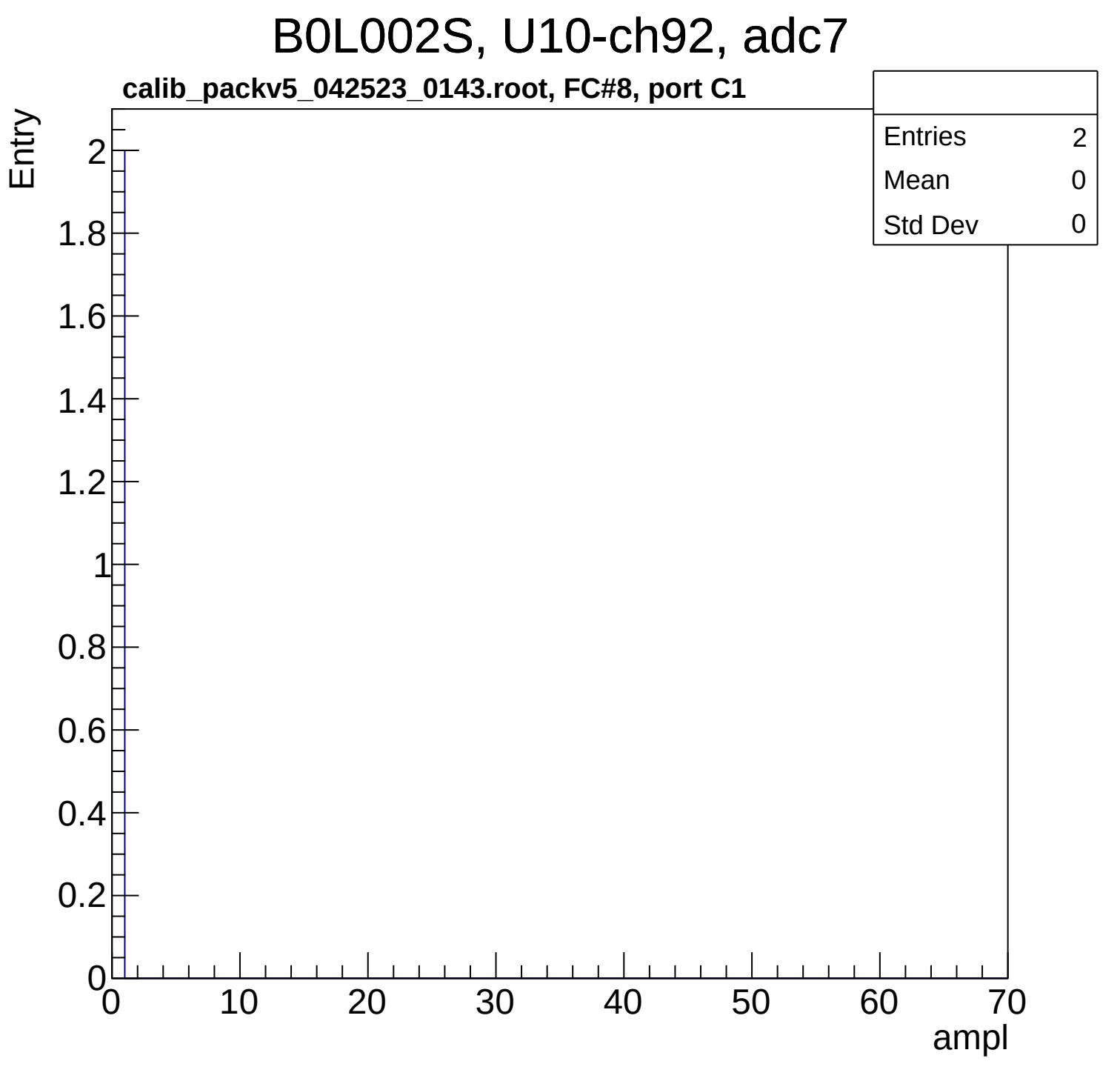

B0L002S, U10-ch92, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.26
Std Dev 2.994
Gaus mean : 44.7776
Gaus Width: 3.1698
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

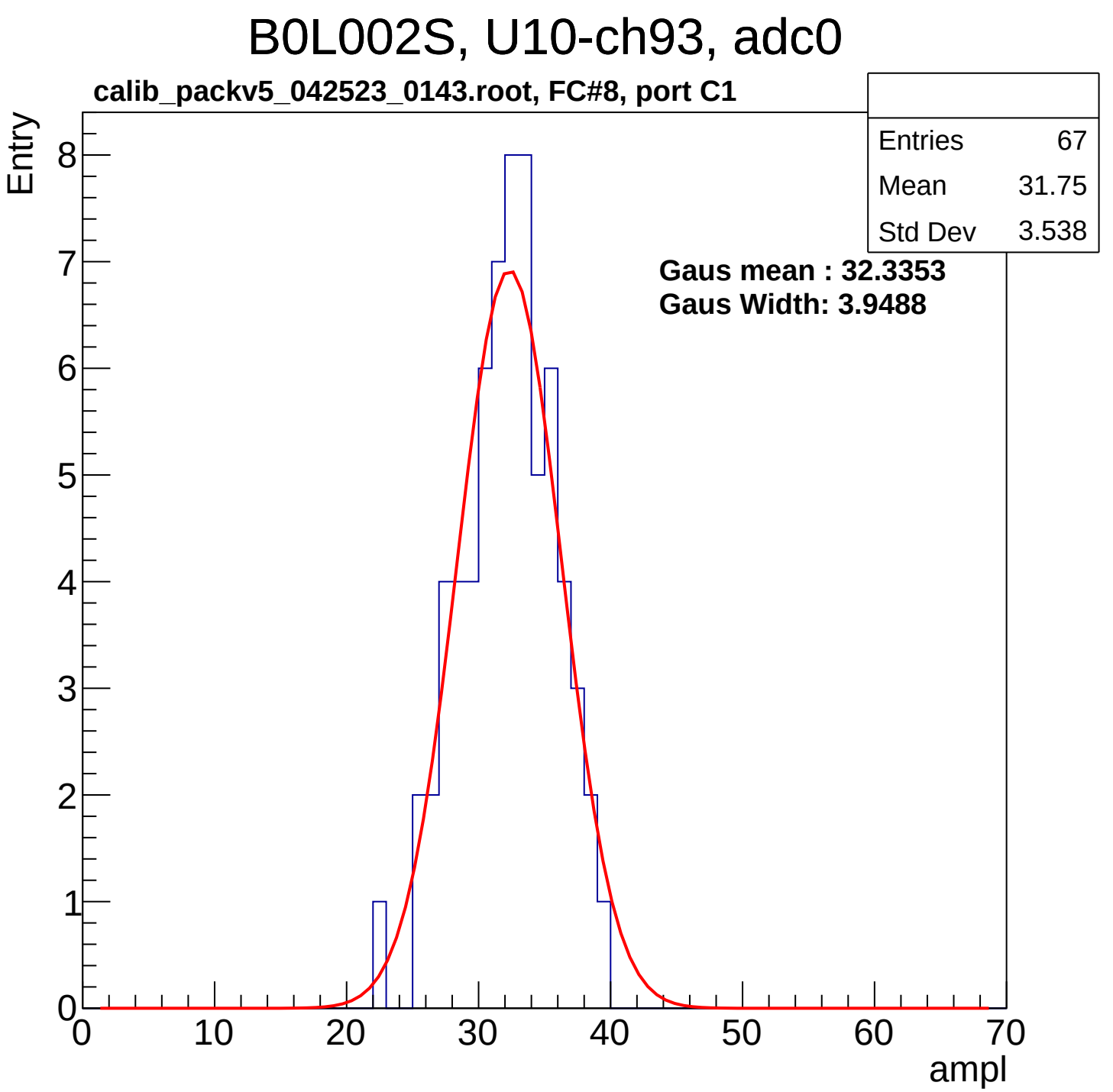

B0L002S, U10-ch92, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 51.41
Std Dev 3.643
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

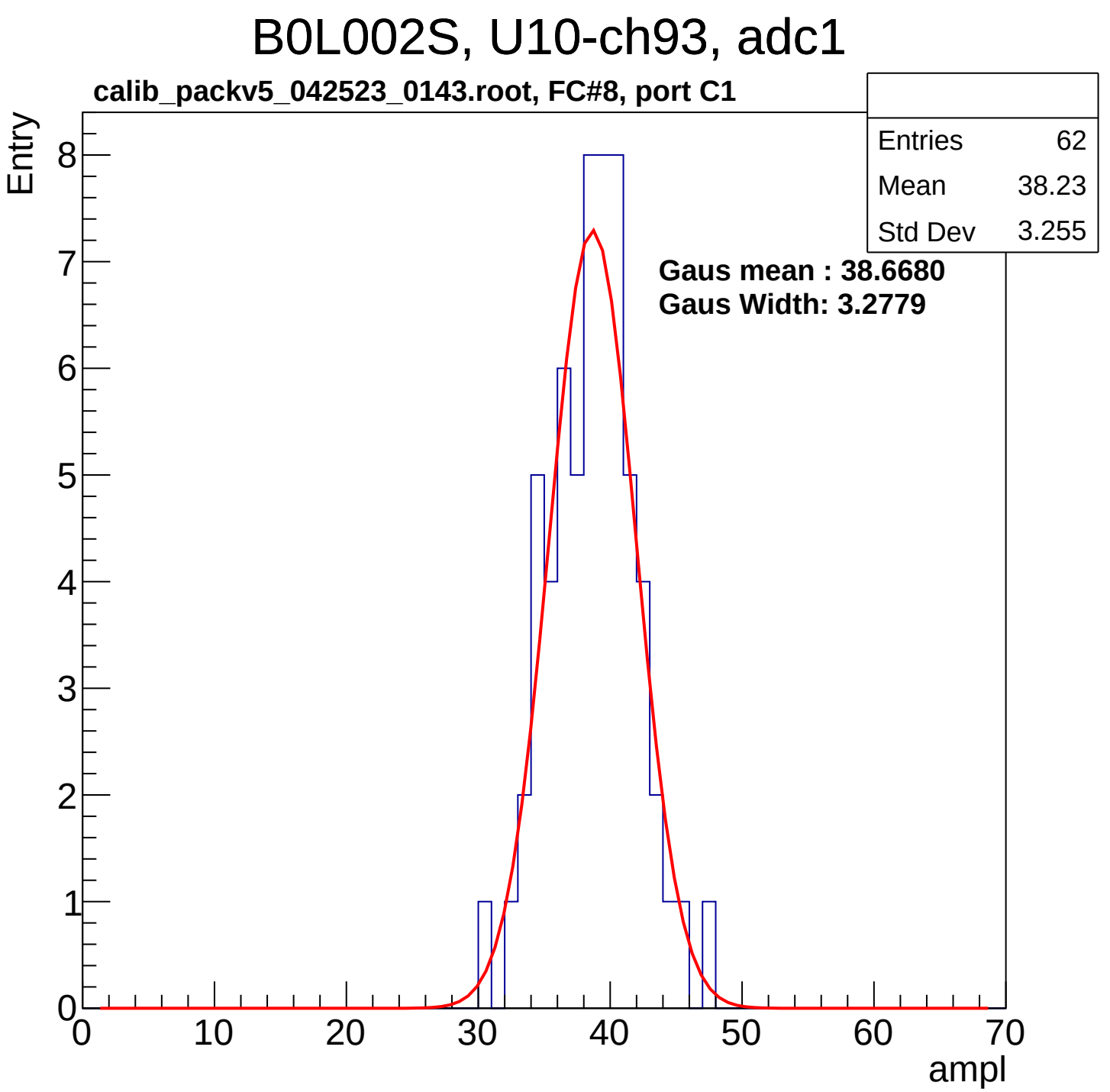

B0L002S, U10-ch92, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 55.9
Std Dev 8.412
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

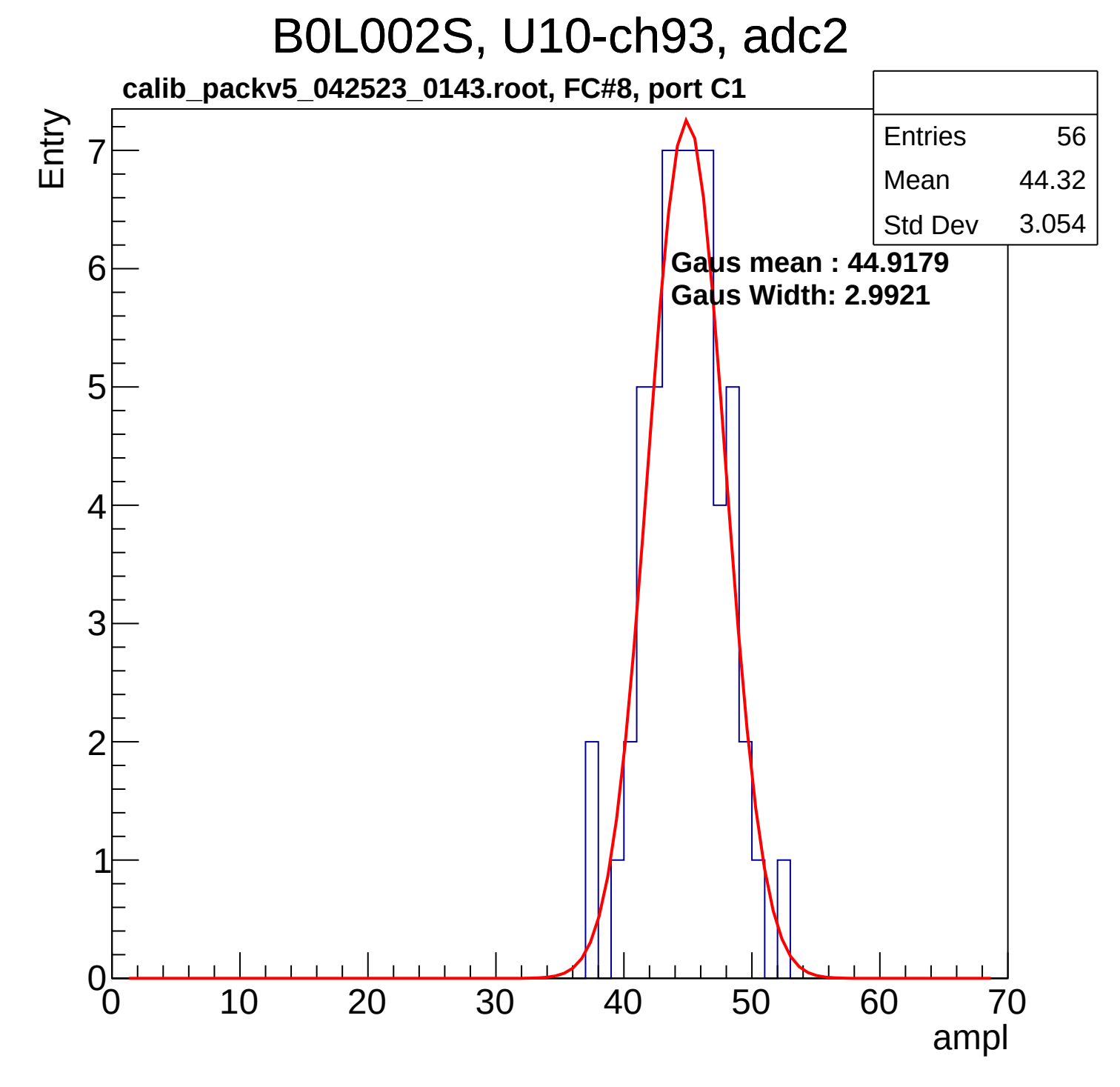

B0L002S, U10-ch92, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 60.85
Std Dev 2.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

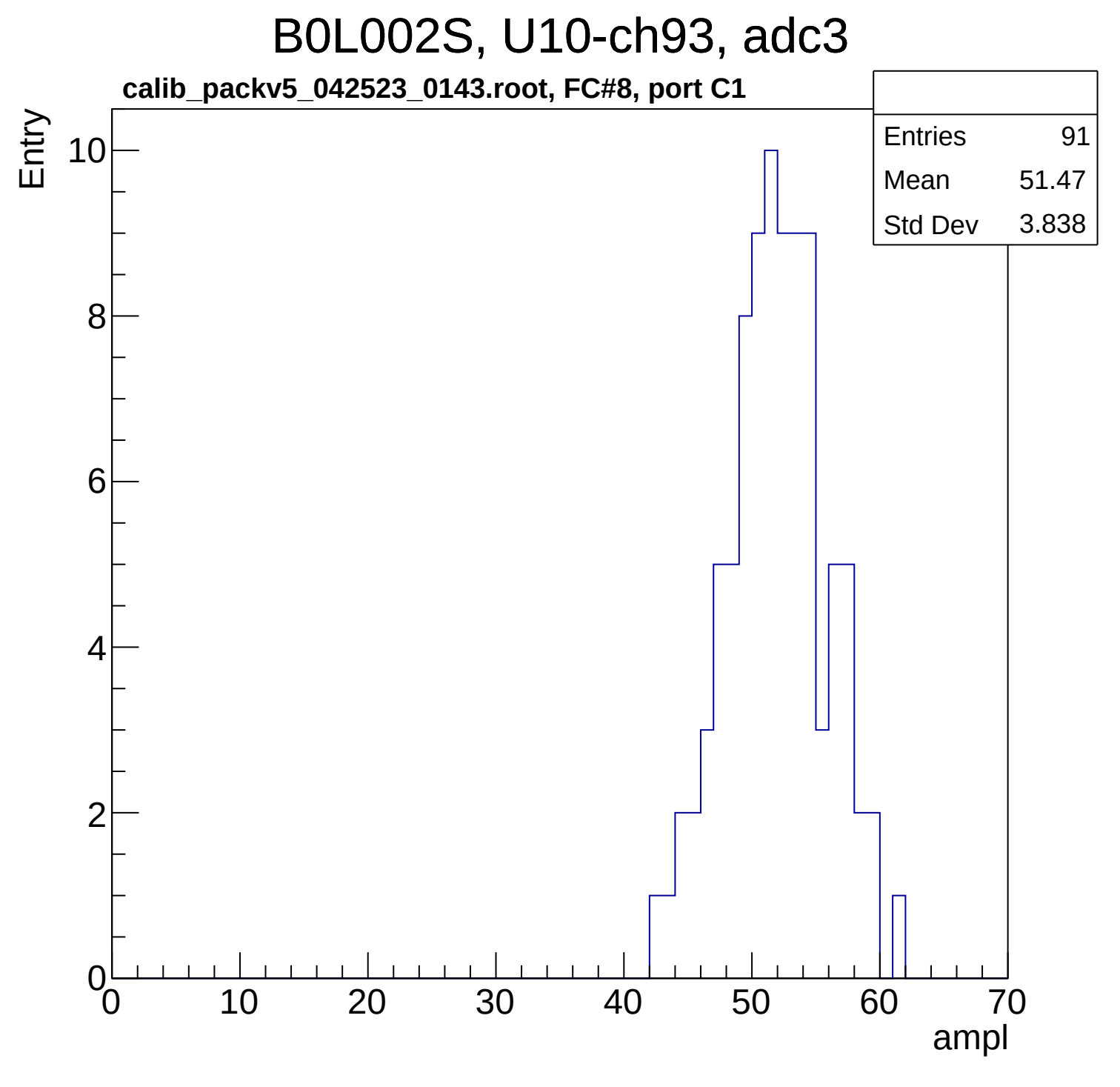

B0L002S, U10-ch92, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

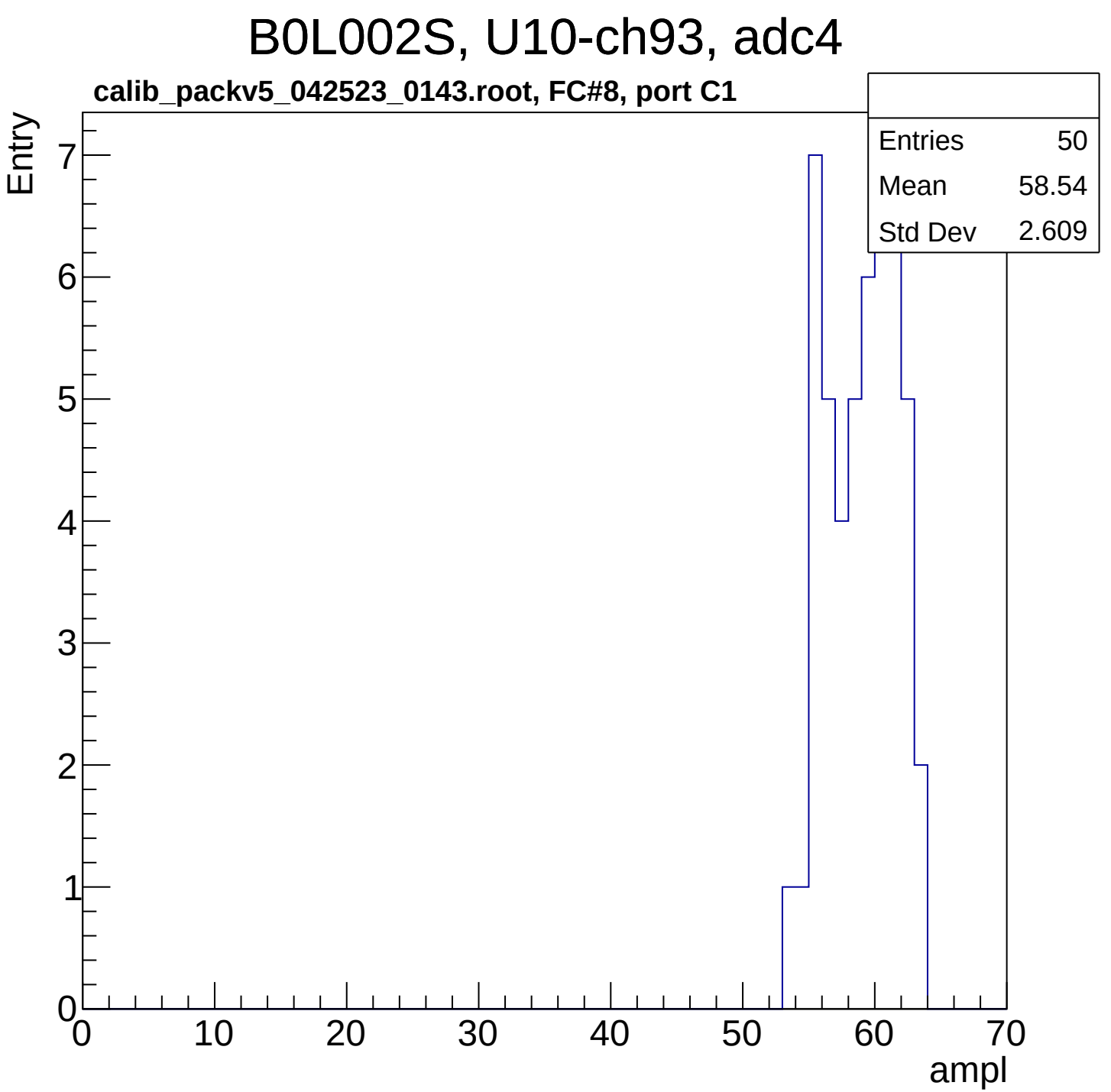

B0L002S, U10-ch92, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

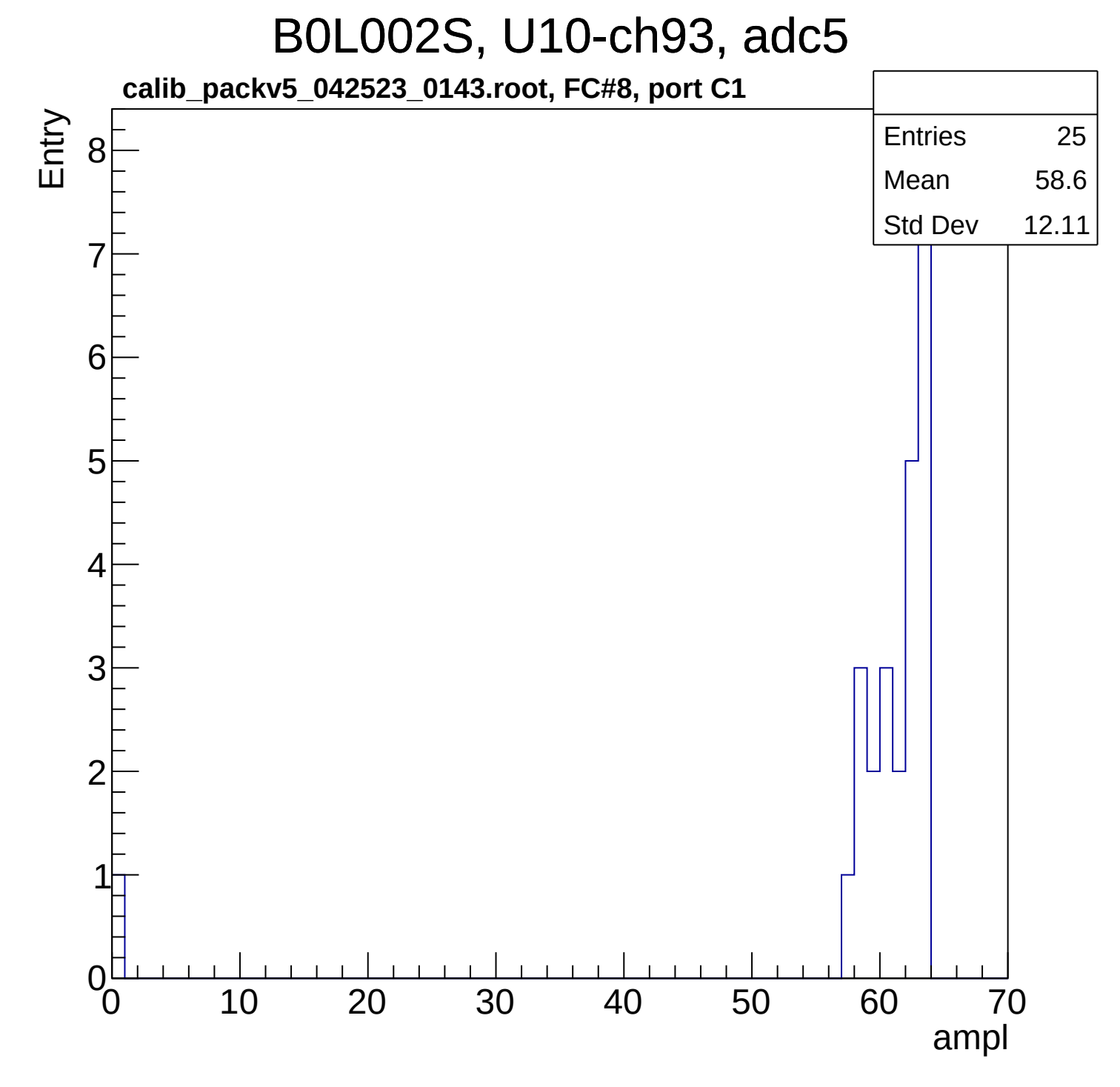

B0L002S, U10-ch93, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 31.75
Std Dev 3.538
Gaus mean : 32.3353
Gaus Width: 3.9488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

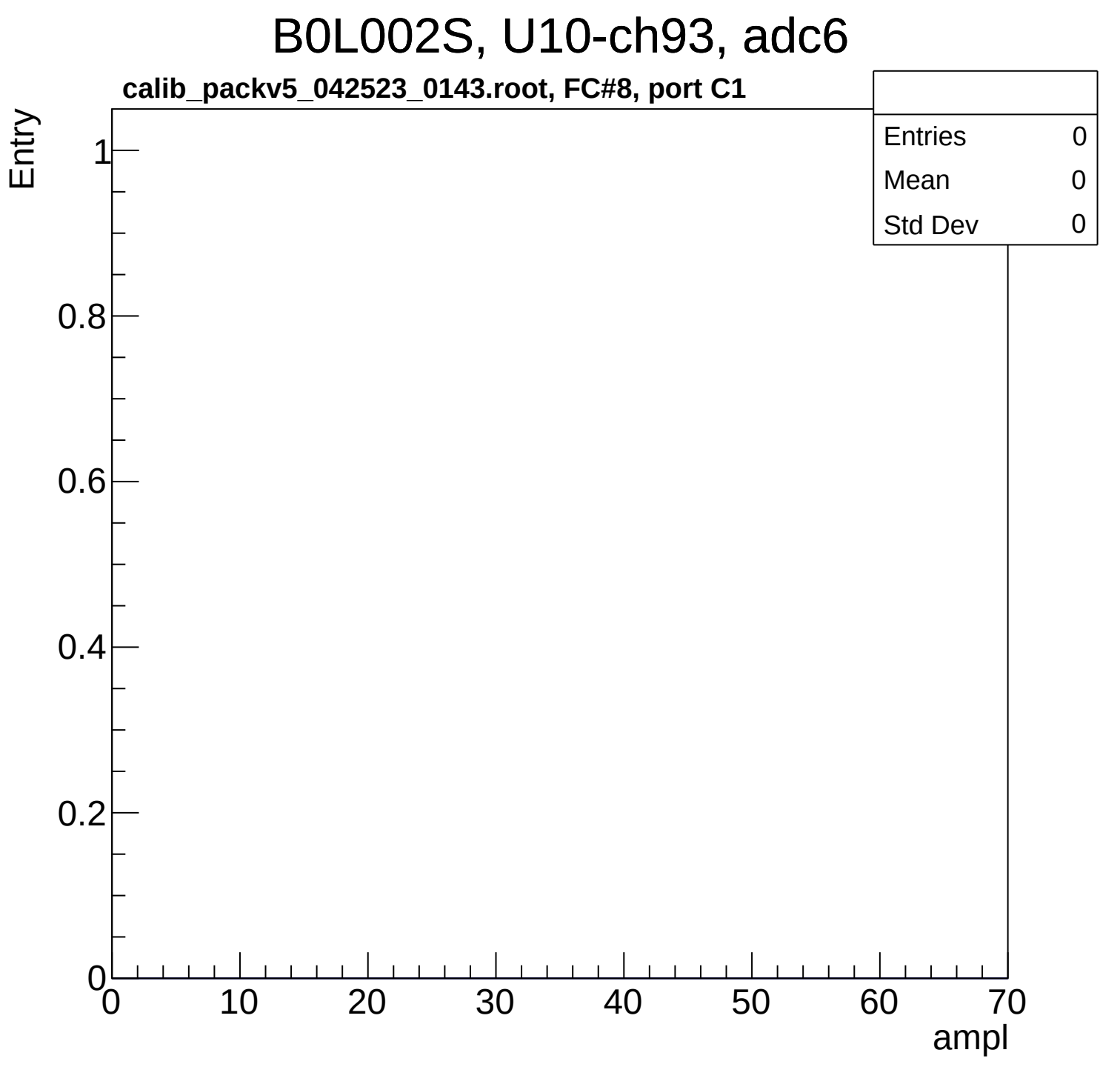

B0L002S, U10-ch93, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 38.23
Std Dev 3.255
Gaus mean : 38.6680
Gaus Width: 3.2779
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

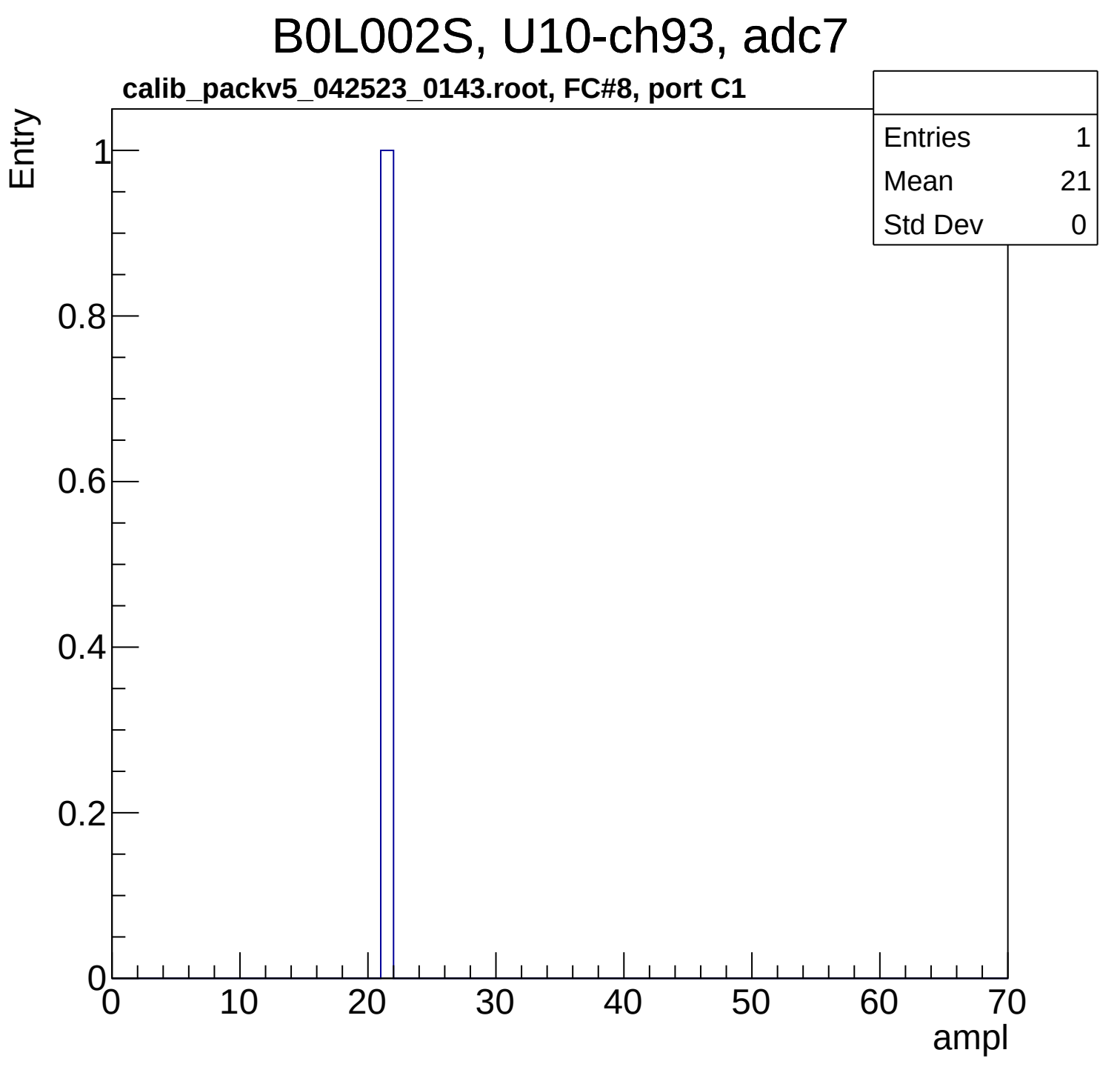

B0L002S, U10-ch93, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 44.32
Std Dev 3.054
Gaus mean : 44.9179
Gaus Width: 2.9921
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

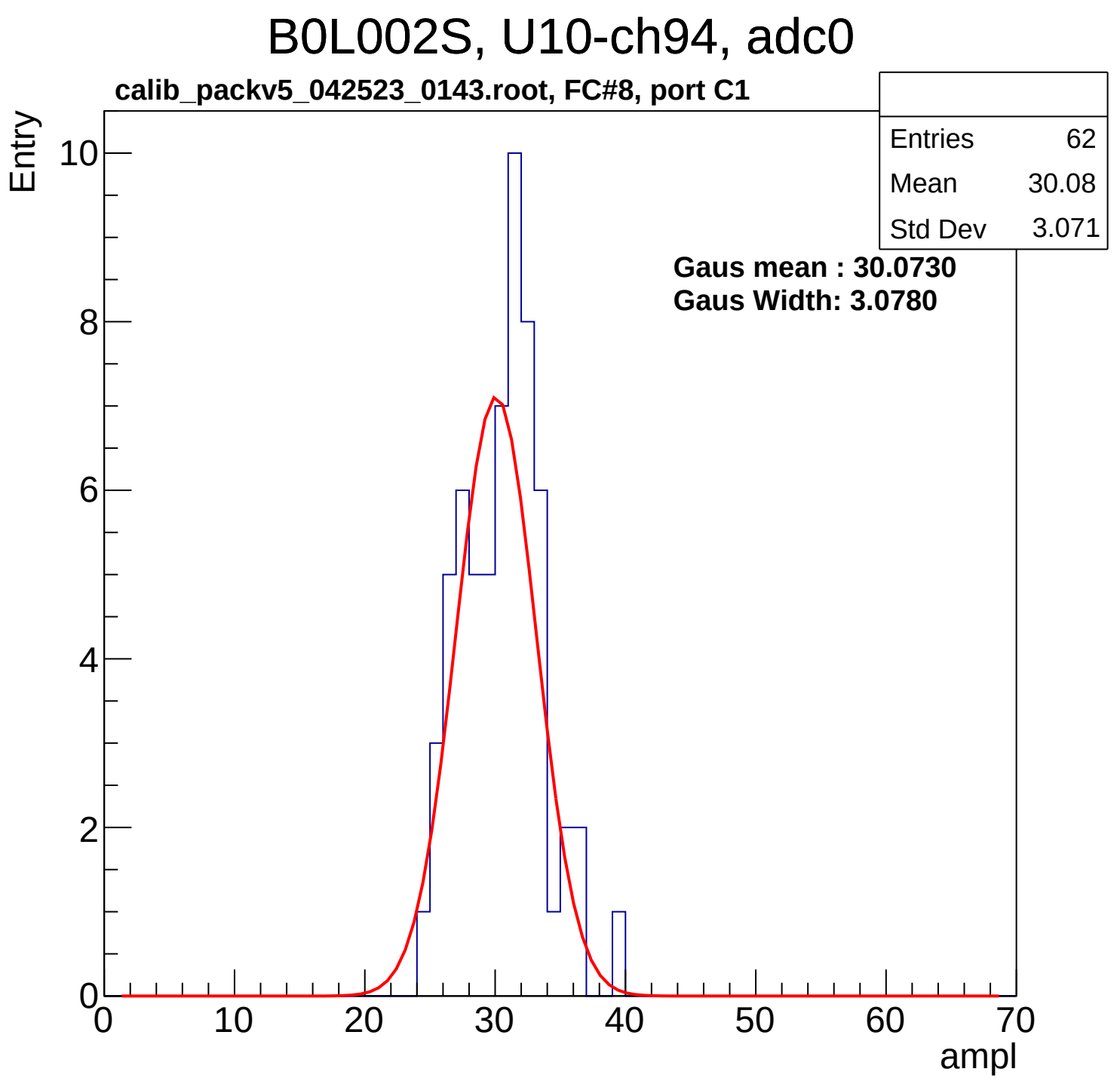

B0L002S, U10-ch93, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 91
Mean 51.47
Std Dev 3.838
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

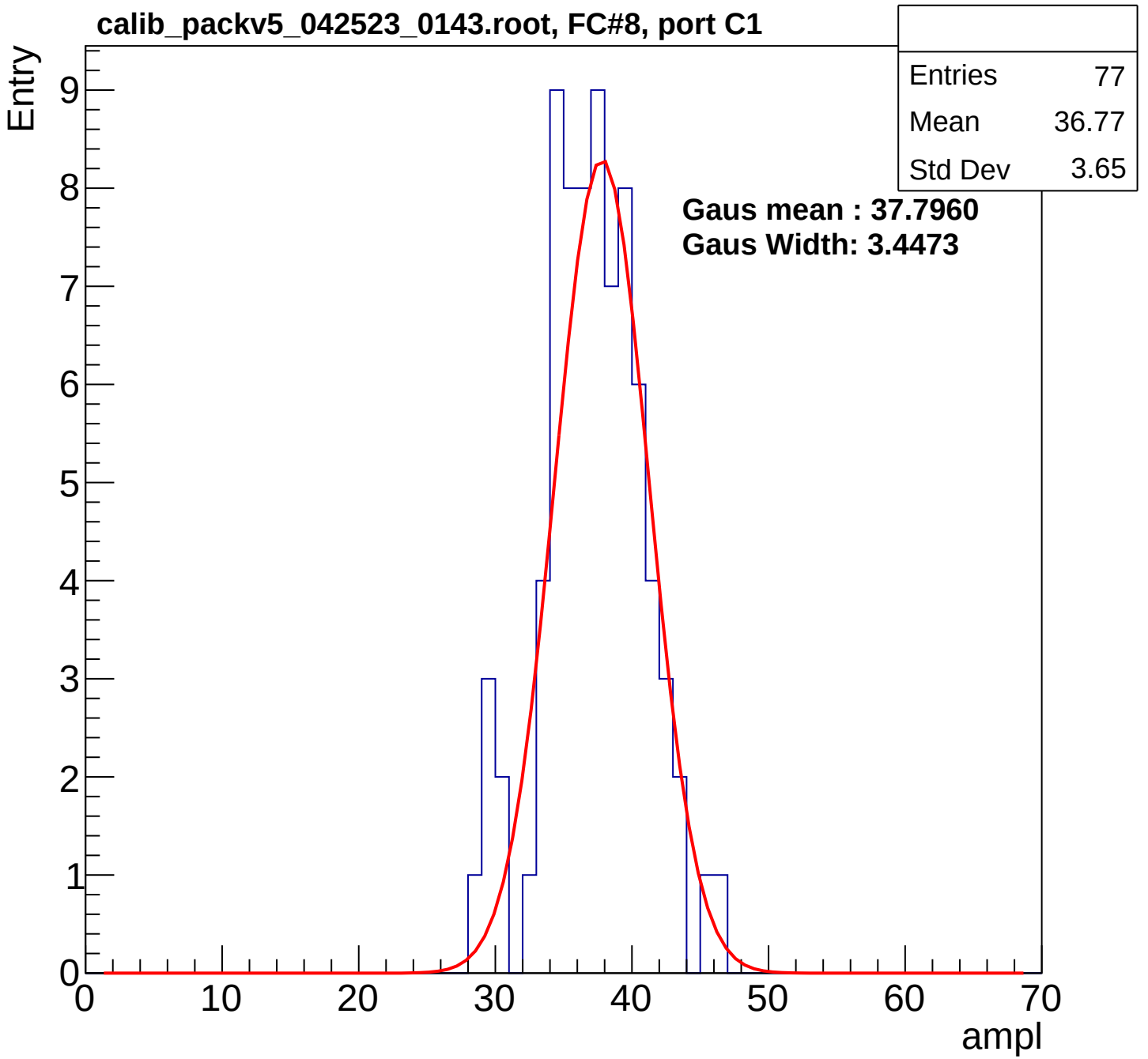

B0L002S, U10-ch93, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 58.54
Std Dev 2.609
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

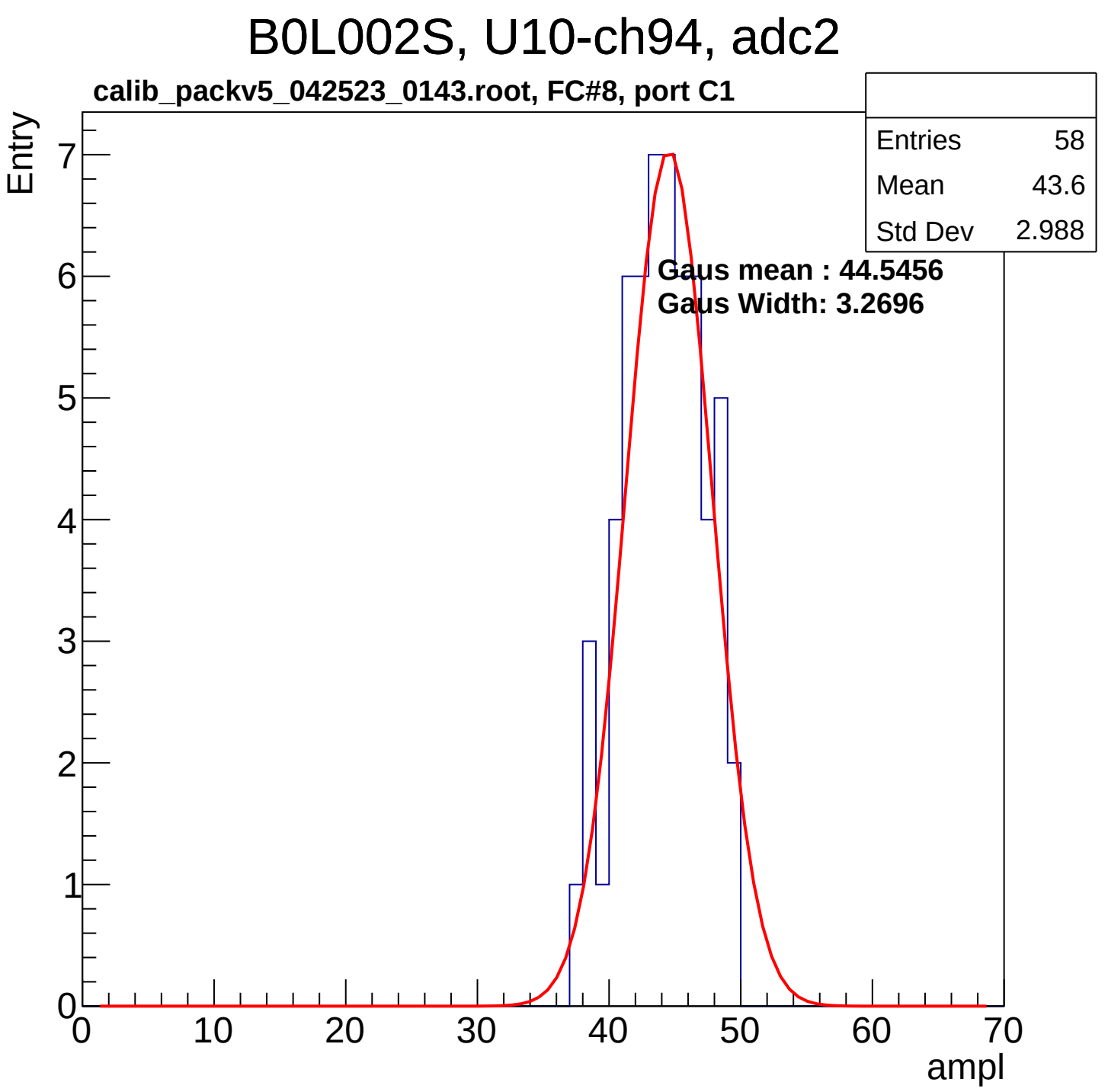

B0L002S, U10-ch93, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 25
Mean 58.6
Std Dev 12.11
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

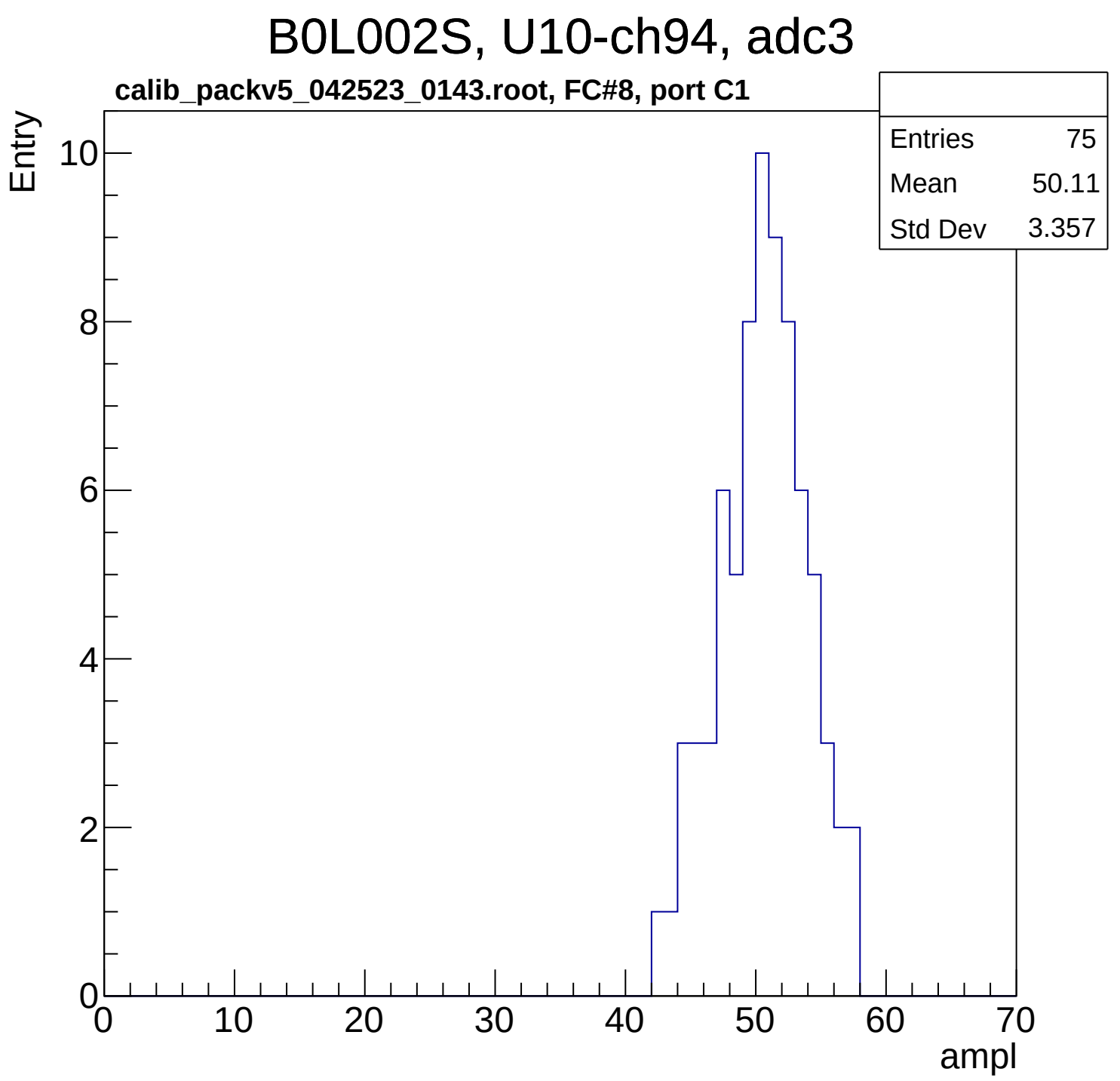

B0L002S, U10-ch93, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

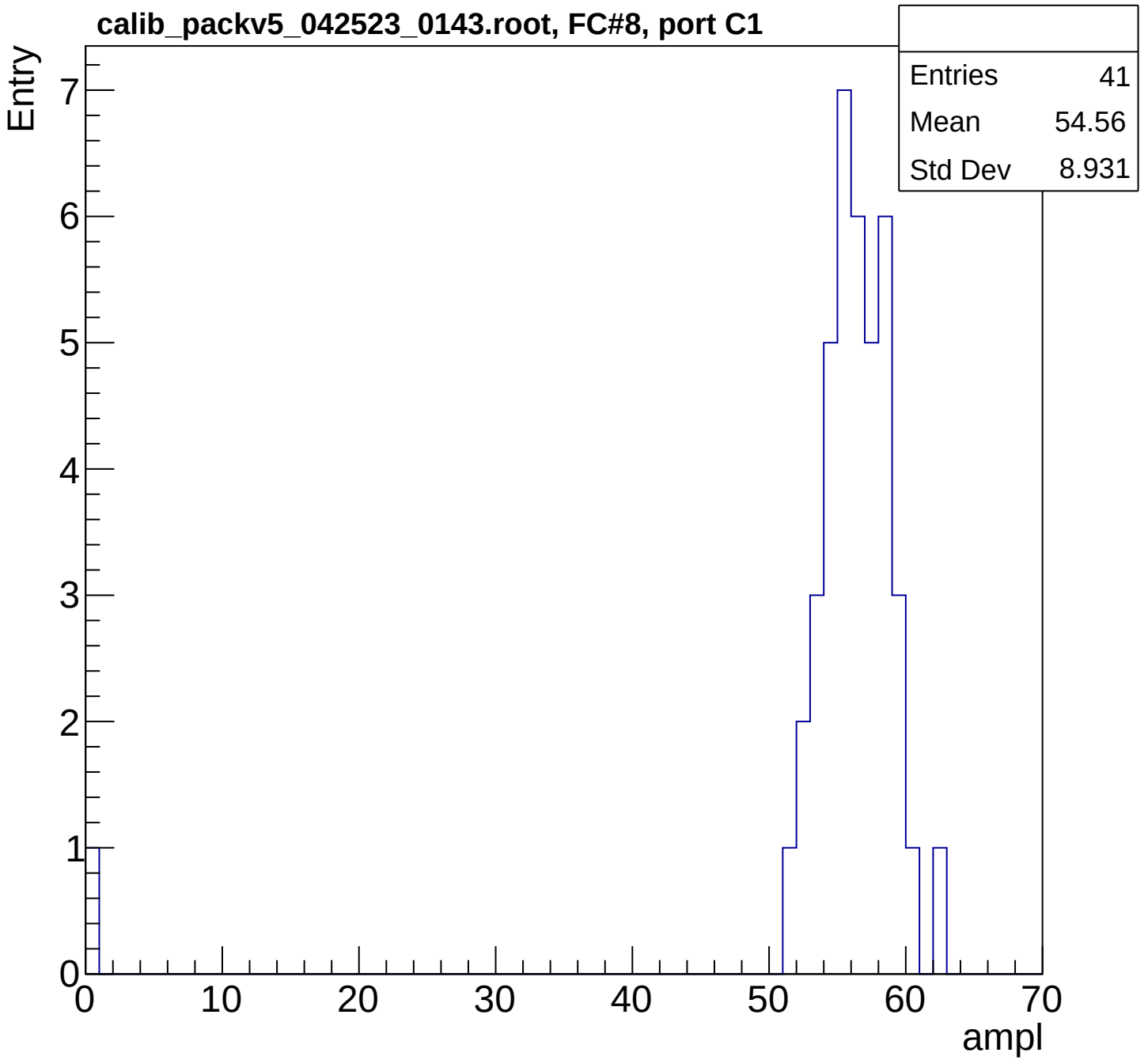

B0L002S, U10-ch93, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

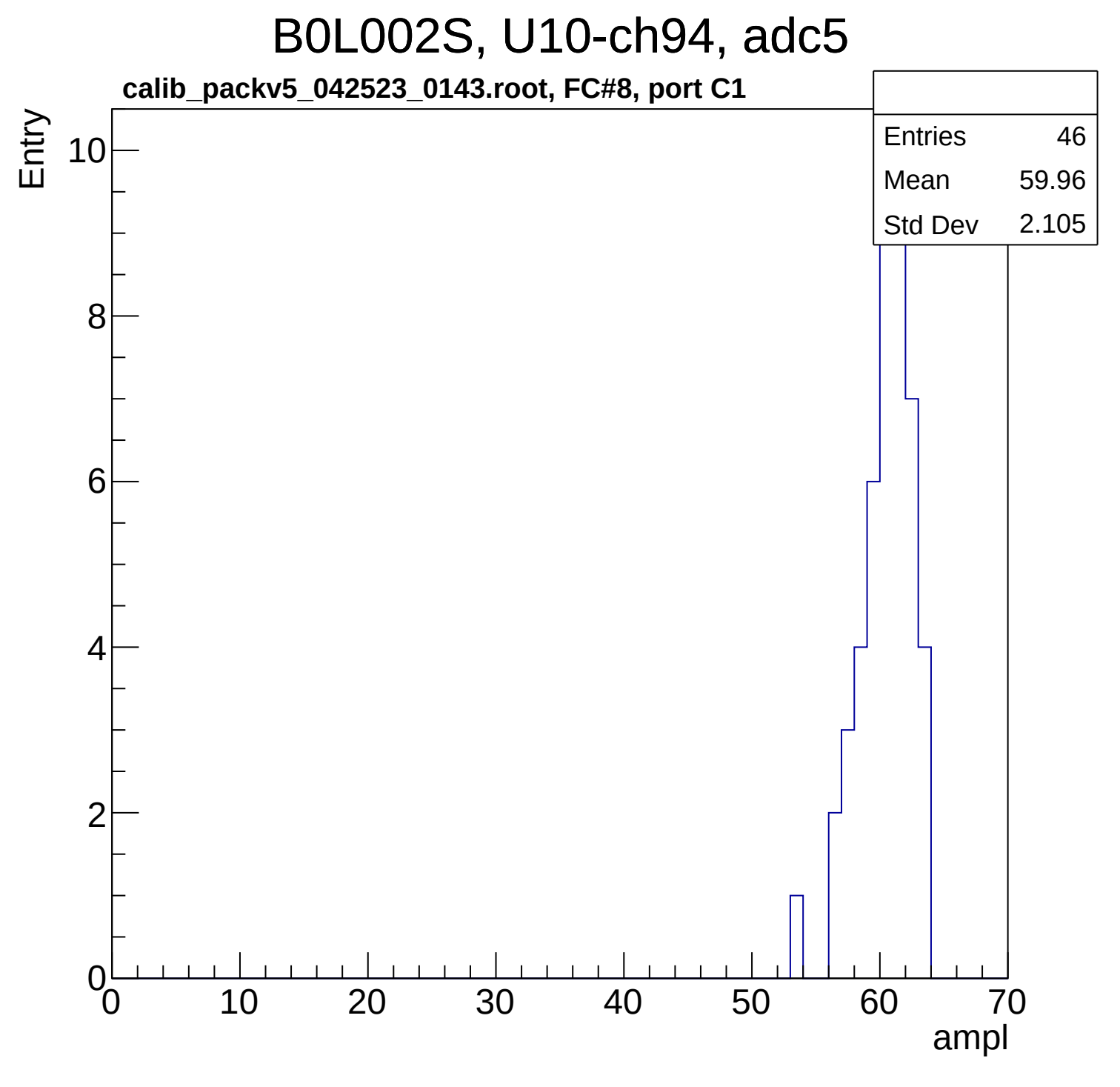

B0L002S, U10-ch94, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 30.08
Std Dev 3.071
Gaus mean : 30.0730
Gaus Width: 3.0780
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

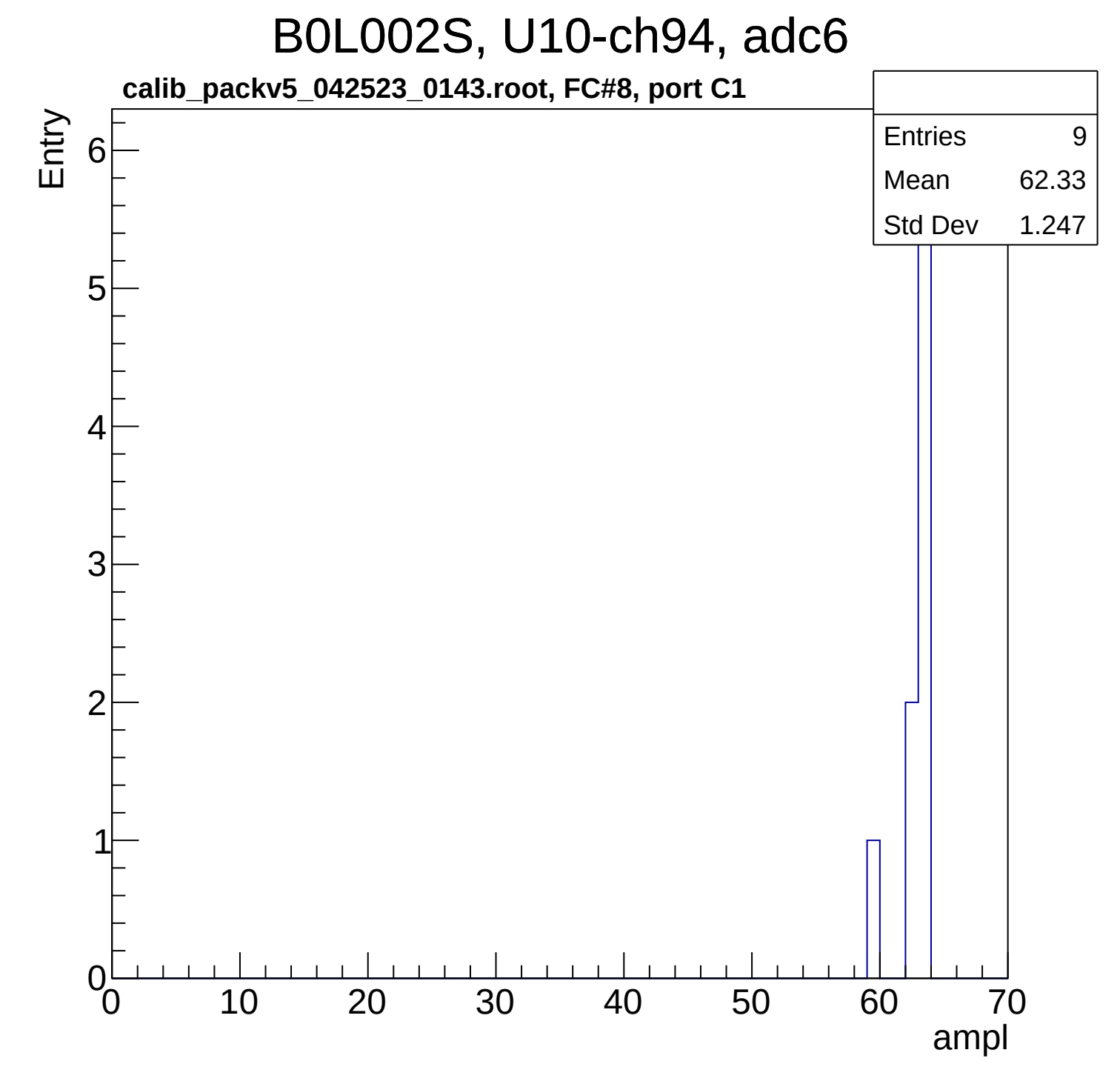

B0L002S, U10-ch94, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 36.77
Std Dev 3.65
Gaus mean : 37.7960
Gaus Width: 3.4473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

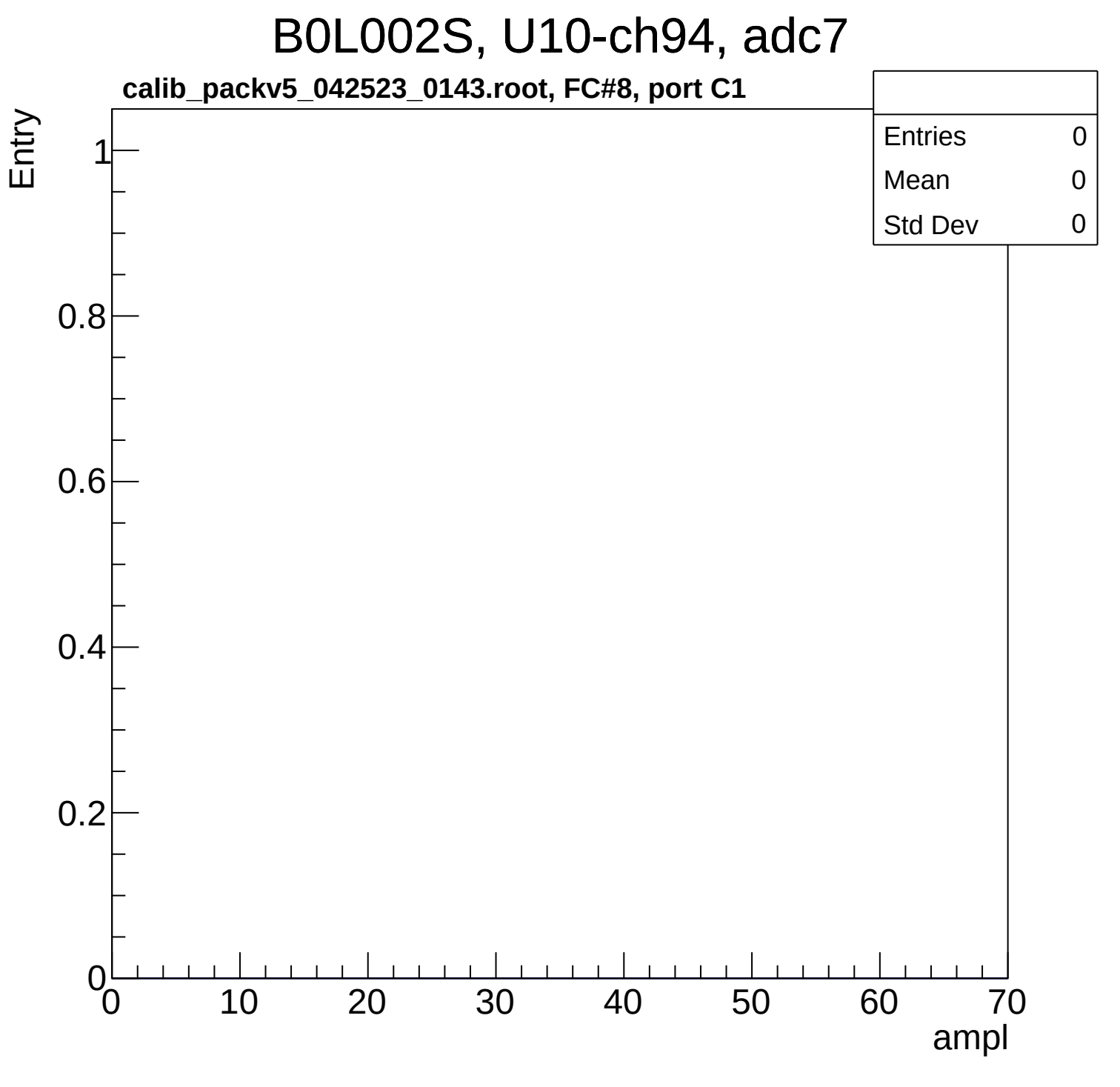

B0L002S, U10-ch94, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 43.6
Std Dev 2.988
Gaus mean : 44.5456
Gaus Width: 3.2696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

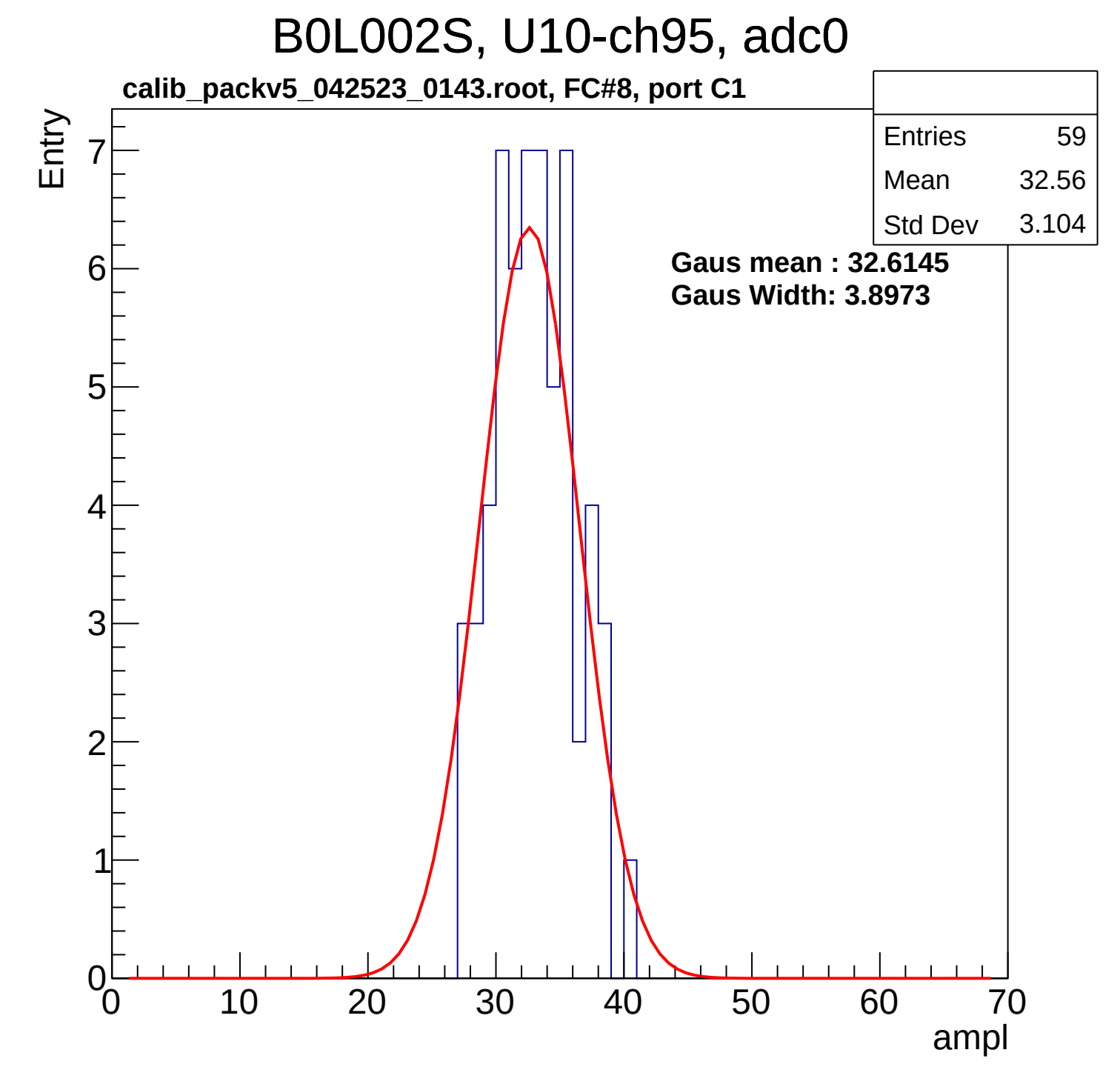

B0L002S, U10-ch94, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 50.11
Std Dev 3.357
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

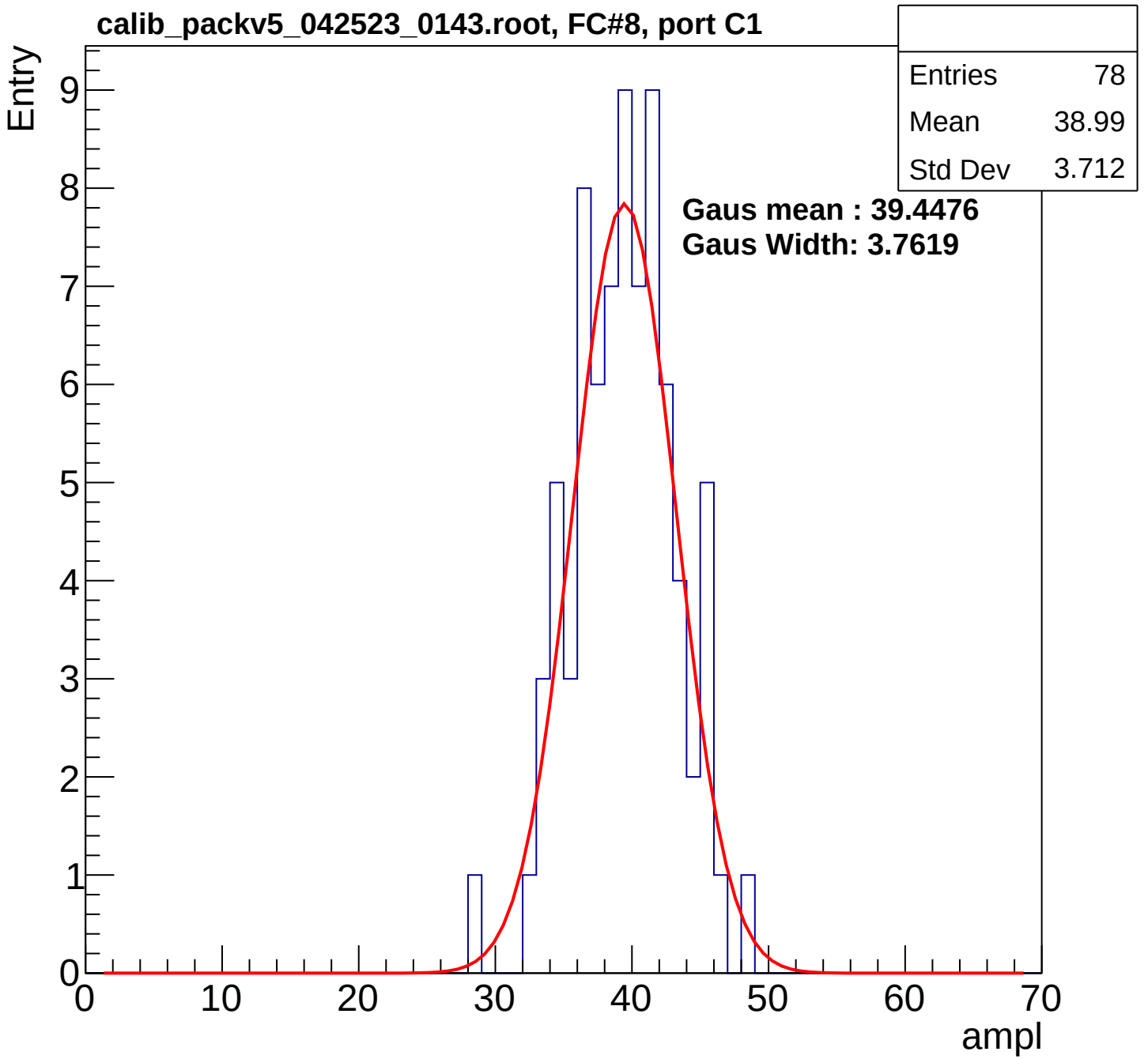

B0L002S, U10-ch94, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 54.56
Std Dev 8.931
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

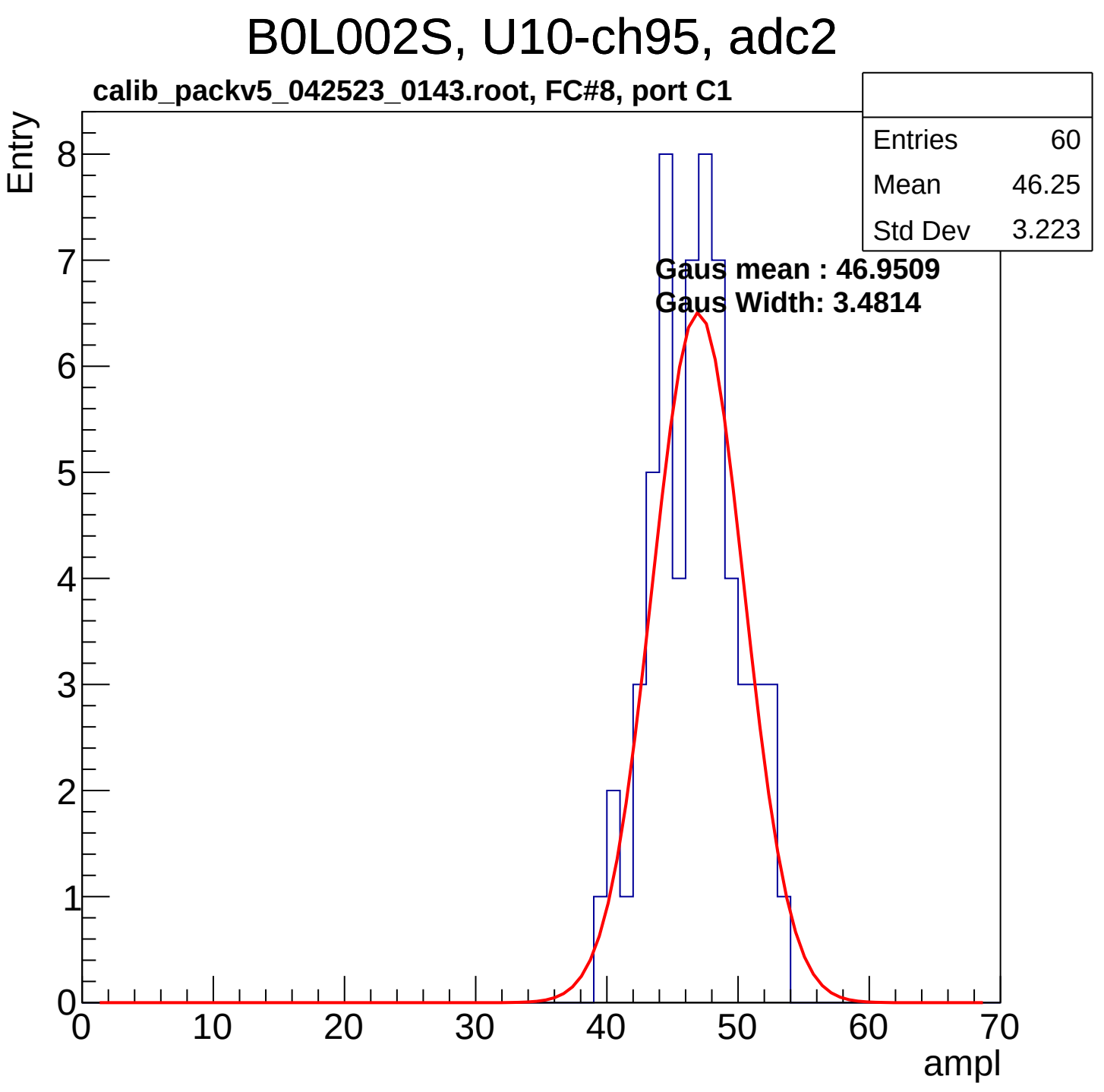

B0L002S, U10-ch94, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.96
Std Dev 2.105
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

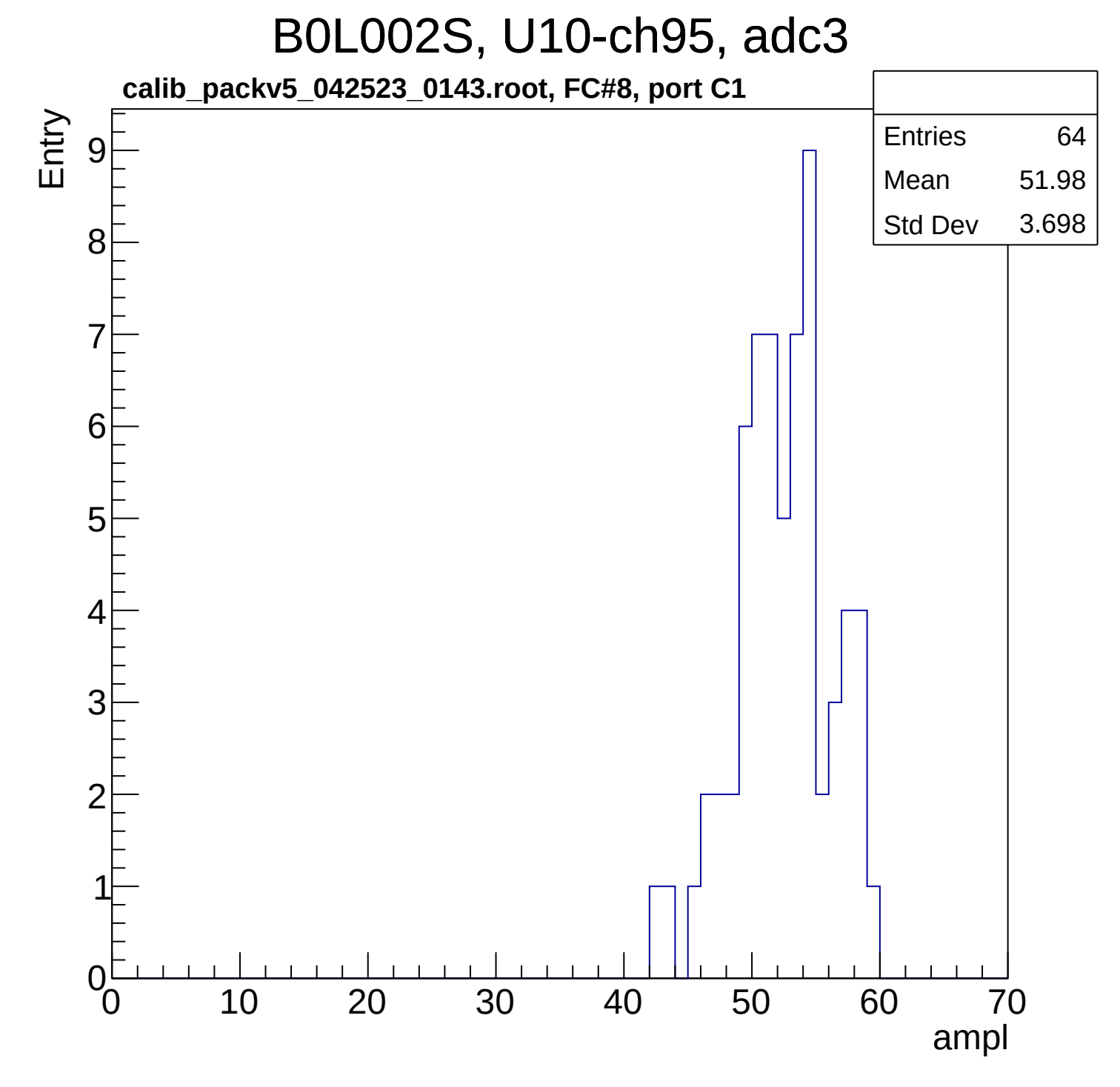

B0L002S, U10-ch94, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 62.33
Std Dev 1.247
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

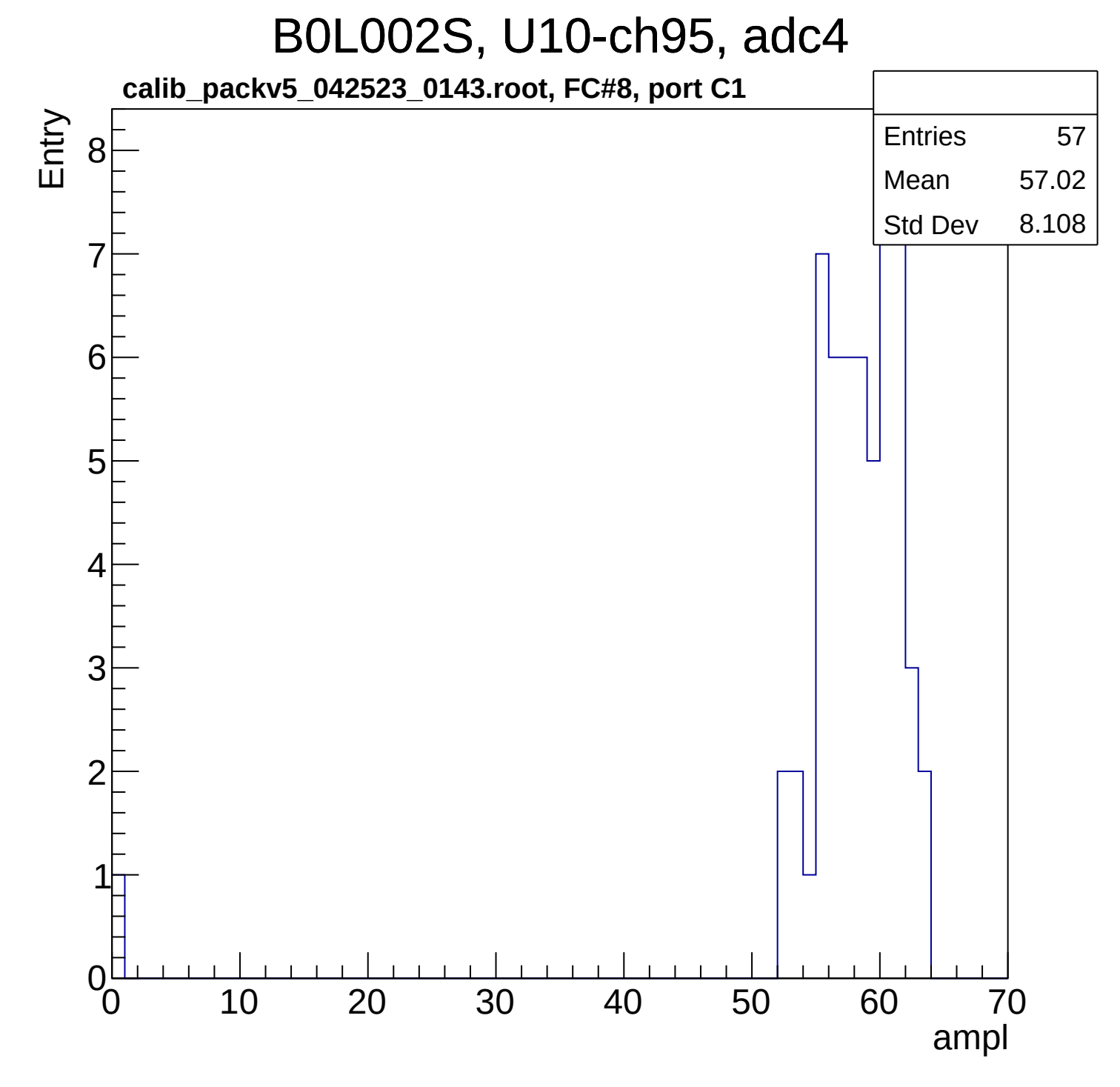

B0L002S, U10-ch94, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

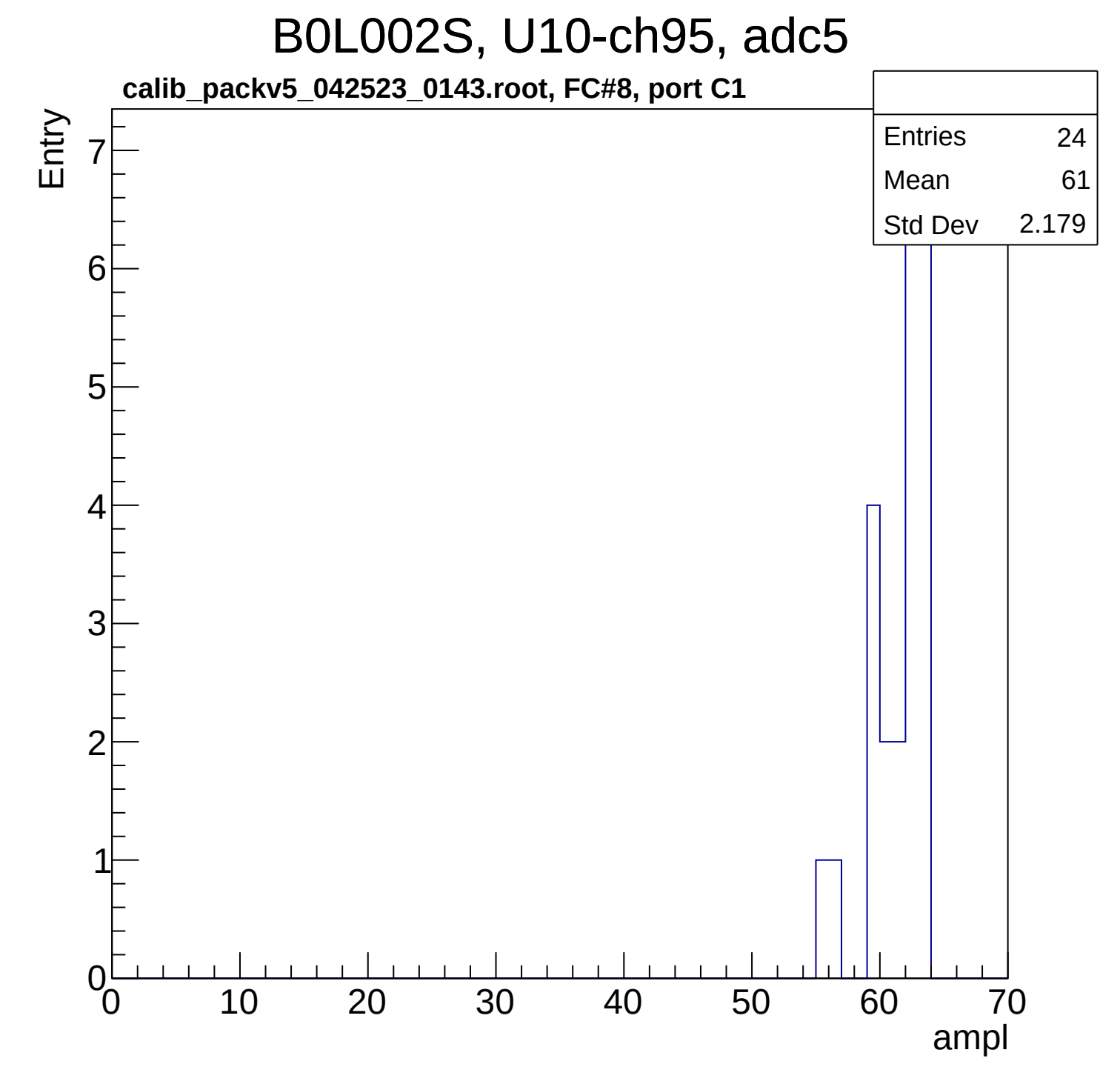

B0L002S, U10-ch95, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 32.56
Std Dev 3.104
Gaus mean : 32.6145
Gaus Width: 3.8973
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

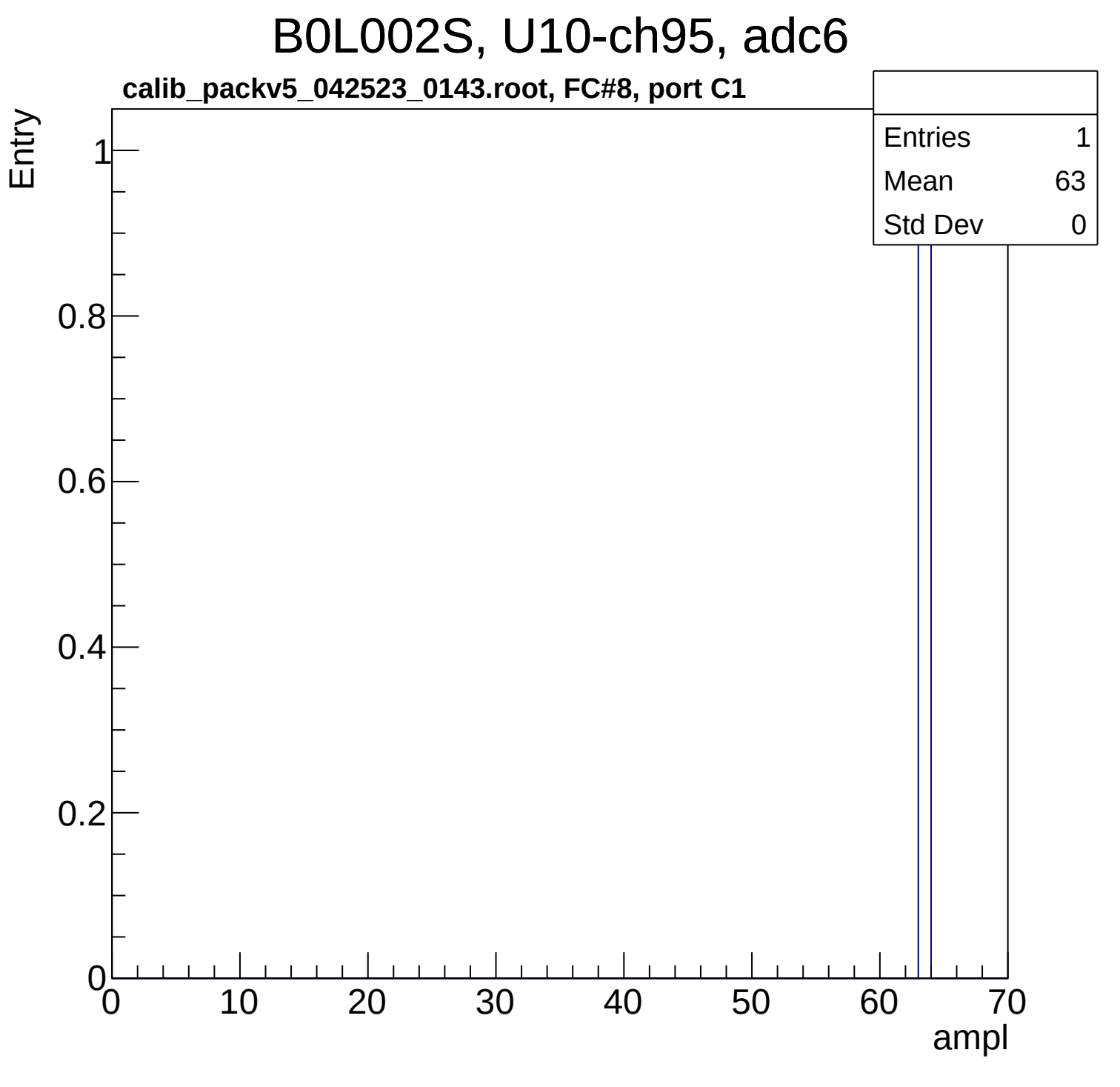

B0L002S, U10-ch95, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 38.99
Std Dev 3.712
Gaus mean : 39.4476
Gaus Width: 3.7619
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

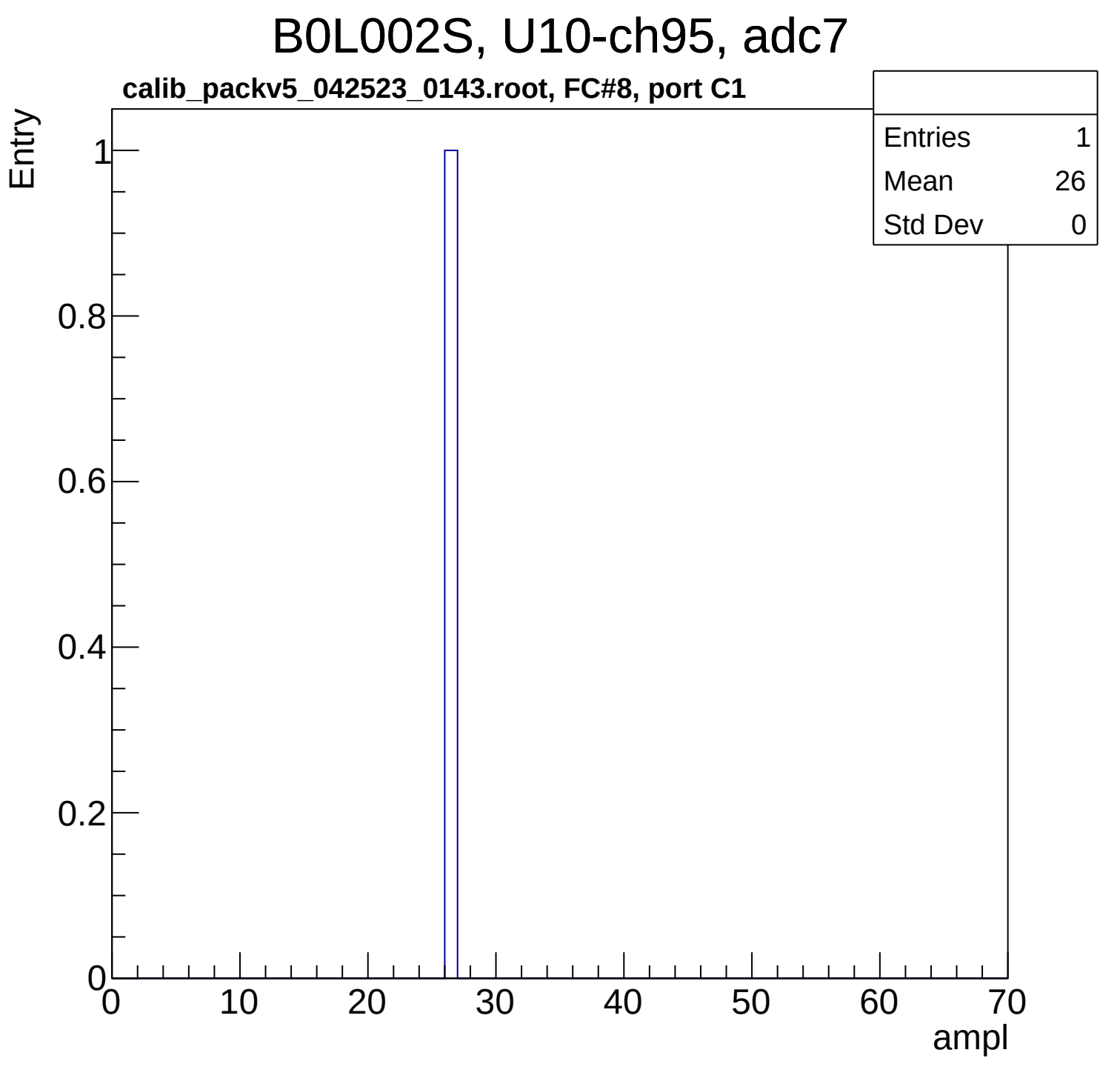

B0L002S, U10-ch95, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 46.25
Std Dev 3.223
Gaus mean : 46.9509
Gaus Width: 3.4814
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

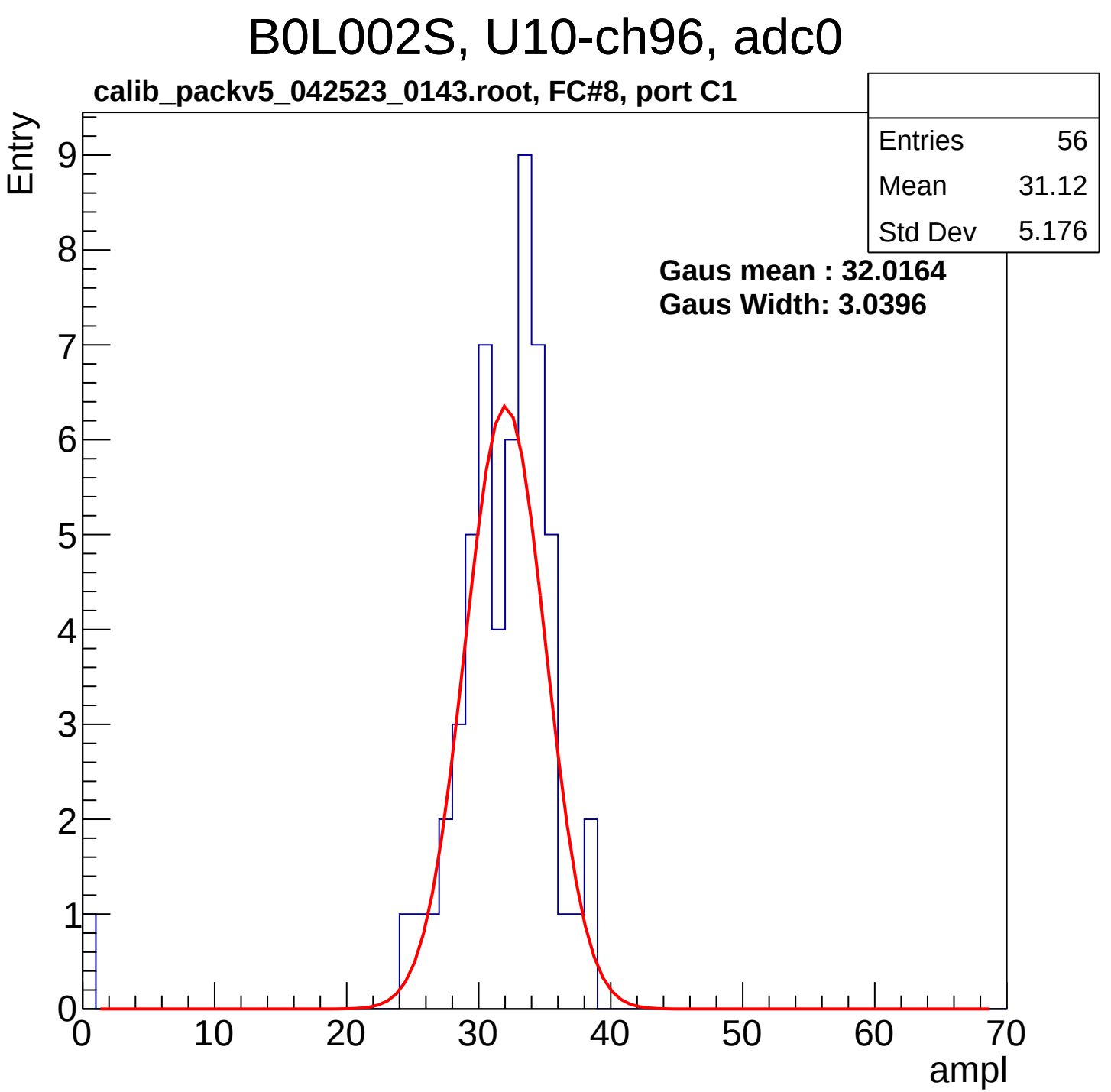

B0L002S, U10-ch95, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 51.98
Std Dev 3.698
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

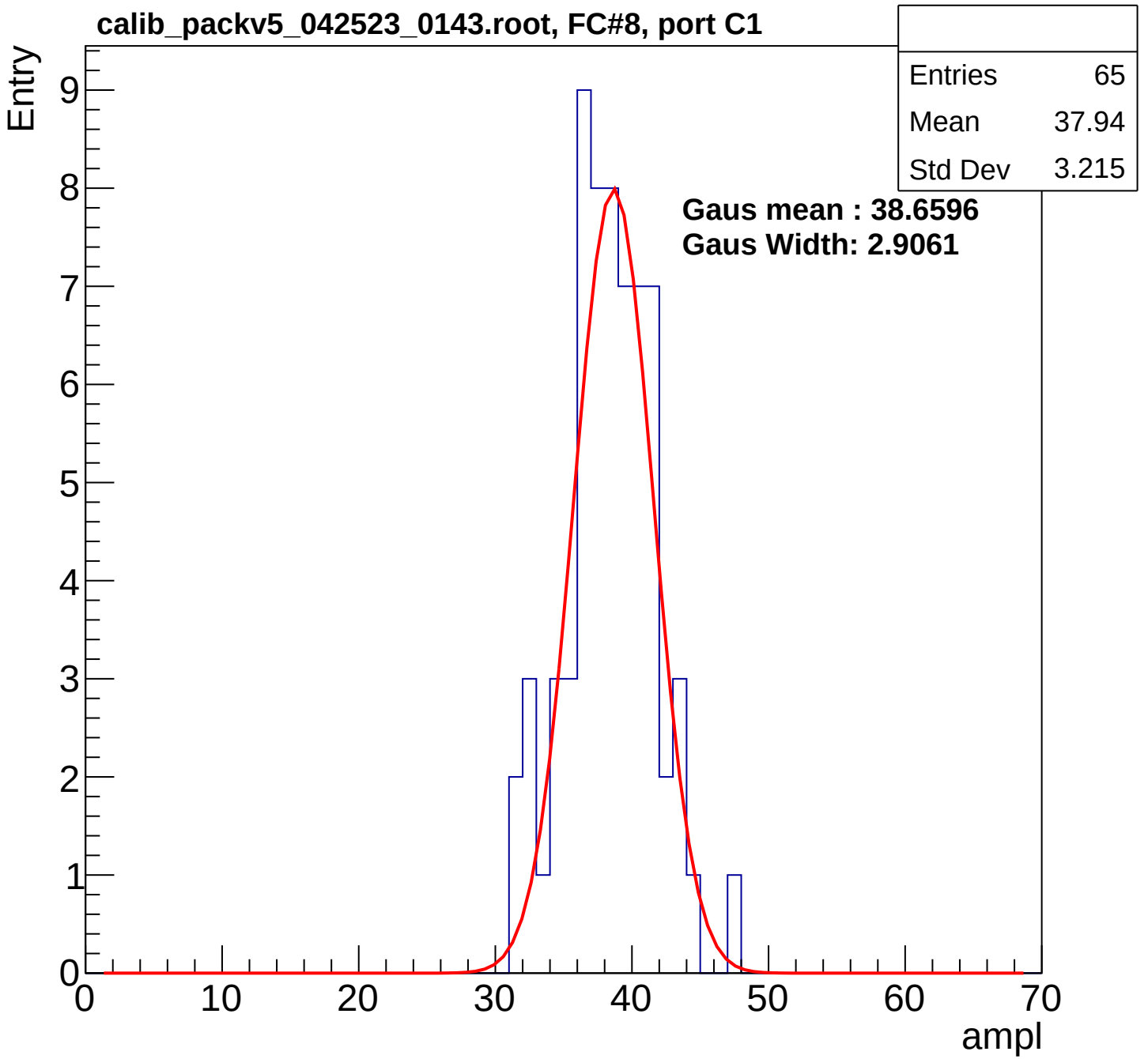

B0L002S, U10-ch95, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 57.02
Std Dev 8.108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

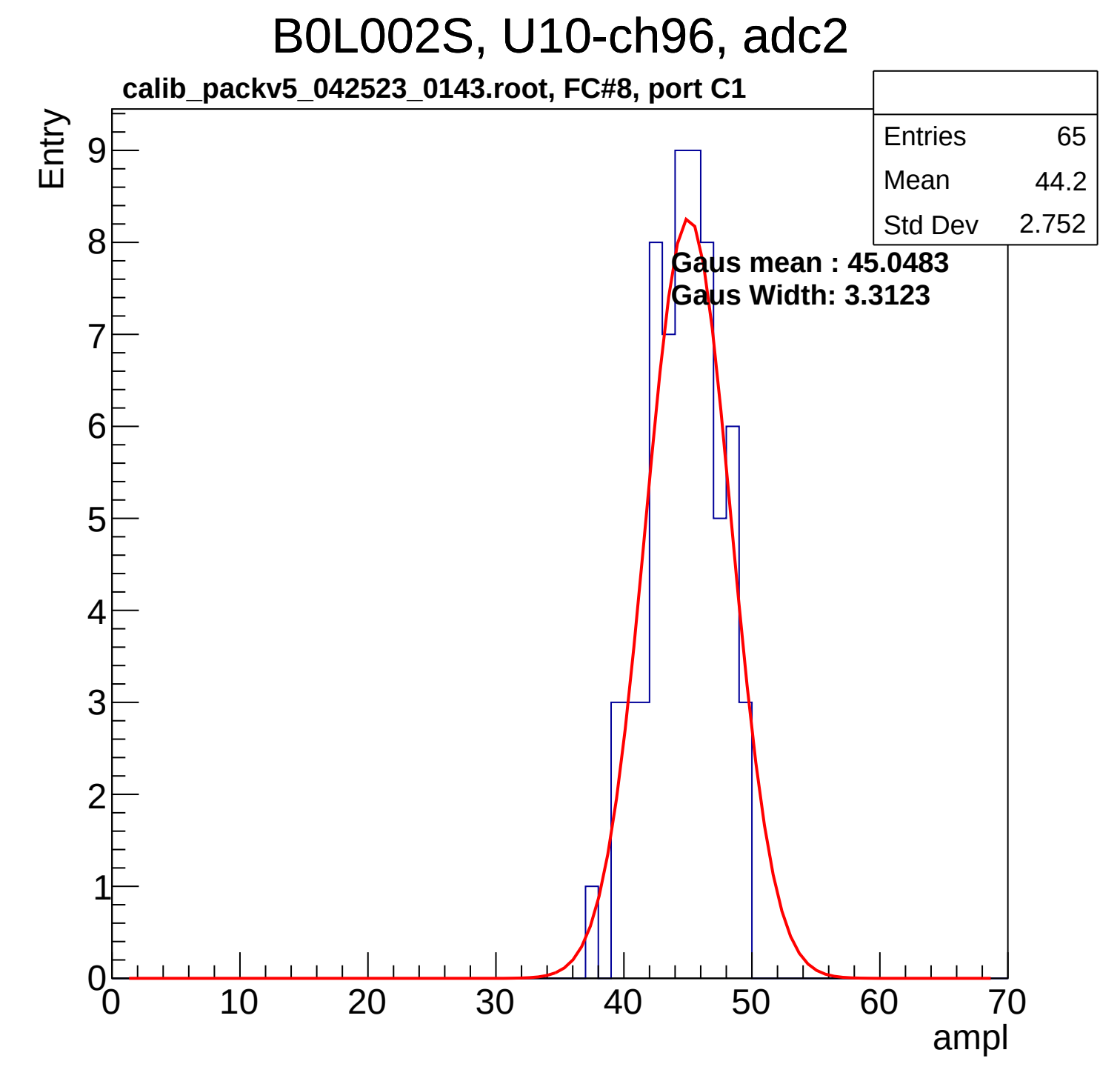

B0L002S, U10-ch95, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61
Std Dev 2.179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

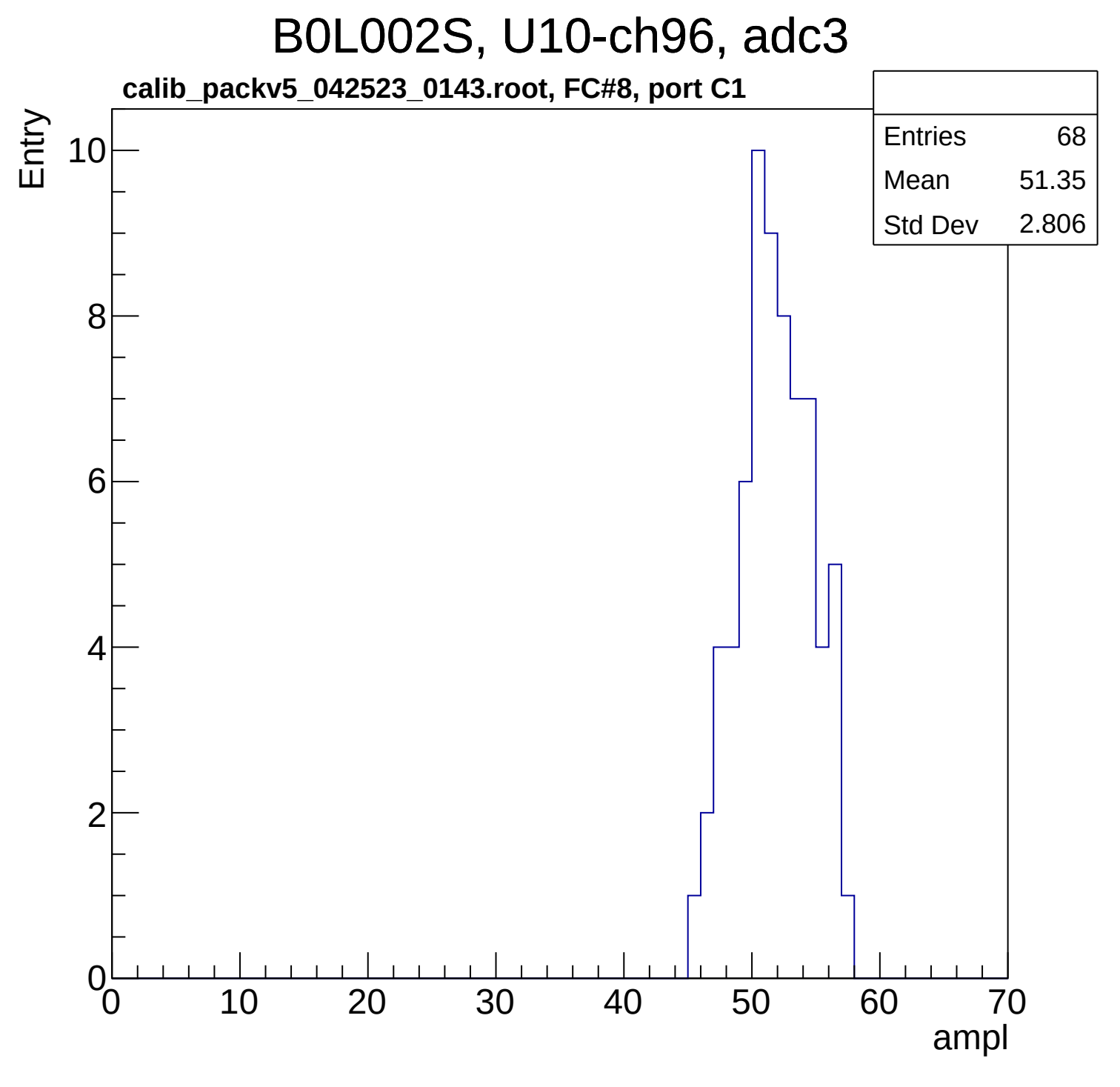

B0L002S, U10-ch95, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

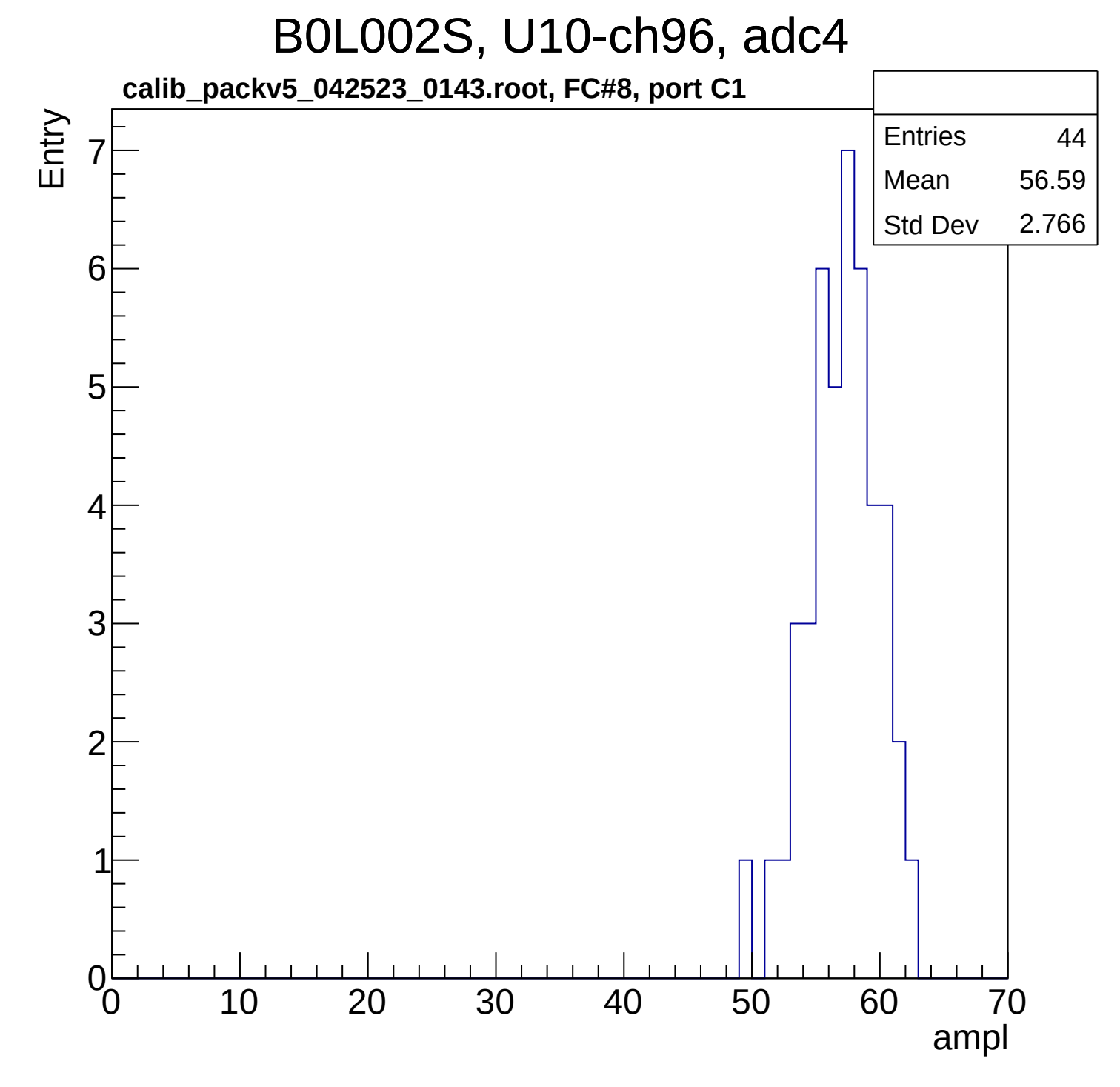

B0L002S, U10-ch95, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 26
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

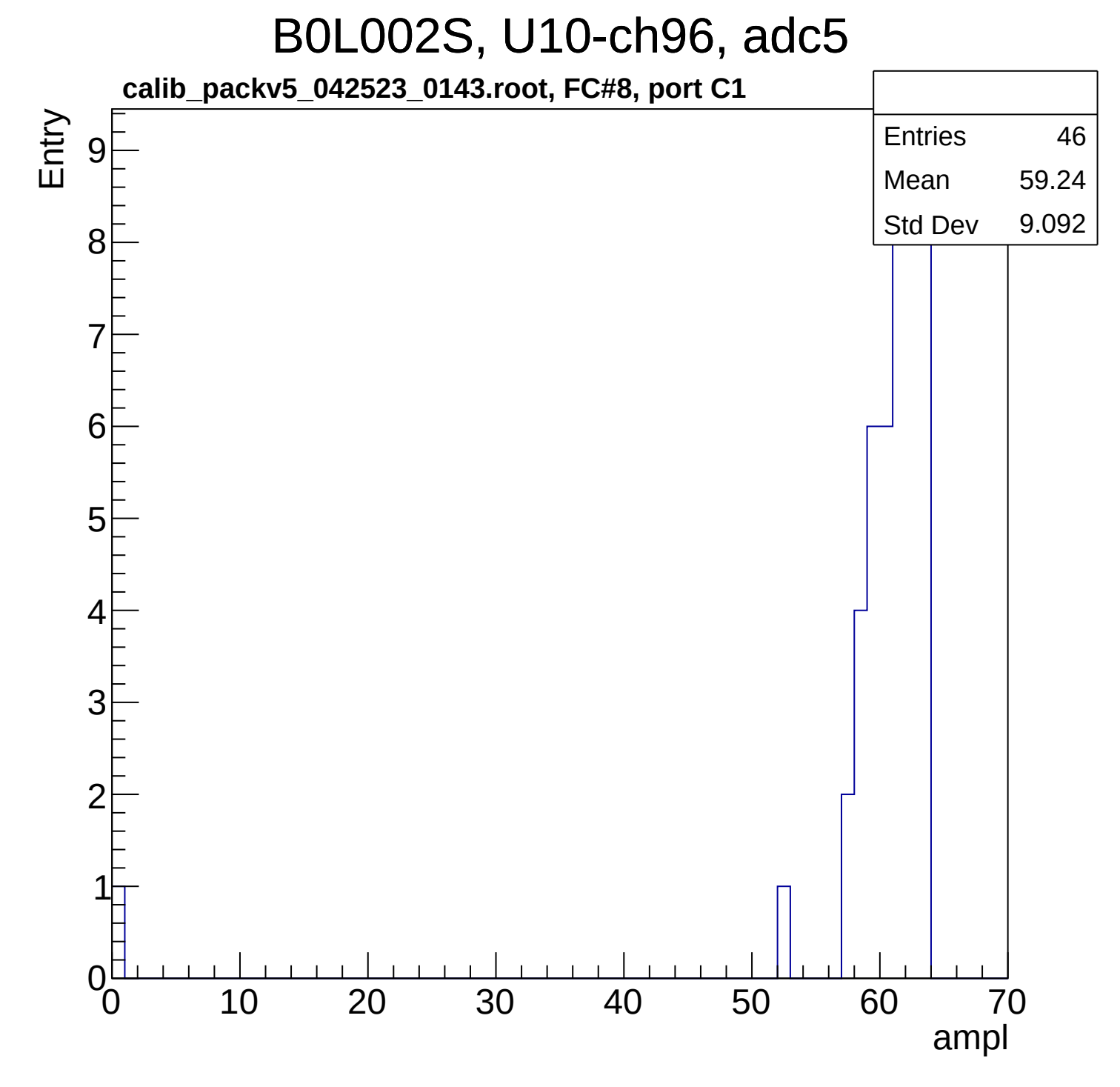

B0L002S, U10-ch96, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 31.12
Std Dev 5.176
Gaus mean : 32.0164
Gaus Width: 3.0396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

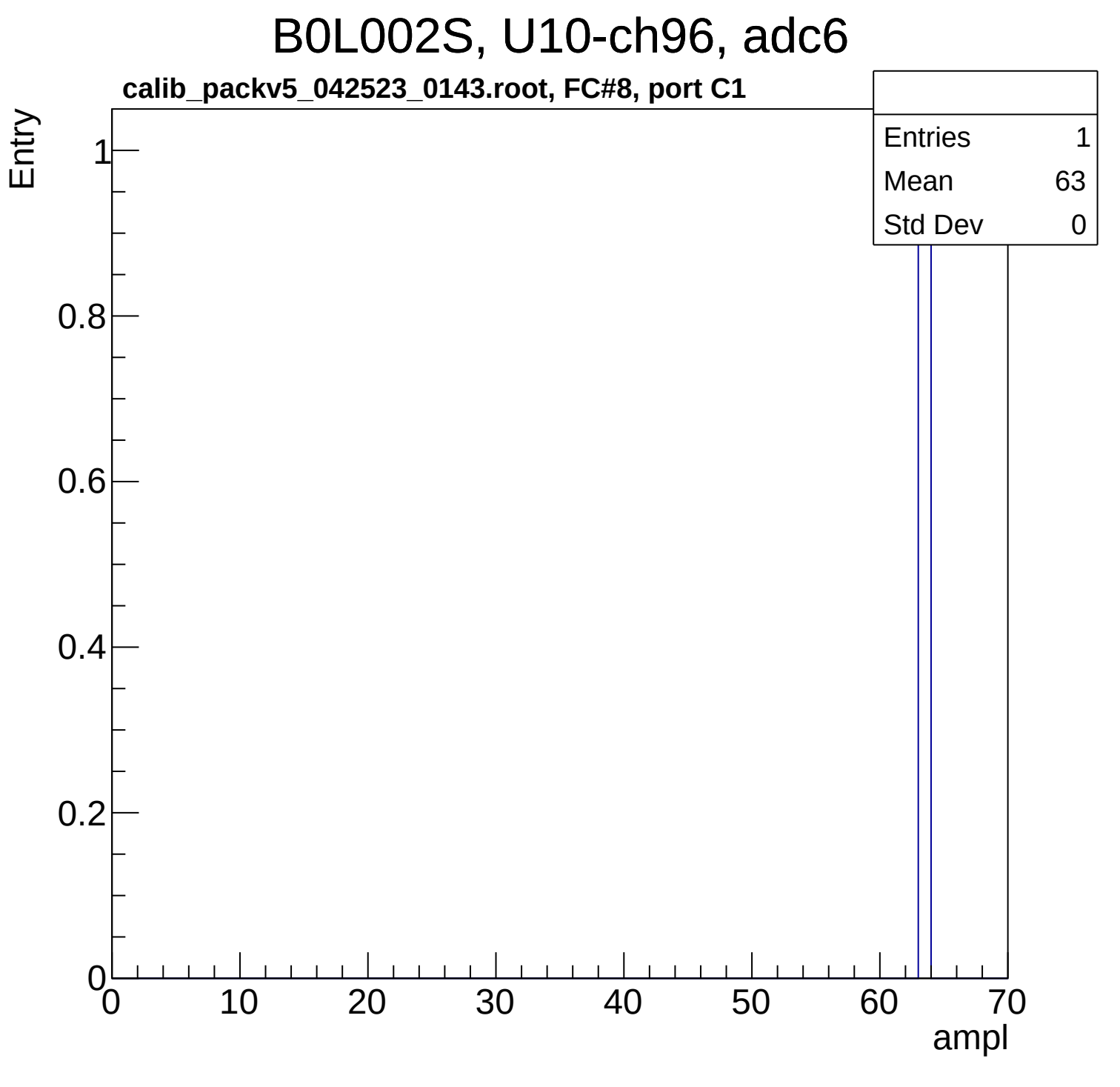

B0L002S, U10-ch96, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 37.94
Std Dev 3.215
Gaus mean : 38.6596
Gaus Width: 2.9061
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

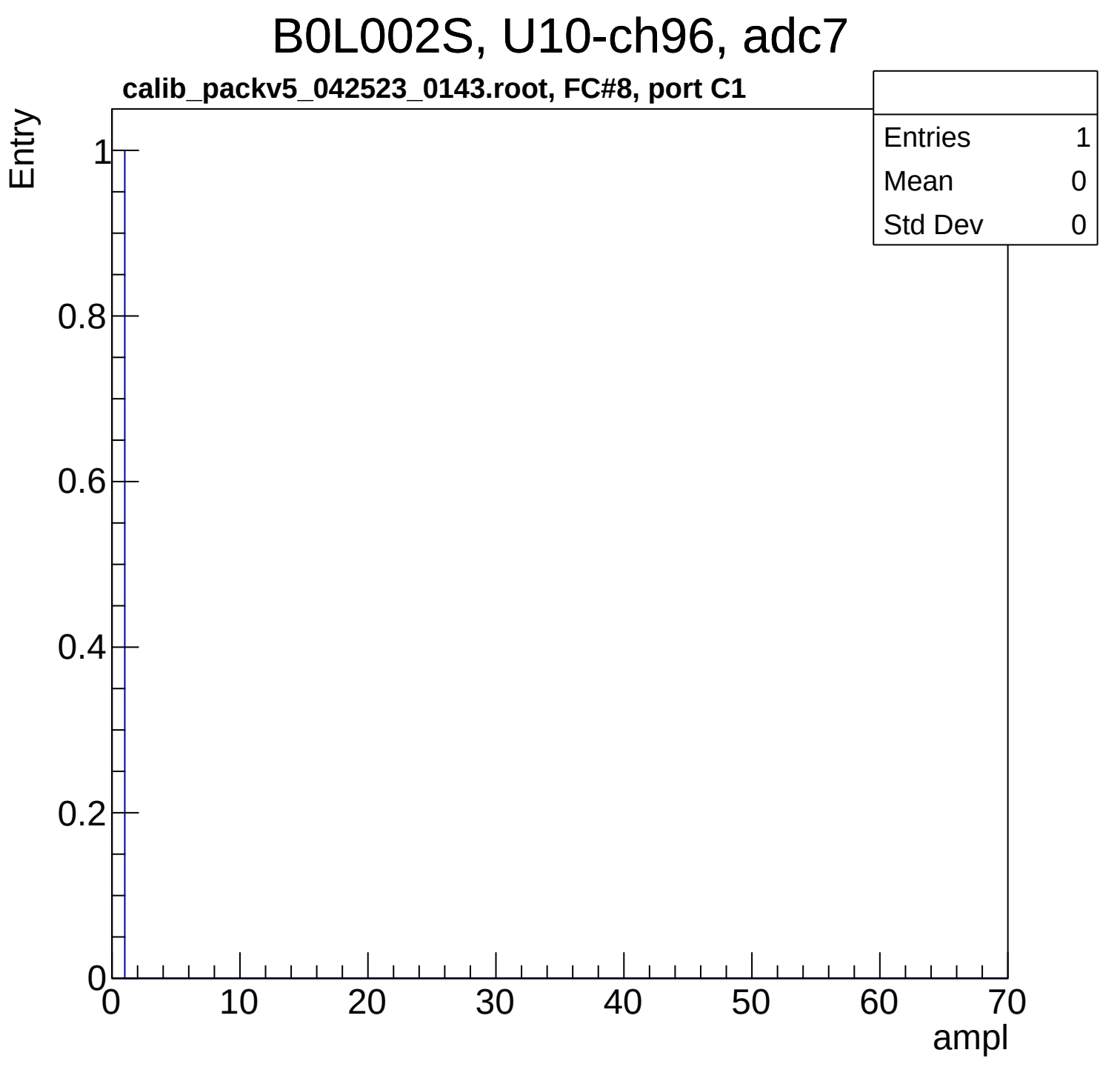

B0L002S, U10-ch96, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 44.2
Std Dev 2.752
Gaus mean : 45.0483
Gaus Width: 3.3123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

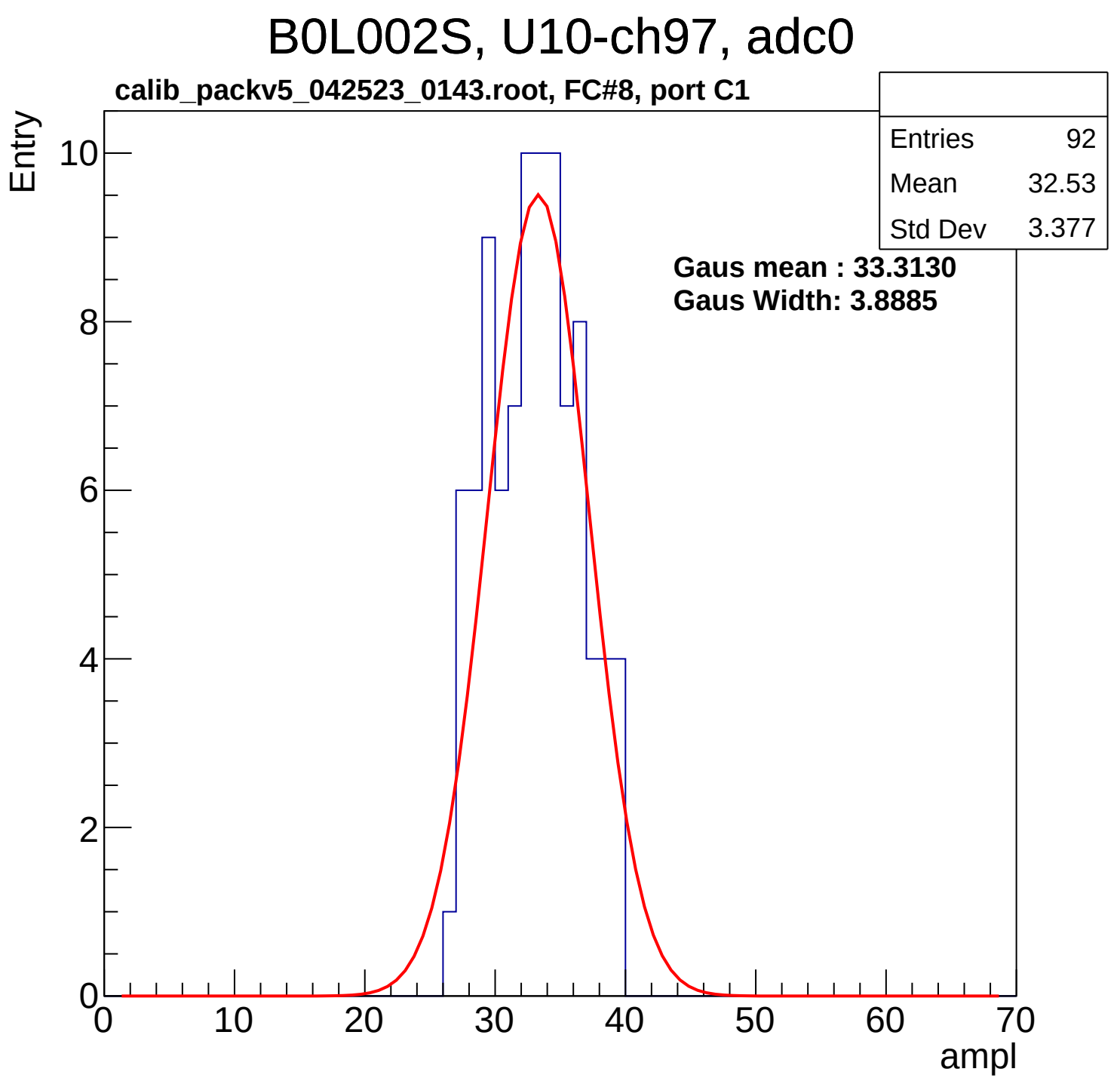

B0L002S, U10-ch96, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 51.35
Std Dev 2.806
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

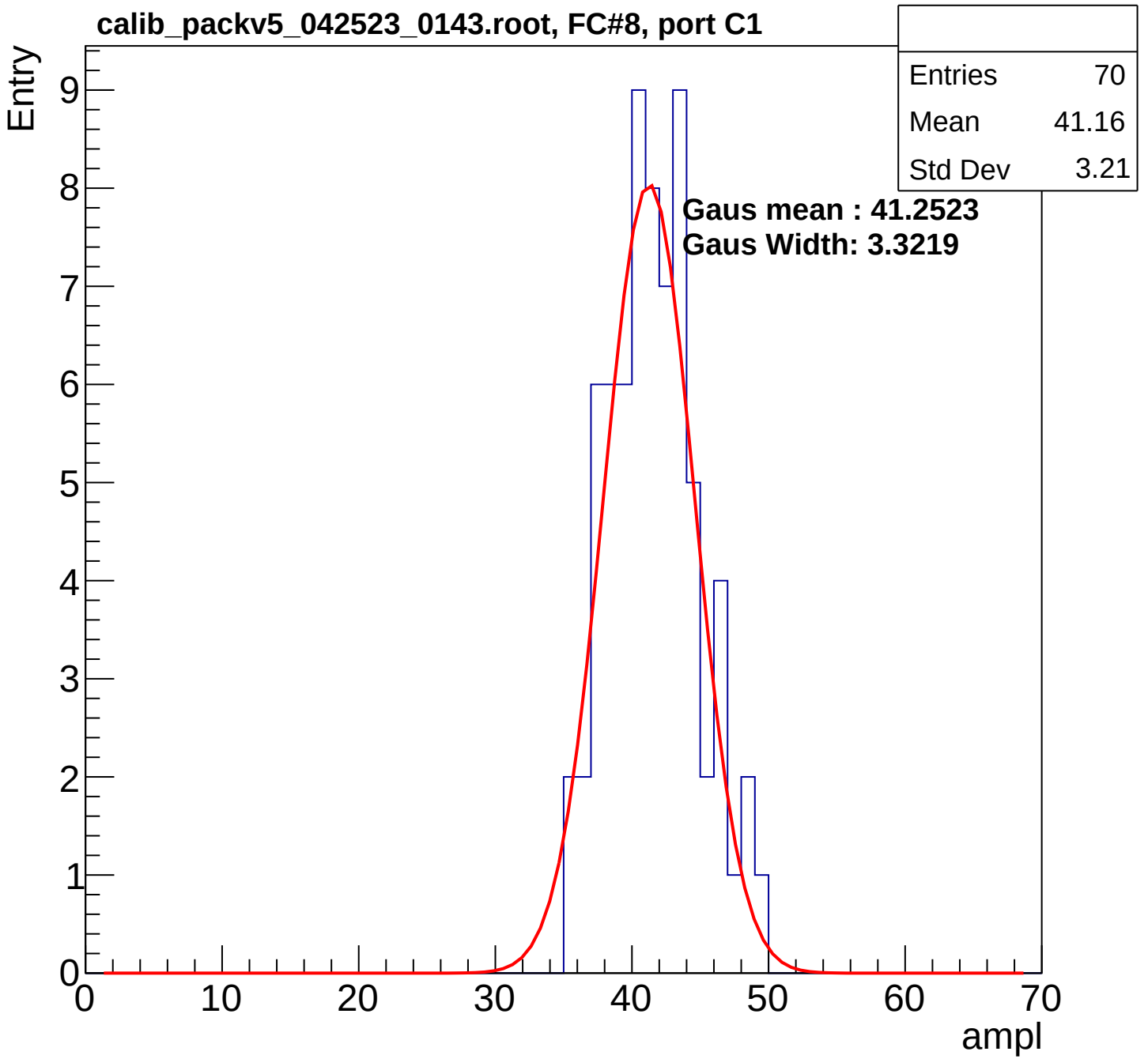

B0L002S, U10-ch96, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 44
Mean 56.59
Std Dev 2.766
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

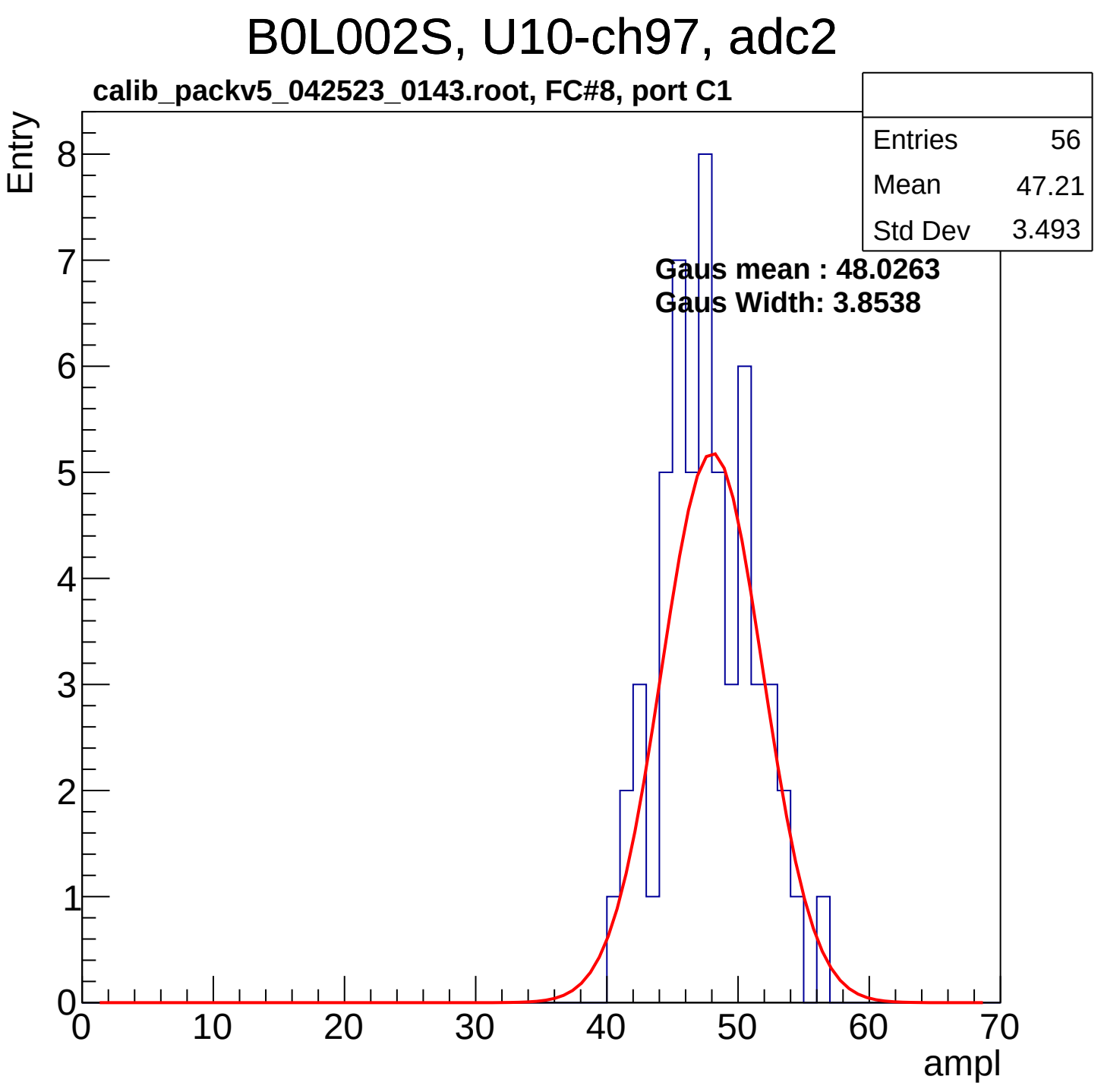

B0L002S, U10-ch96, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 59.24
Std Dev 9.092
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

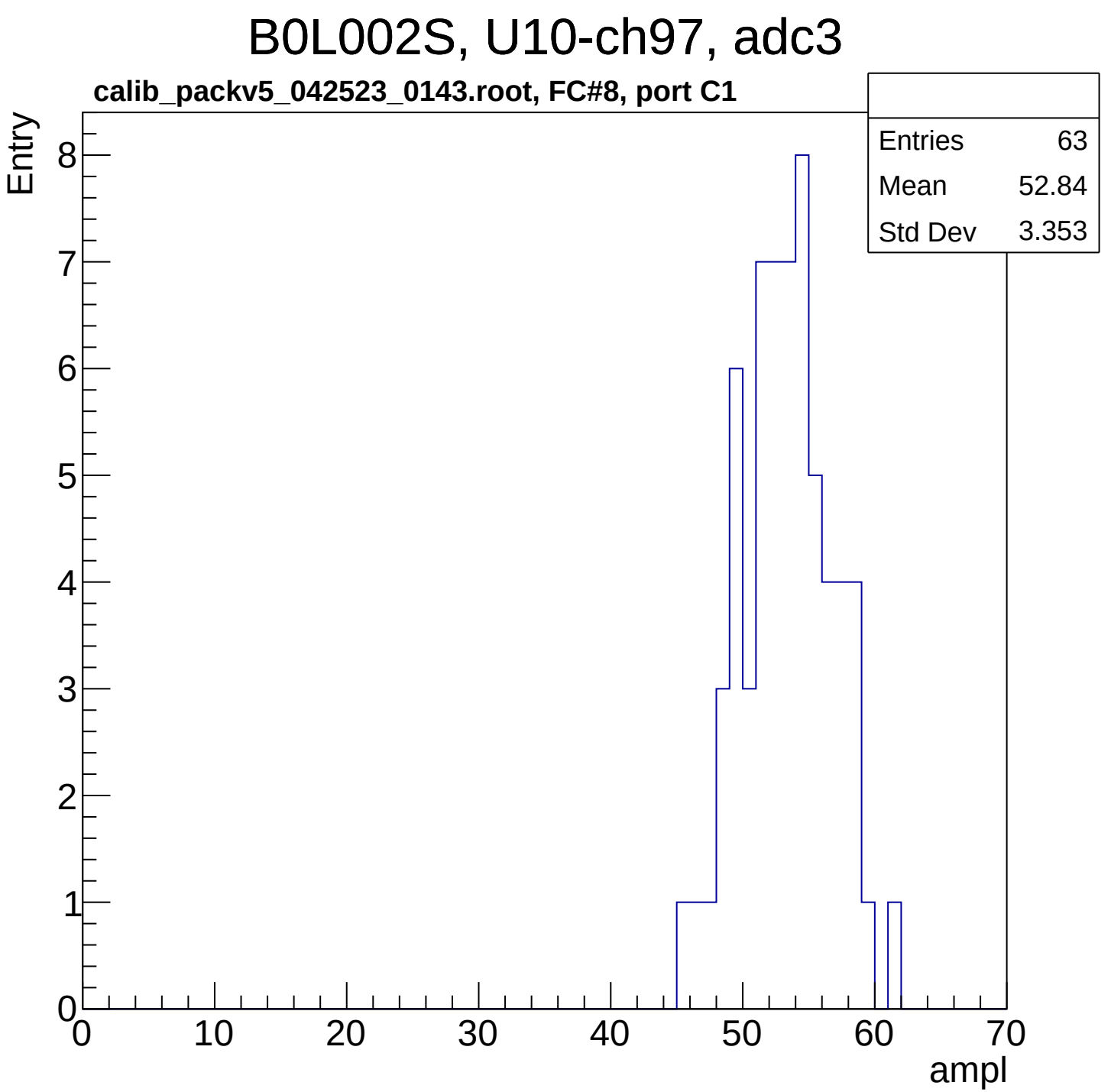

B0L002S, U10-ch96, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

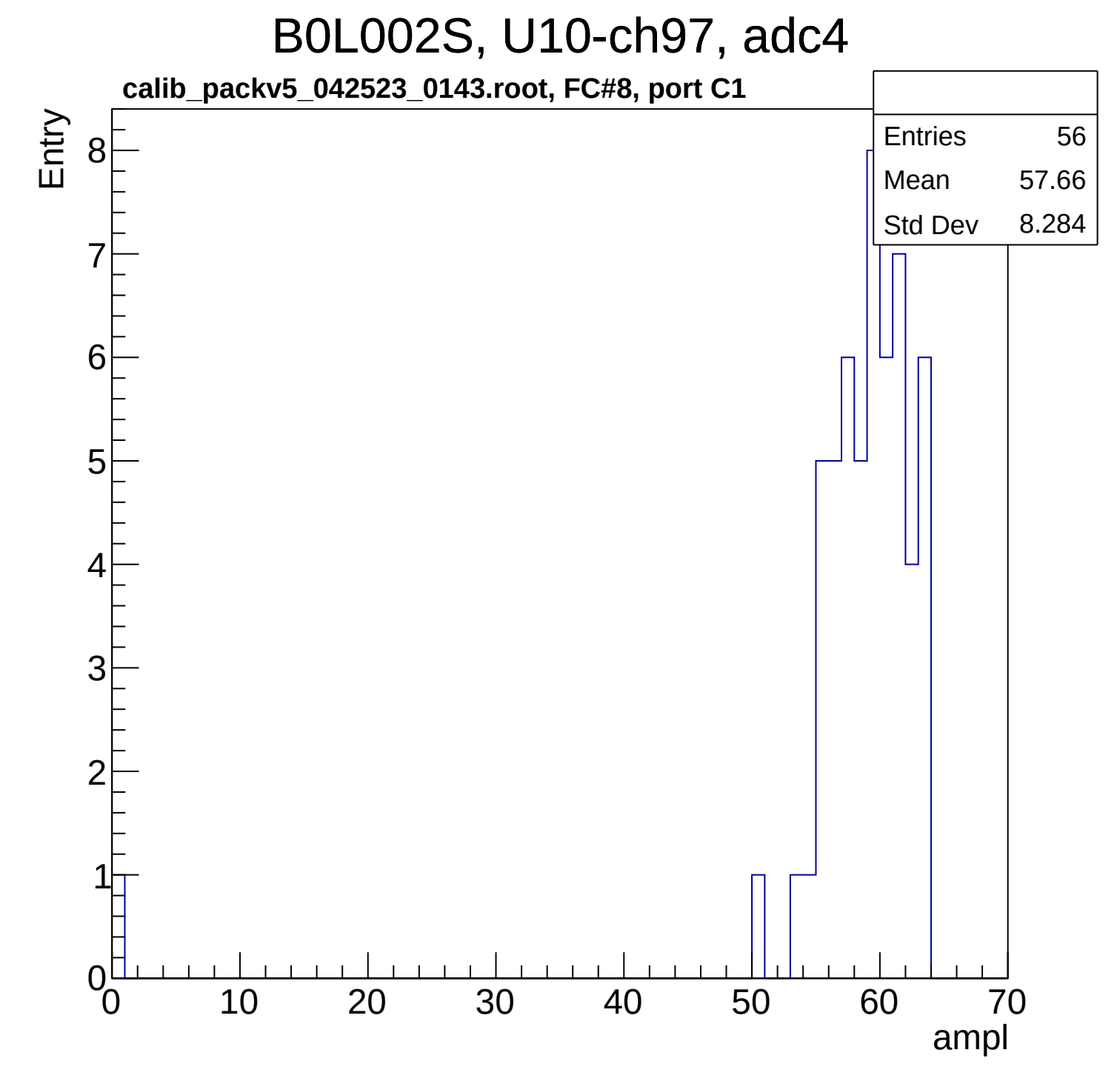

B0L002S, U10-ch96, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

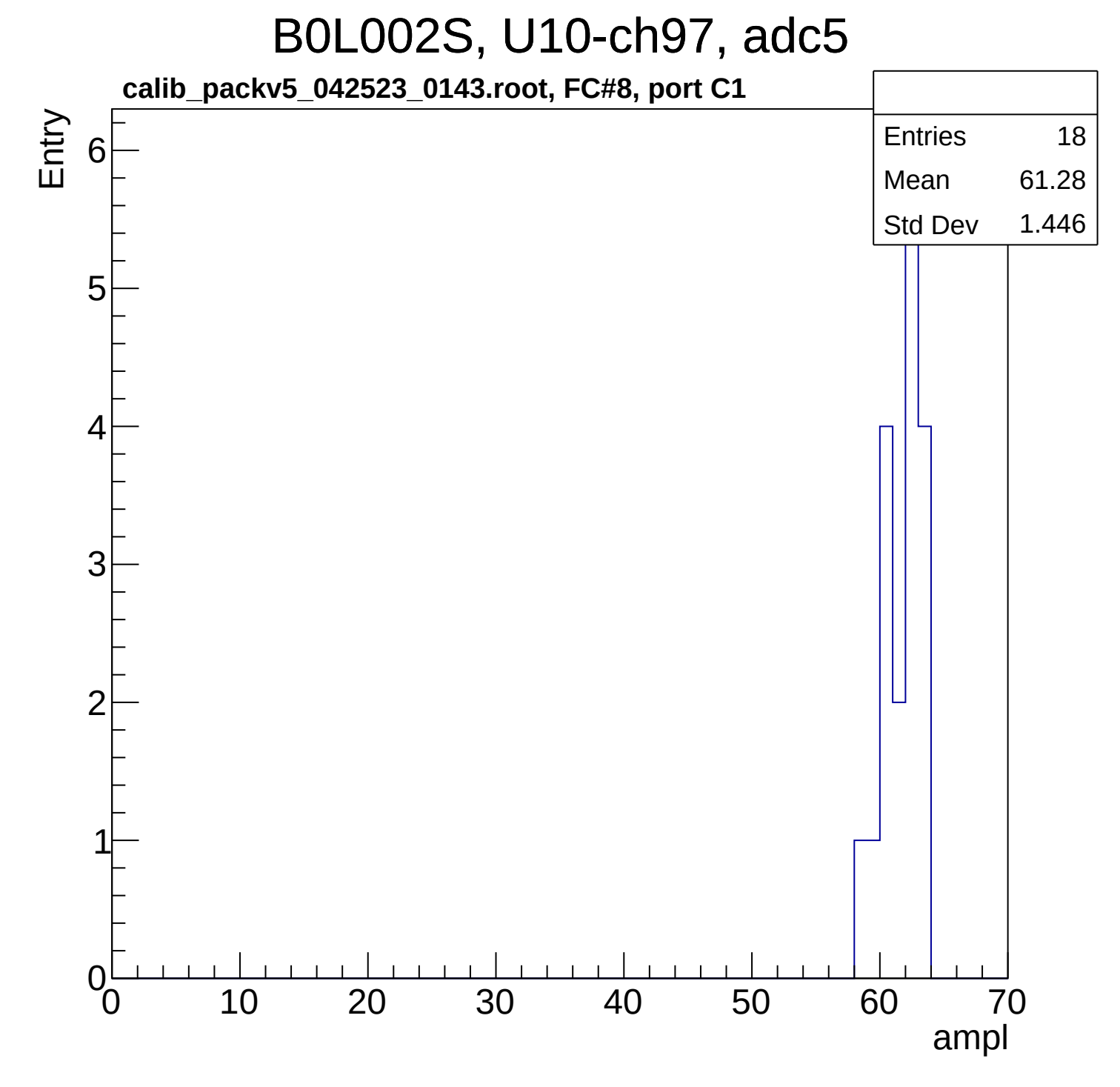

B0L002S, U10-ch97, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 92
Mean 32.53
Std Dev 3.377
Gaus mean : 33.3130
Gaus Width: 3.8885
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

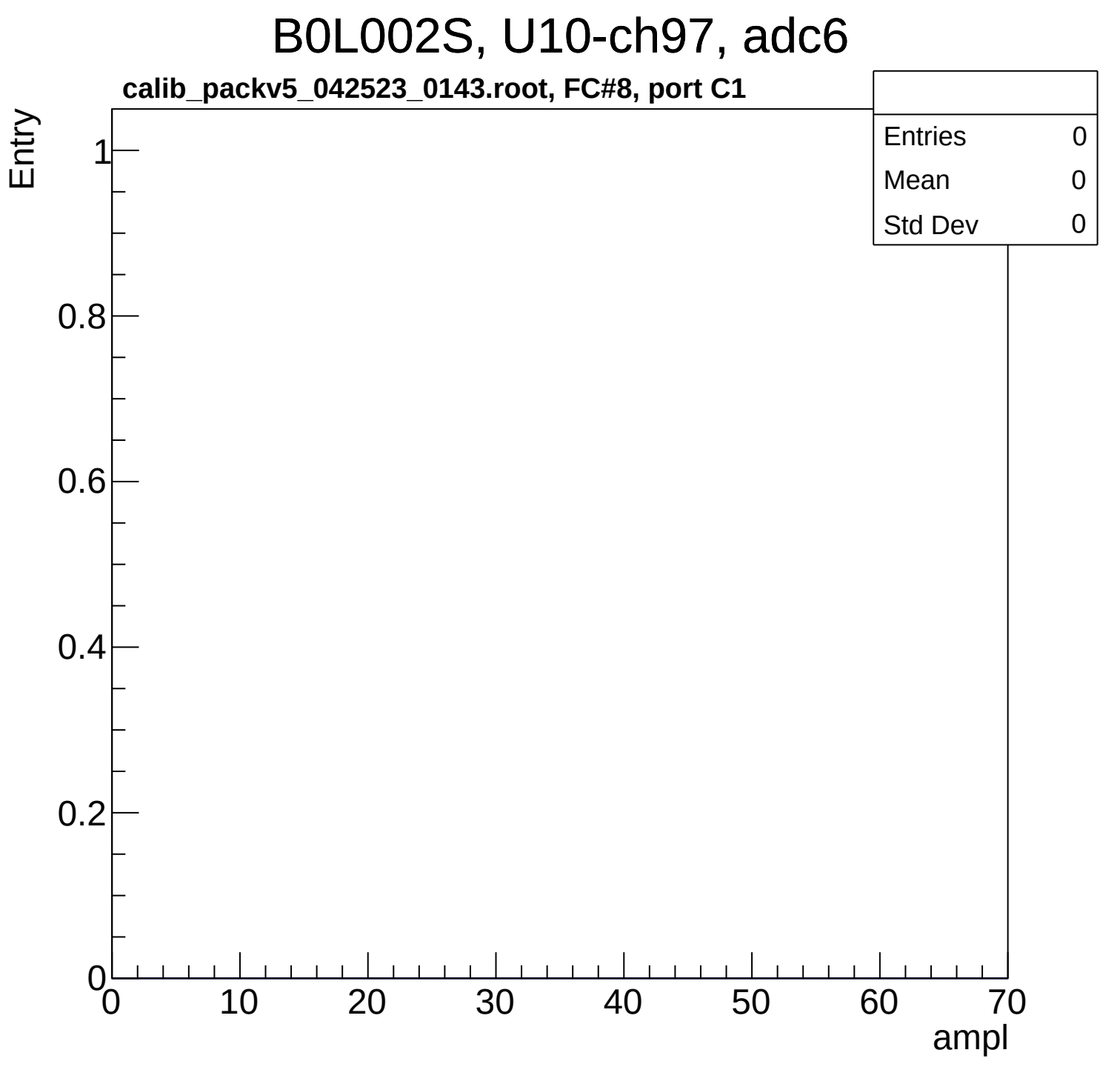

B0L002S, U10-ch97, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 41.16
Std Dev 3.21
Gaus mean : 41.2523
Gaus Width: 3.3219
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

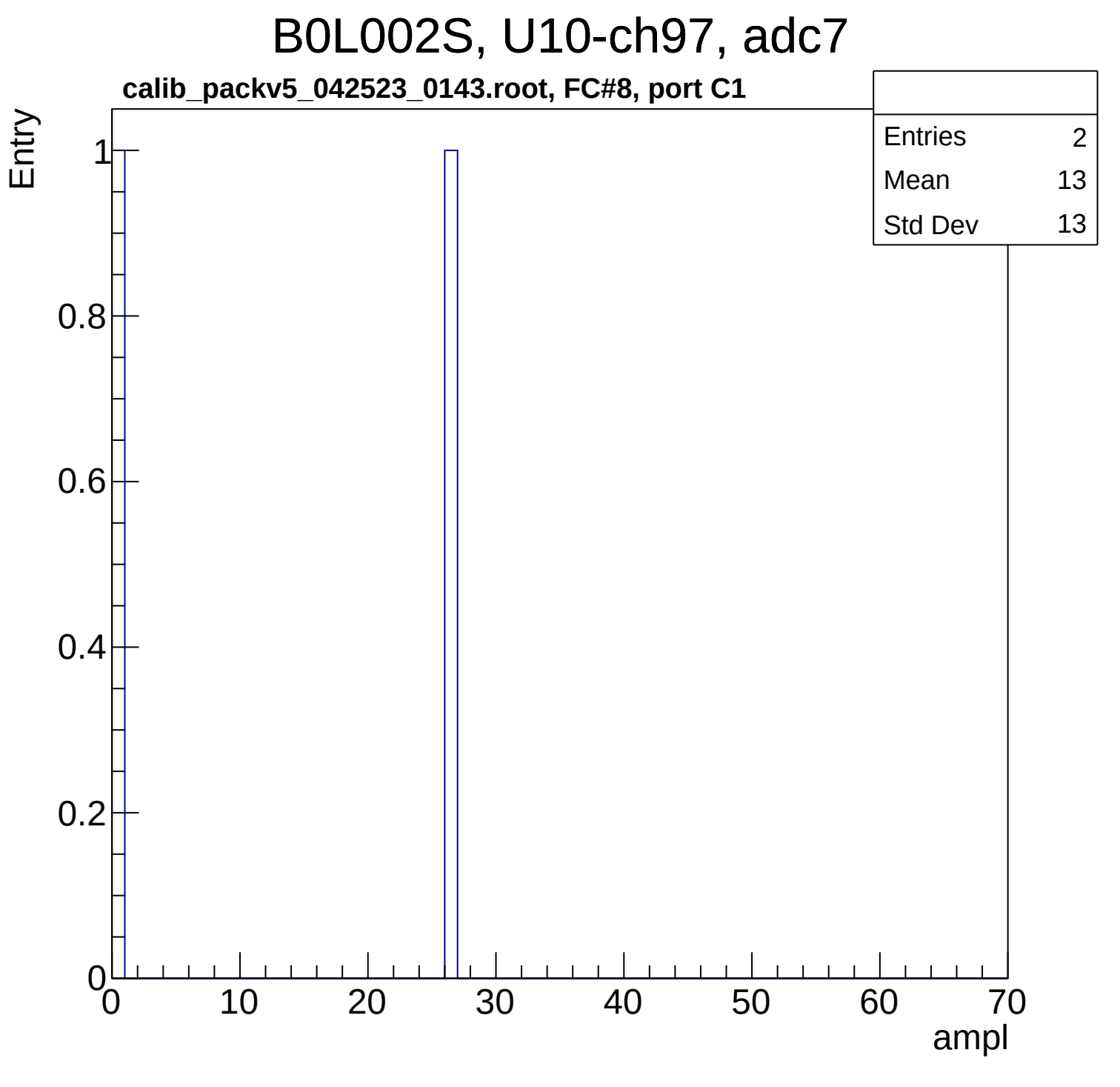

B0L002S, U10-ch97, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 47.21
Std Dev 3.493
Gaus mean : 48.0263
Gaus Width: 3.8538
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

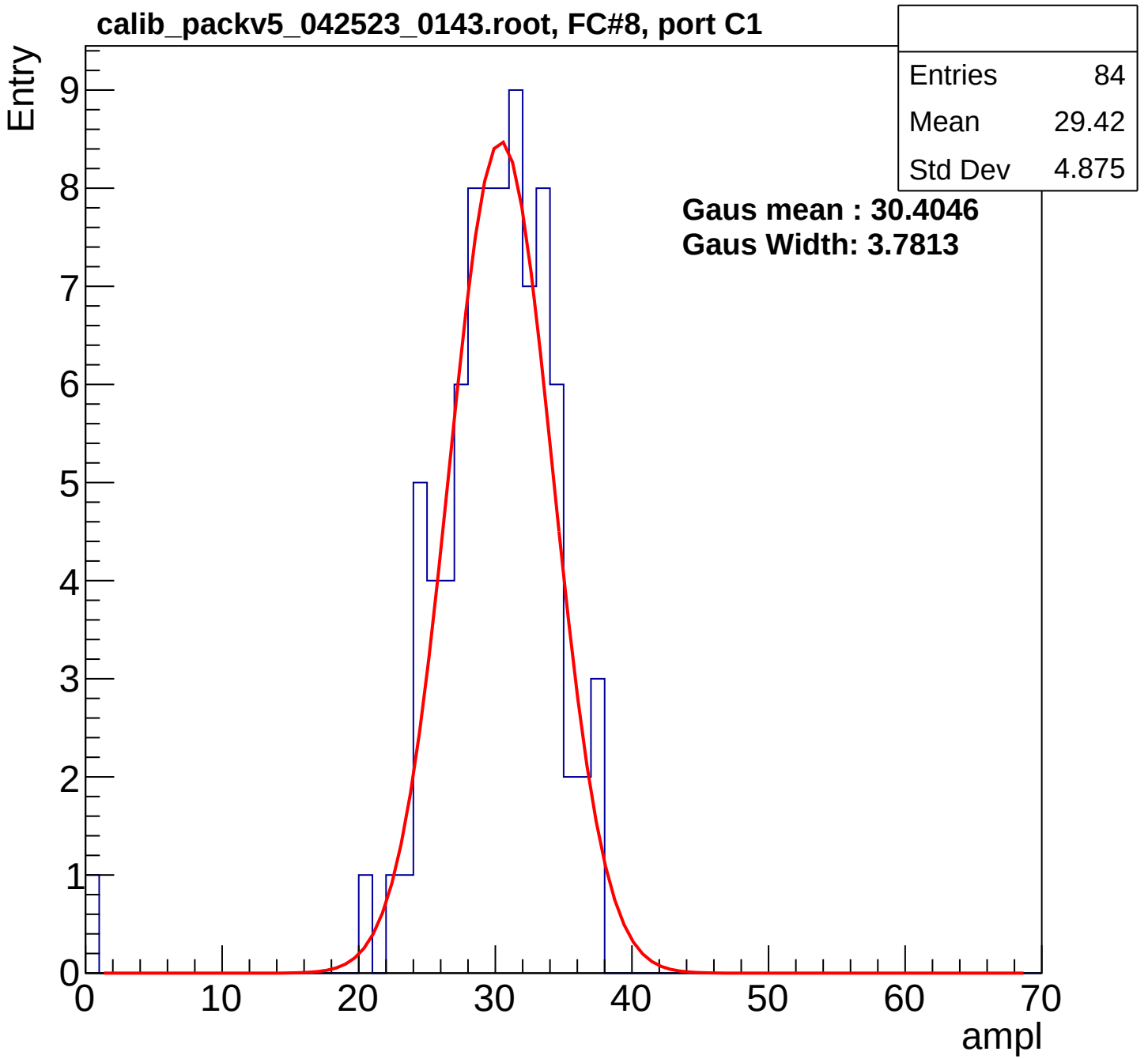

B0L002S, U10-ch97, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 52.84
Std Dev 3.353
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

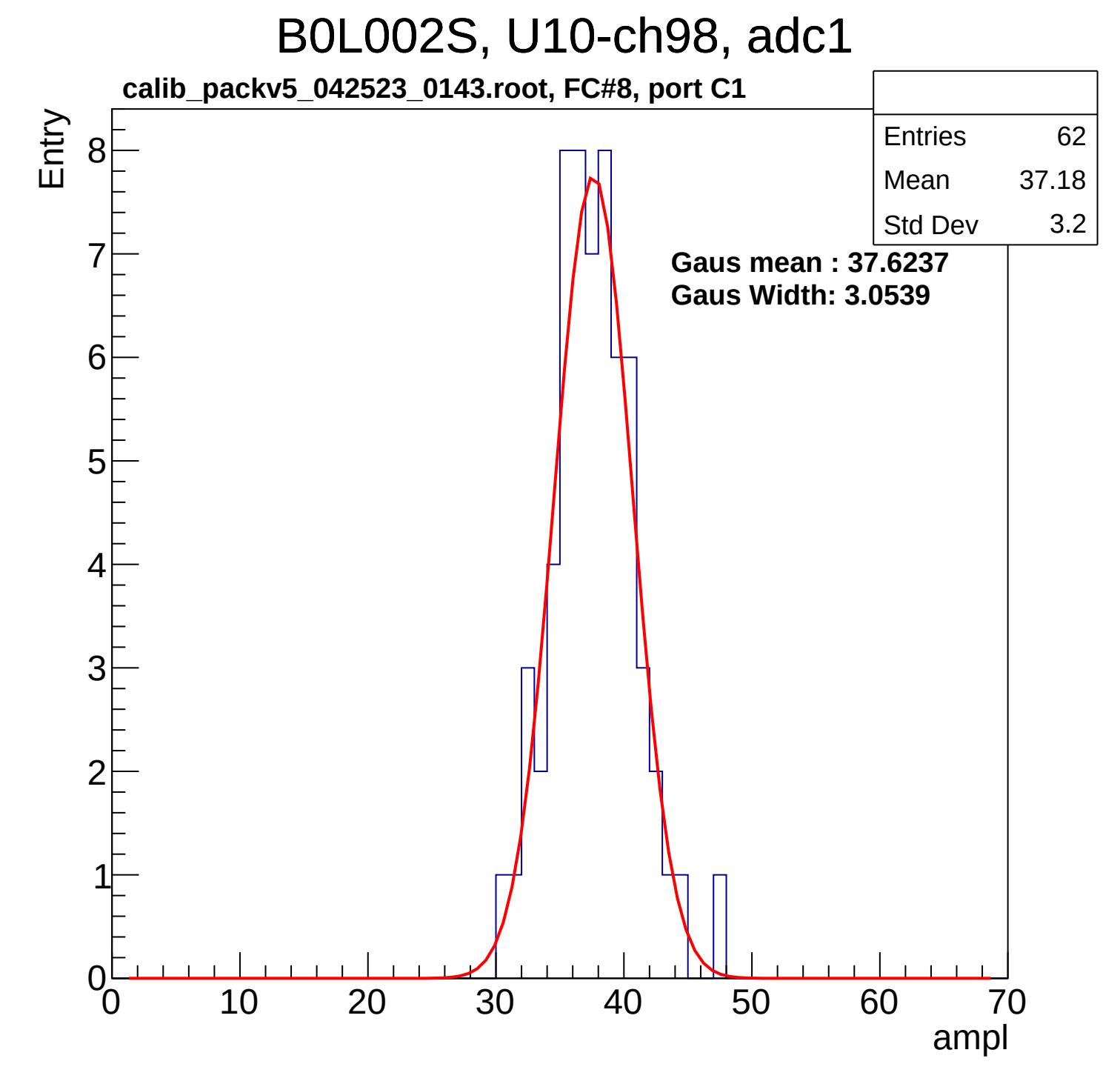

B0L002S, U10-ch97, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.66
Std Dev 8.284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

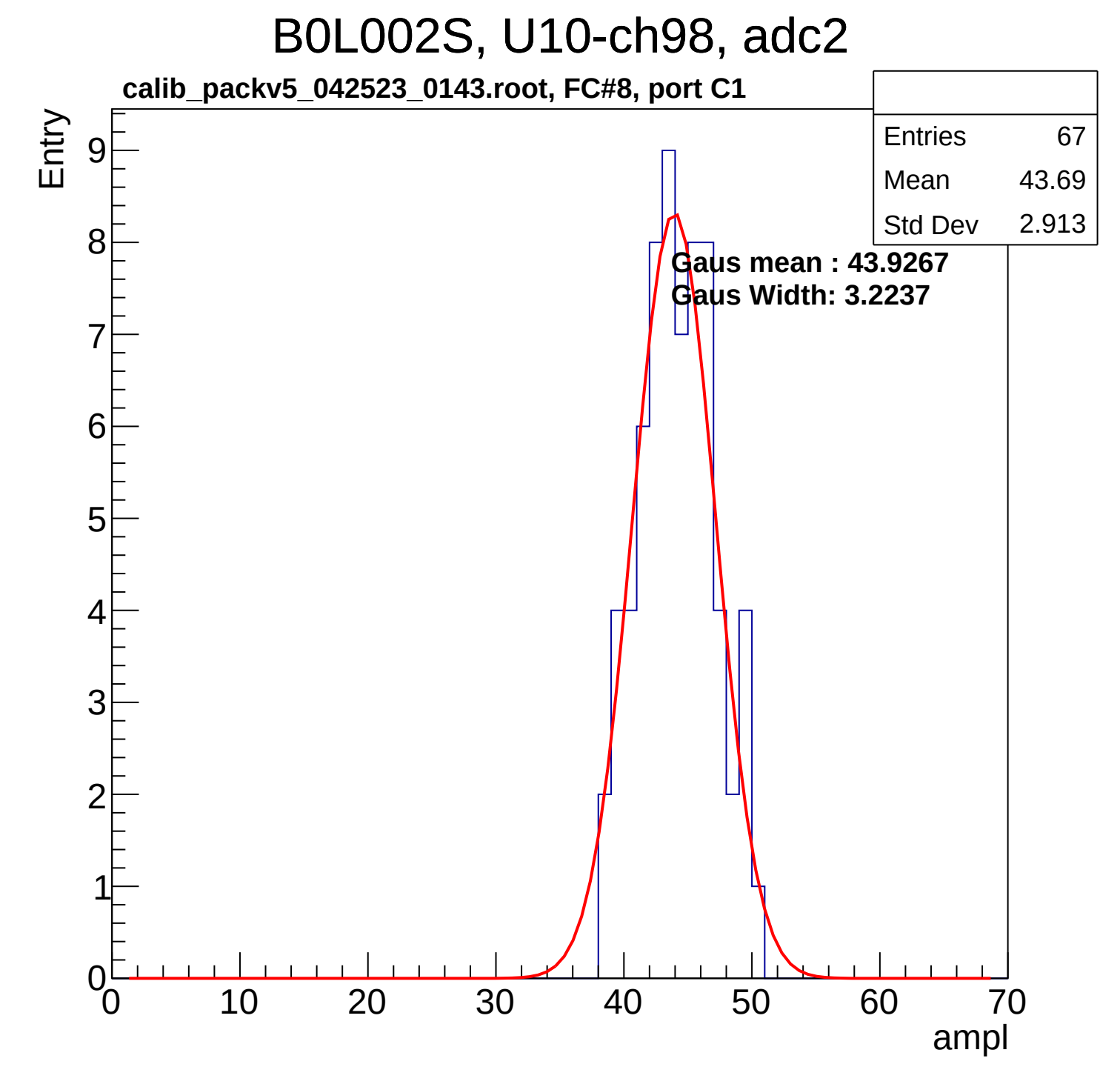

B0L002S, U10-ch97, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 61.28
Std Dev 1.446
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

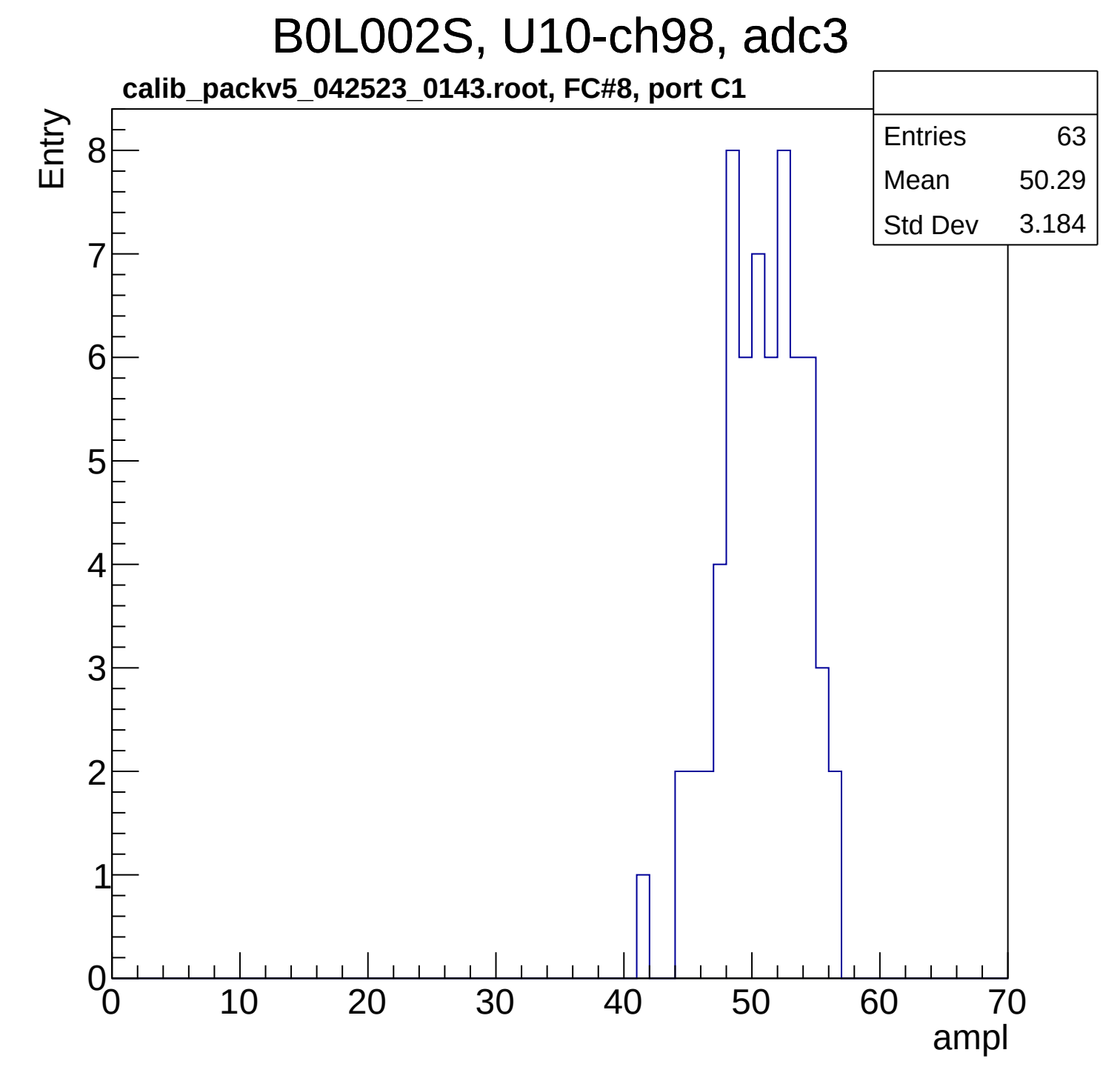

B0L002S, U10-ch97, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

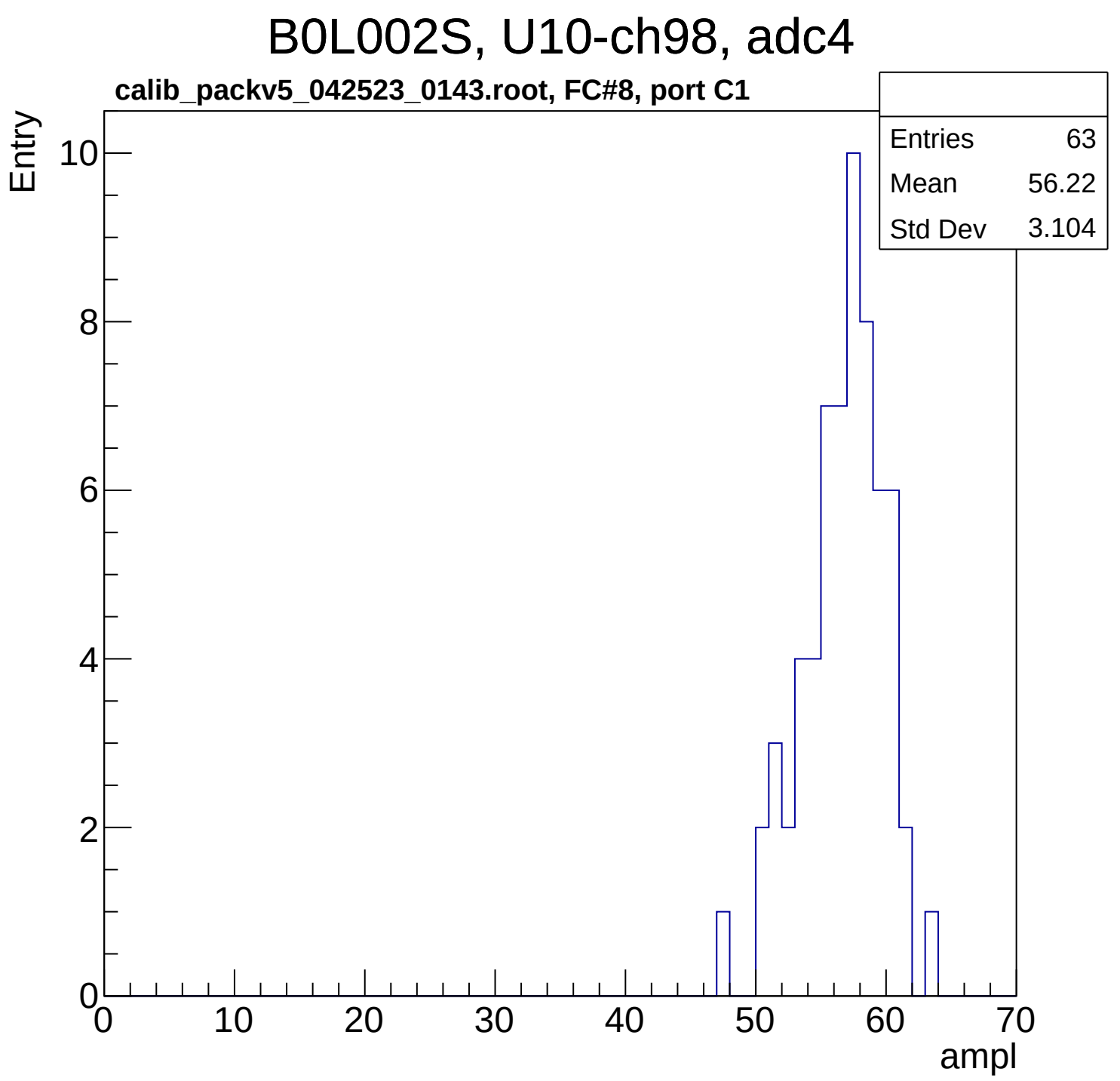

B0L002S, U10-ch97, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 13
Std Dev 13
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

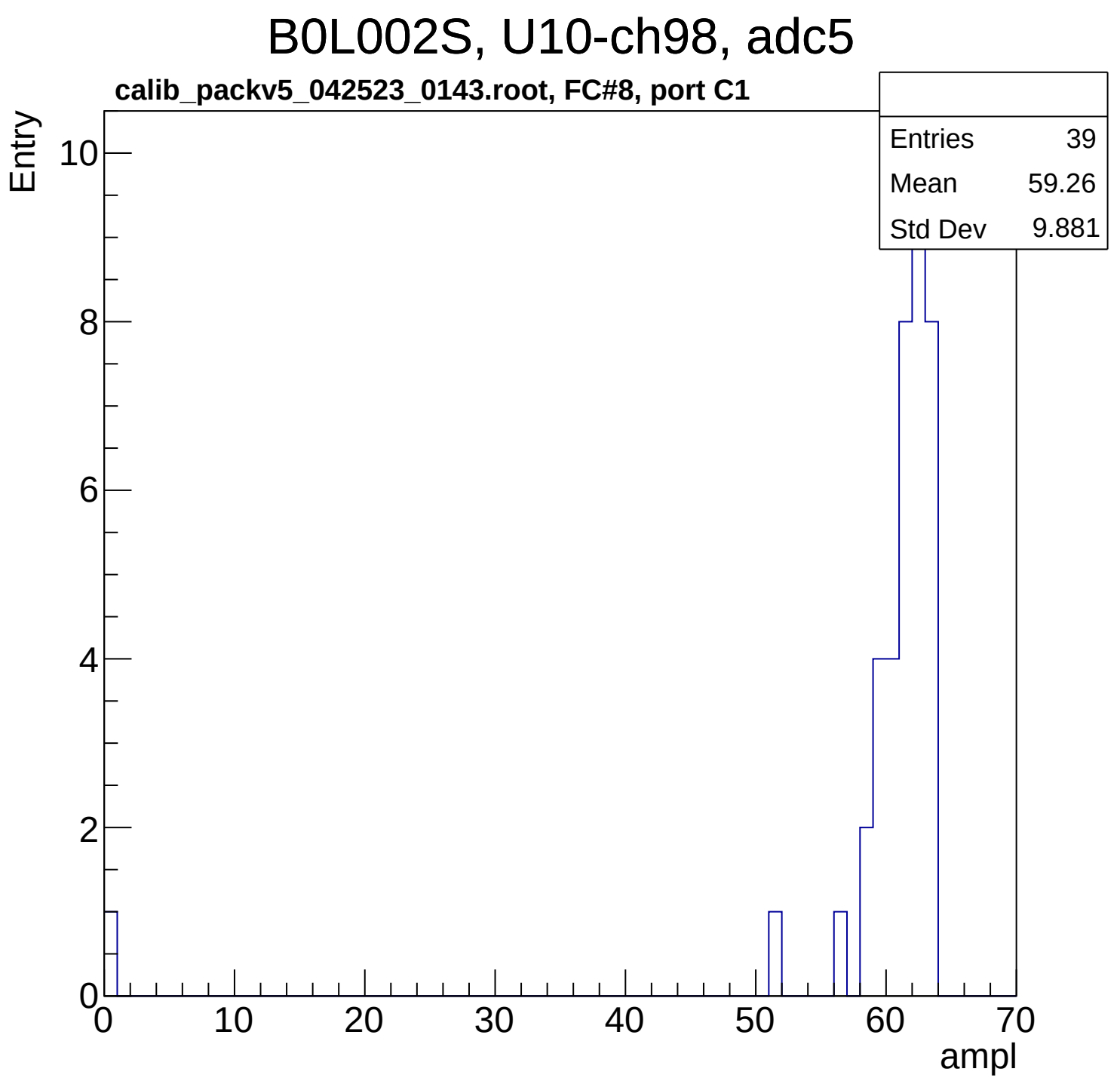

B0L002S, U10-ch98, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 84
Mean 29.42
Std Dev 4.875
Gaus mean : 30.4046
Gaus Width: 3.7813
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

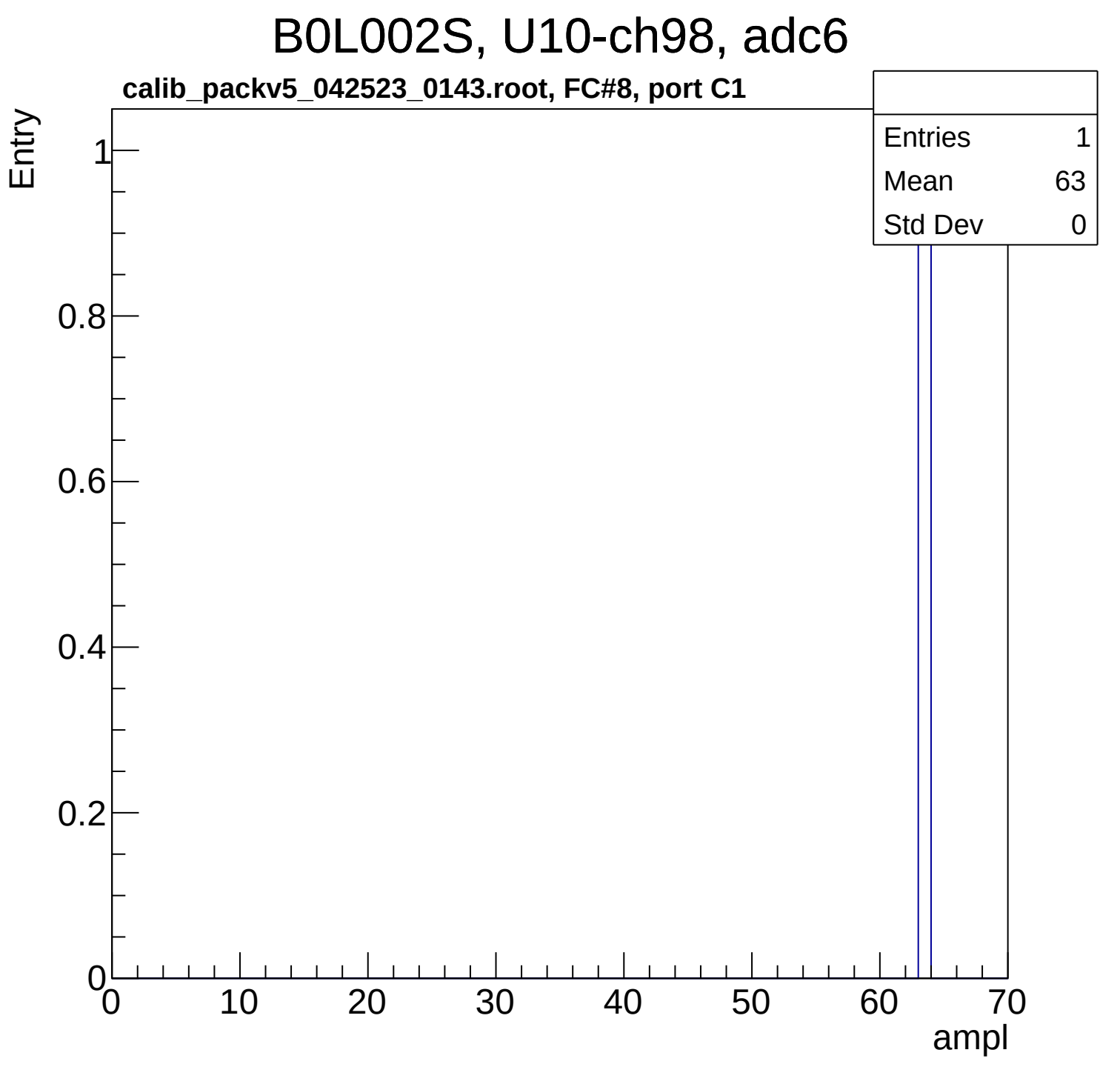

B0L002S, U10-ch98, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 37.18
Std Dev 3.2
Gaus mean : 37.6237
Gaus Width: 3.0539
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

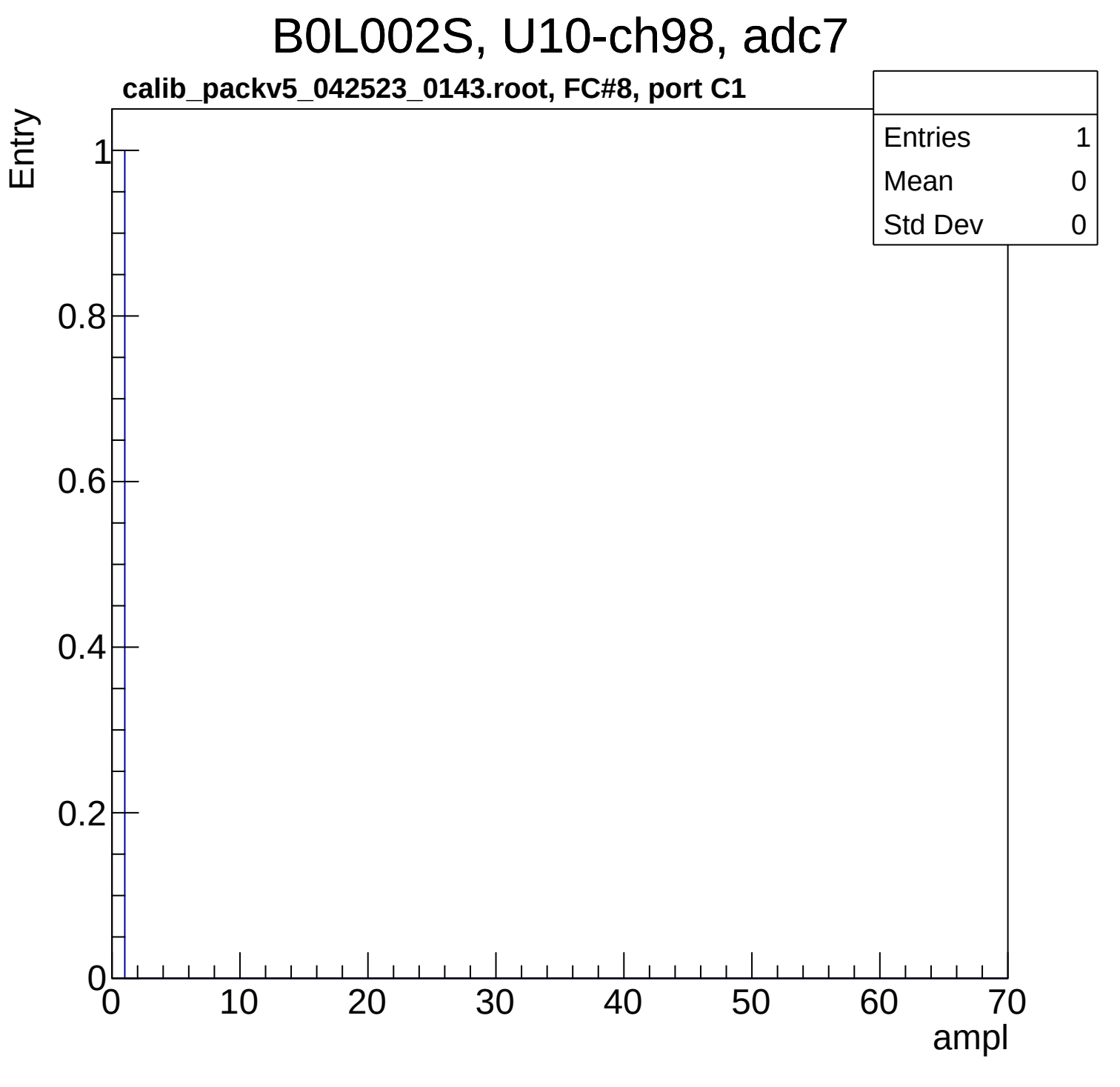

B0L002S, U10-ch98, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 43.69
Std Dev 2.913
Gaus mean : 43.9267
Gaus Width: 3.2237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

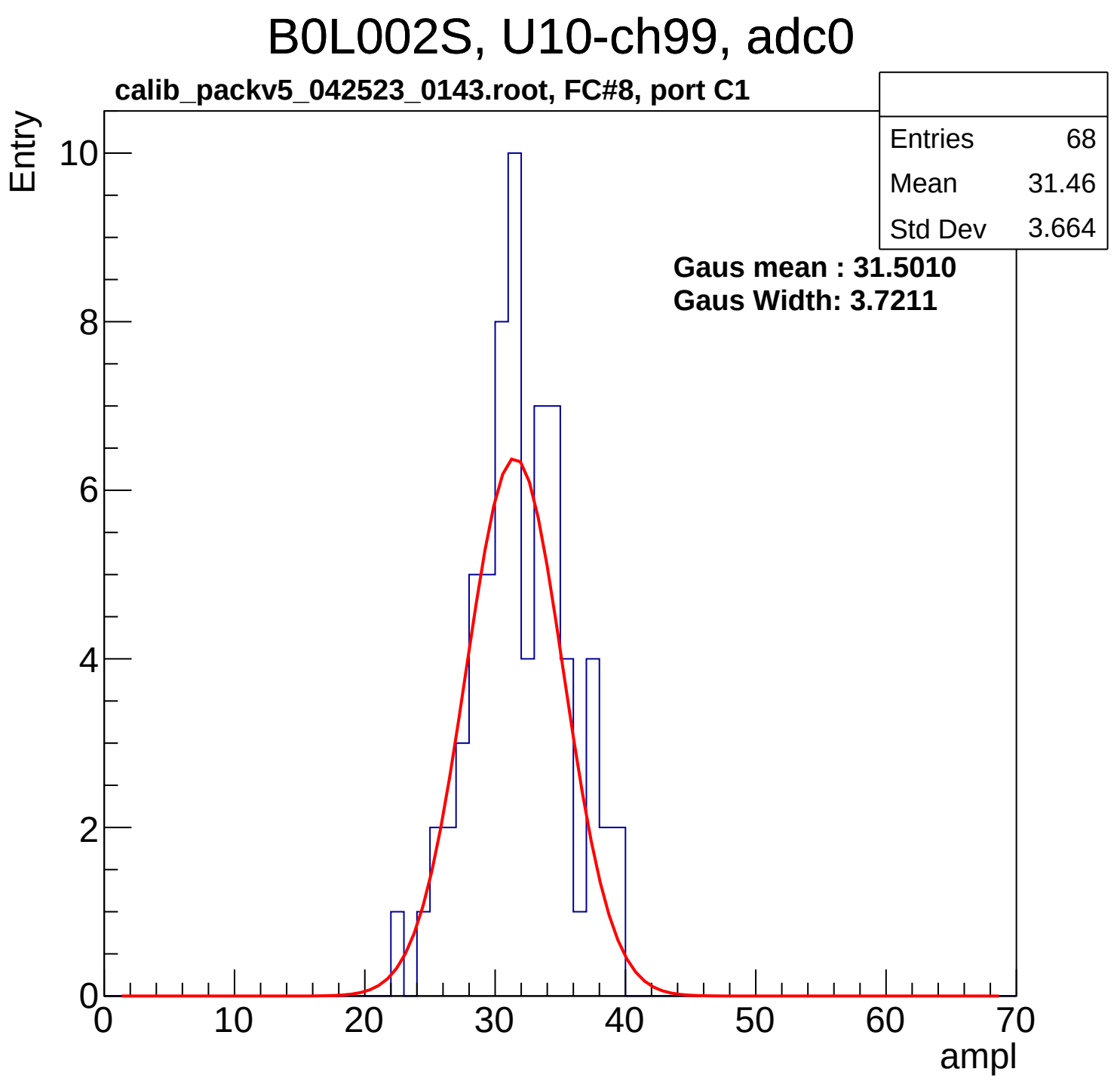

B0L002S, U10-ch98, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.29
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

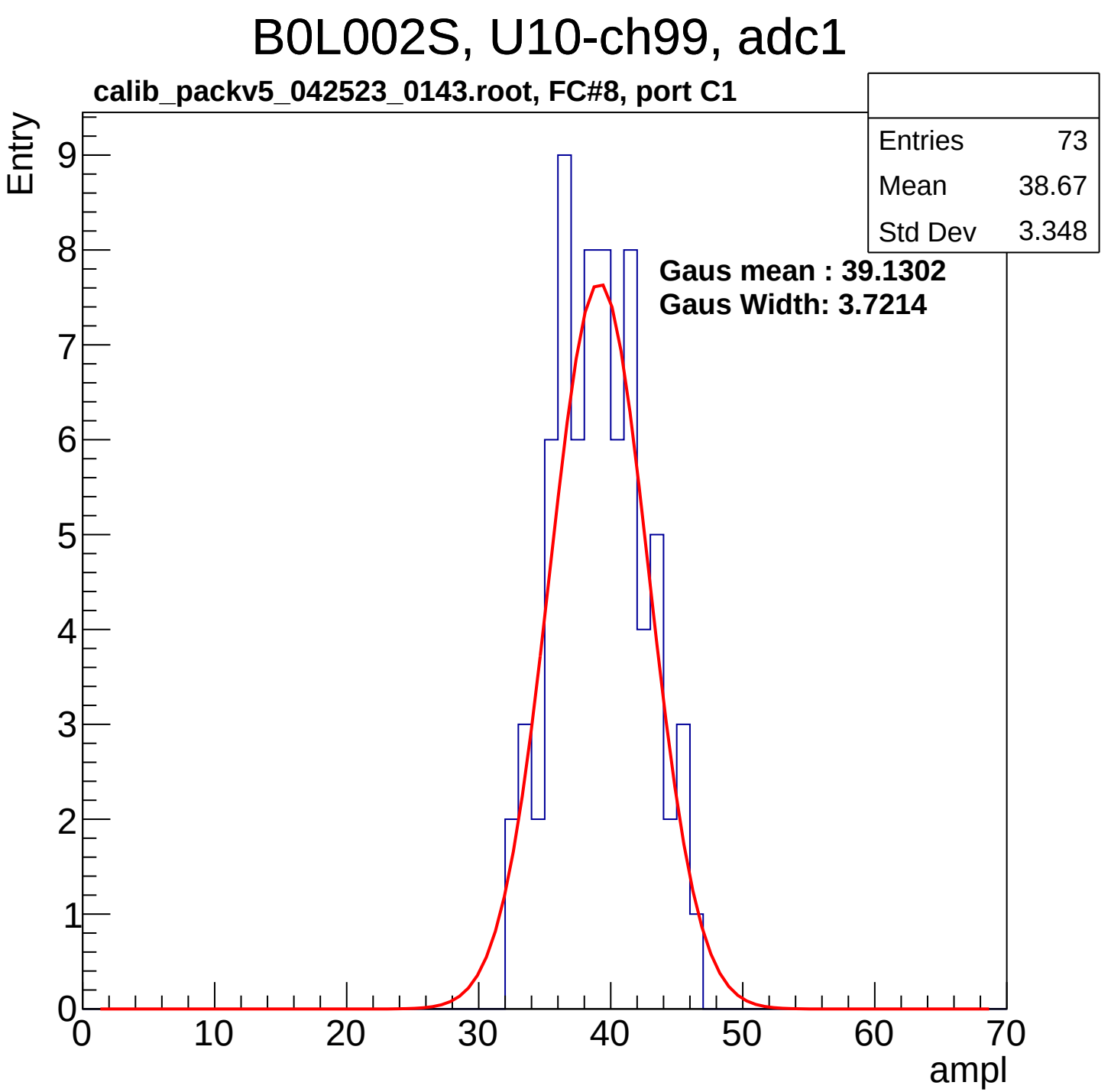

B0L002S, U10-ch98, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.22
Std Dev 3.104
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

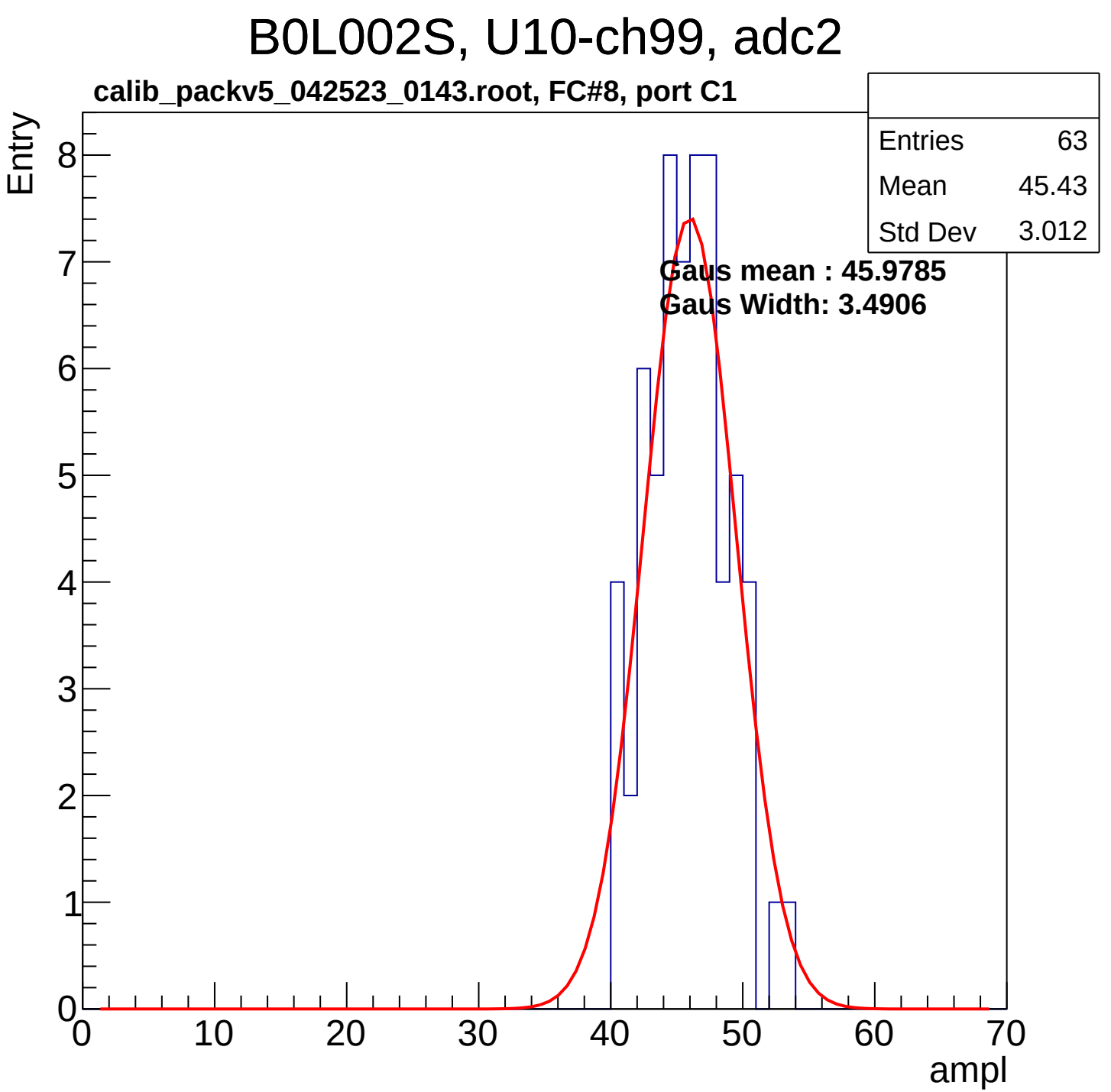

B0L002S, U10-ch98, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.26
Std Dev 9.881
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

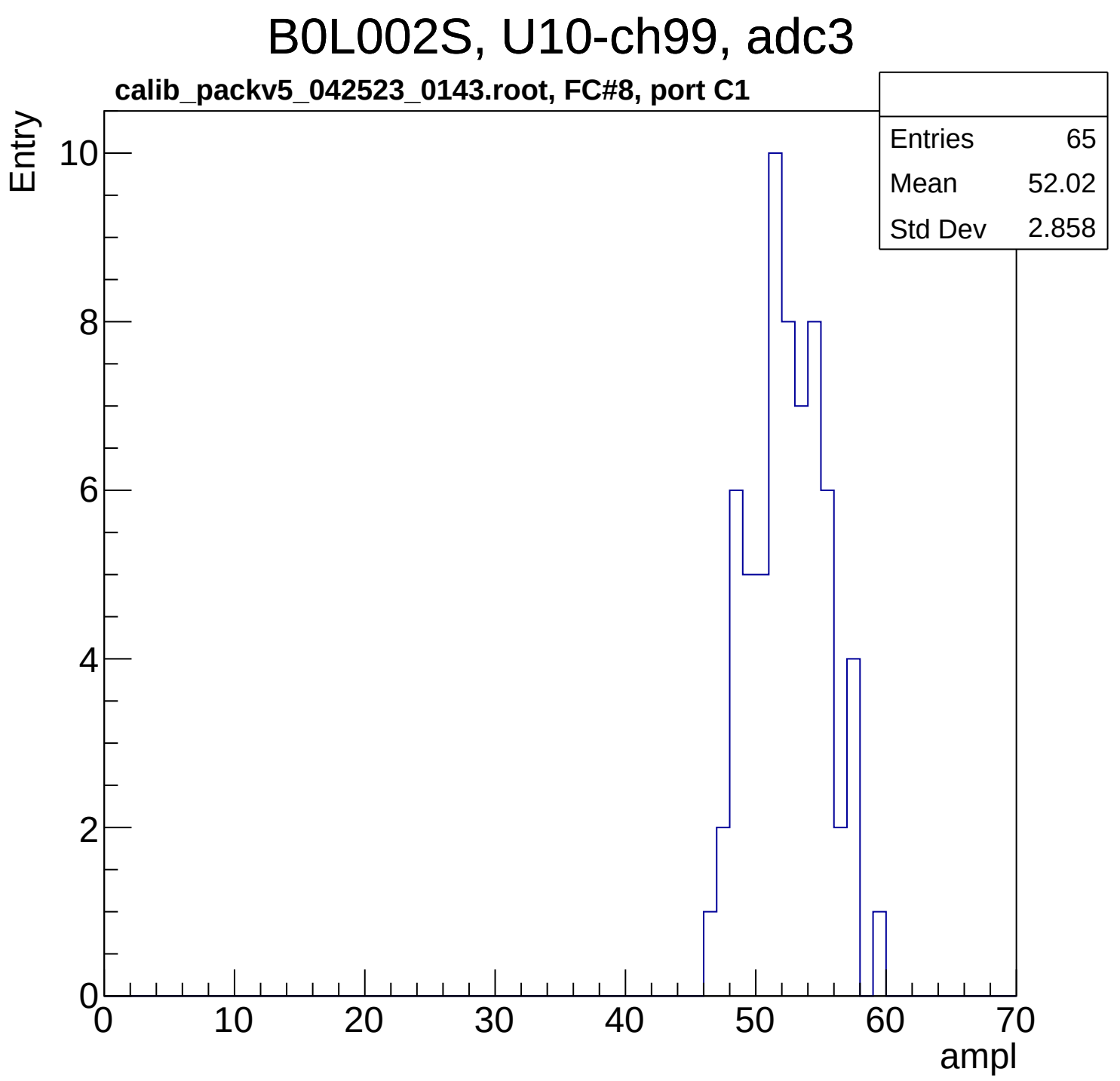

B0L002S, U10-ch98, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

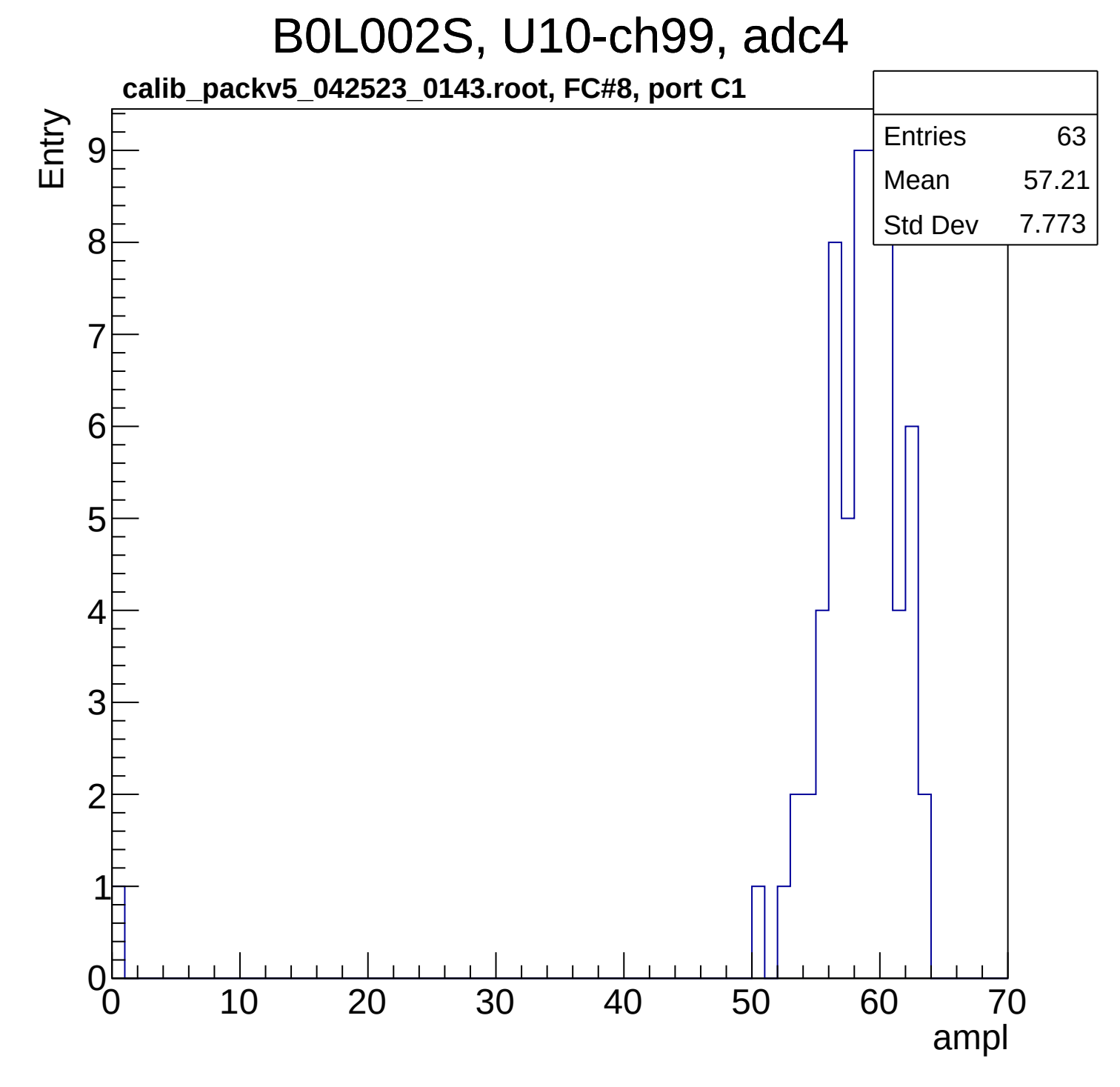

B0L002S, U10-ch98, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

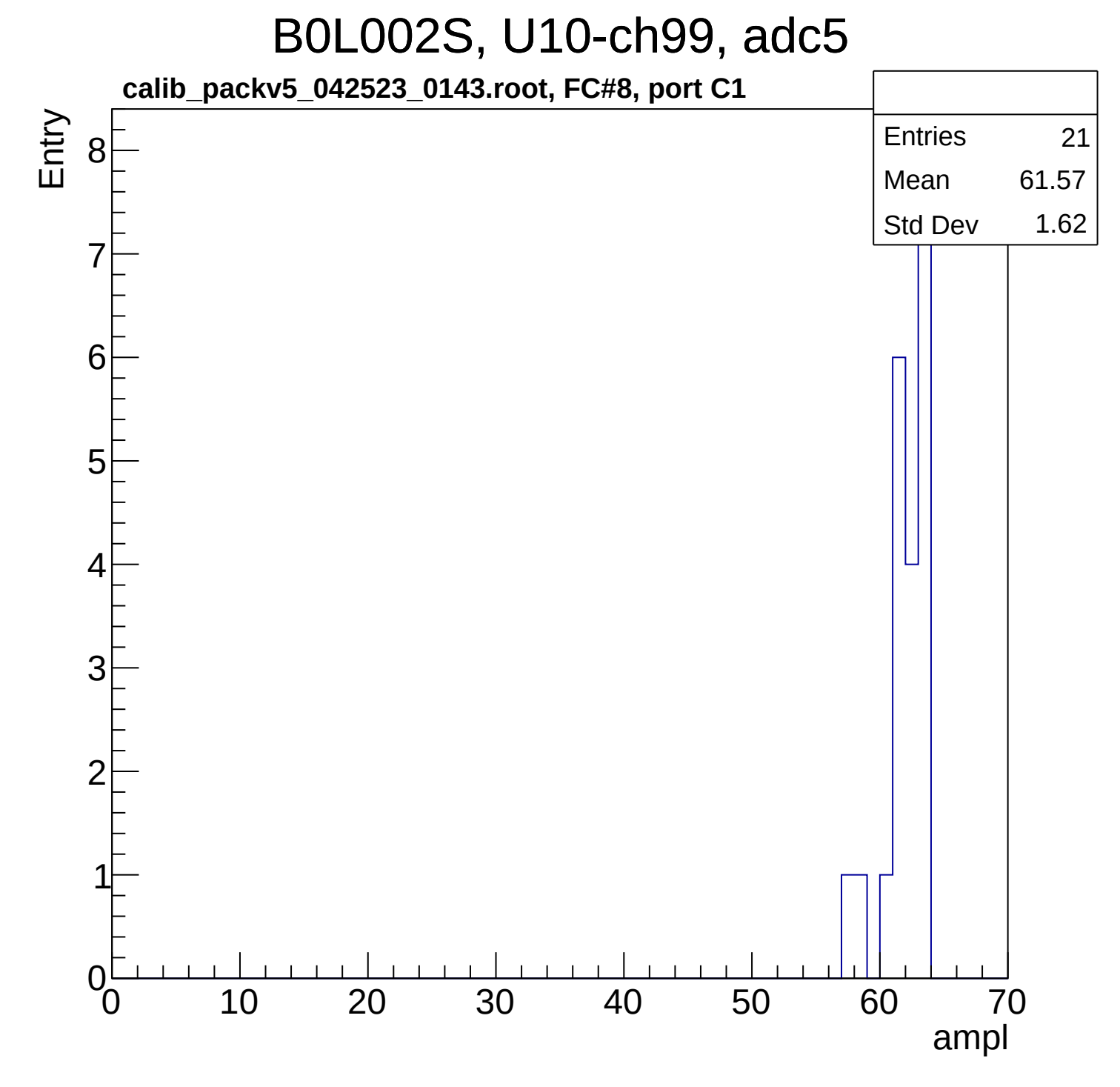

B0L002S, U10-ch99, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 31.46
Std Dev 3.664
Gaus mean : 31.5010
Gaus Width: 3.7211
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

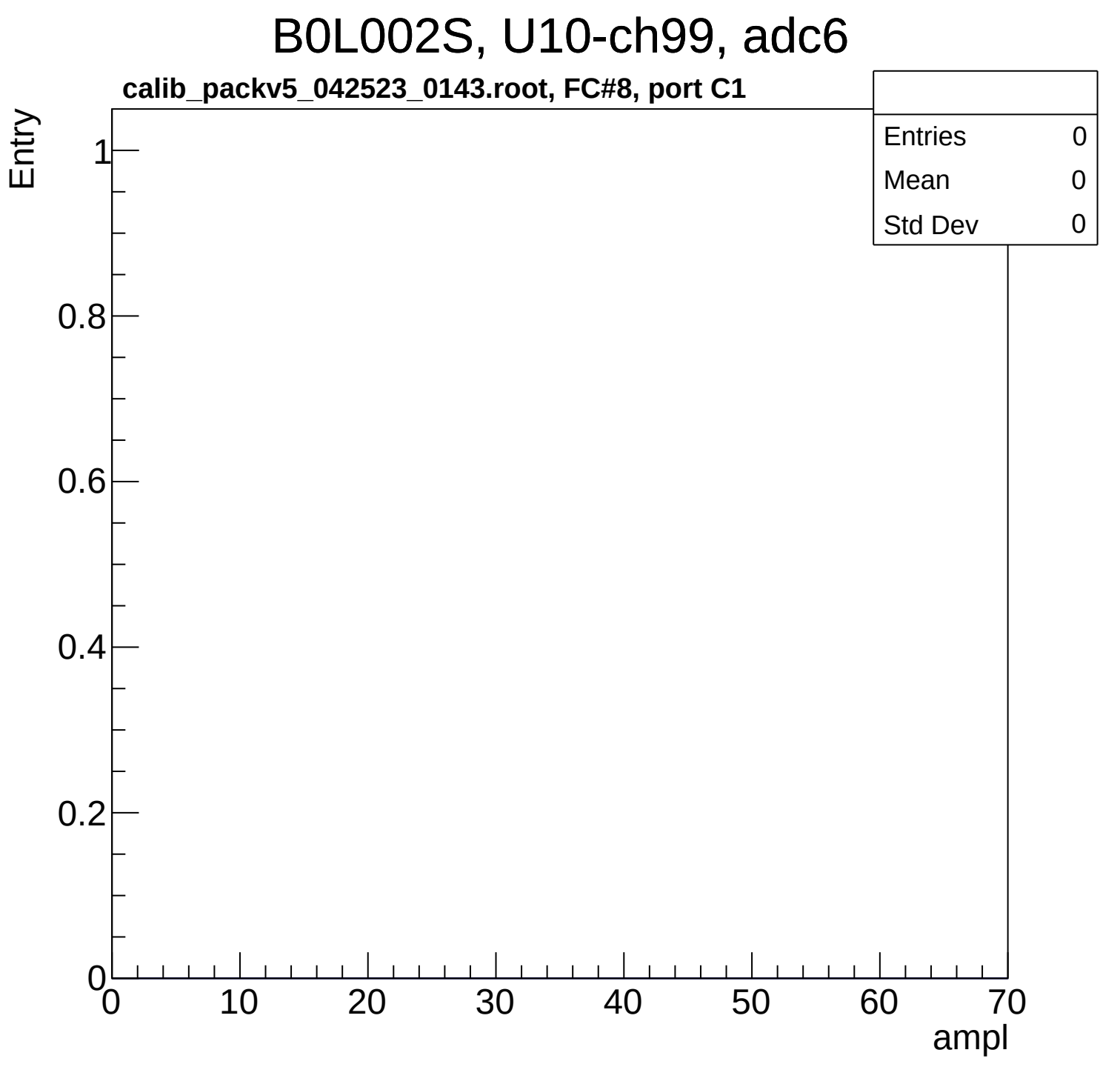

B0L002S, U10-ch99, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 38.67
Std Dev 3.348
Gaus mean : 39.1302
Gaus Width: 3.7214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

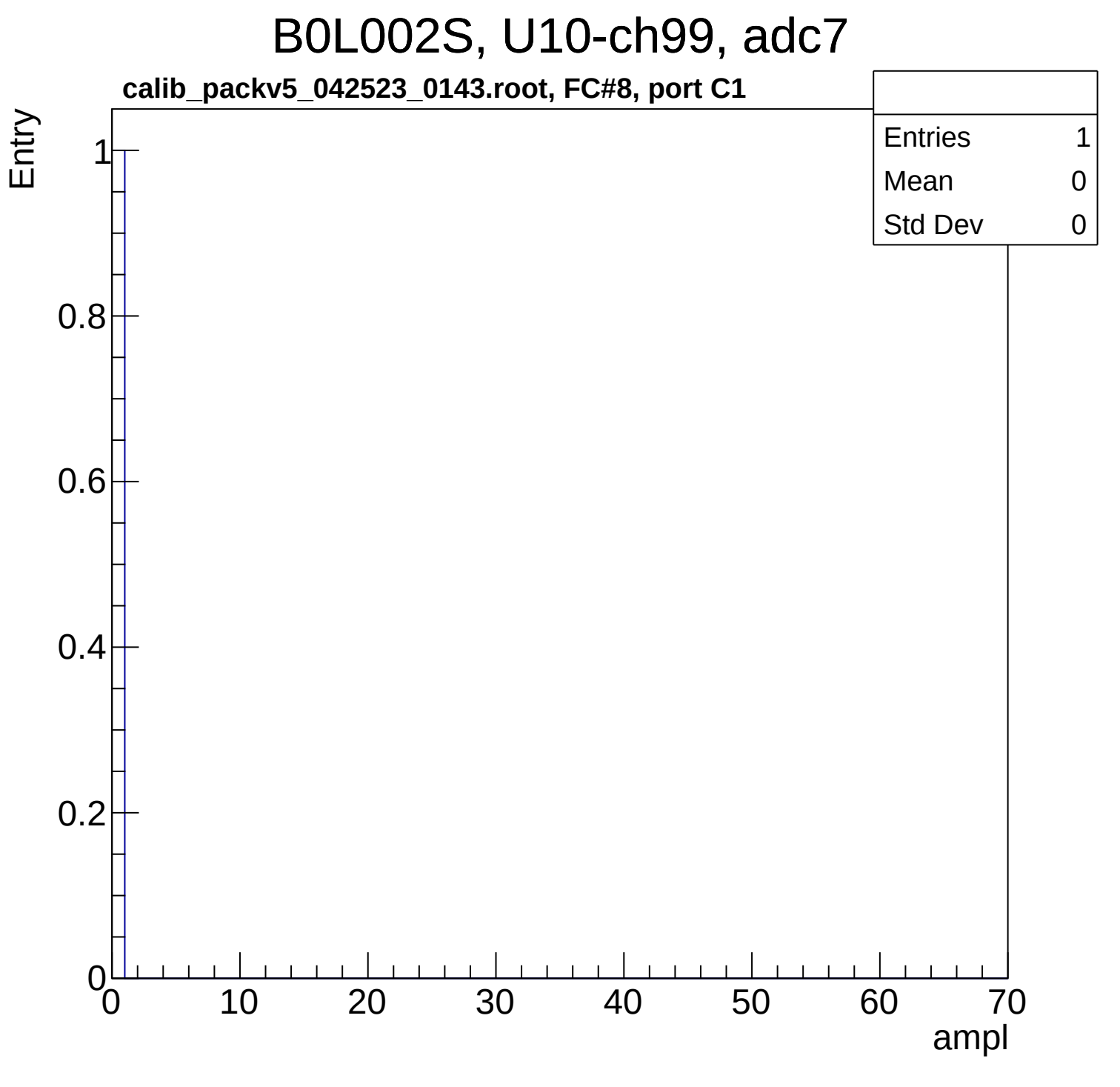

B0L002S, U10-ch99, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 45.43
Std Dev 3.012
Gaus mean : 45.9785
Gaus Width: 3.4906
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

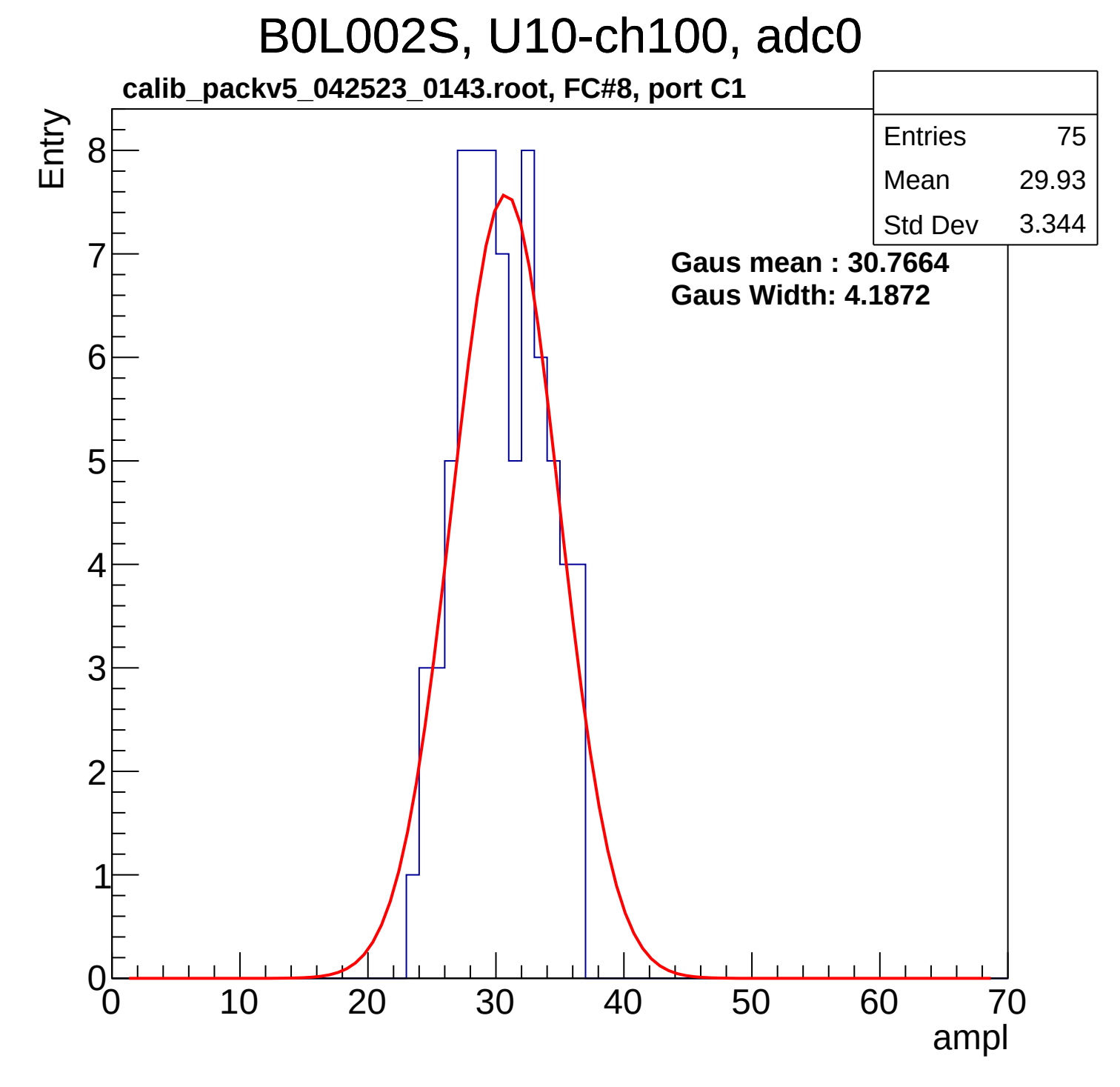

B0L002S, U10-ch99, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 52.02
Std Dev 2.858
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

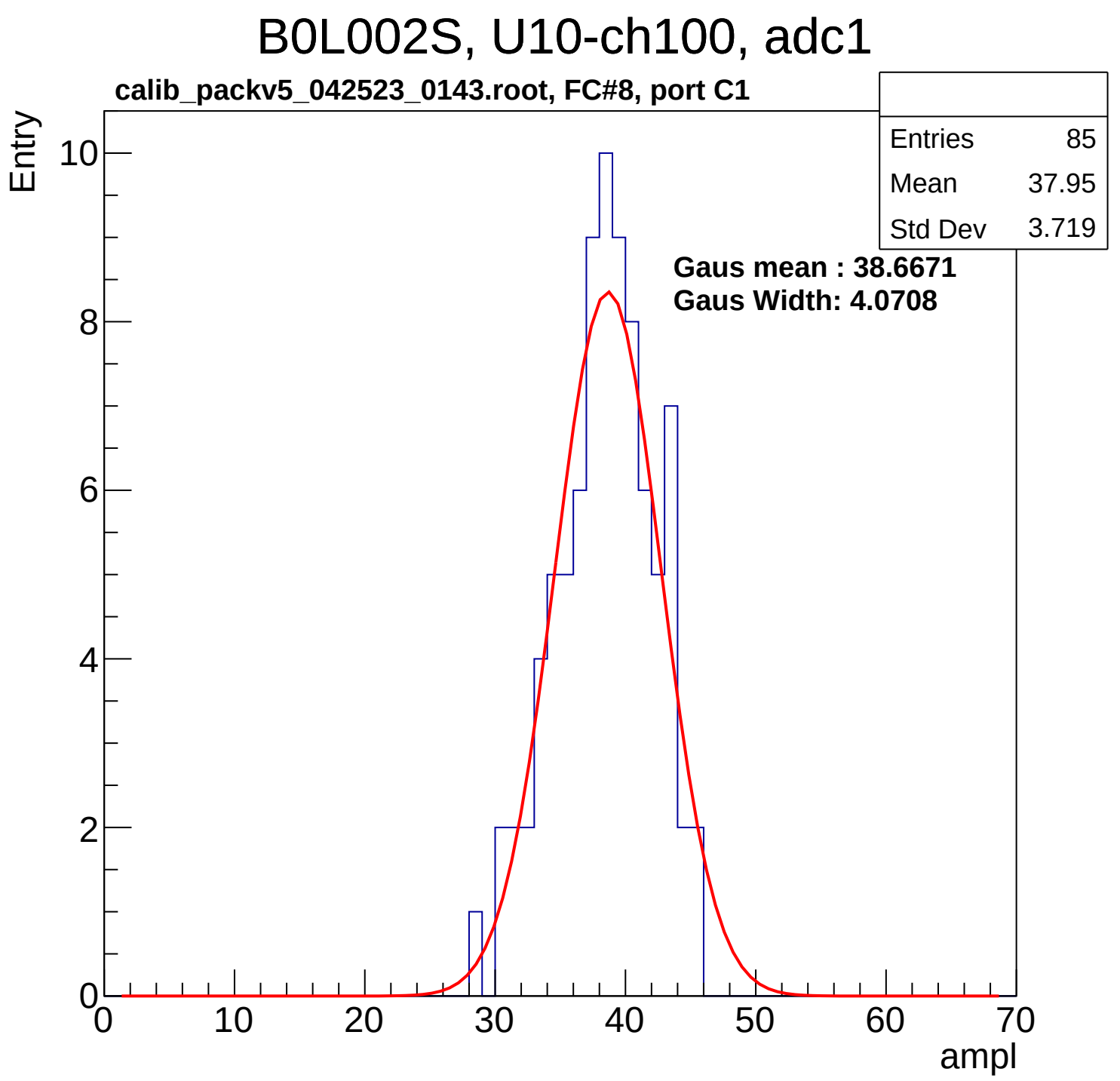

B0L002S, U10-ch99, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.21
Std Dev 7.773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

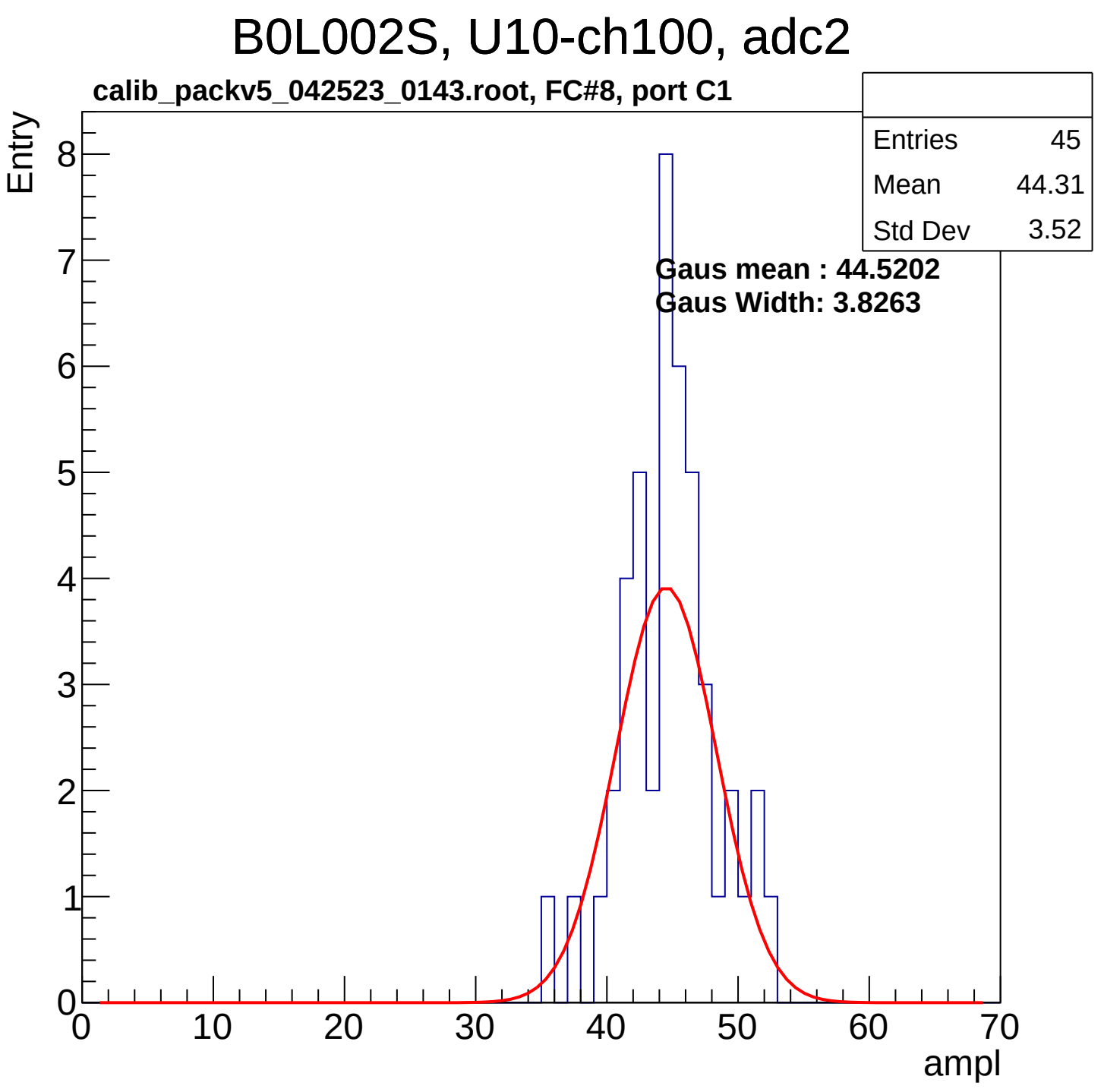

B0L002S, U10-ch99, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 61.57
Std Dev 1.62
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

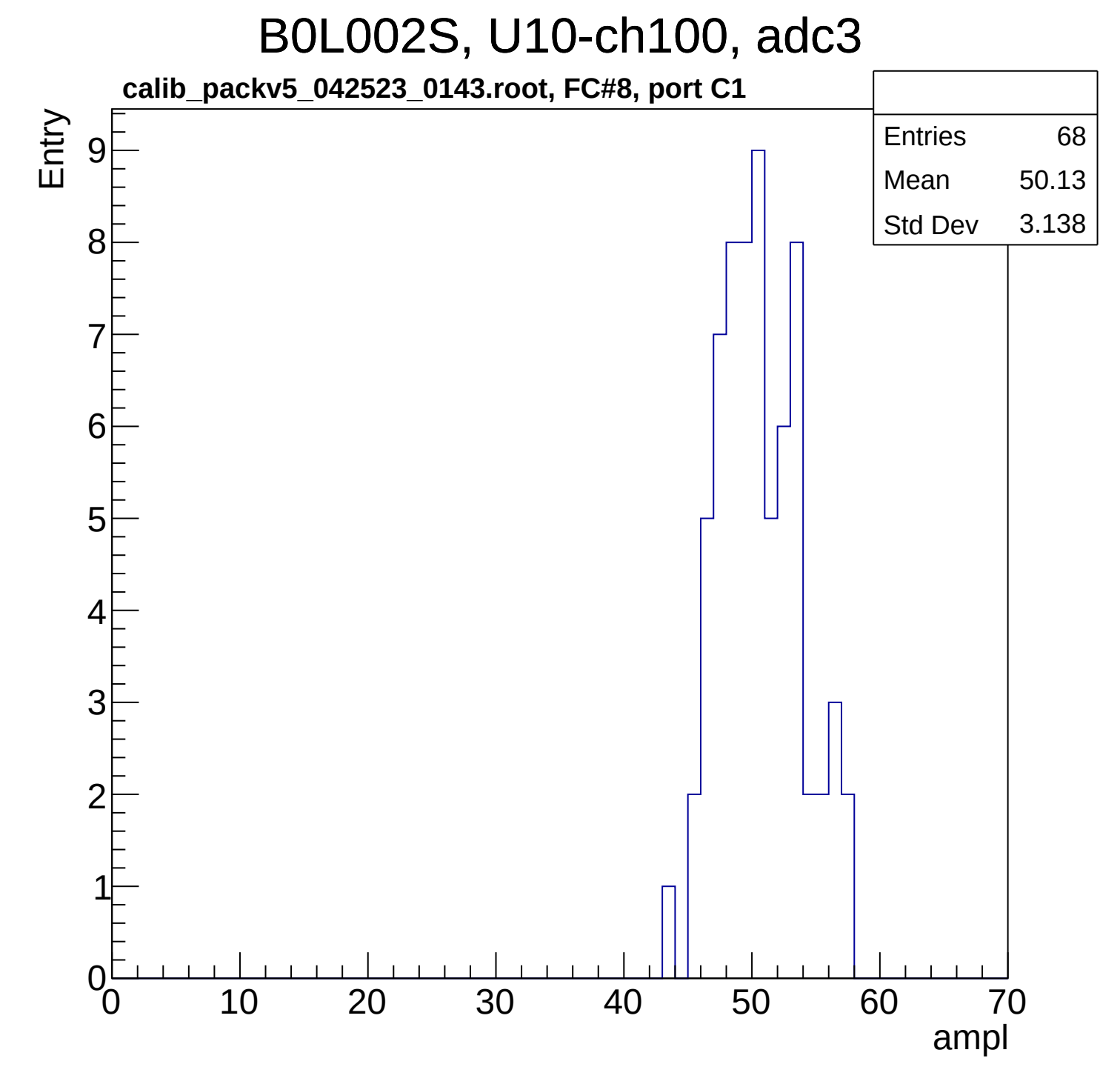

B0L002S, U10-ch99, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

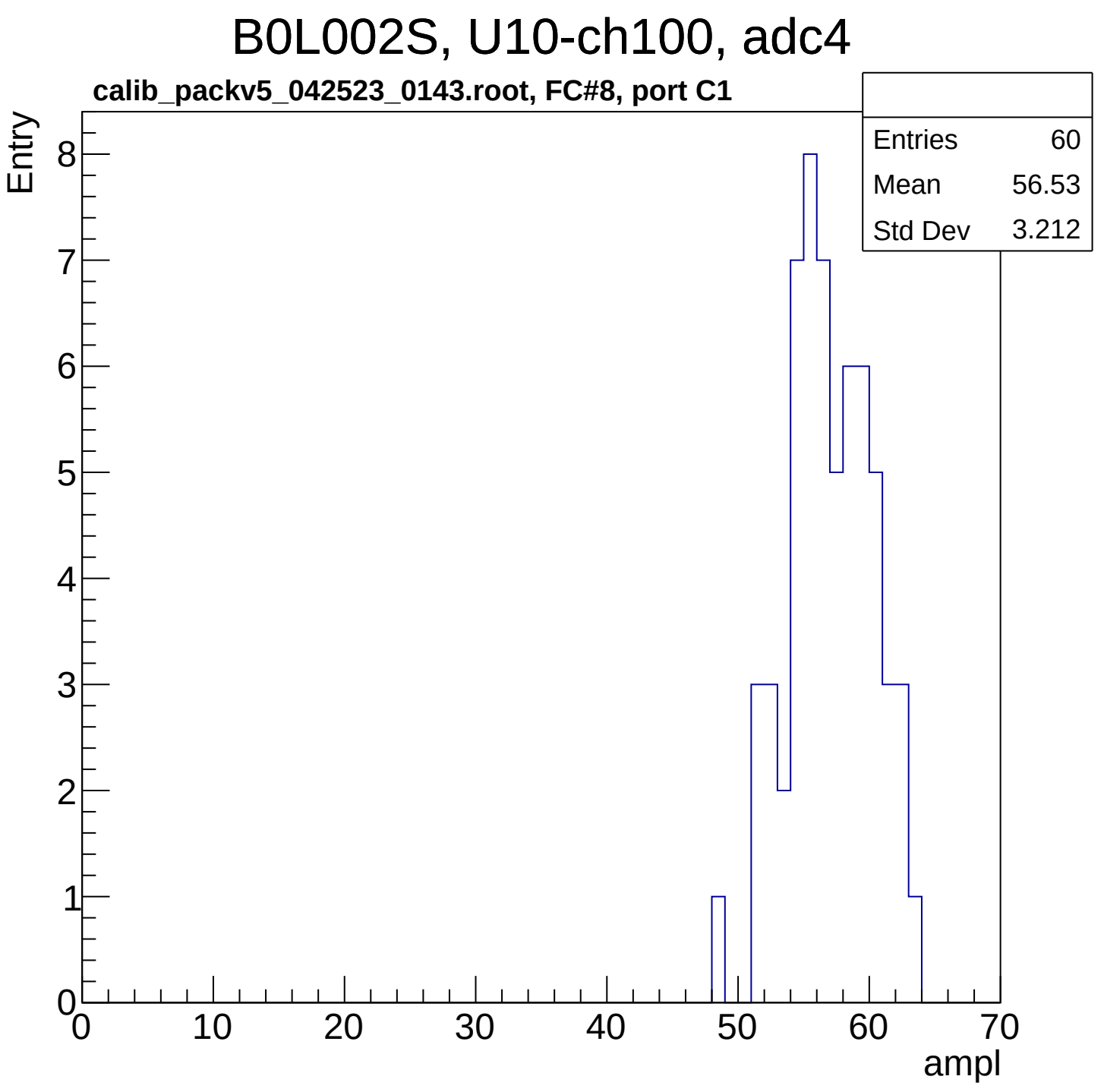

B0L002S, U10-ch99, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

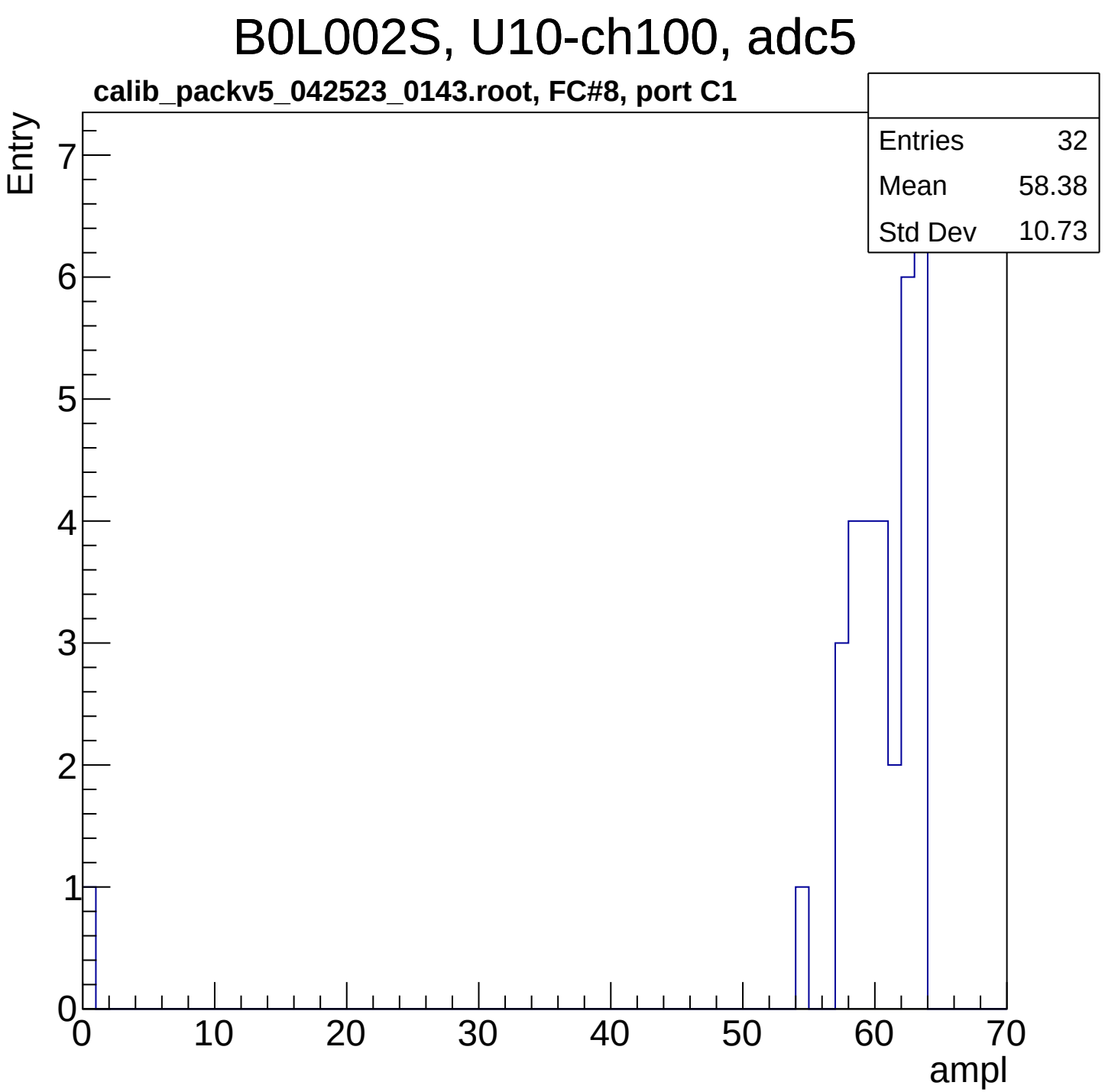

B0L002S, U10-ch100, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 29.93
Std Dev 3.344
Gaus mean : 30.7664
Gaus Width: 4.1872
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

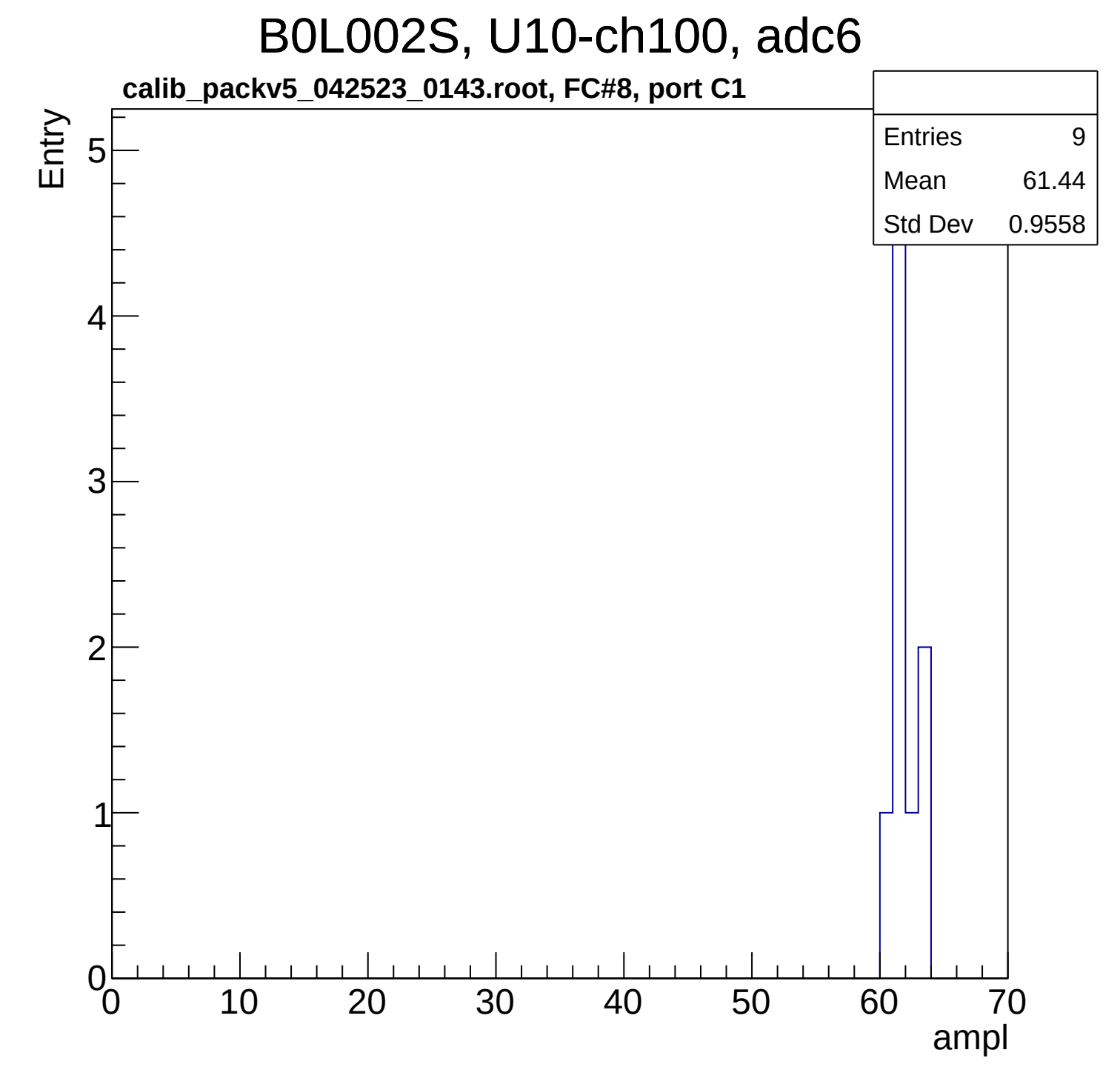

B0L002S, U10-ch100, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 37.95
Std Dev 3.719
Gaus mean : 38.6671
Gaus Width: 4.0708
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

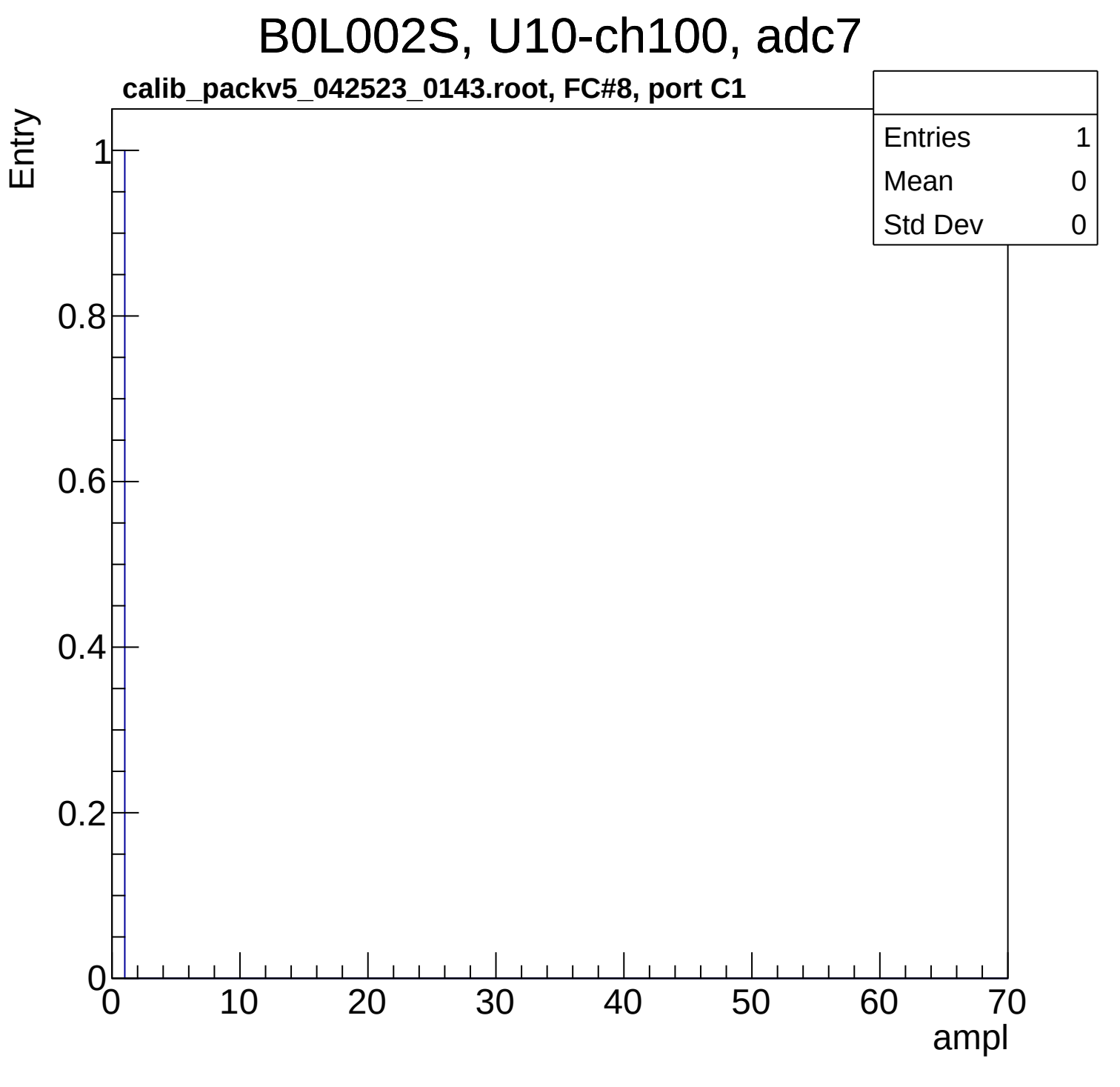

B0L002S, U10-ch100, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 45
Mean 44.31
Std Dev 3.52
Gaus mean : 44.5202
Gaus Width: 3.8263
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

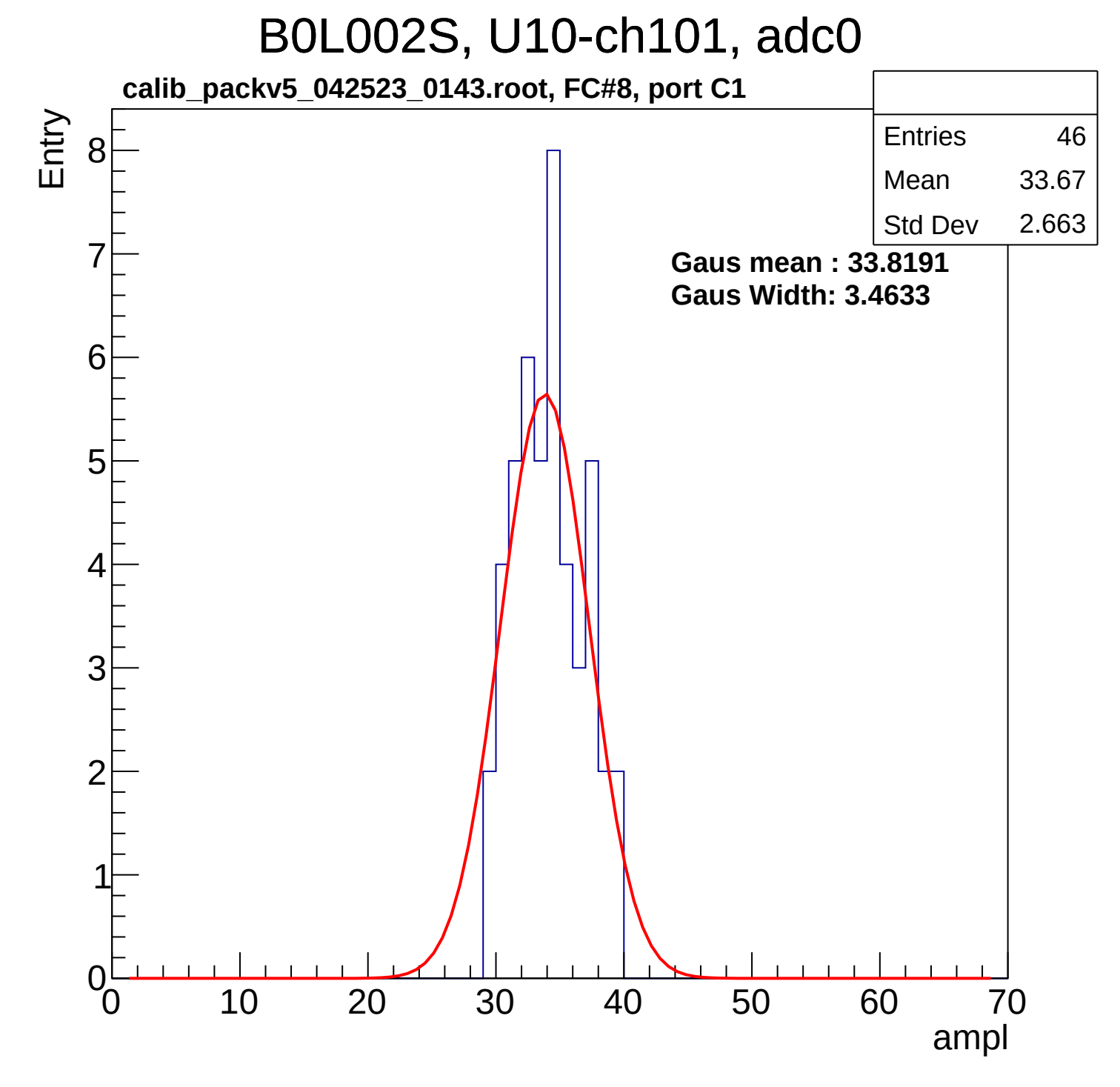

B0L002S, U10-ch100, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.13
Std Dev 3.138
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

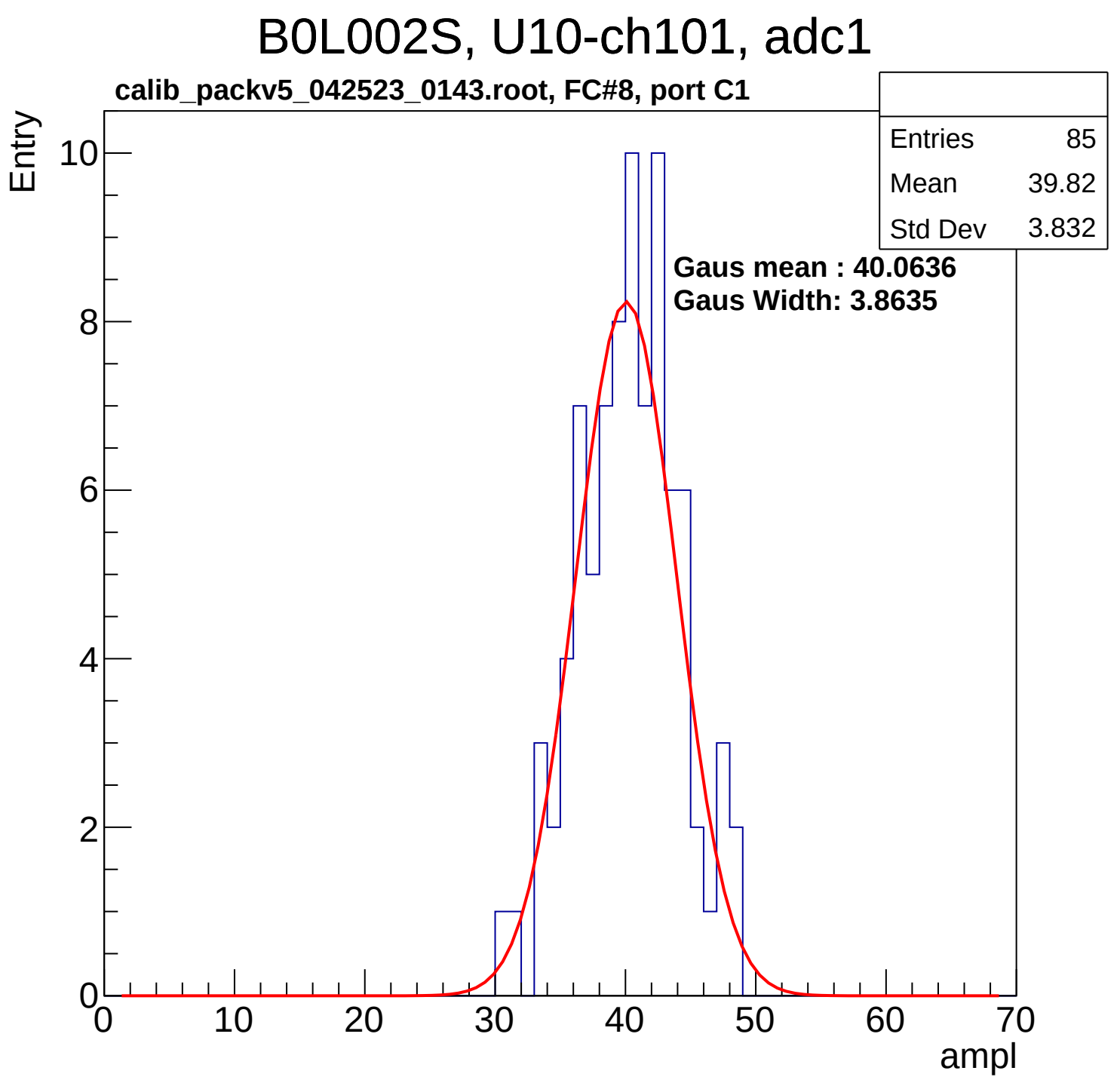

B0L002S, U10-ch100, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 56.53
Std Dev 3.212
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

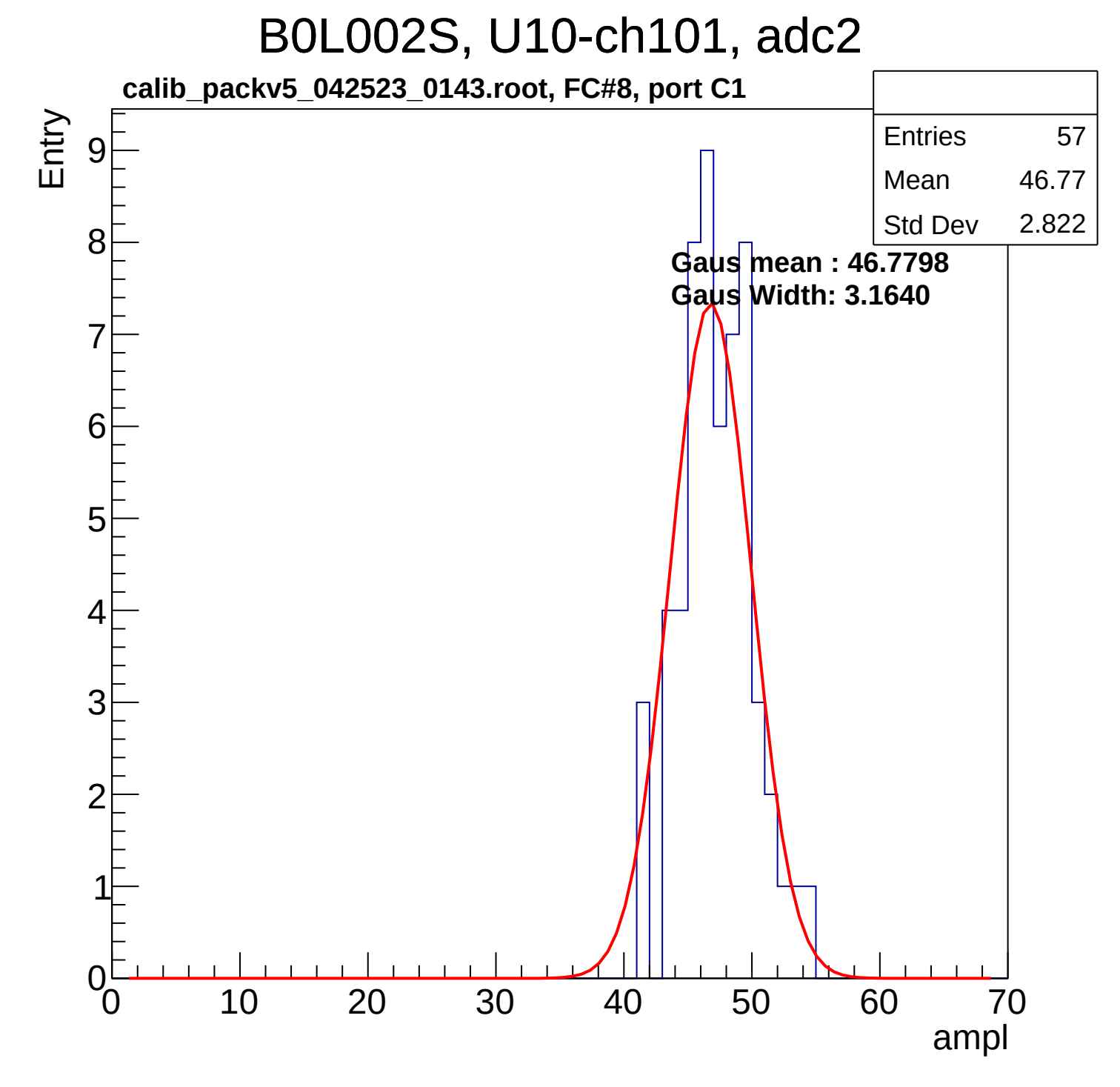

B0L002S, U10-ch100, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 32
Mean 58.38
Std Dev 10.73
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

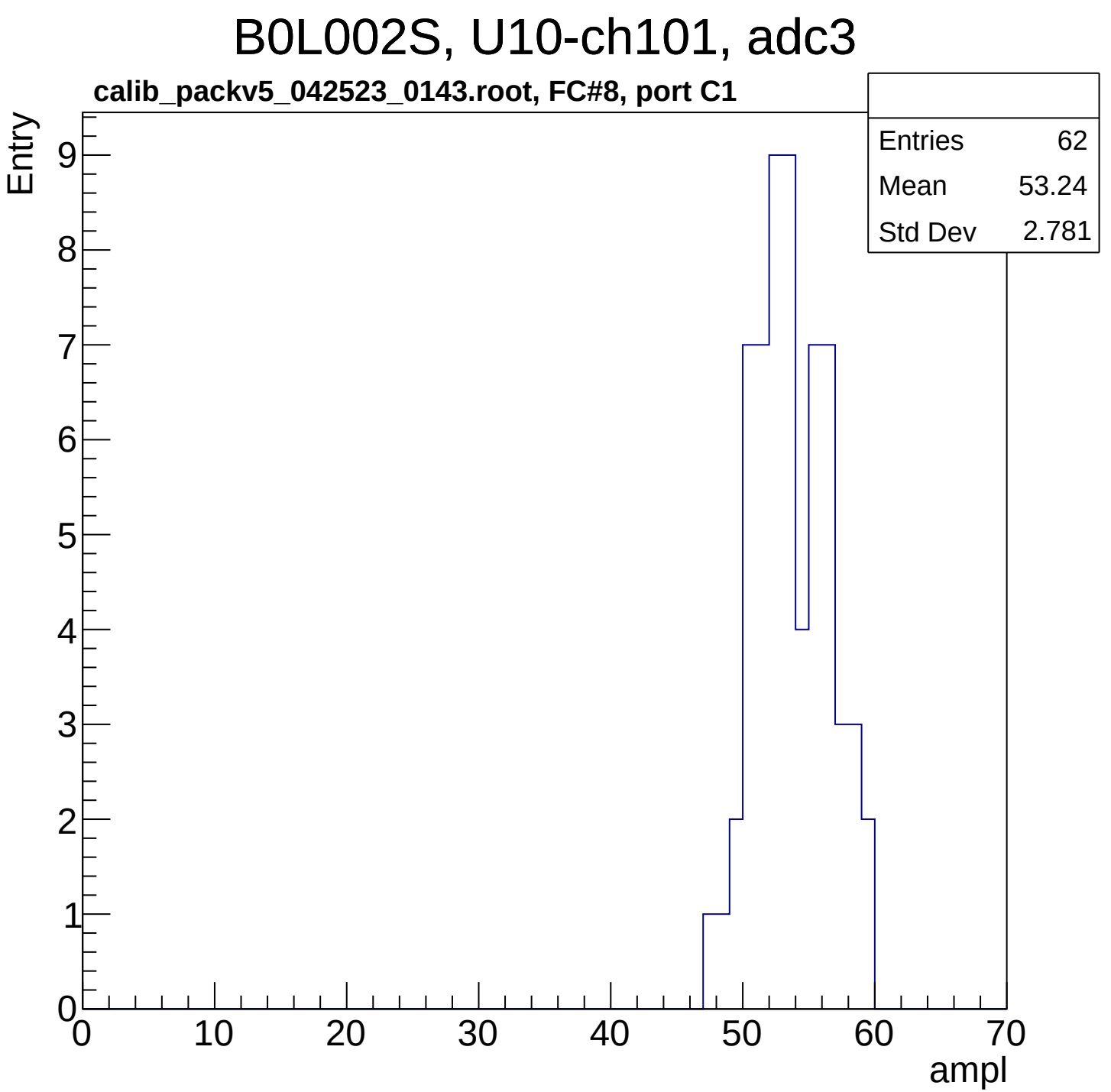

B0L002S, U10-ch100, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 9
Mean 61.44
Std Dev 0.9558
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

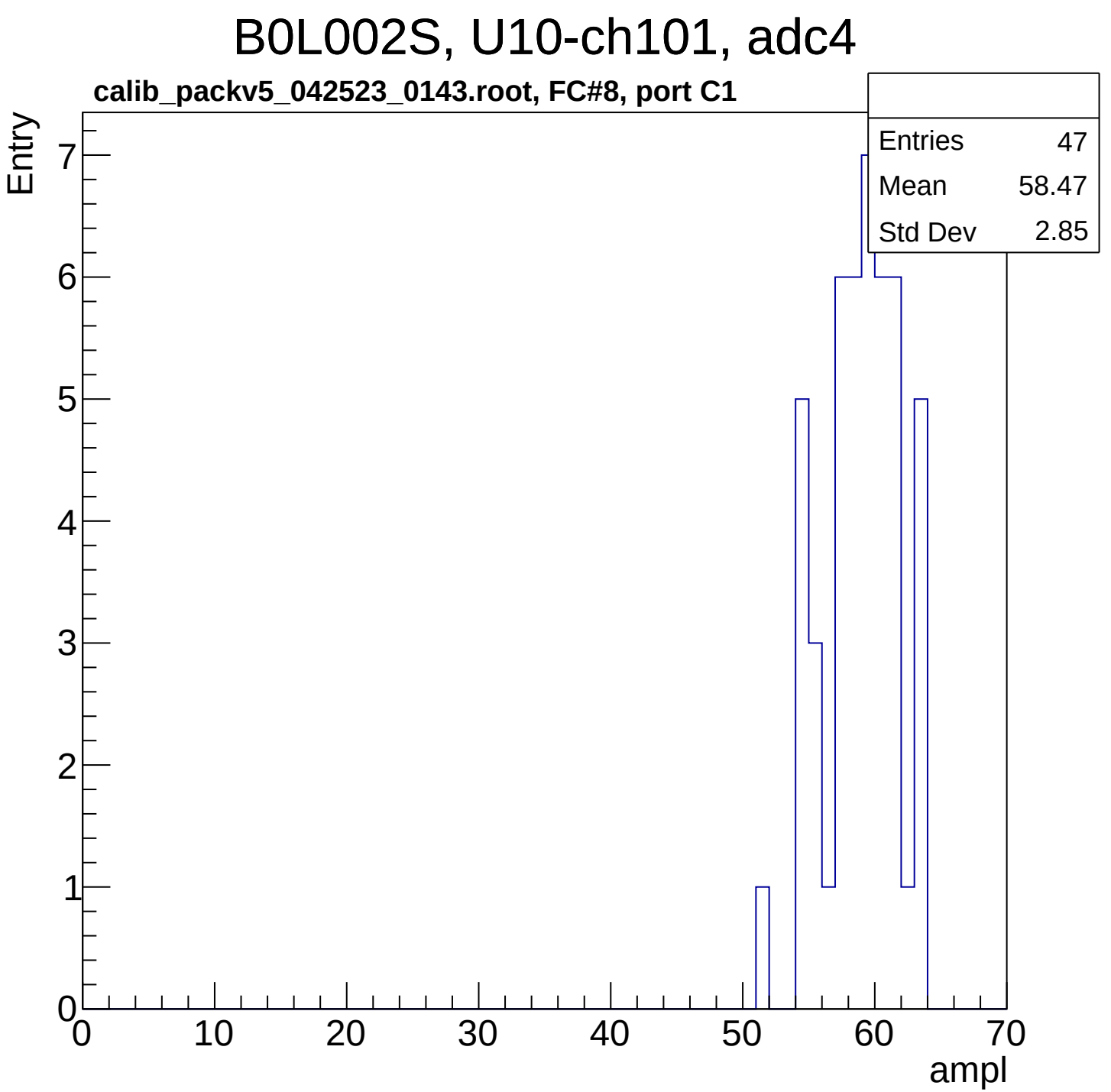

B0L002S, U10-ch100, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

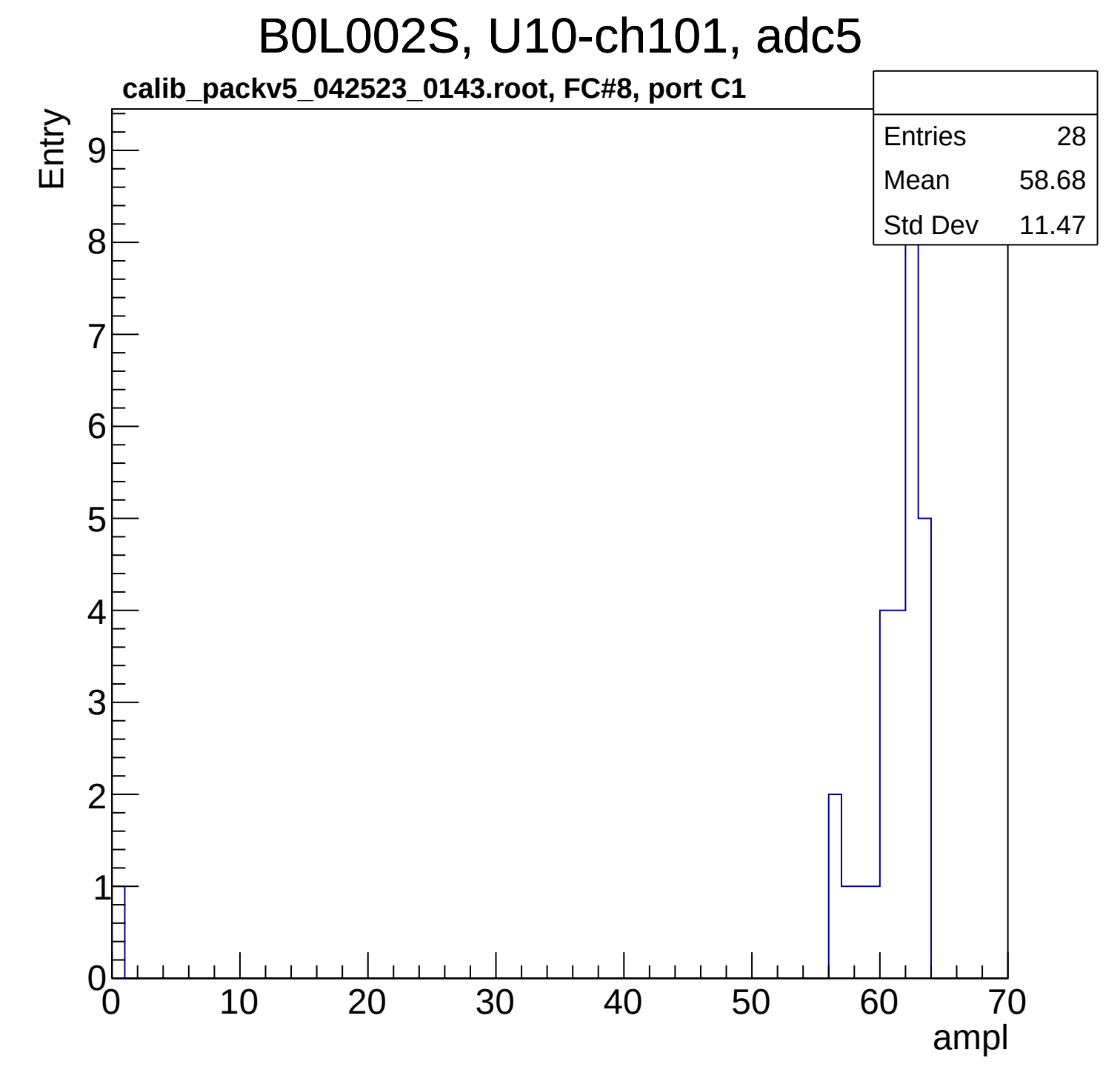

B0L002S, U10-ch101, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 33.67
Std Dev 2.663
Gaus mean : 33.8191
Gaus Width: 3.4633
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

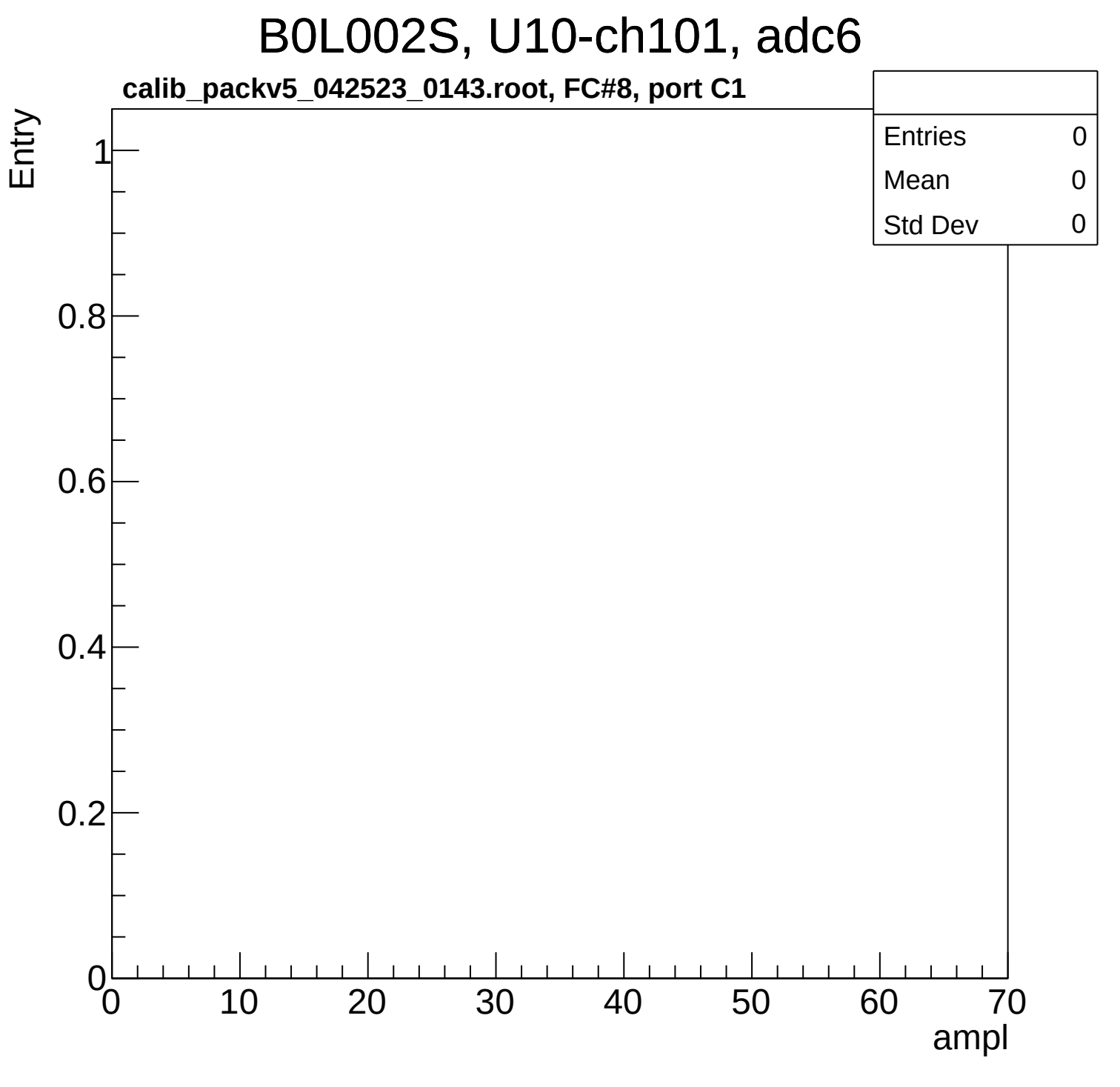

B0L002S, U10-ch101, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 39.82
Std Dev 3.832
Gaus mean : 40.0636
Gaus Width: 3.8635
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

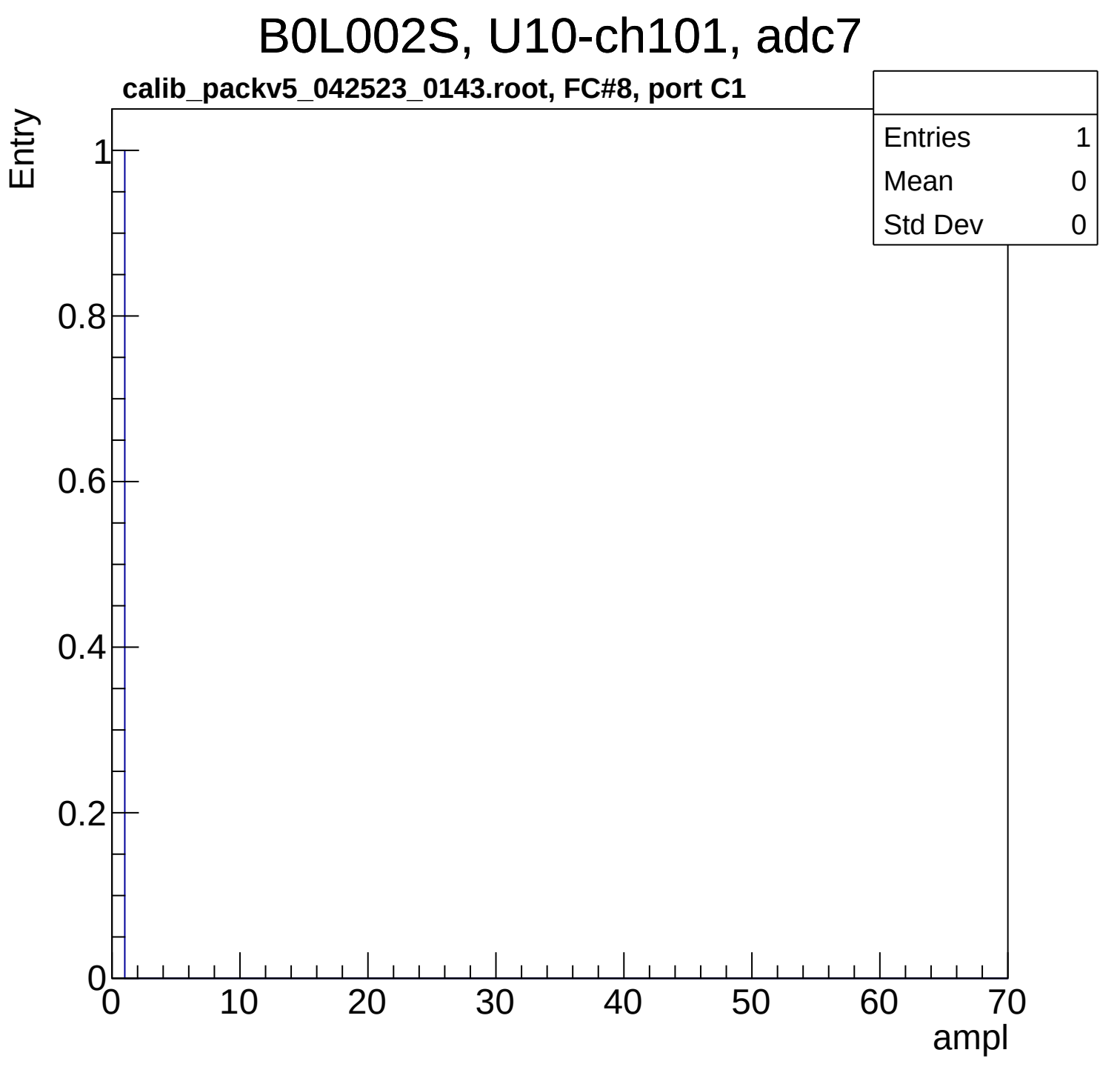

B0L002S, U10-ch101, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 46.77
Std Dev 2.822
Gaus mean : 46.7798
Gaus Width: 3.1640
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

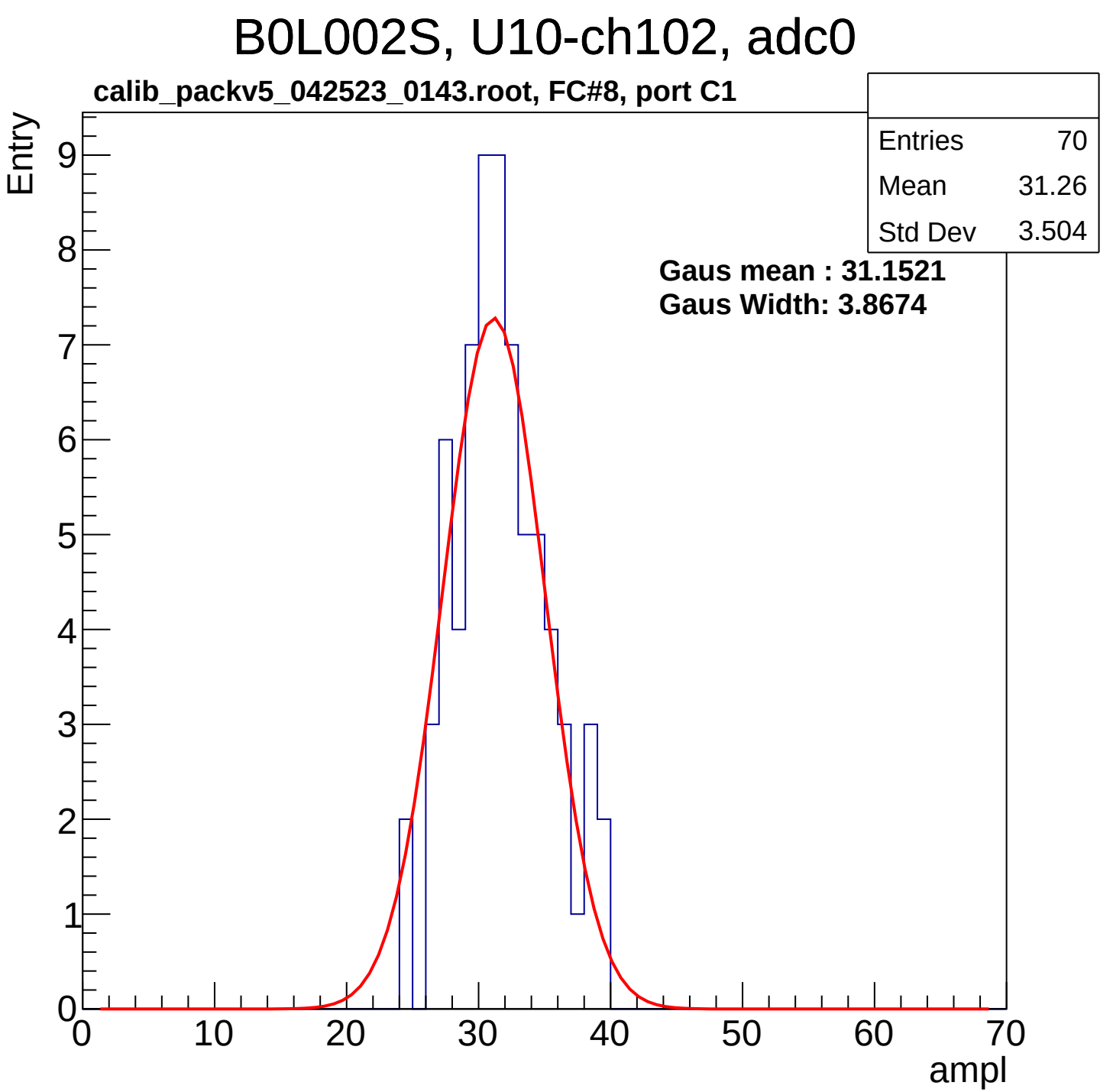

B0L002S, U10-ch101, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 53.24
Std Dev 2.781
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

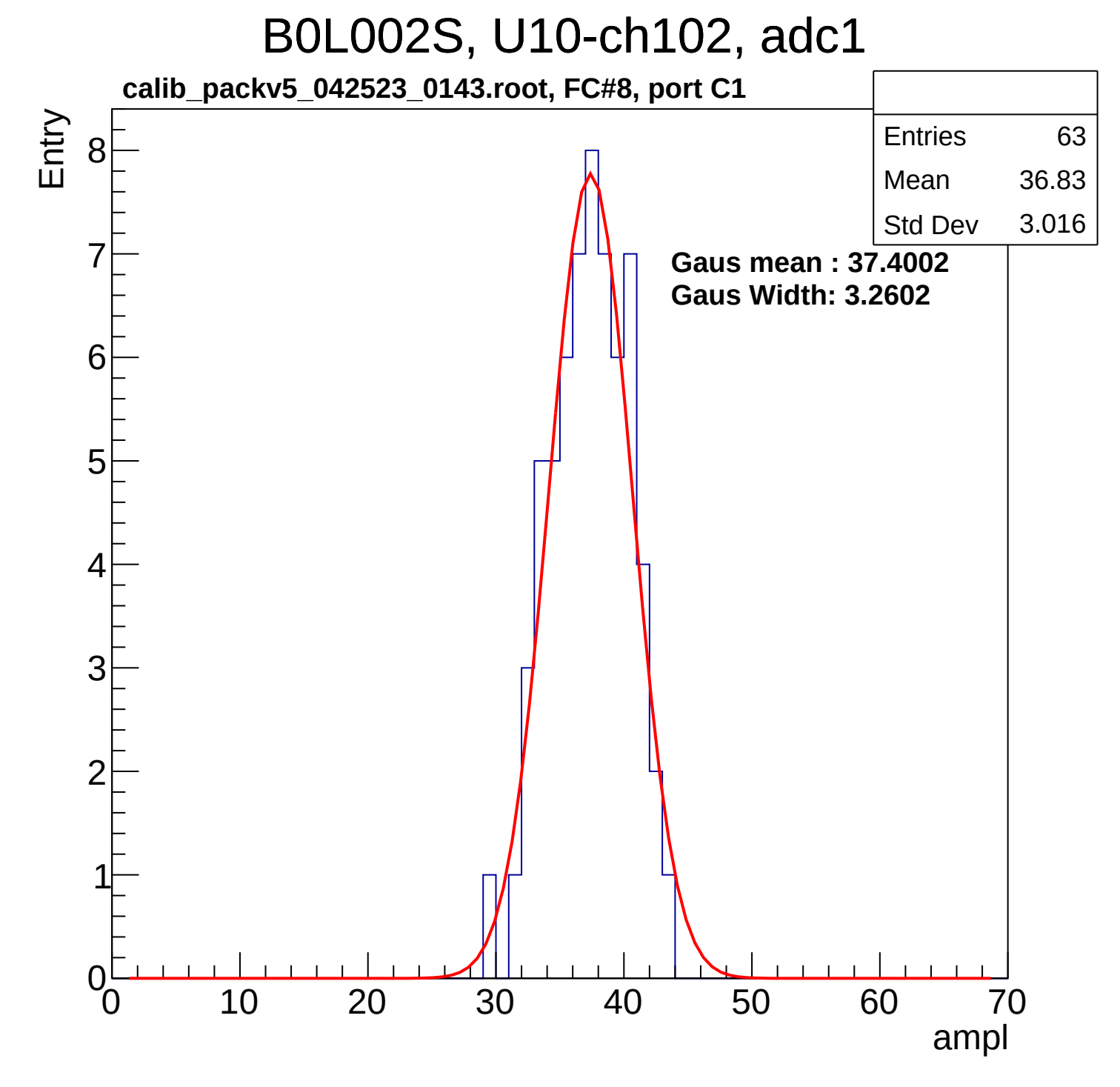

B0L002S, U10-ch101, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 47
Mean 58.47
Std Dev 2.85
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

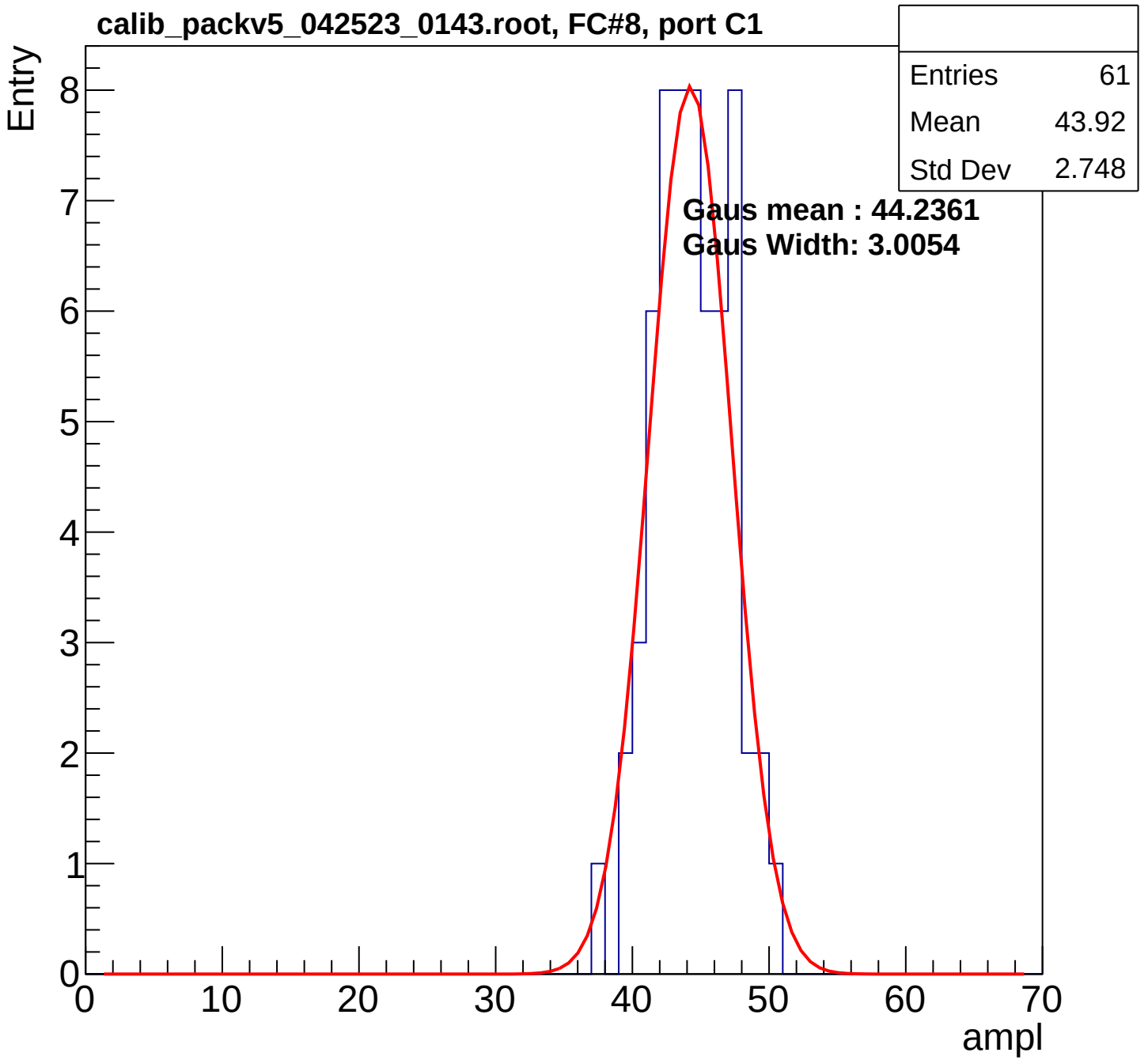

B0L002S, U10-ch101, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 58.68
Std Dev 11.47
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

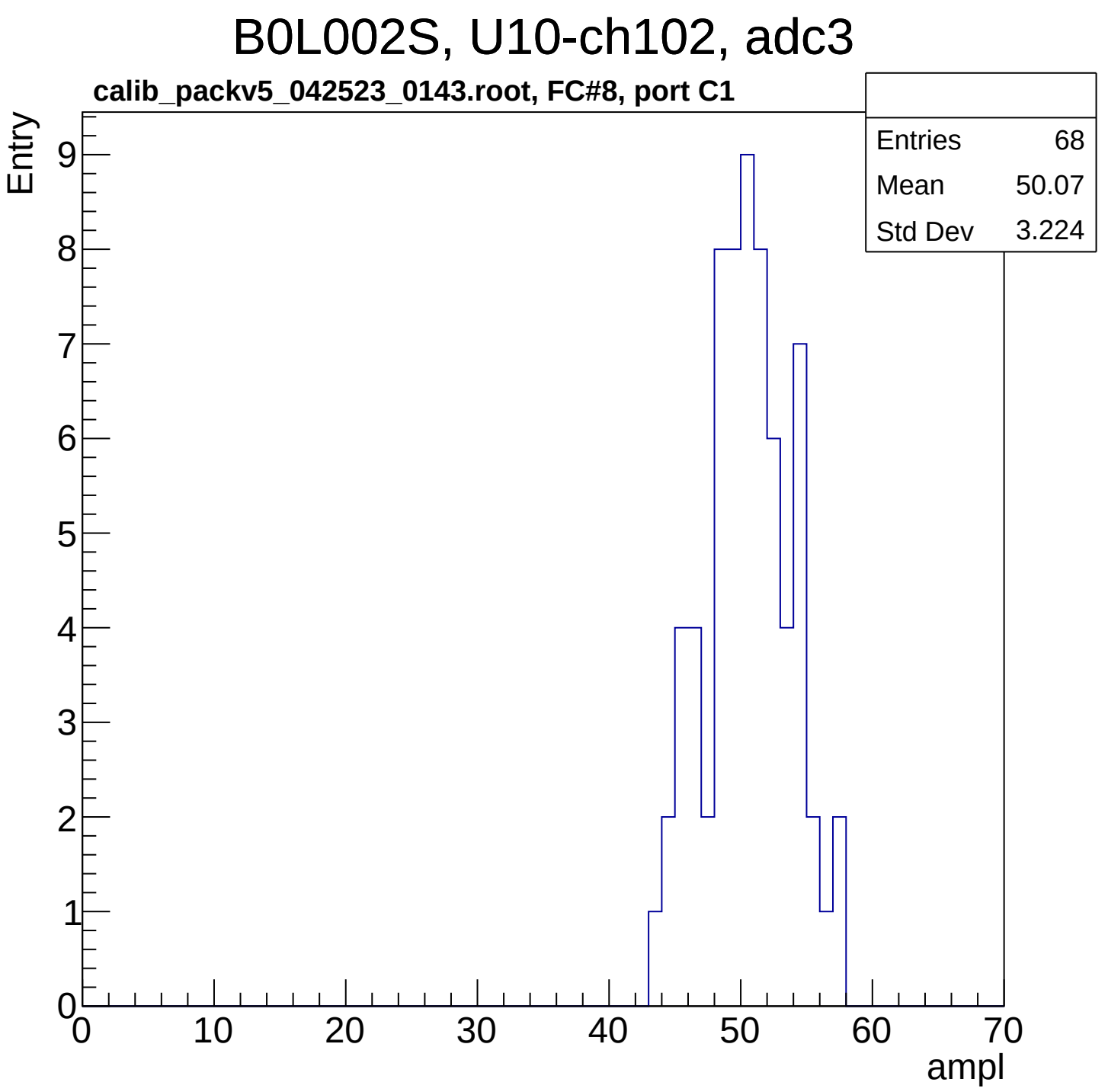

B0L002S, U10-ch101, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

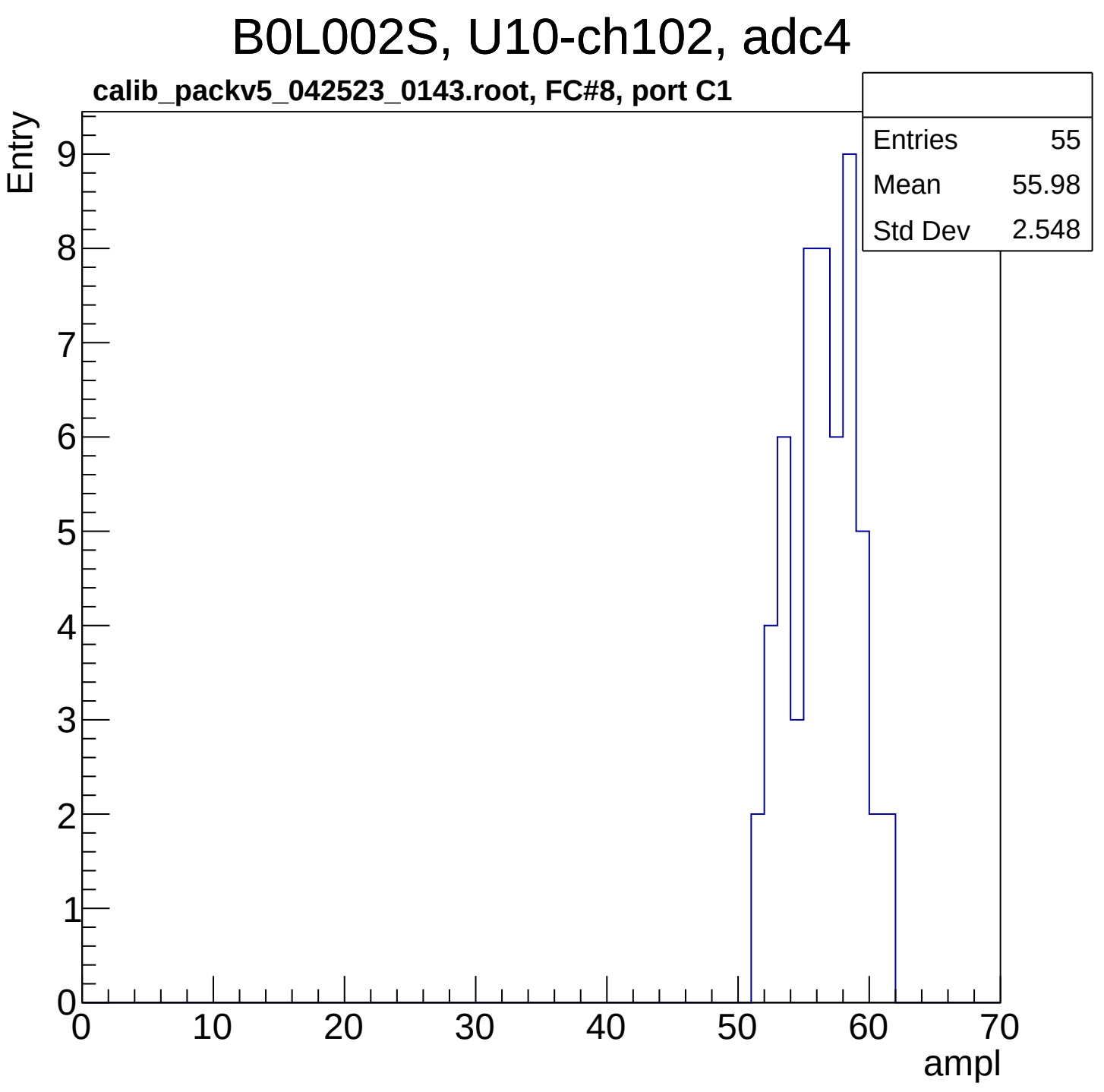

B0L002S, U10-ch101, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

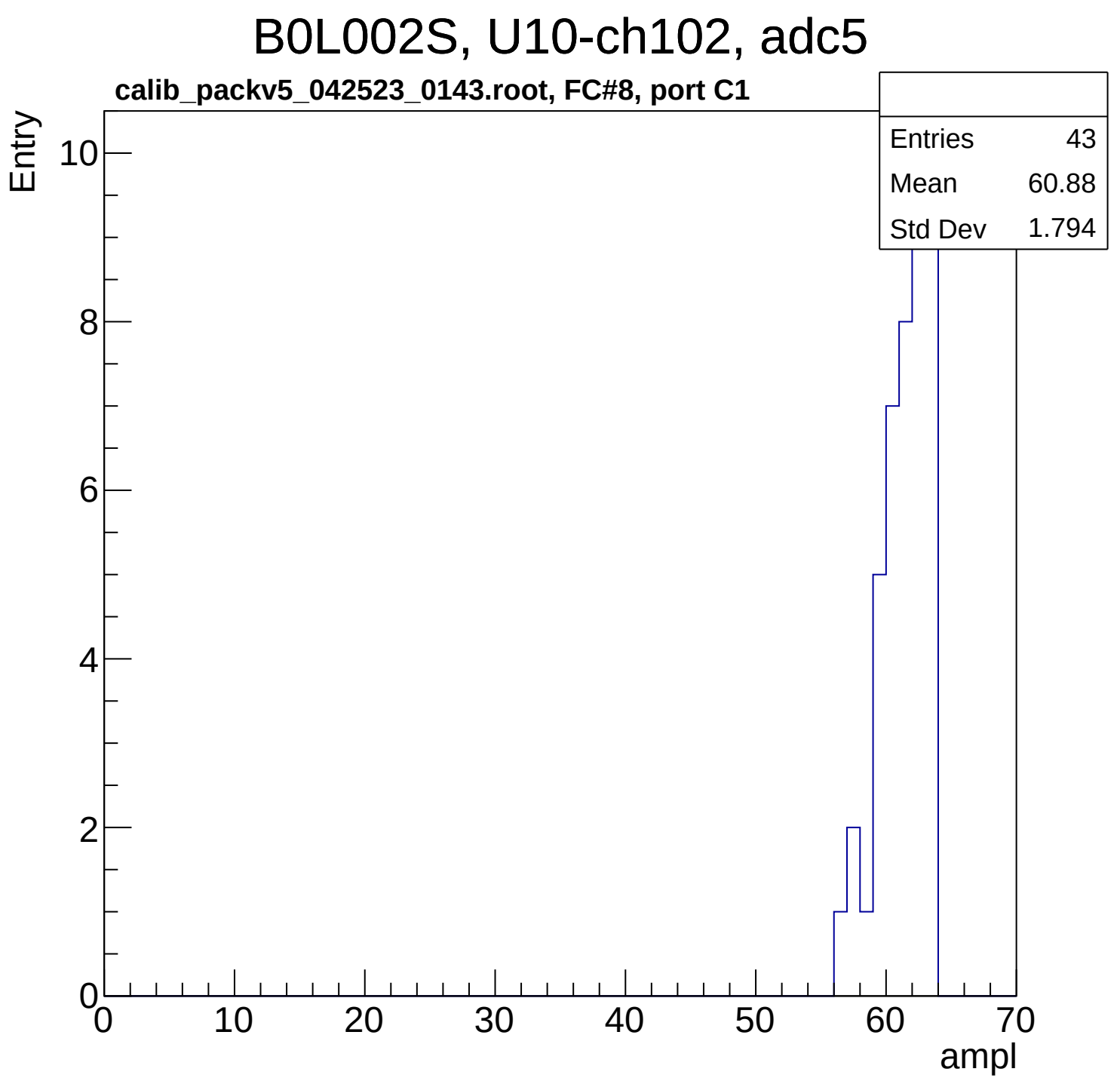

B0L002S, U10-ch102, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 31.26
Std Dev 3.504
Gaus mean : 31.1521
Gaus Width: 3.8674
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

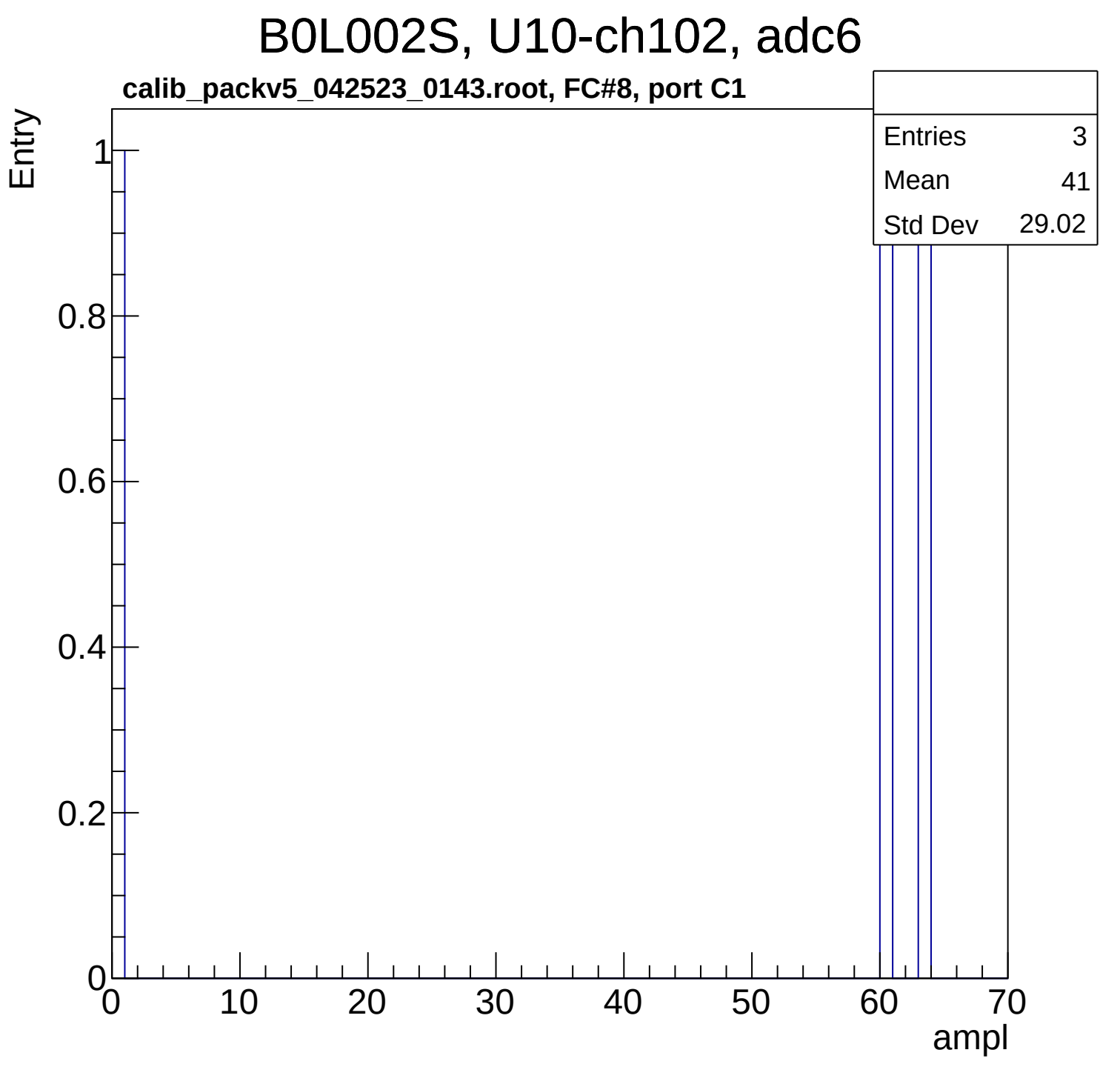

B0L002S, U10-ch102, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 36.83
Std Dev 3.016
Gaus mean : 37.4002
Gaus Width: 3.2602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

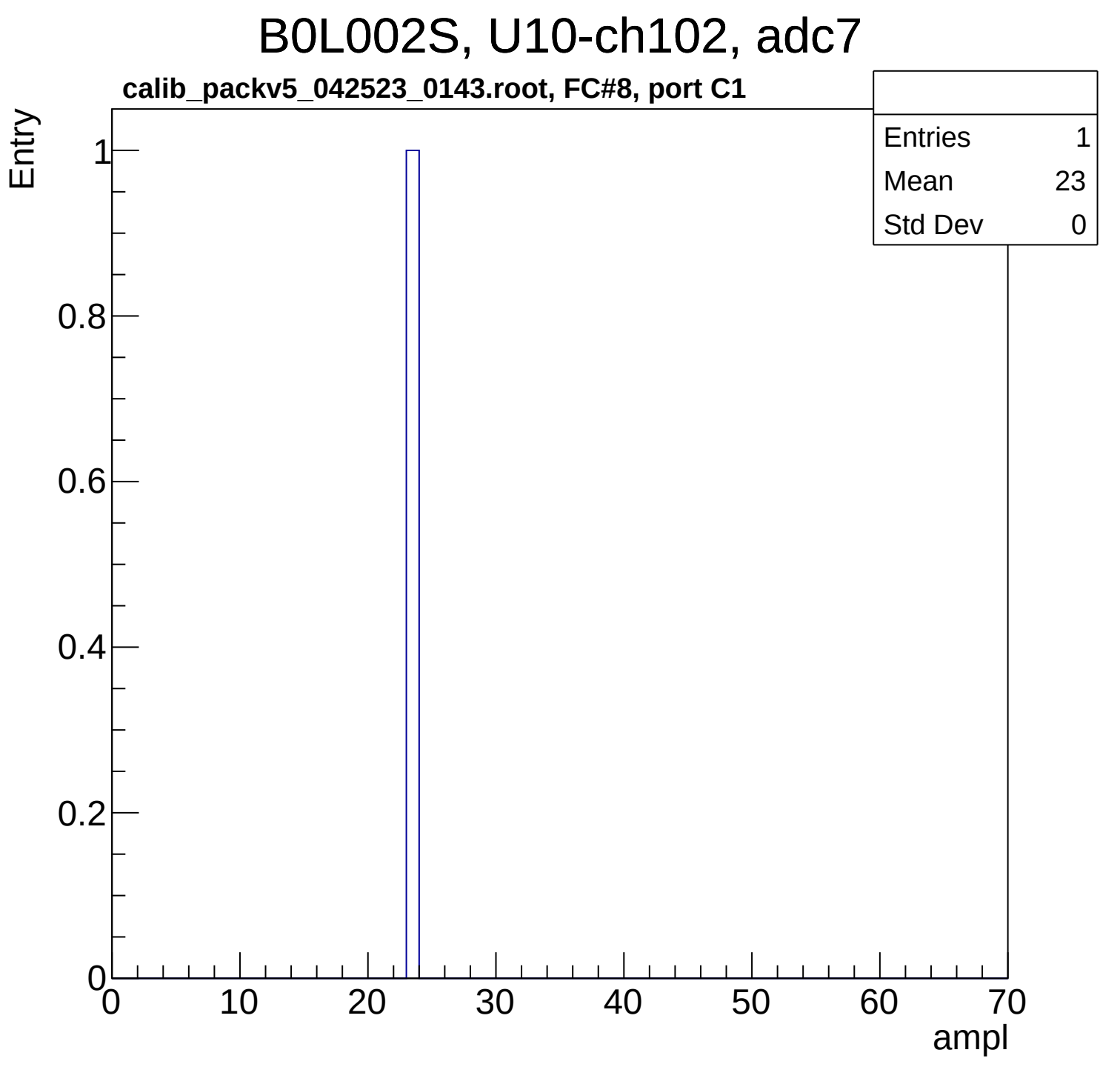

B0L002S, U10-ch102, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 43.92
Std Dev 2.748
Gaus mean : 44.2361
Gaus Width: 3.0054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

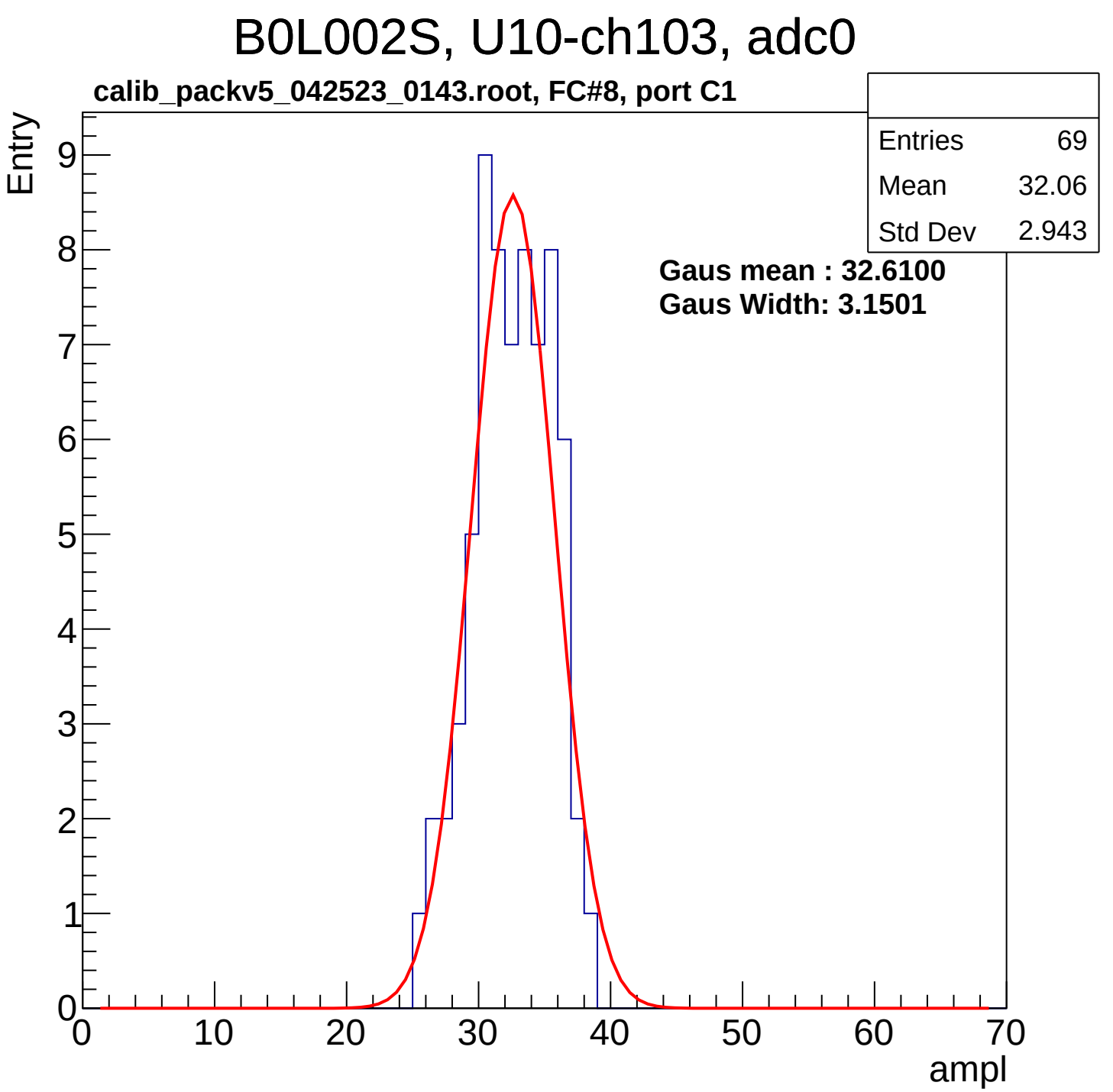

B0L002S, U10-ch102, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.07
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

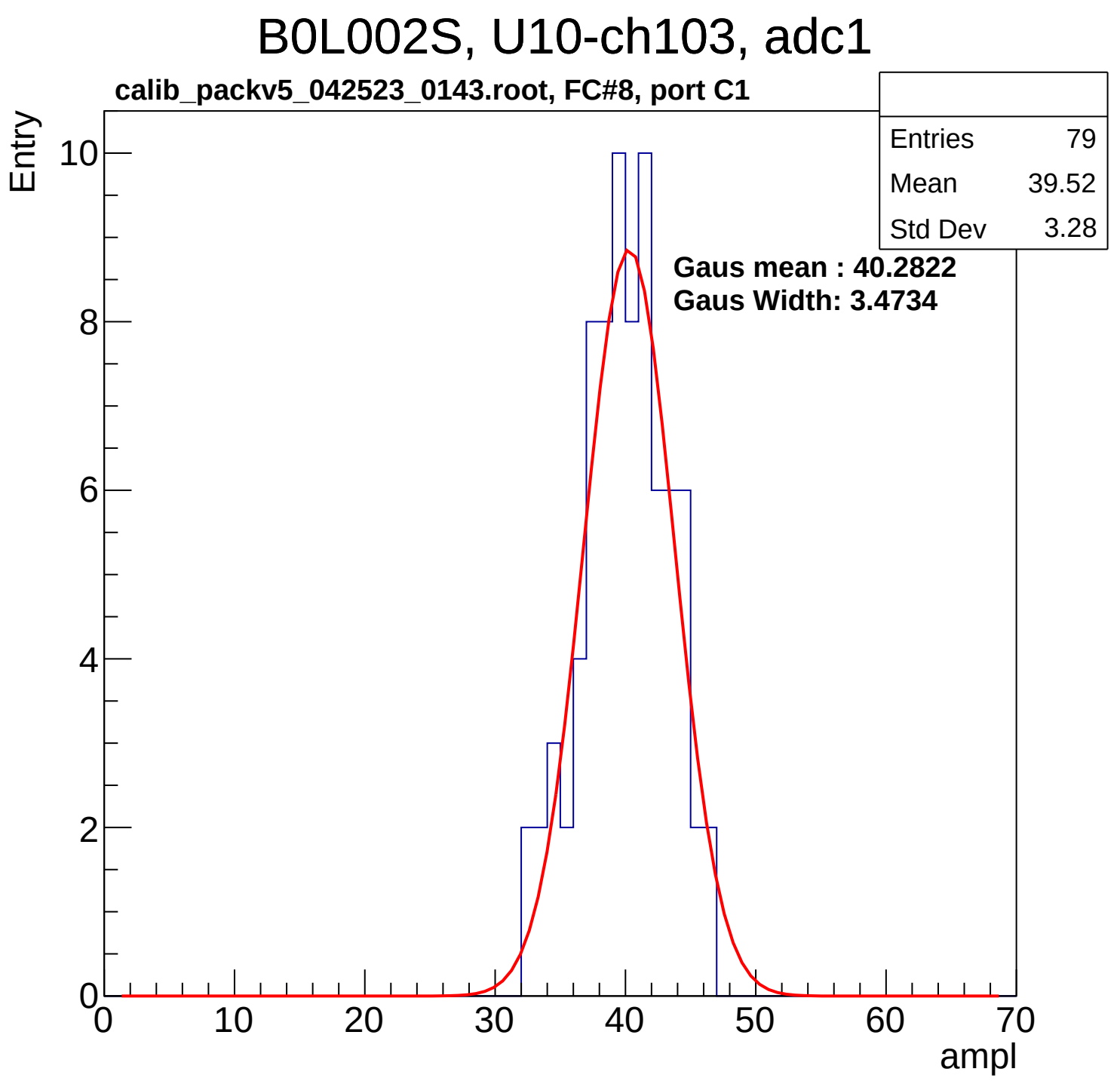

B0L002S, U10-ch102, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 55.98
Std Dev 2.548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

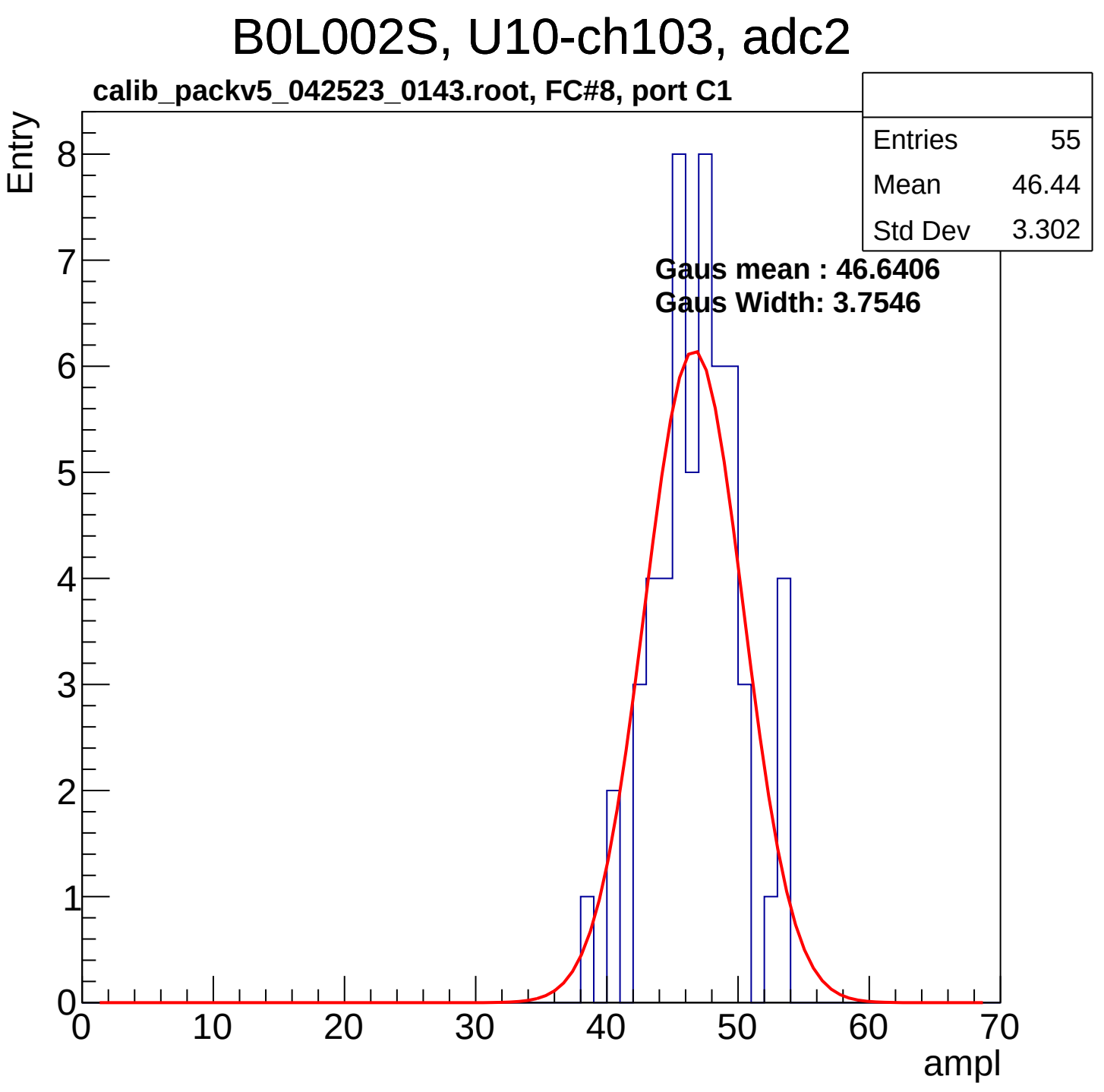

B0L002S, U10-ch102, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 60.88
Std Dev 1.794
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

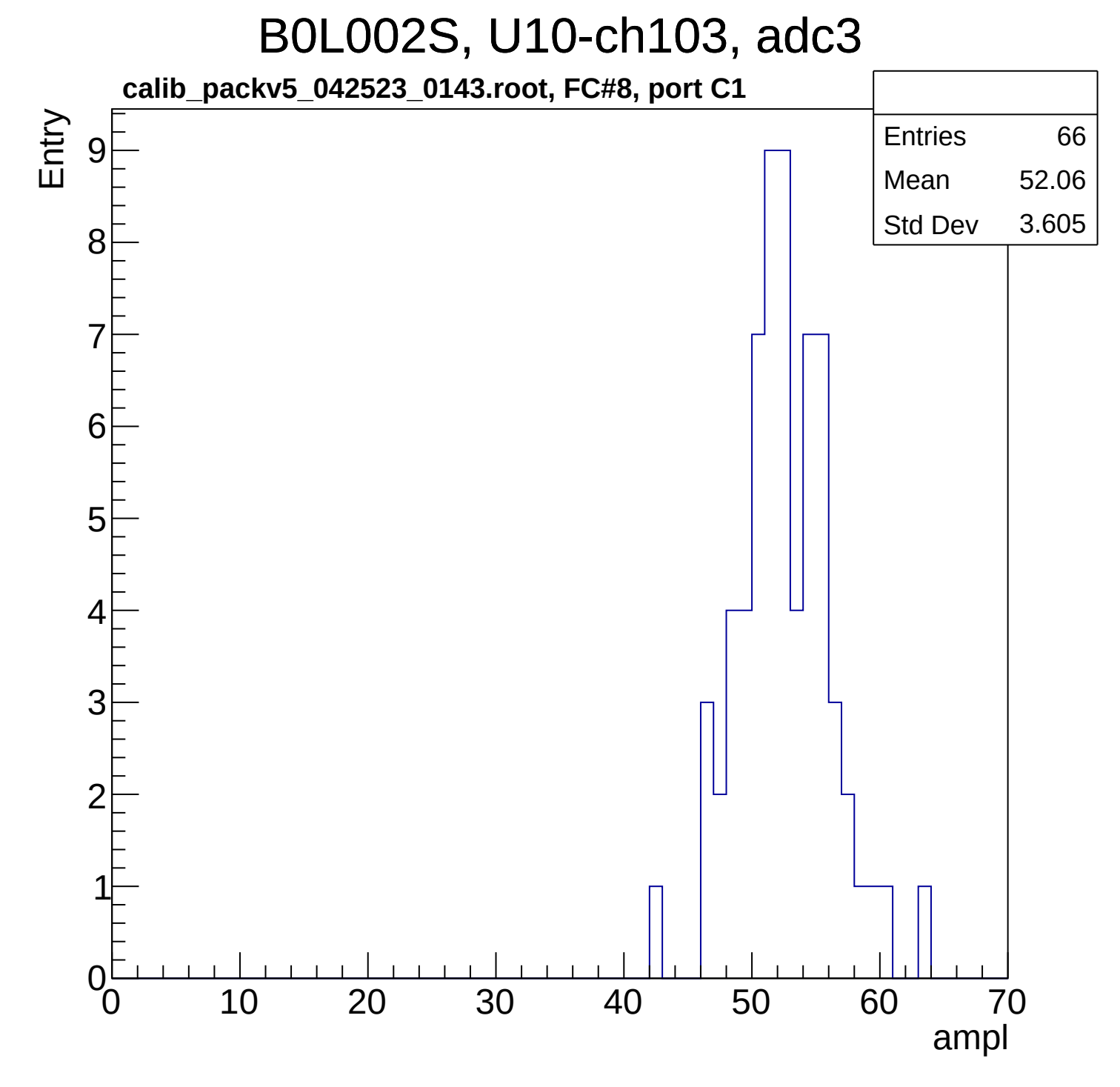

B0L002S, U10-ch102, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 41
Std Dev 29.02
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

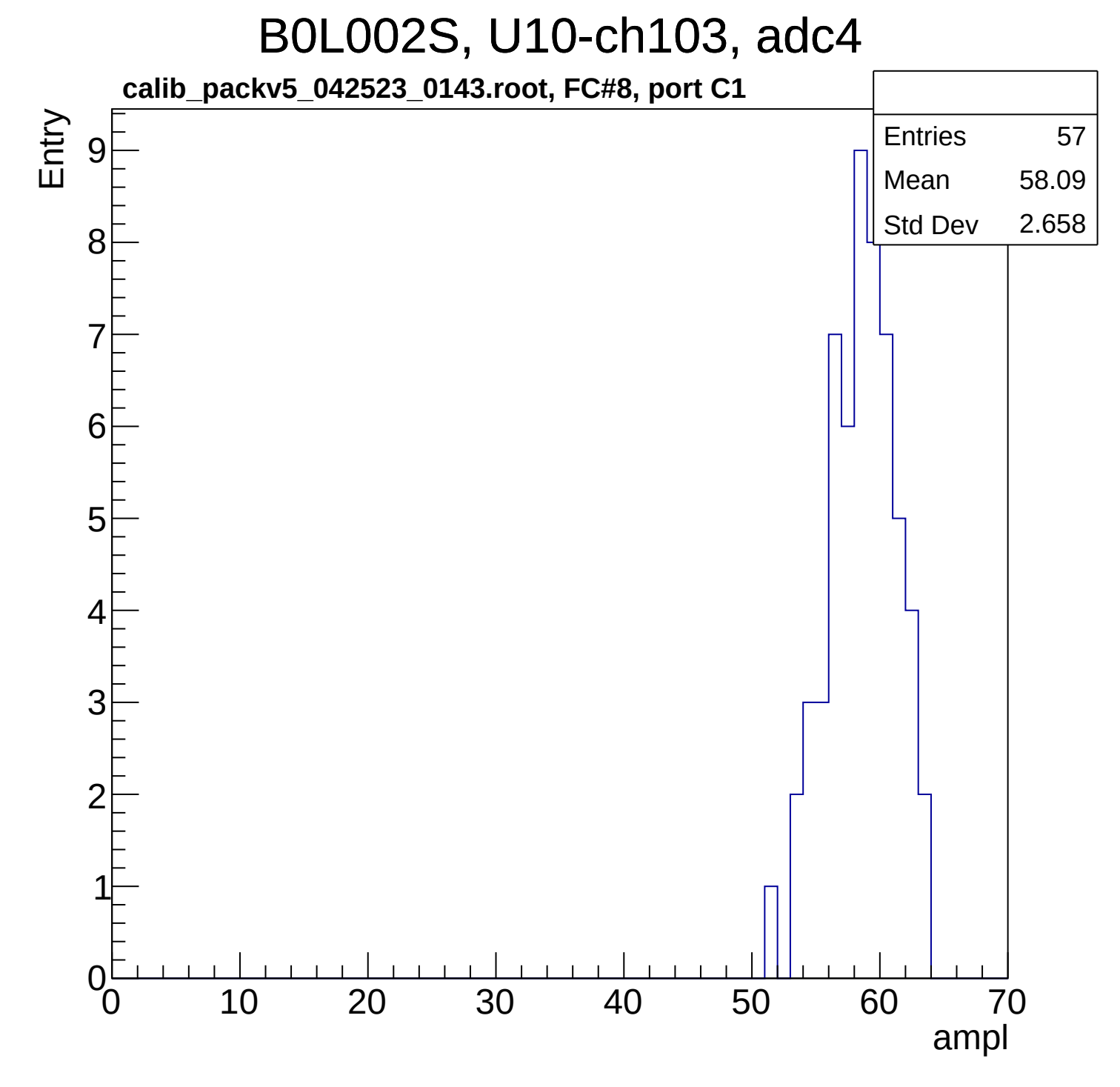

B0L002S, U10-ch102, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

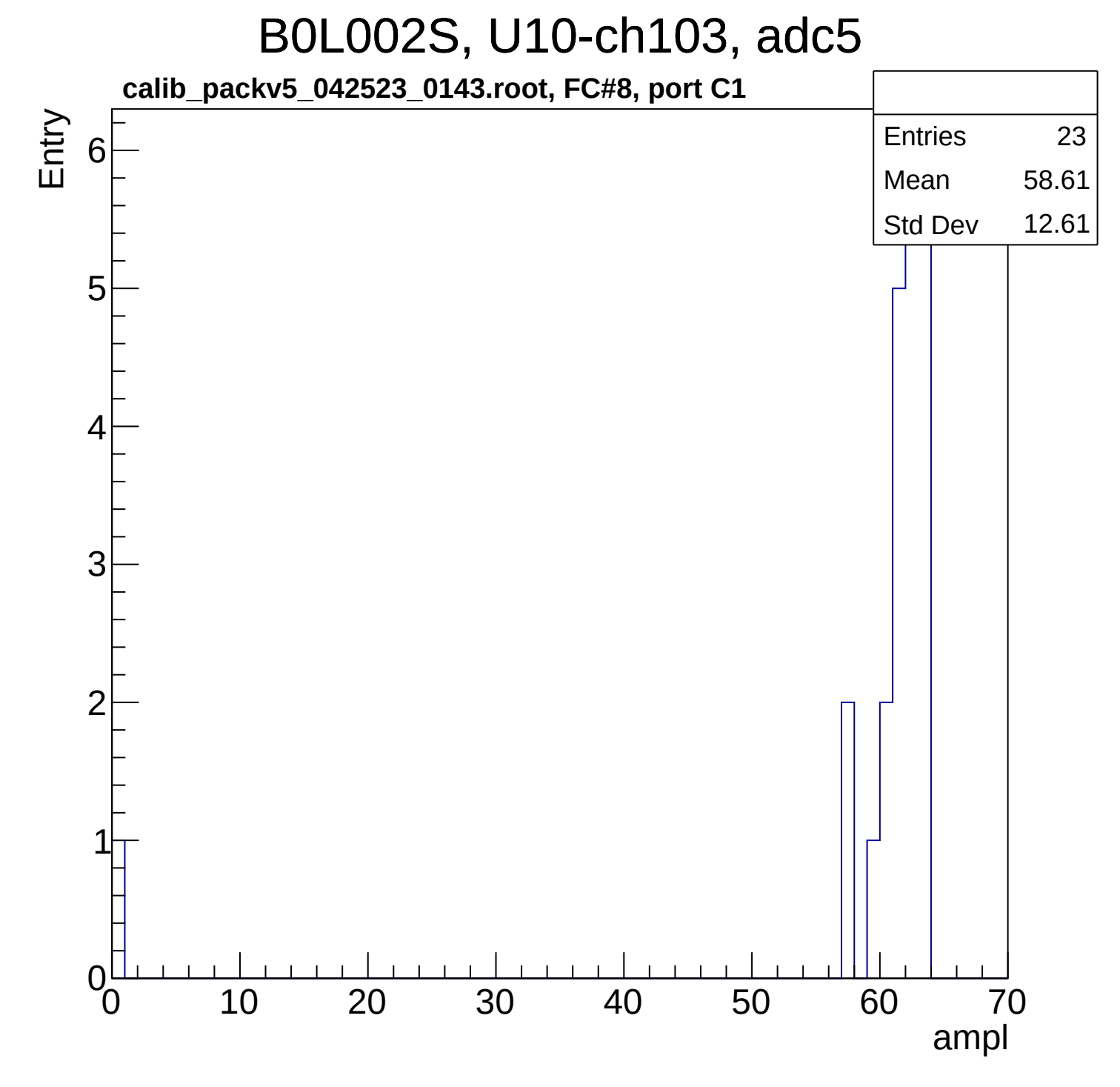

B0L002S, U10-ch103, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 32.06
Std Dev 2.943
Gaus mean : 32.6100
Gaus Width: 3.1501
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

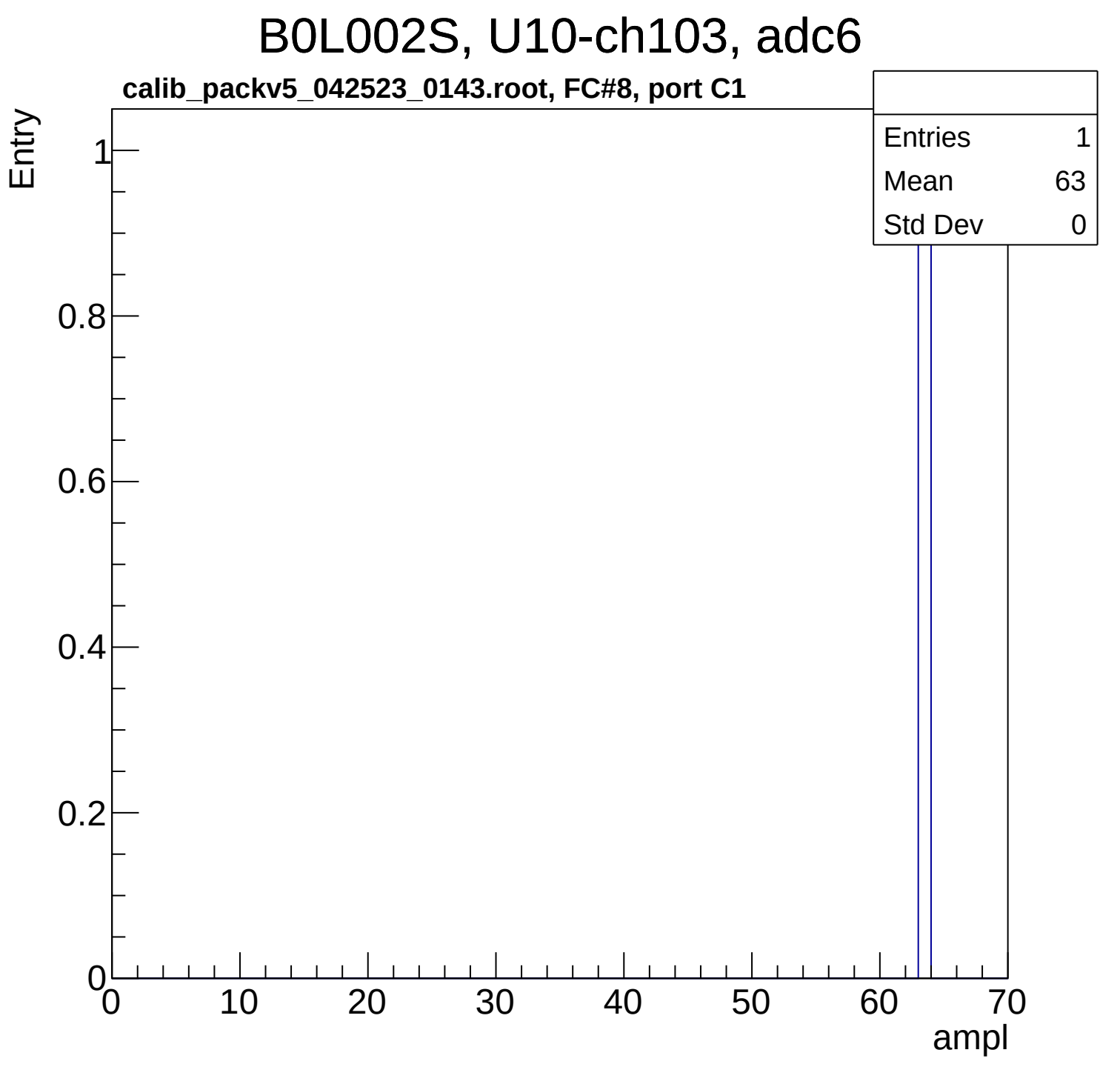

B0L002S, U10-ch103, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 39.52
Std Dev 3.28
Gaus mean : 40.2822
Gaus Width: 3.4734
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

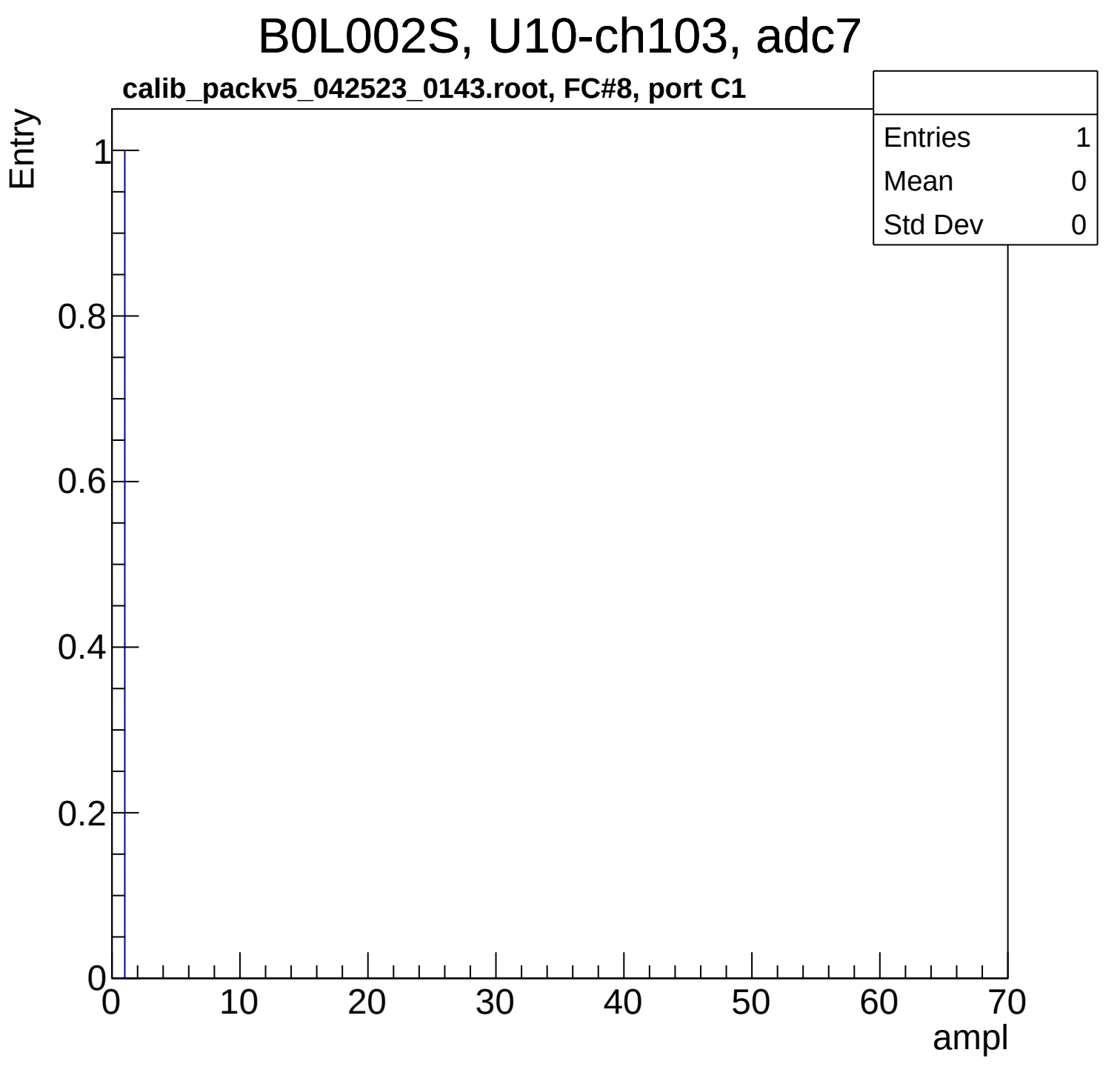

B0L002S, U10-ch103, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 46.44
Std Dev 3.302
Gaus mean : 46.6406
Gaus Width: 3.7546
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

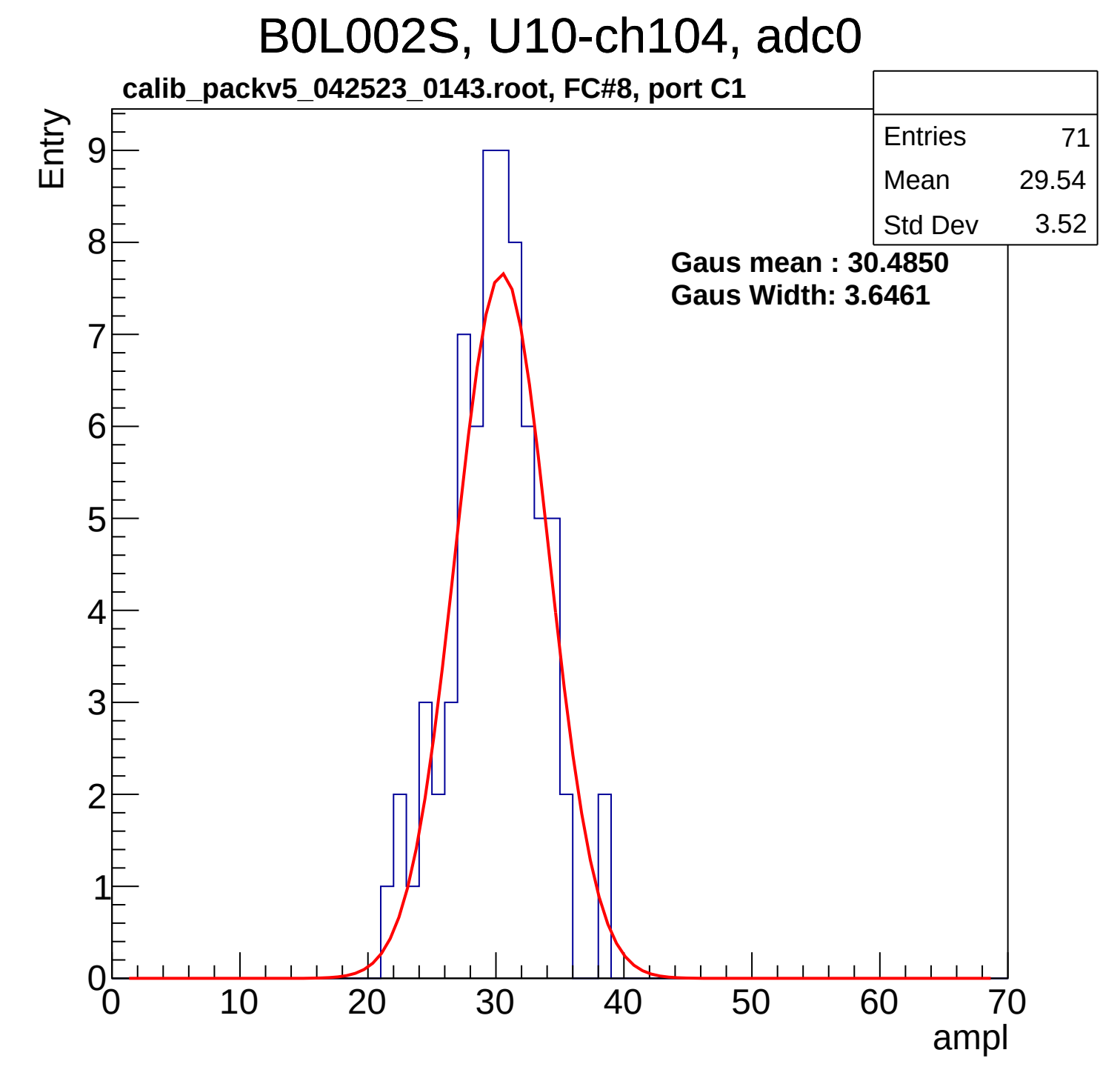

B0L002S, U10-ch103, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 52.06
Std Dev 3.605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

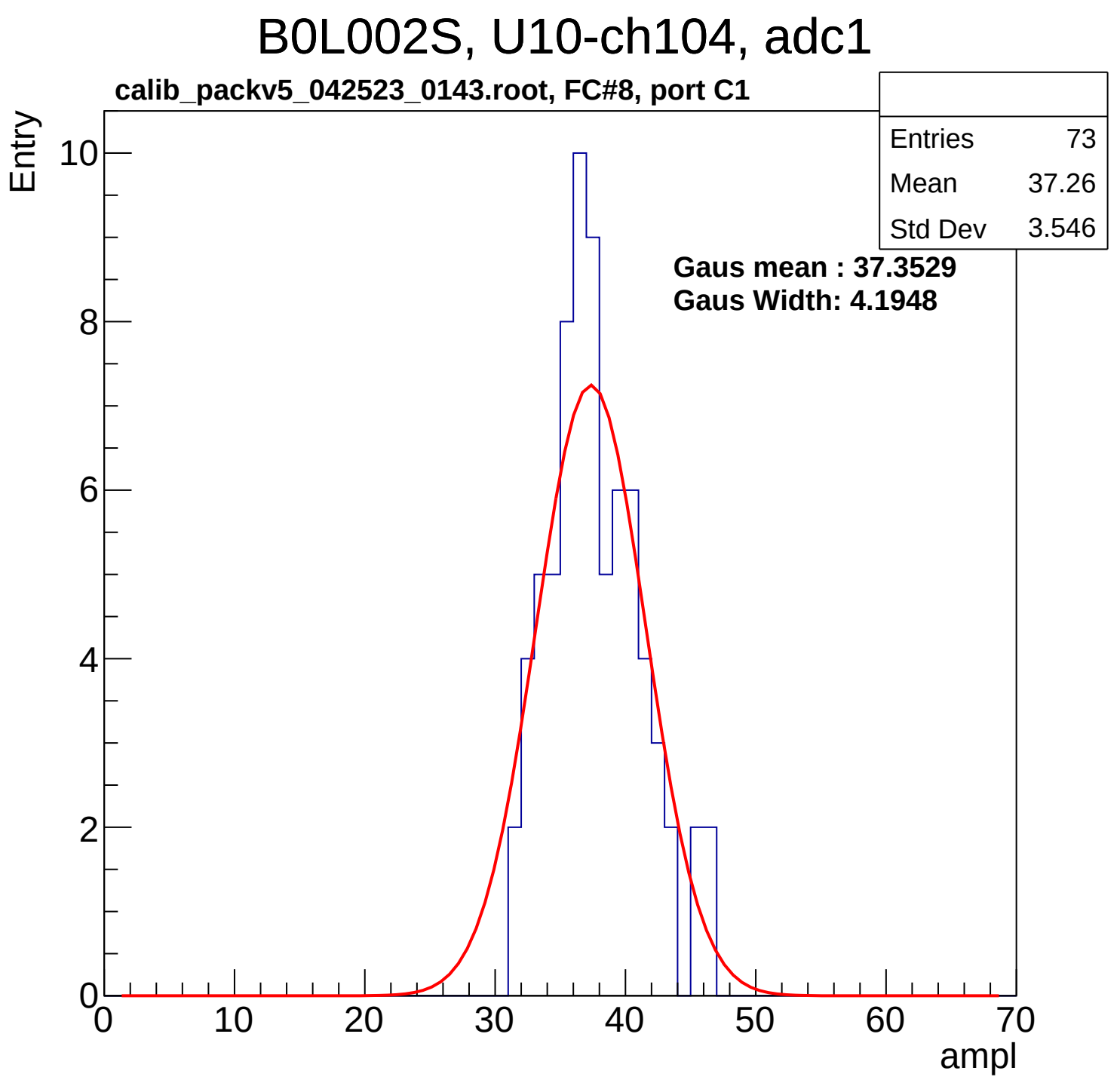

B0L002S, U10-ch103, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 58.09
Std Dev 2.658
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

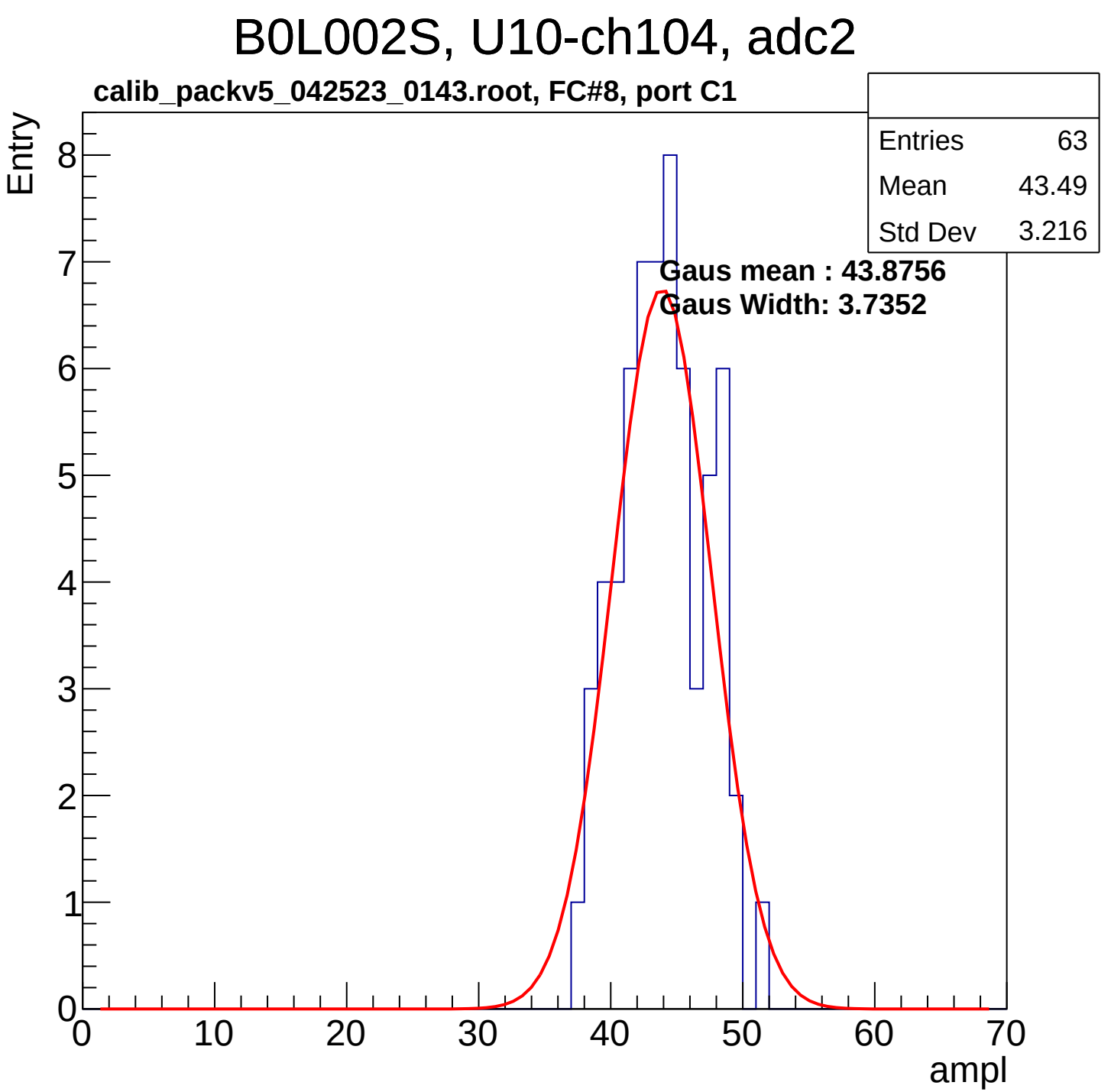

B0L002S, U10-ch103, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 23
Mean 58.61
Std Dev 12.61
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

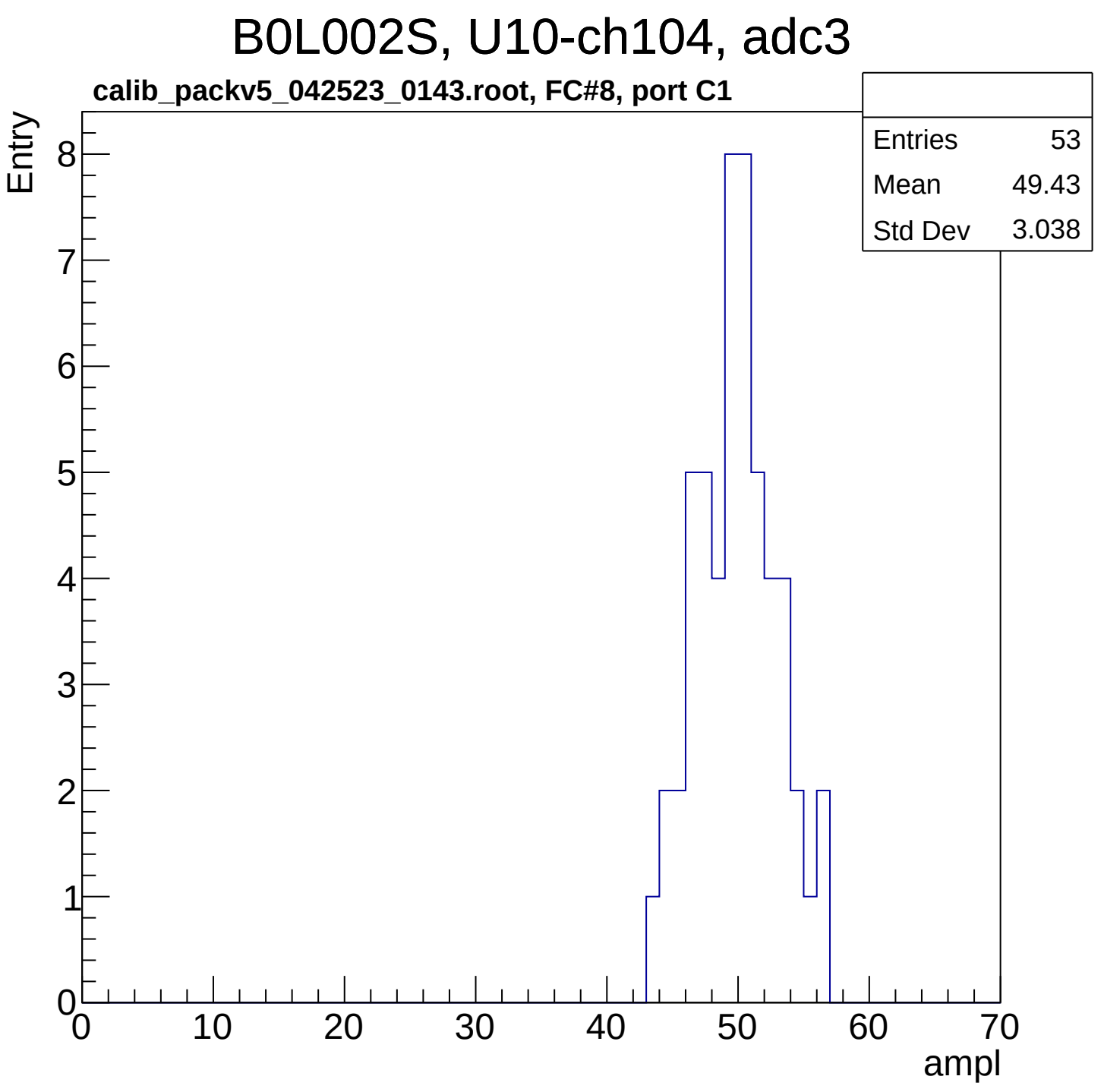

B0L002S, U10-ch103, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

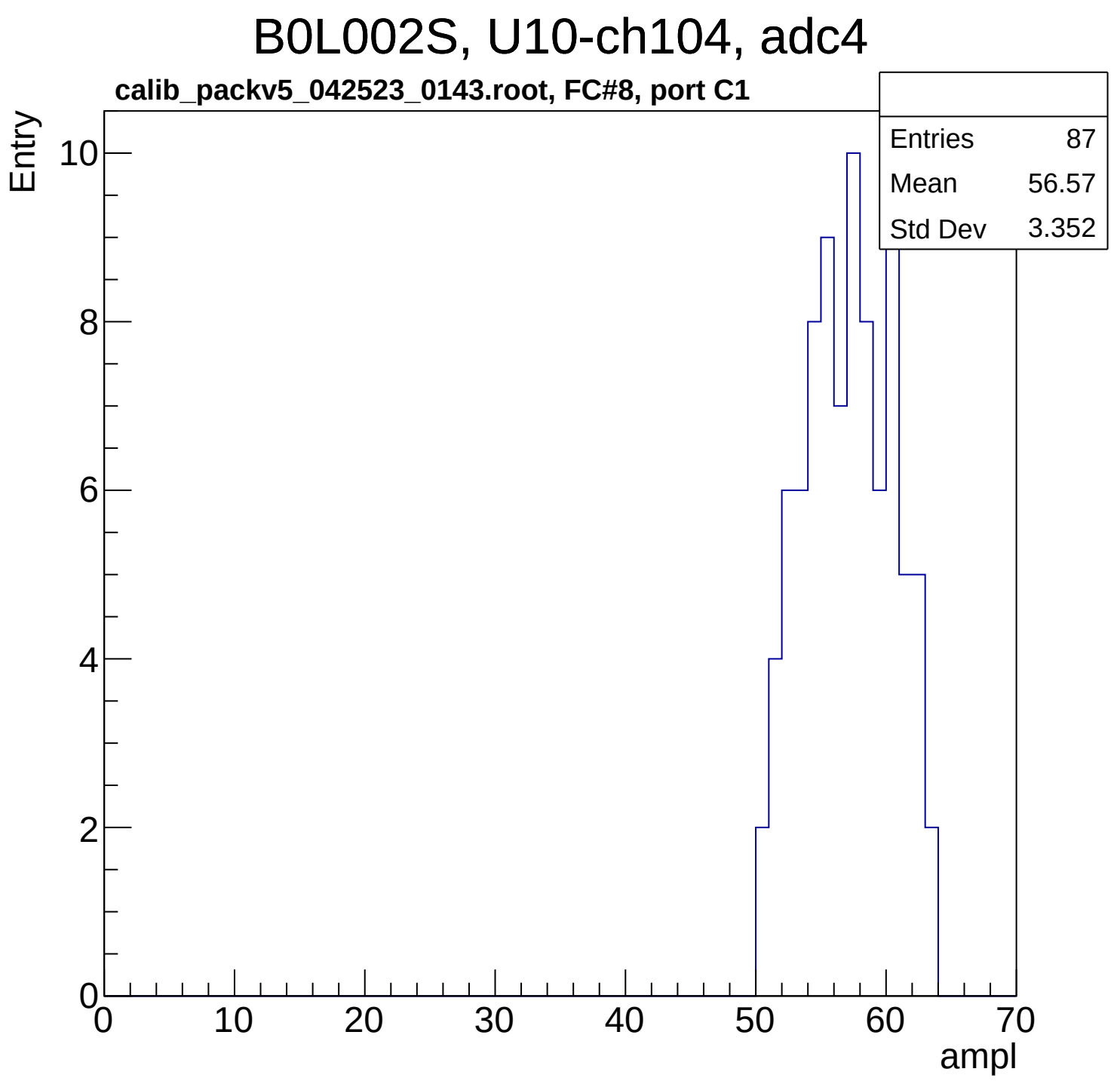

B0L002S, U10-ch103, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

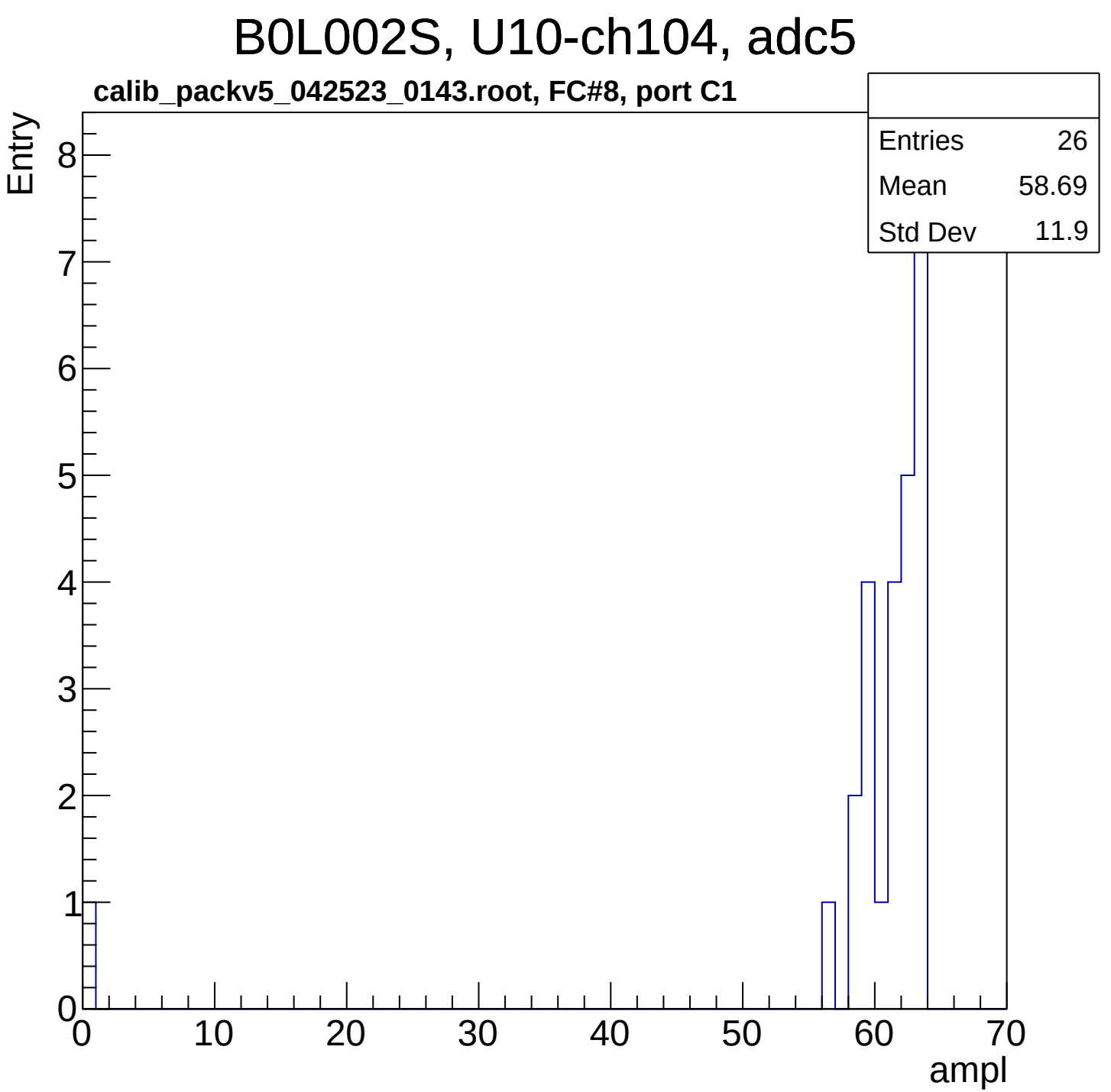

B0L002S, U10-ch104, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 29.54
Std Dev 3.52
Gaus mean : 30.4850
Gaus Width: 3.6461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

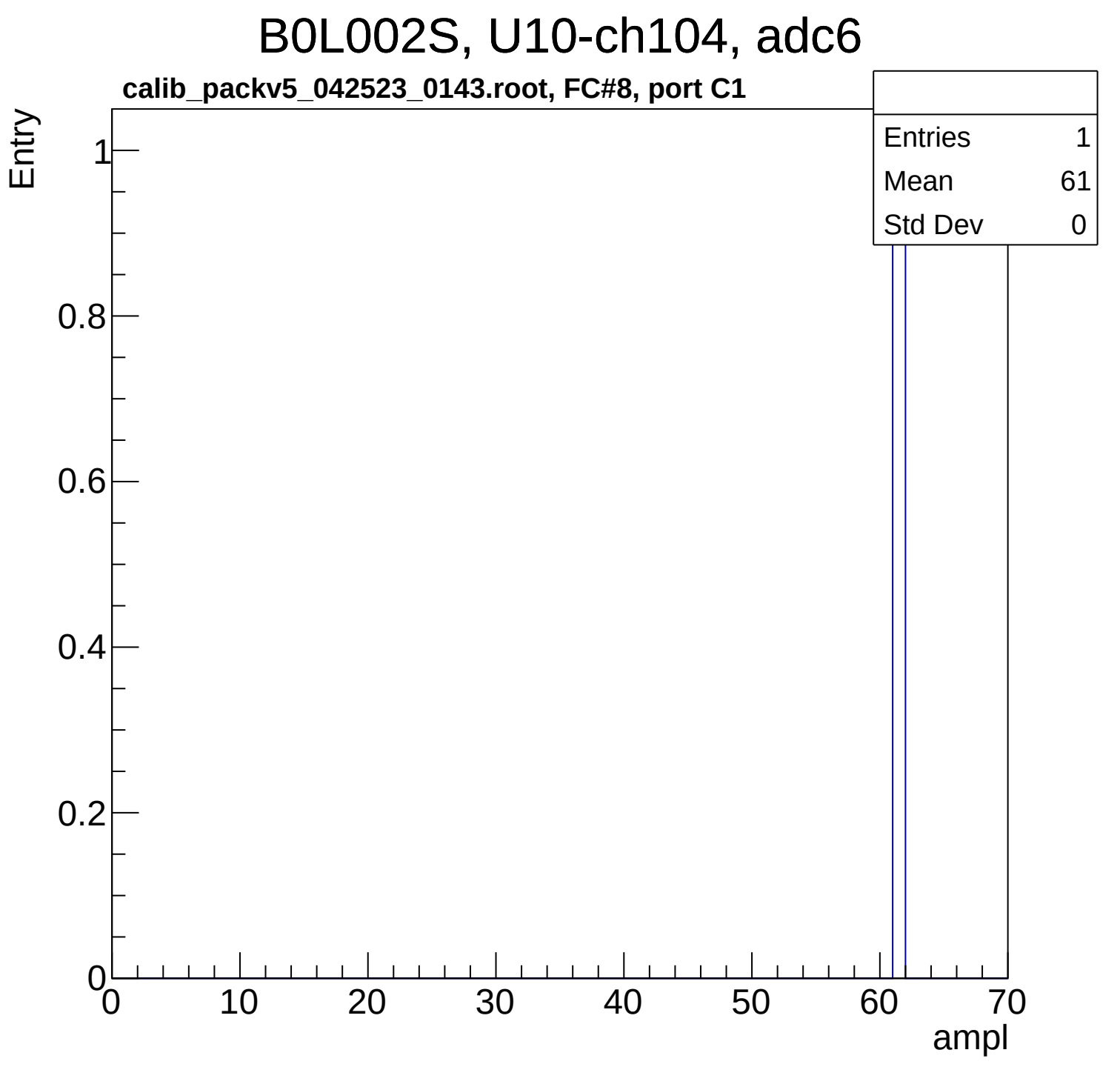

B0L002S, U10-ch104, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 37.26
Std Dev 3.546
Gaus mean : 37.3529
Gaus Width: 4.1948
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

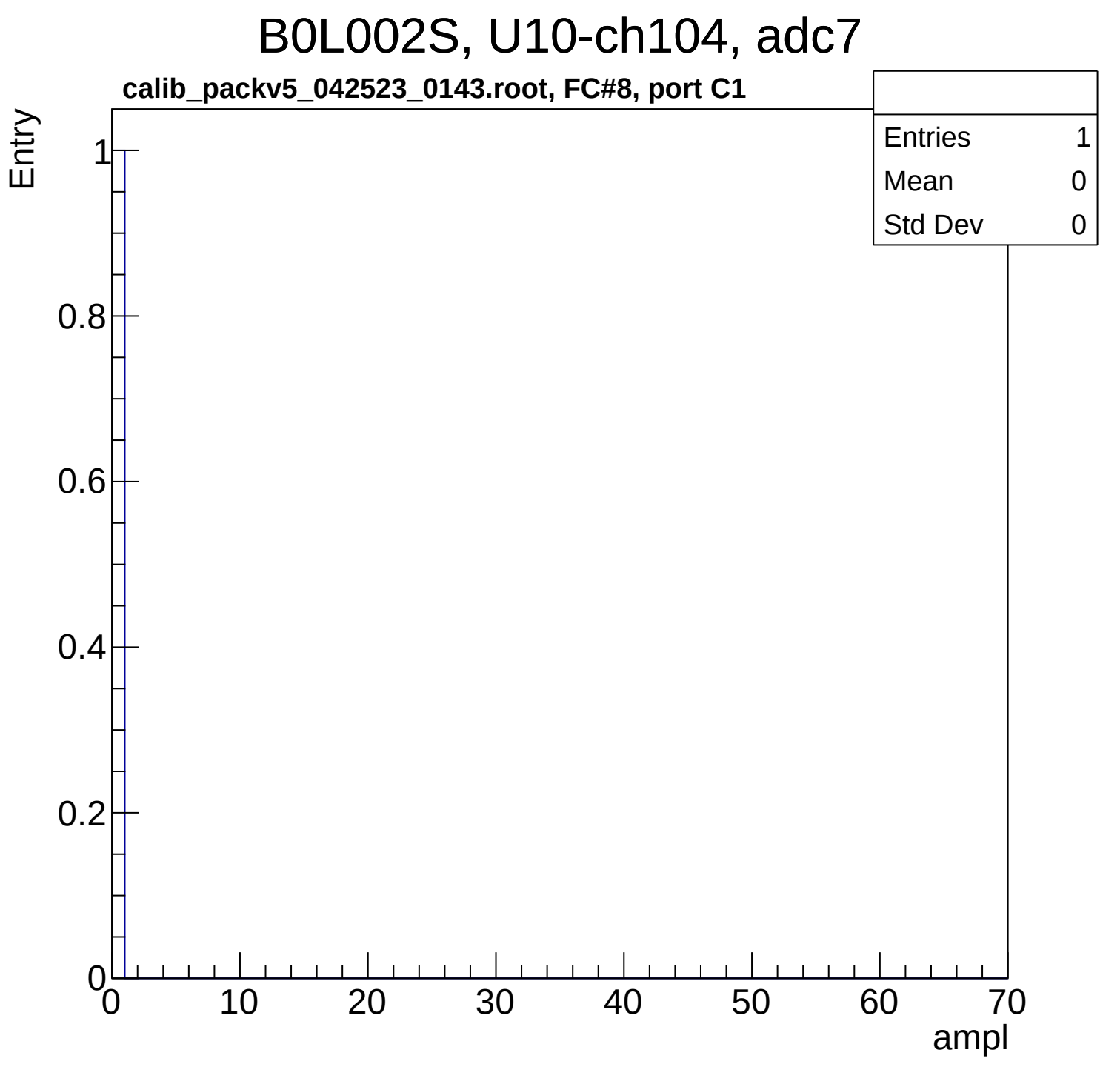

B0L002S, U10-ch104, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 43.49
Std Dev 3.216
Gaus mean : 43.8756
Gaus Width: 3.7352
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

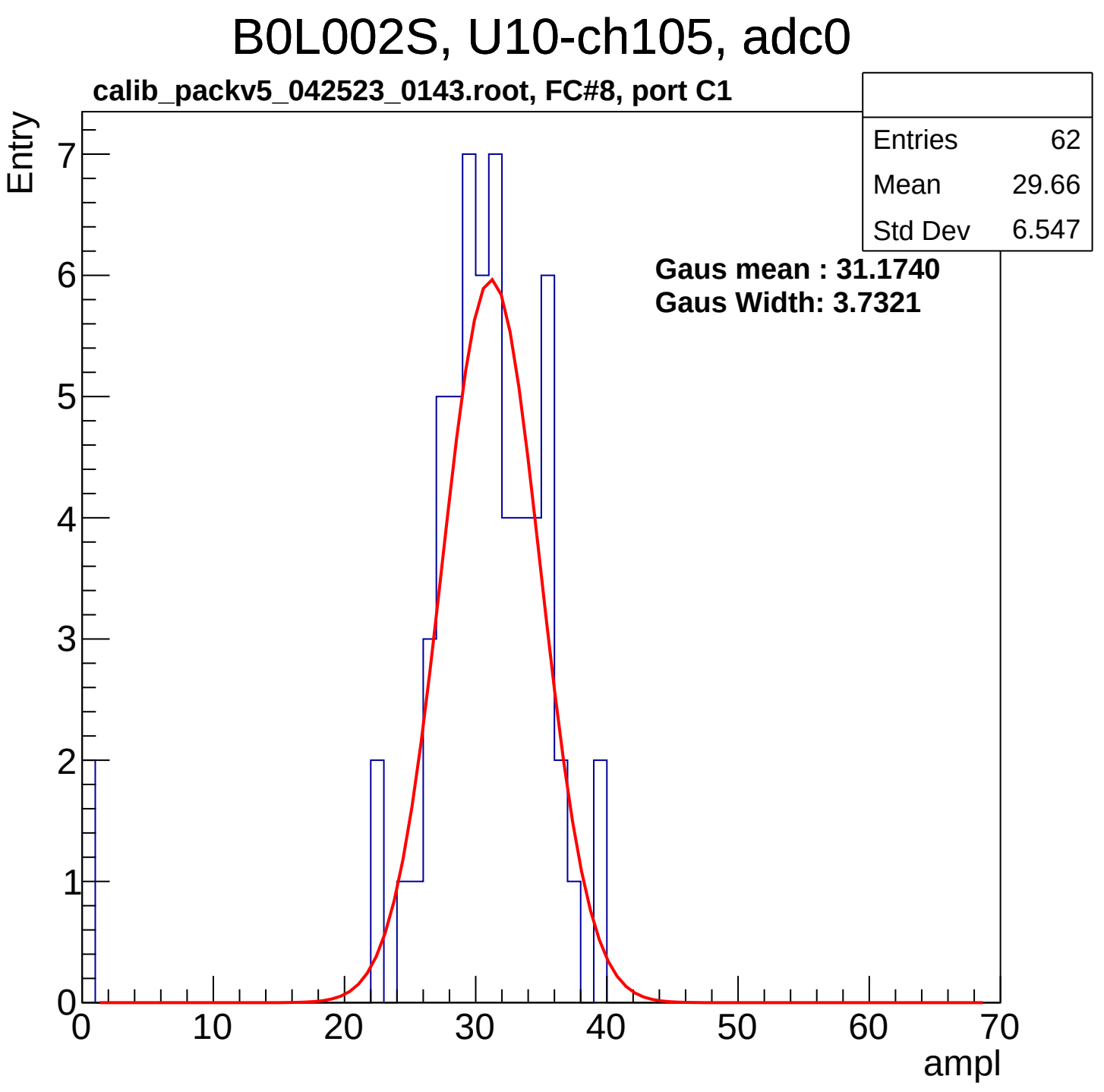

B0L002S, U10-ch104, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 49.43
Std Dev 3.038
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

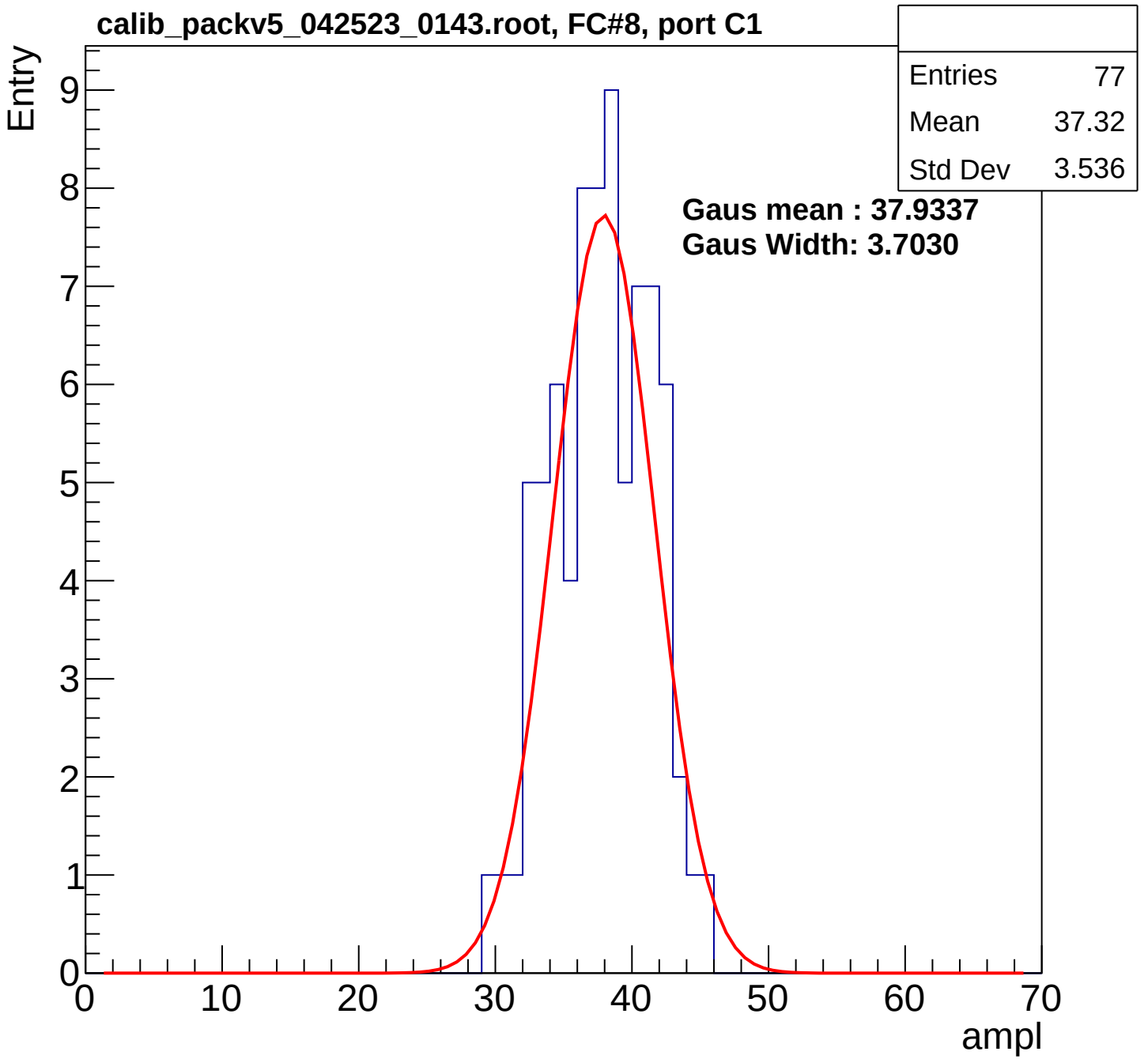

B0L002S, U10-ch104, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 56.57
Std Dev 3.352
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

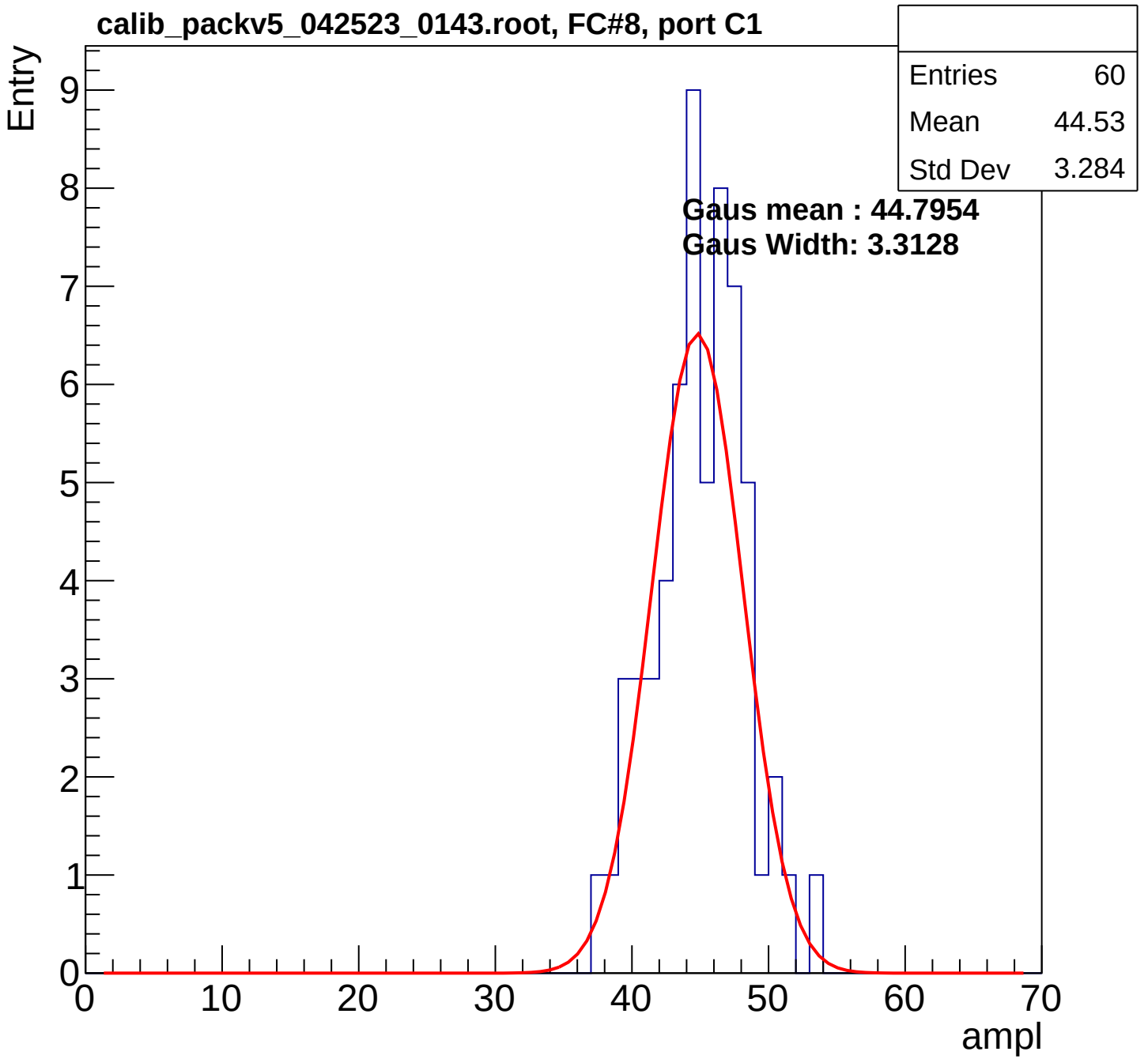

B0L002S, U10-ch104, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 26
Mean 58.69
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

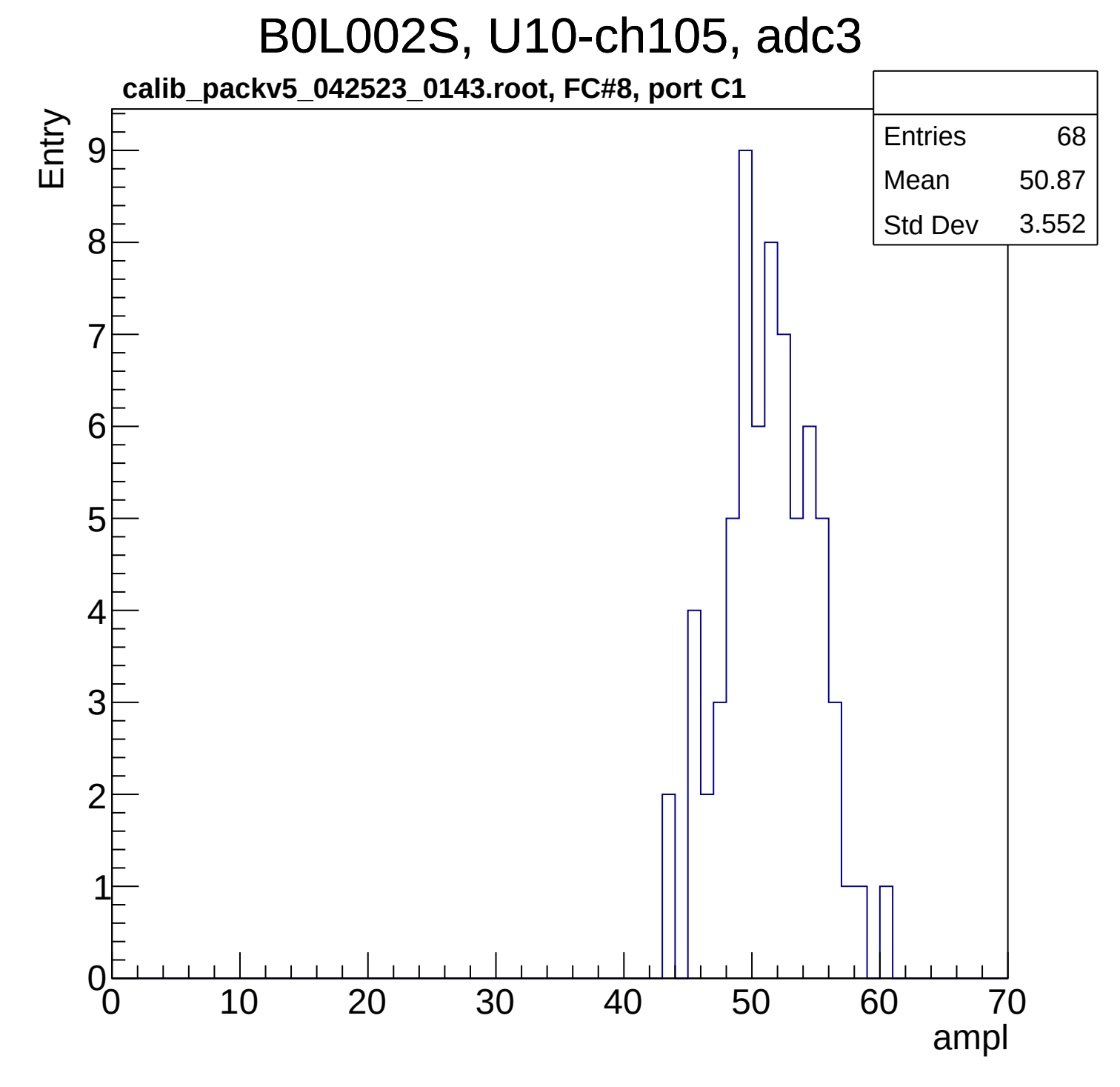

B0L002S, U10-ch104, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

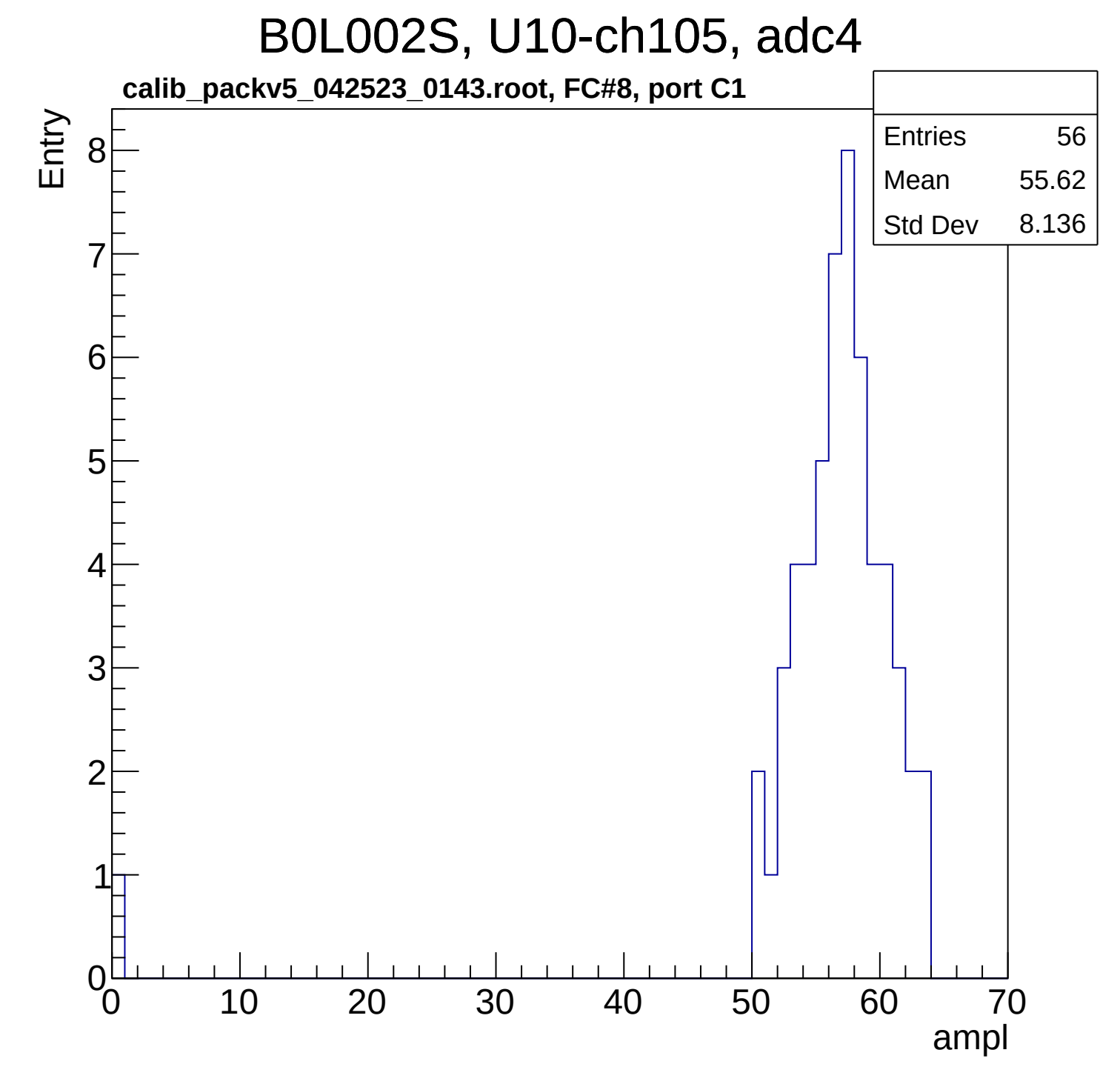

B0L002S, U10-ch104, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

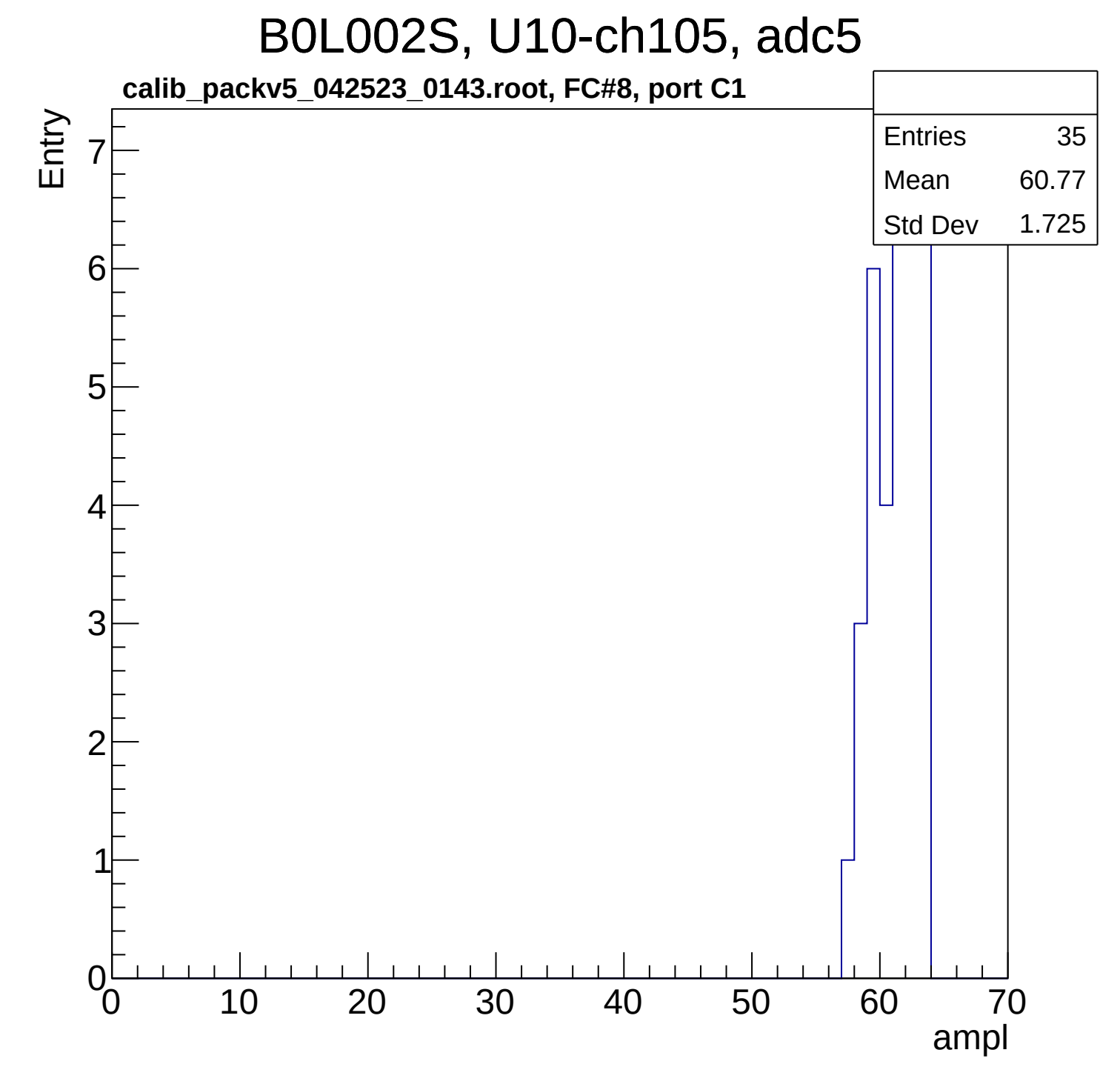

B0L002S, U10-ch105, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 29.66
Std Dev 6.547
Gaus mean : 31.1740
Gaus Width: 3.7321
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

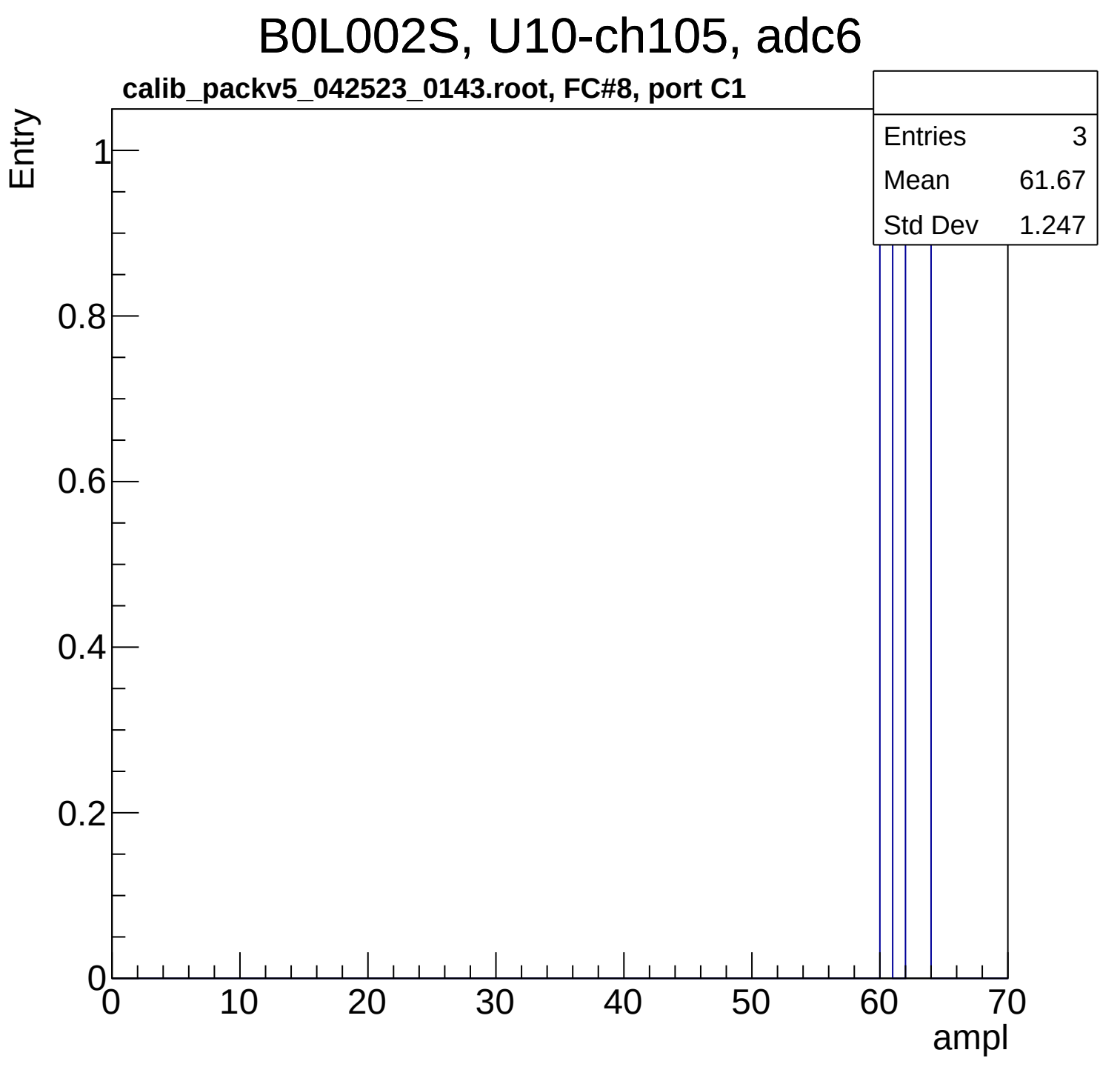

B0L002S, U10-ch105, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 37.32
Std Dev 3.536
Gaus mean : 37.9337
Gaus Width: 3.7030
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

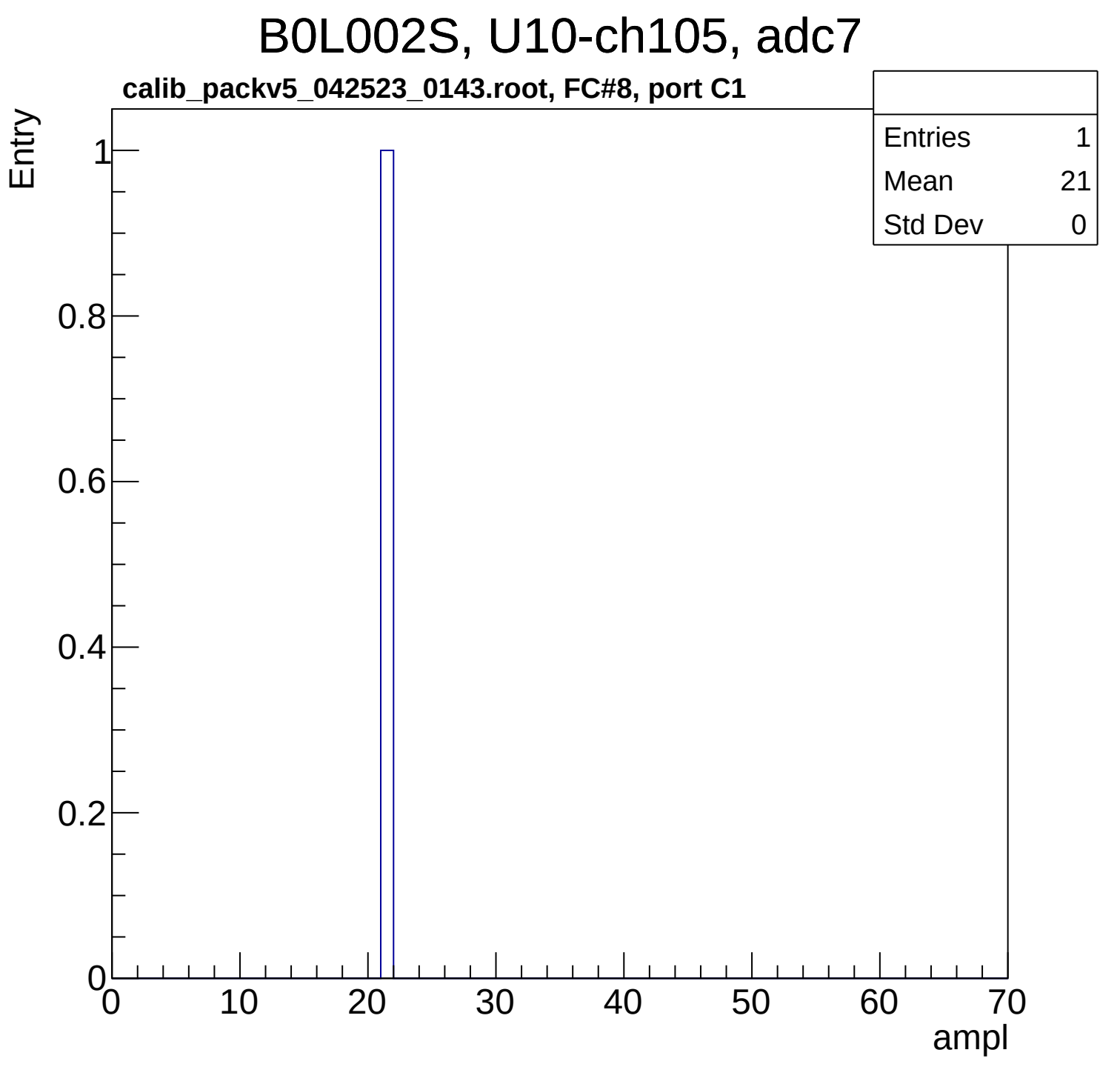

B0L002S, U10-ch105, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 44.53
Std Dev 3.284
Gaus mean : 44.7954
Gaus Width: 3.3128
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

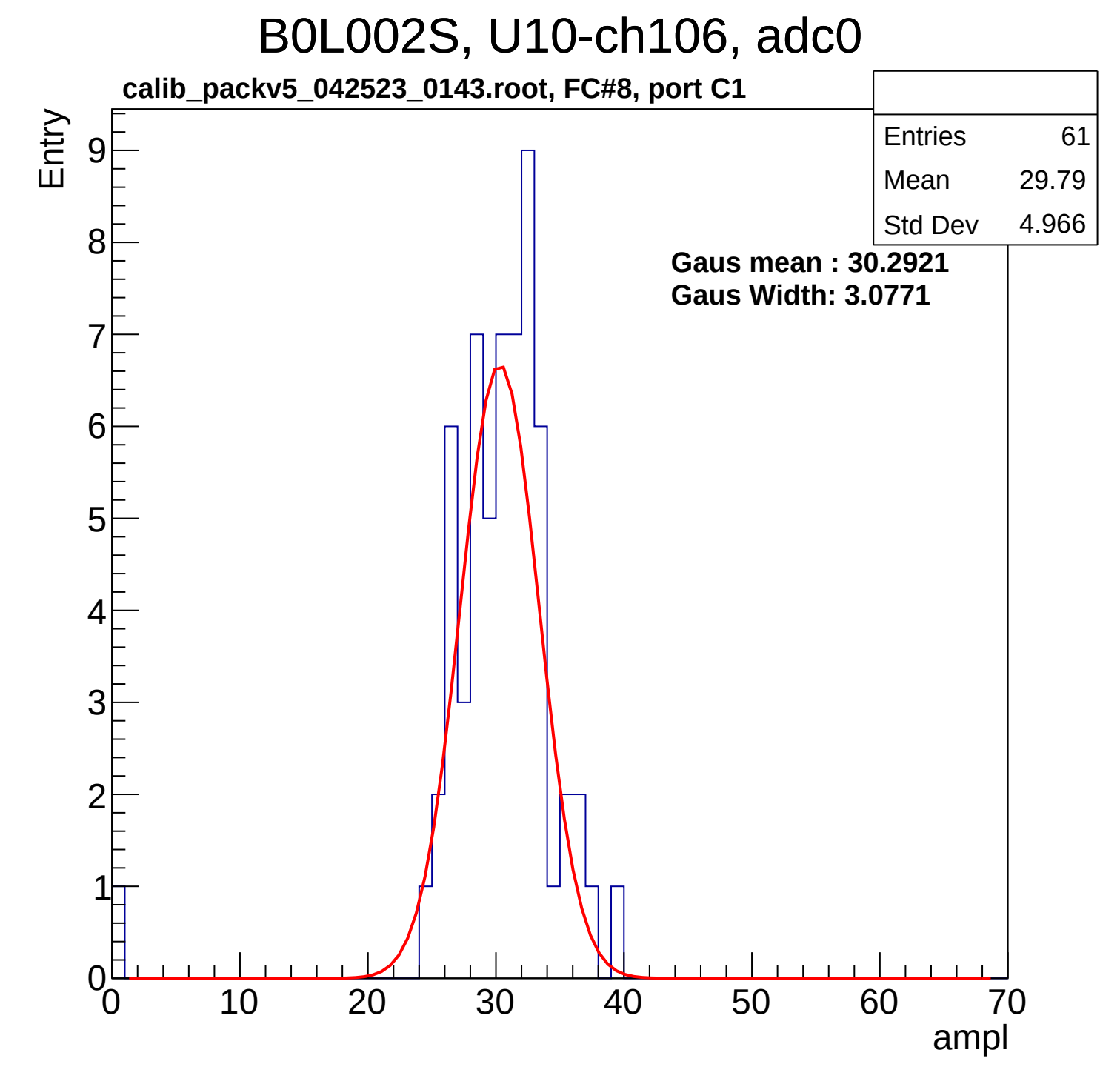

B0L002S, U10-ch105, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 50.87
Std Dev 3.552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

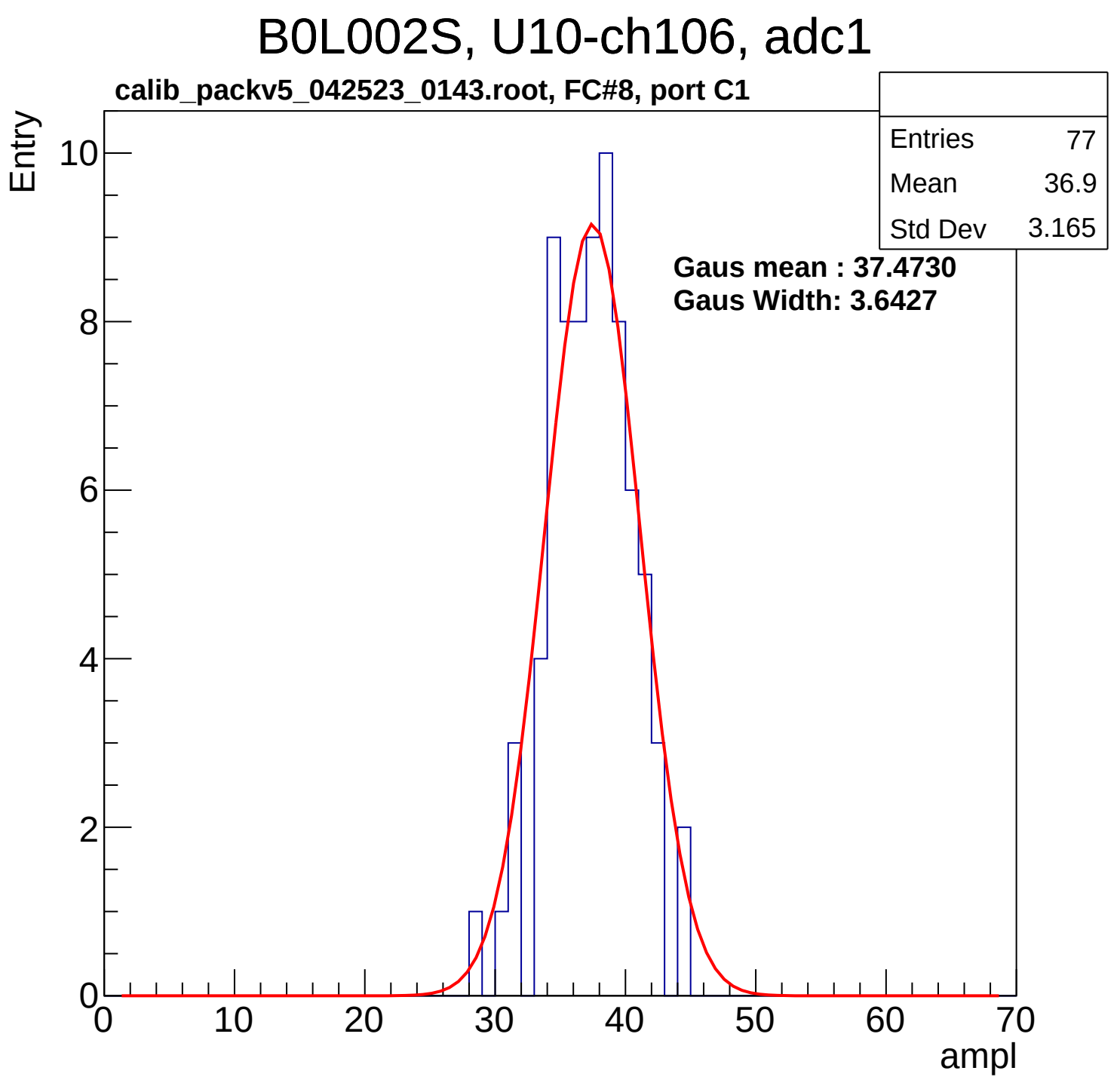

B0L002S, U10-ch105, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 55.62
Std Dev 8.136
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

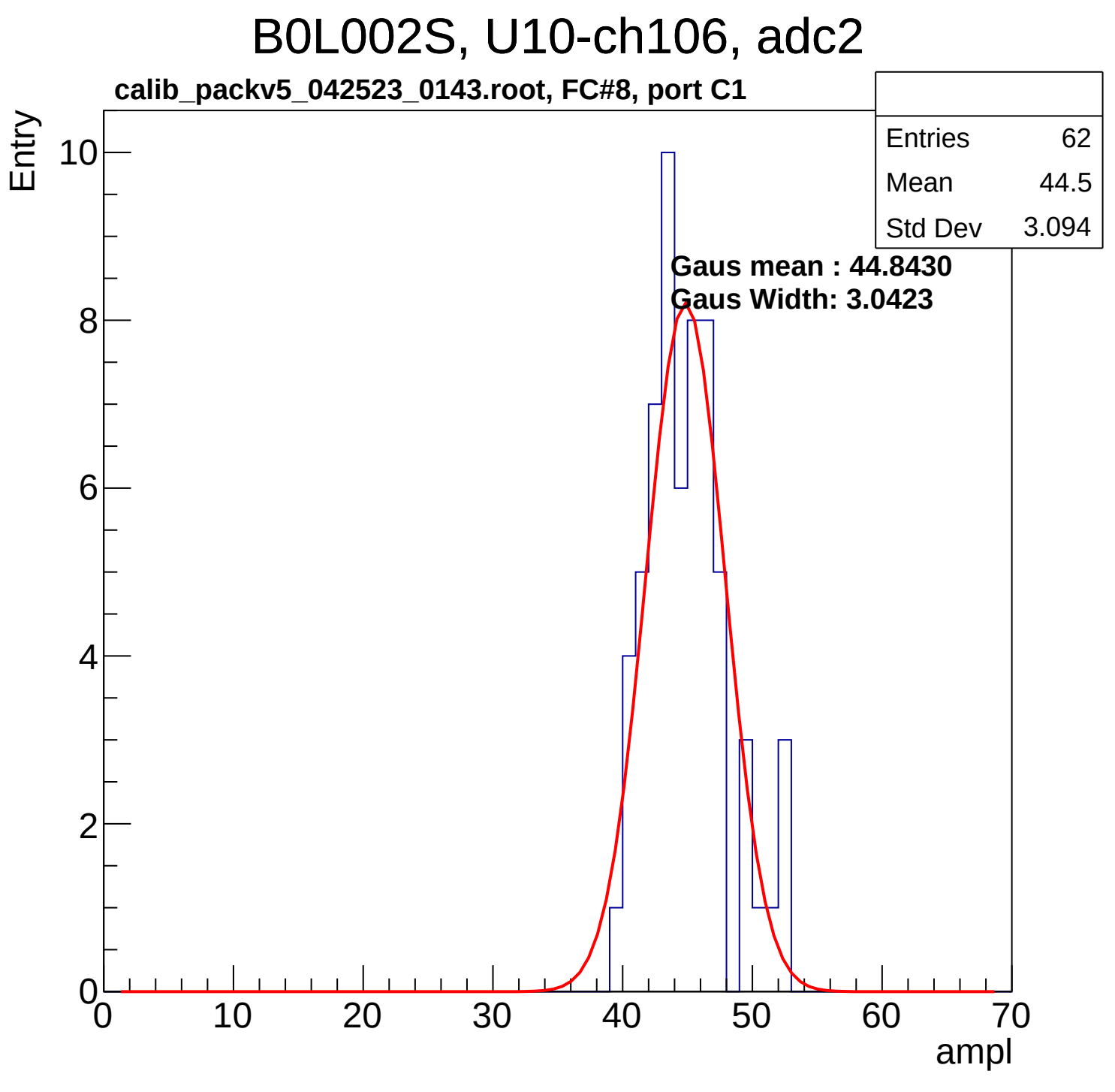

B0L002S, U10-ch105, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 60.77
Std Dev 1.725
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

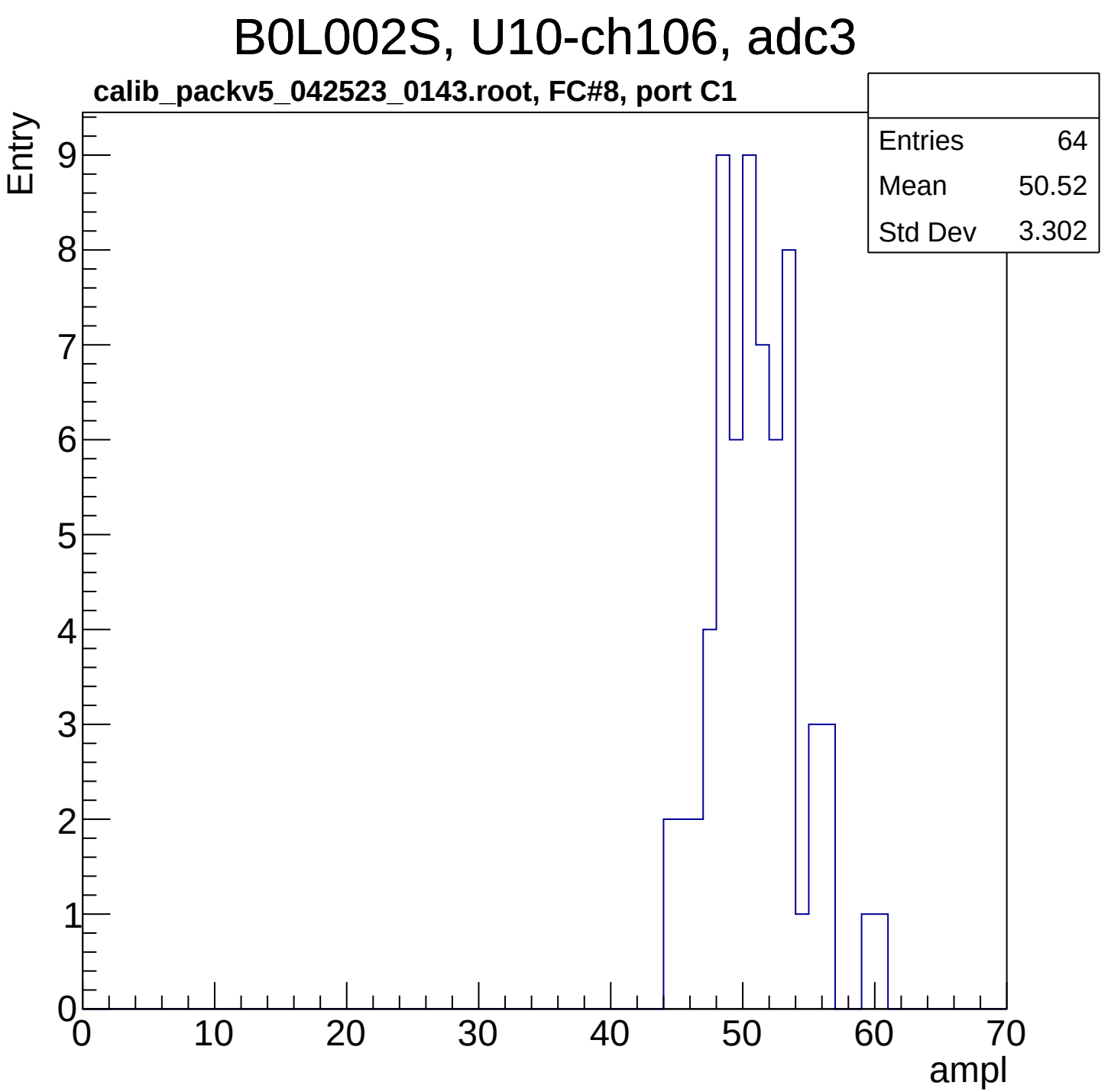

B0L002S, U10-ch105, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

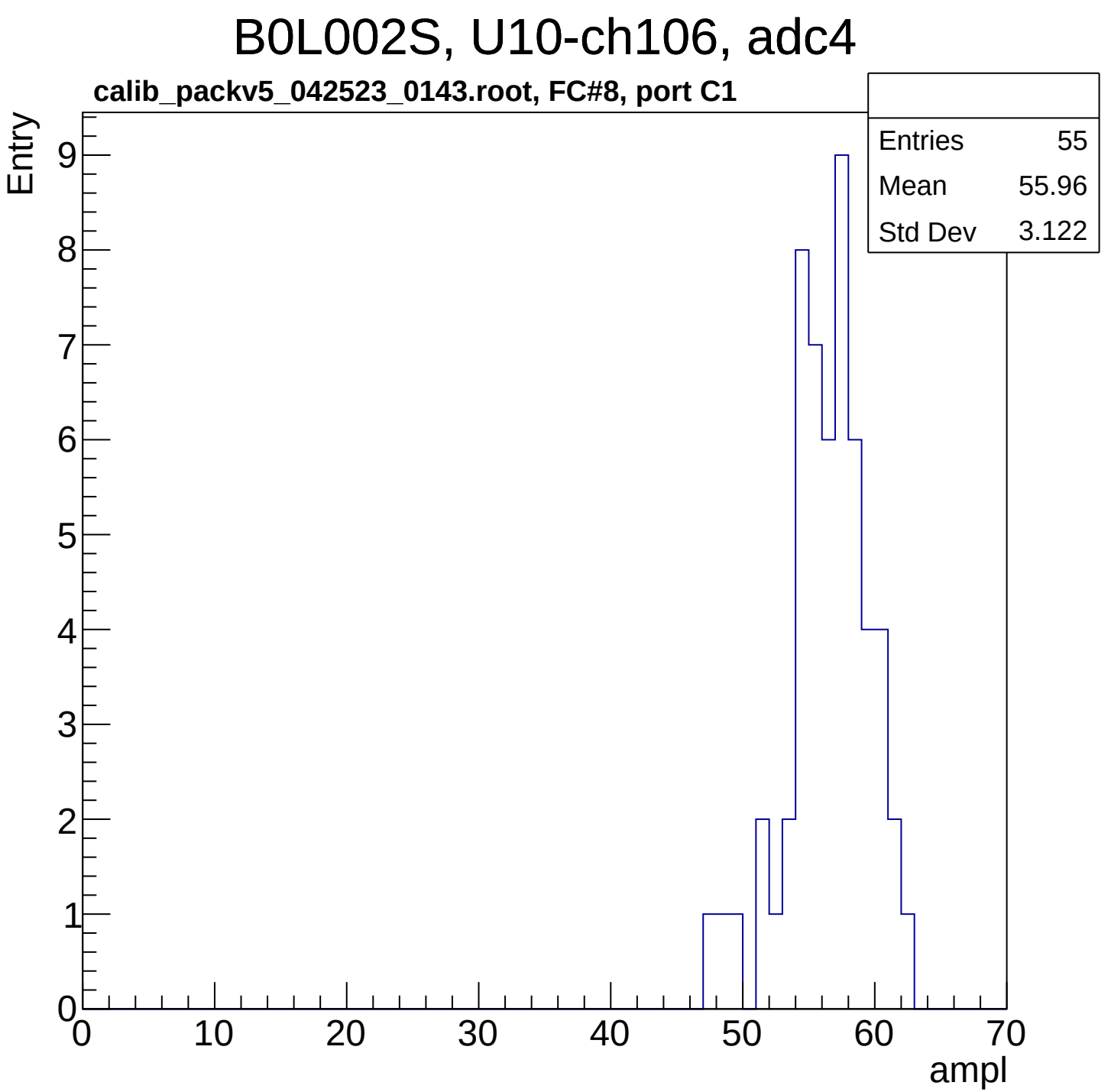

B0L002S, U10-ch105, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

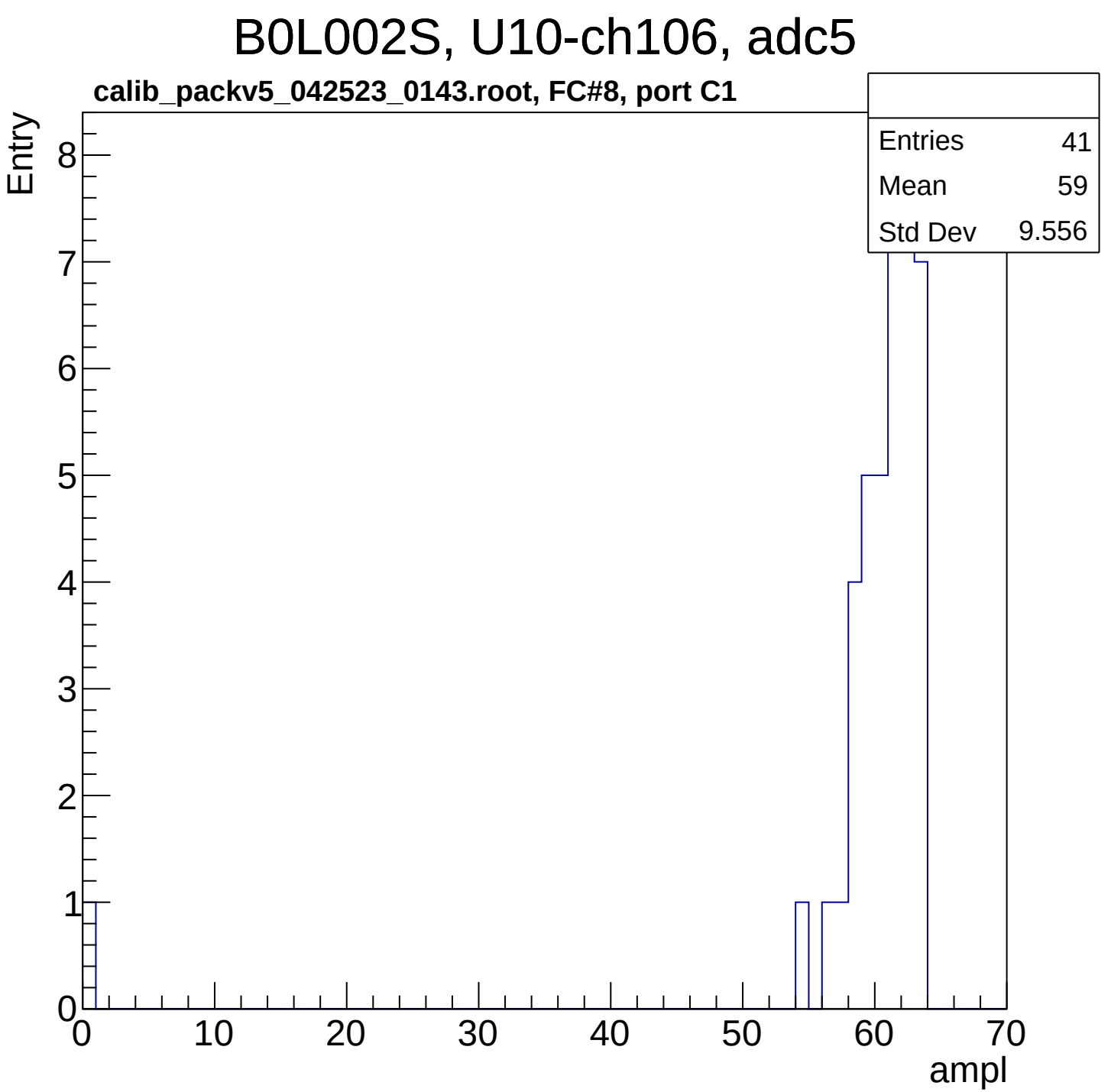

B0L002S, U10-ch106, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.79
Std Dev 4.966
Gaus mean : 30.2921
Gaus Width: 3.0771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

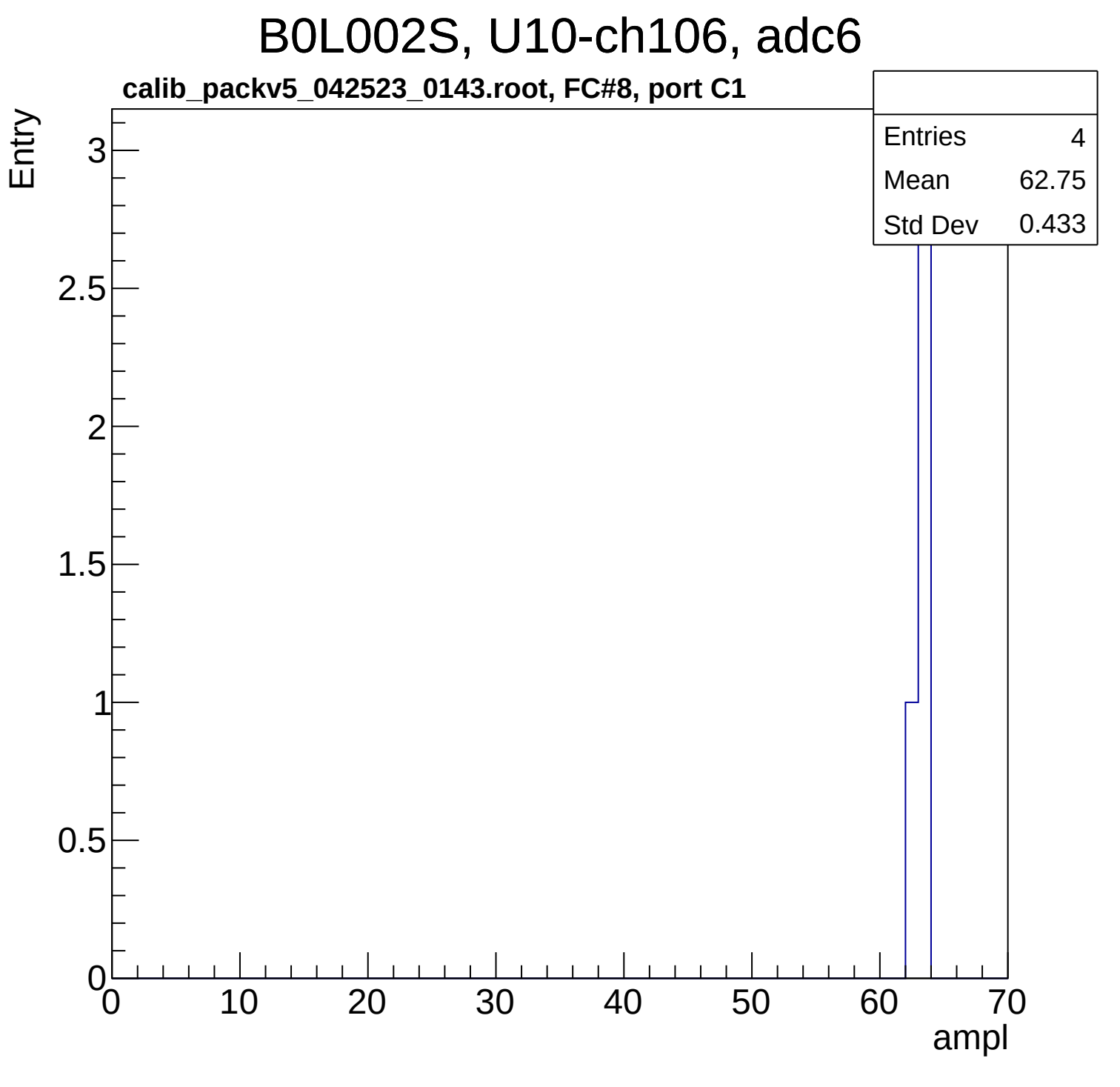

B0L002S, U10-ch106, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 36.9
Std Dev 3.165
Gaus mean : 37.4730
Gaus Width: 3.6427
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

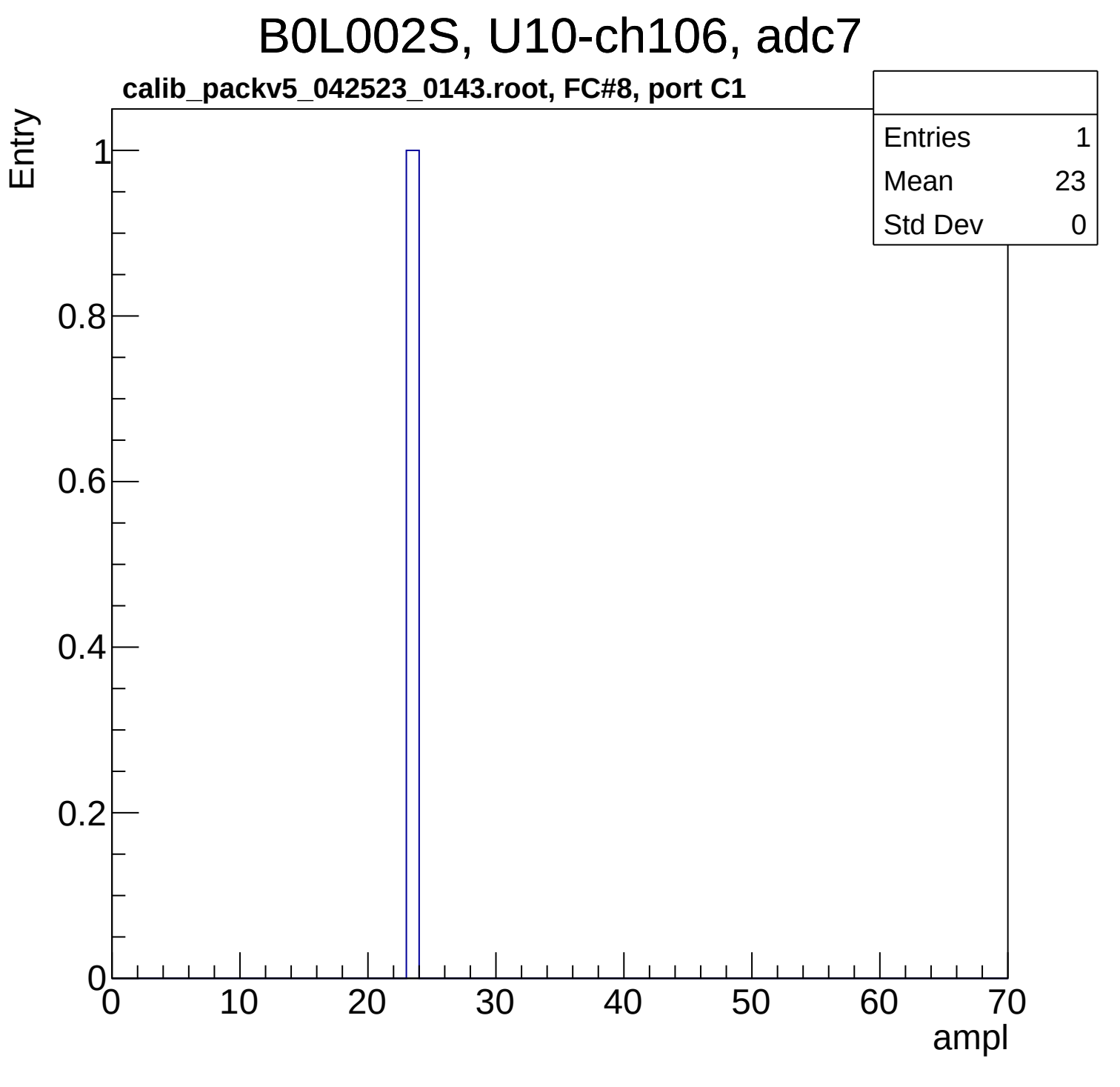

B0L002S, U10-ch106, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 44.5
Std Dev 3.094
Gaus mean : 44.8430
Gaus Width: 3.0423
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

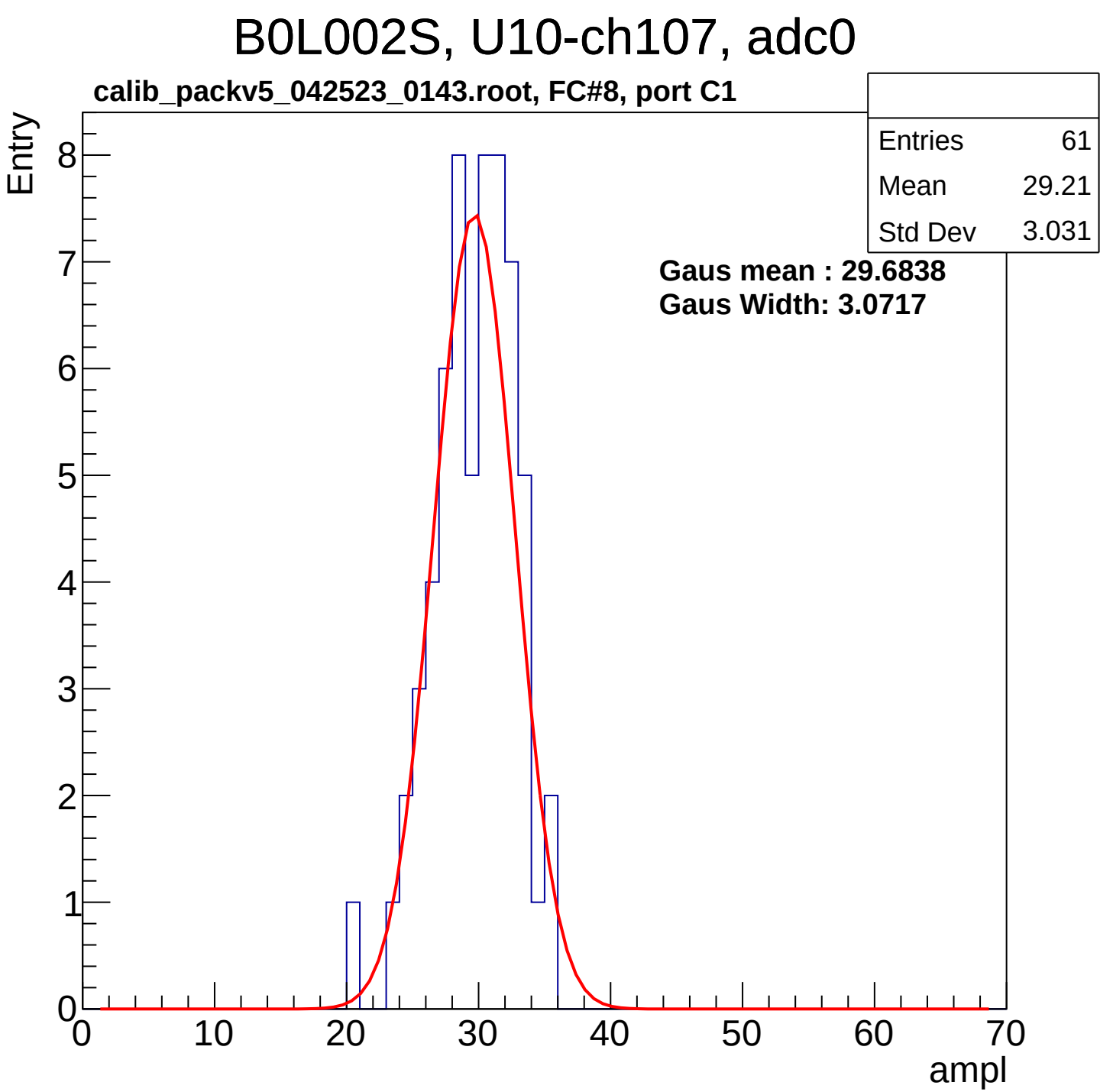

B0L002S, U10-ch106, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 50.52
Std Dev 3.302
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

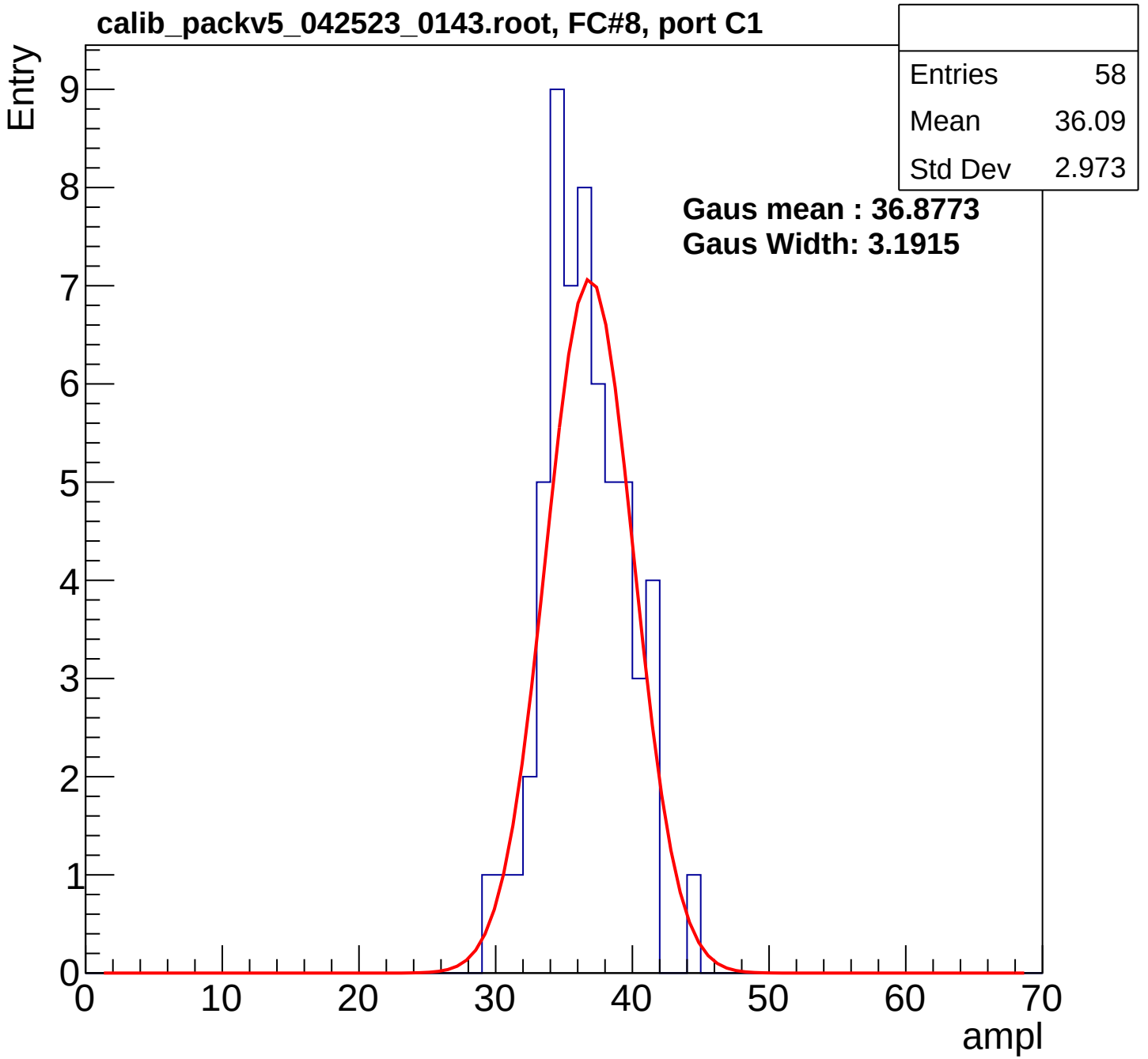

B0L002S, U10-ch106, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 55.96
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

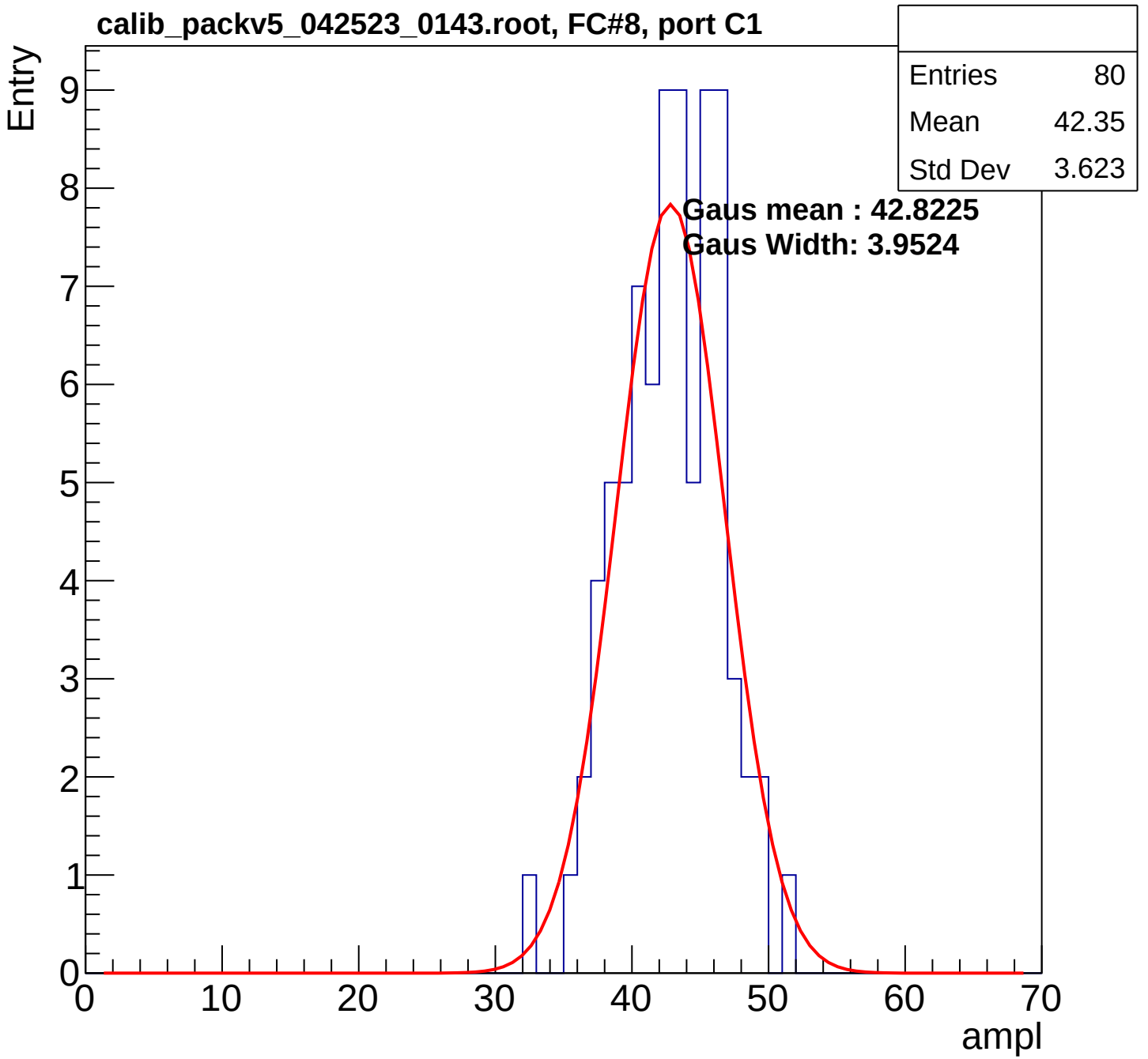

B0L002S, U10-ch106, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 59
Std Dev 9.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

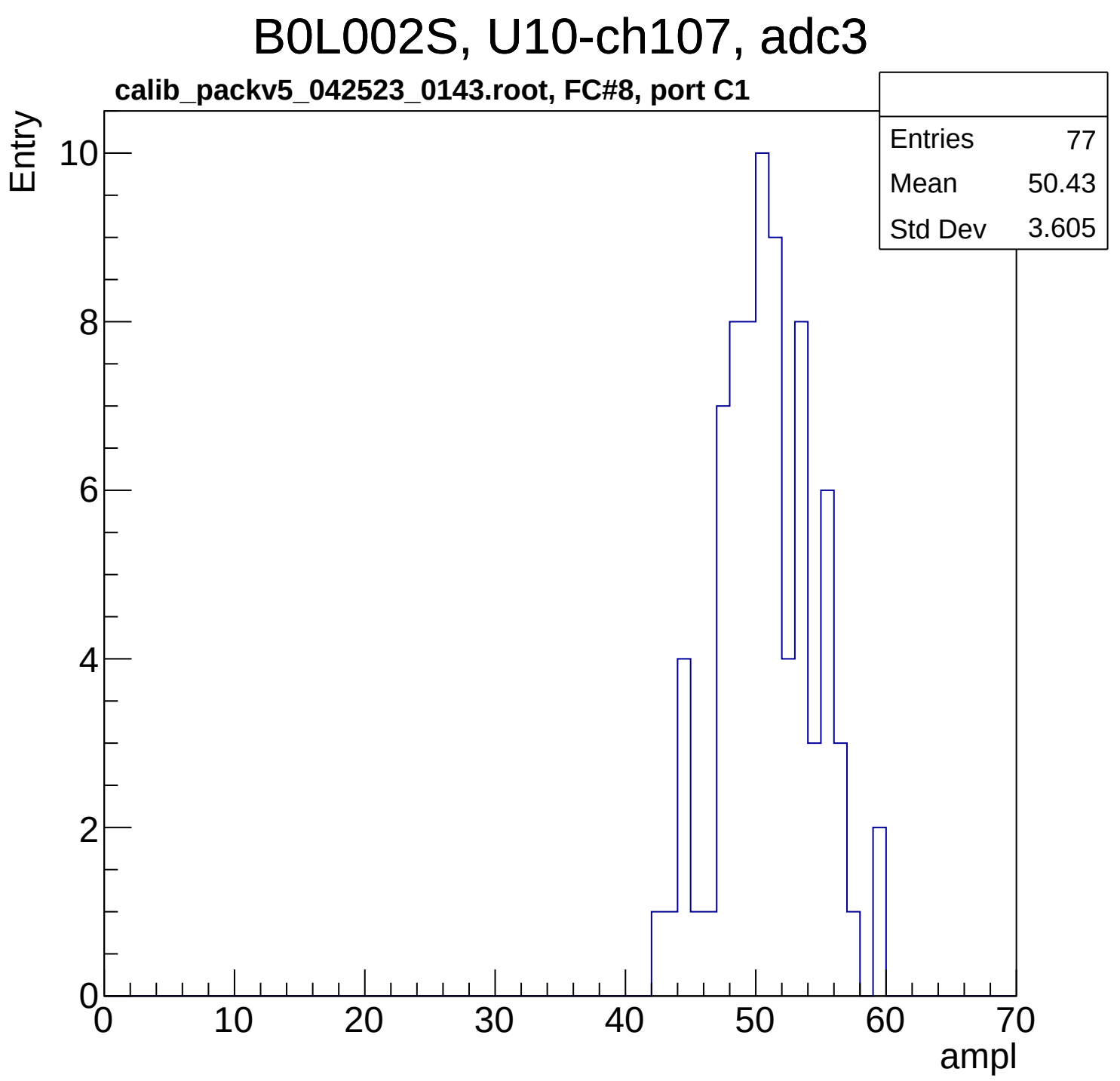

B0L002S, U10-ch106, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

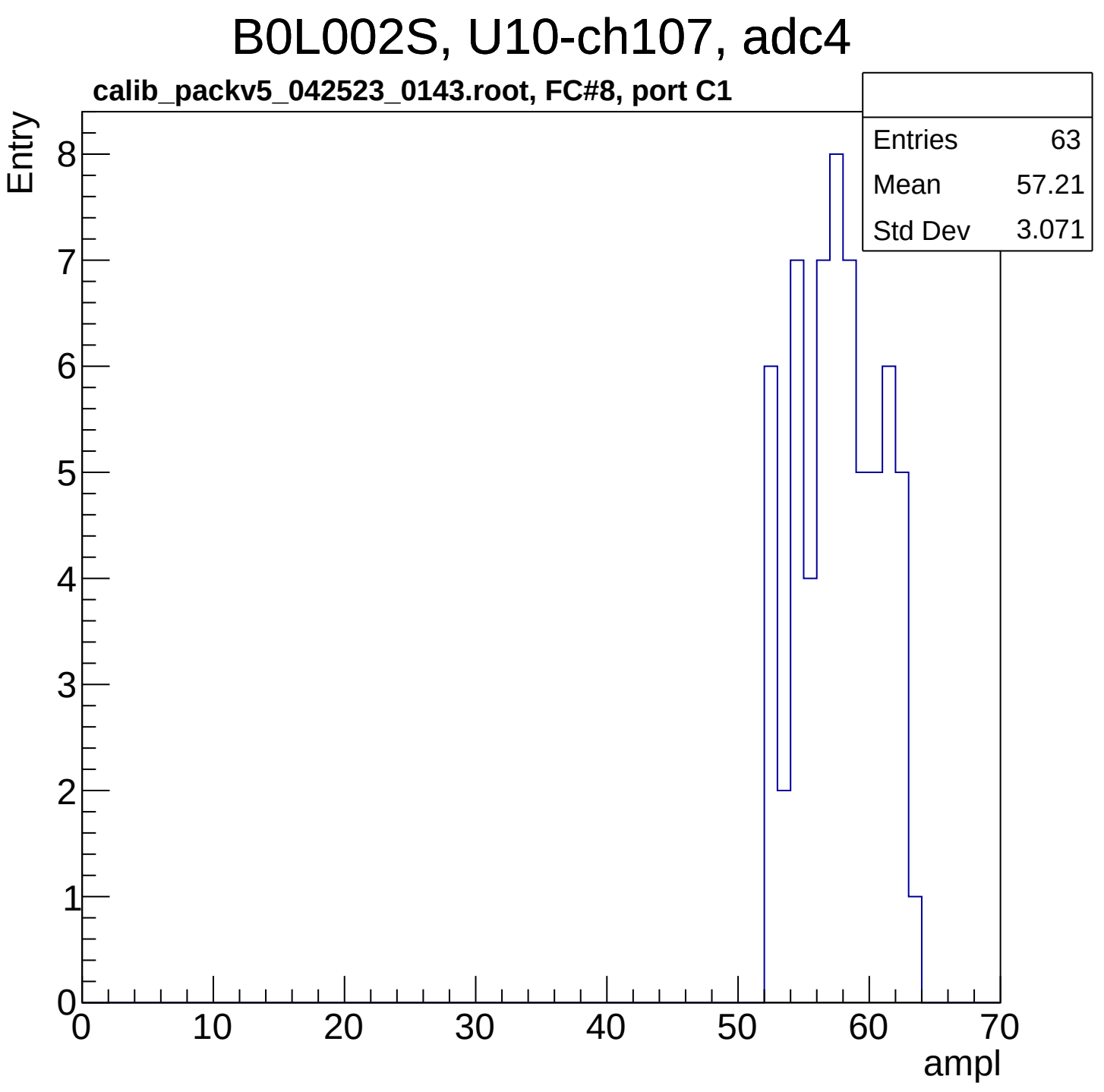

B0L002S, U10-ch106, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

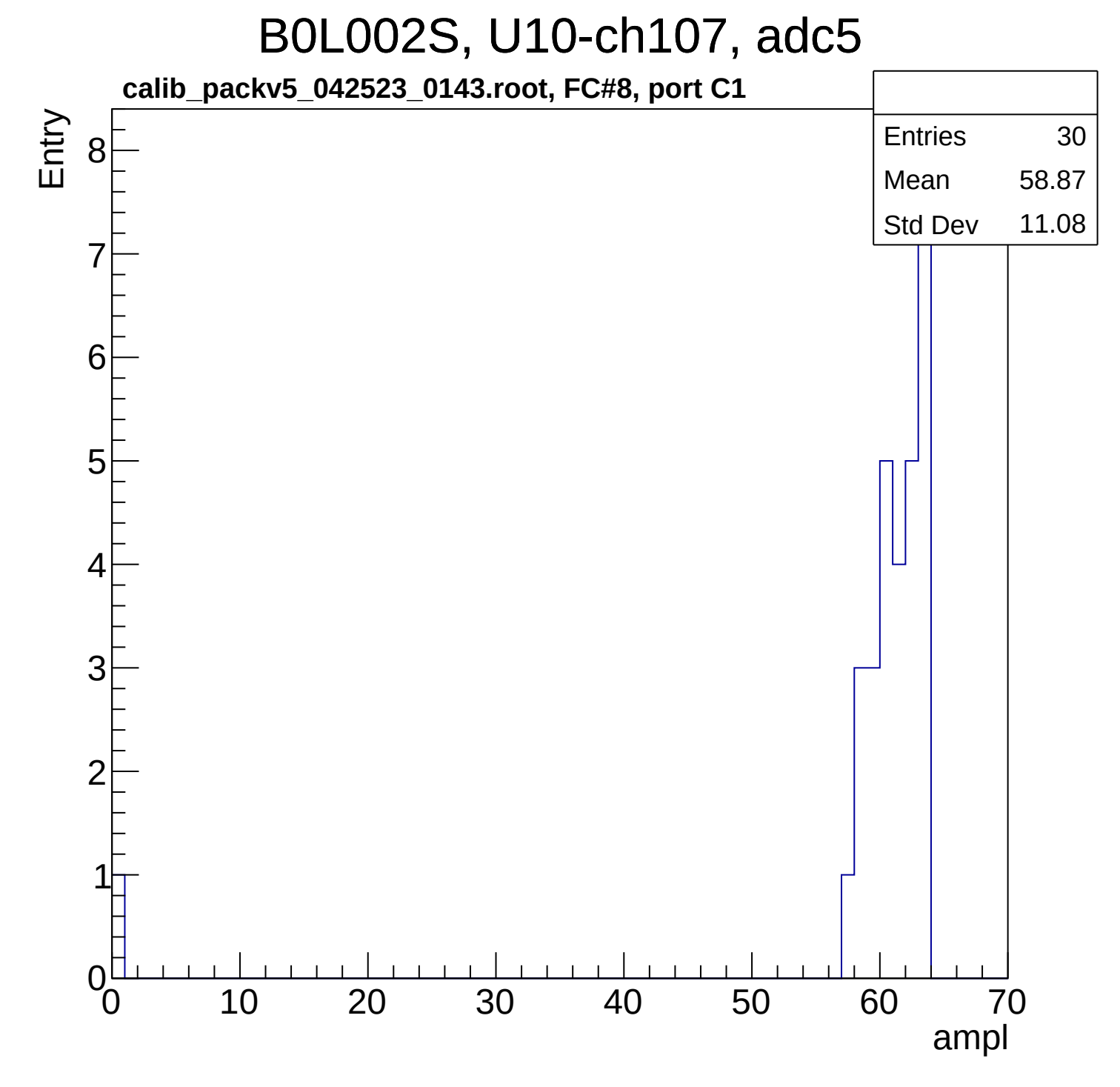

B0L002S, U10-ch107, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 29.21
Std Dev 3.031
Gaus mean : 29.6838
Gaus Width: 3.0717
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

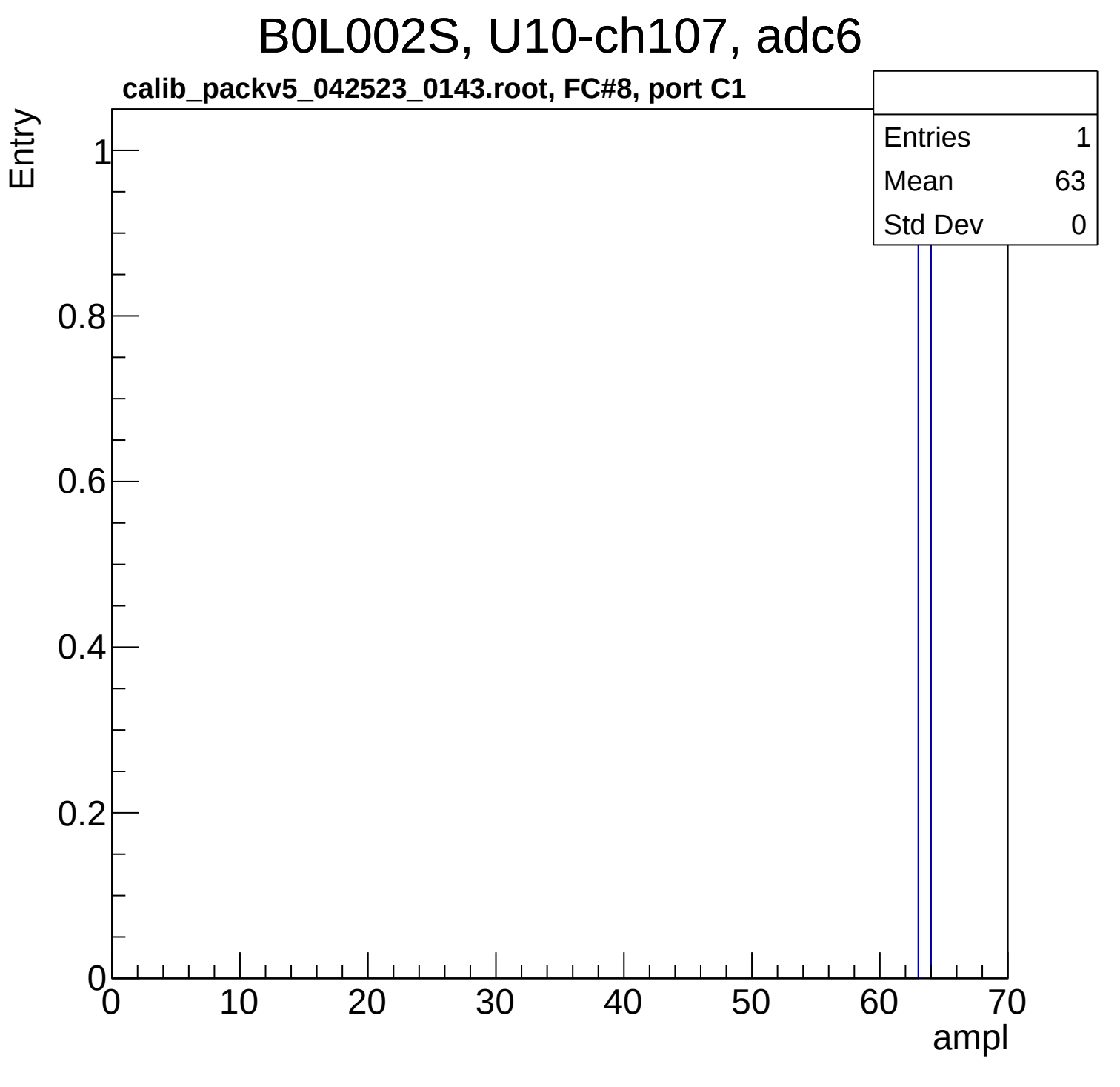

B0L002S, U10-ch107, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 36.09
Std Dev 2.973
Gaus mean : 36.8773
Gaus Width: 3.1915
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

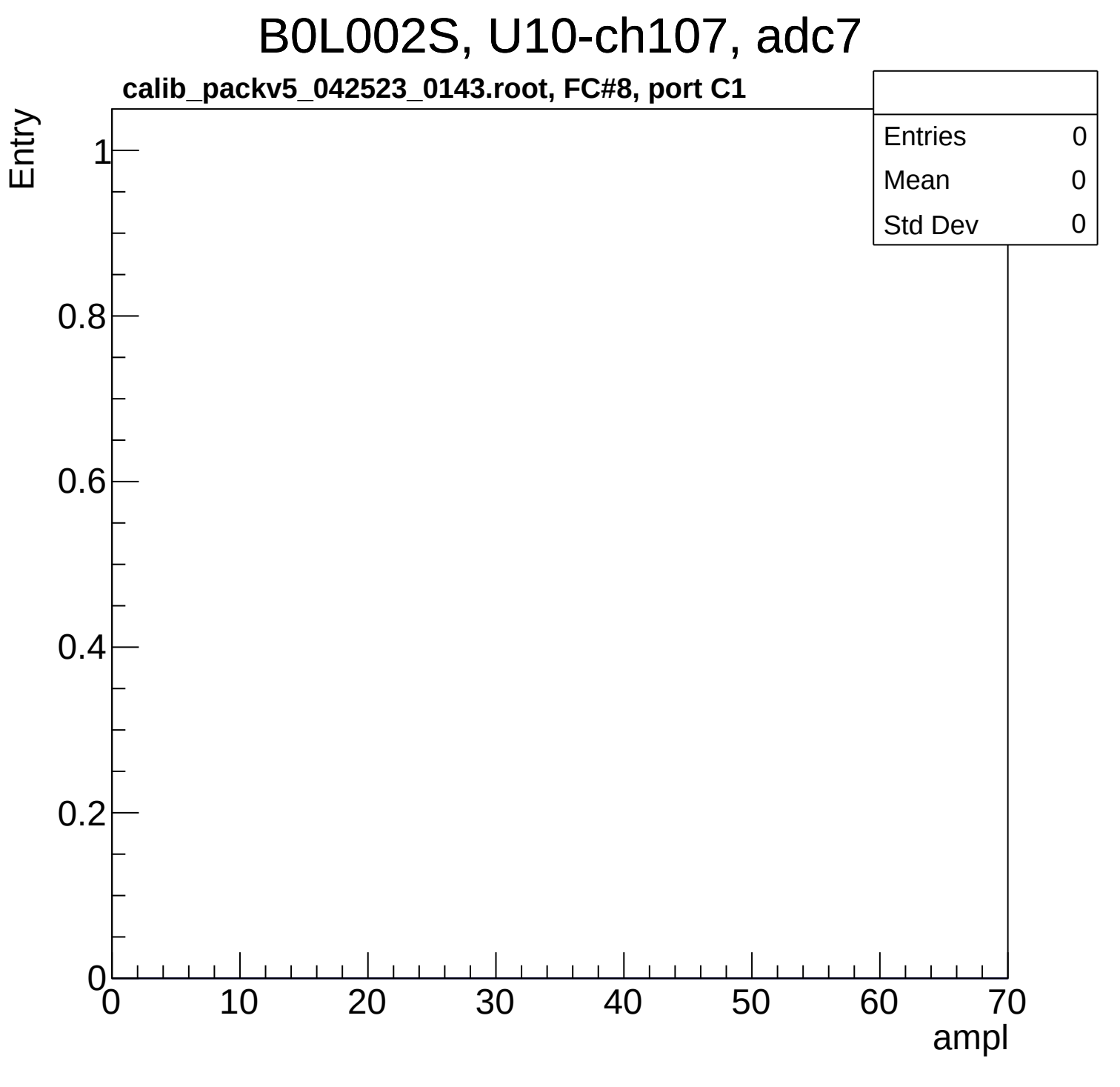

B0L002S, U10-ch107, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 42.35
Std Dev 3.623
Gaus mean : 42.8225
Gaus Width: 3.9524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

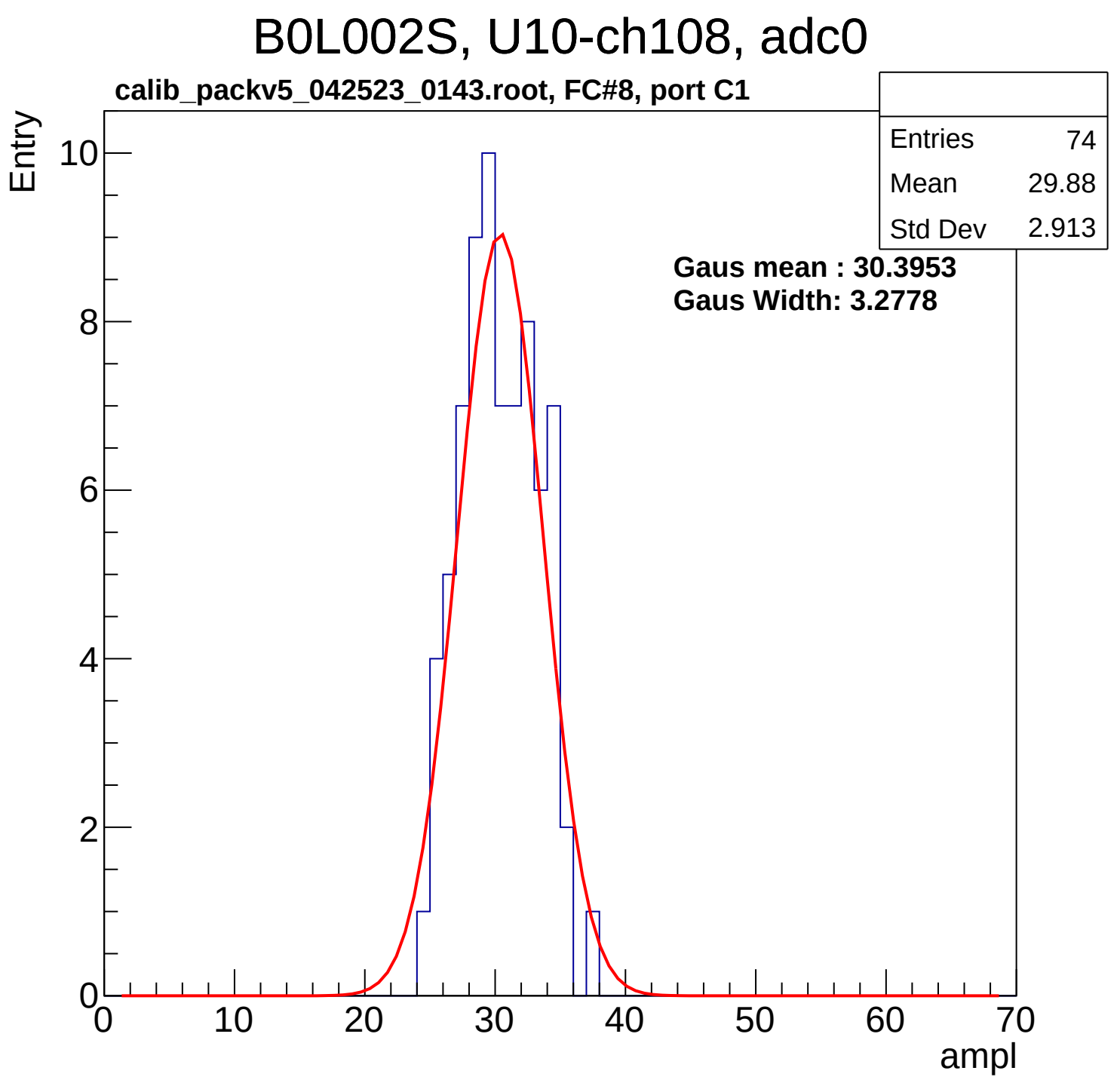

B0L002S, U10-ch107, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 50.43
Std Dev 3.605
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

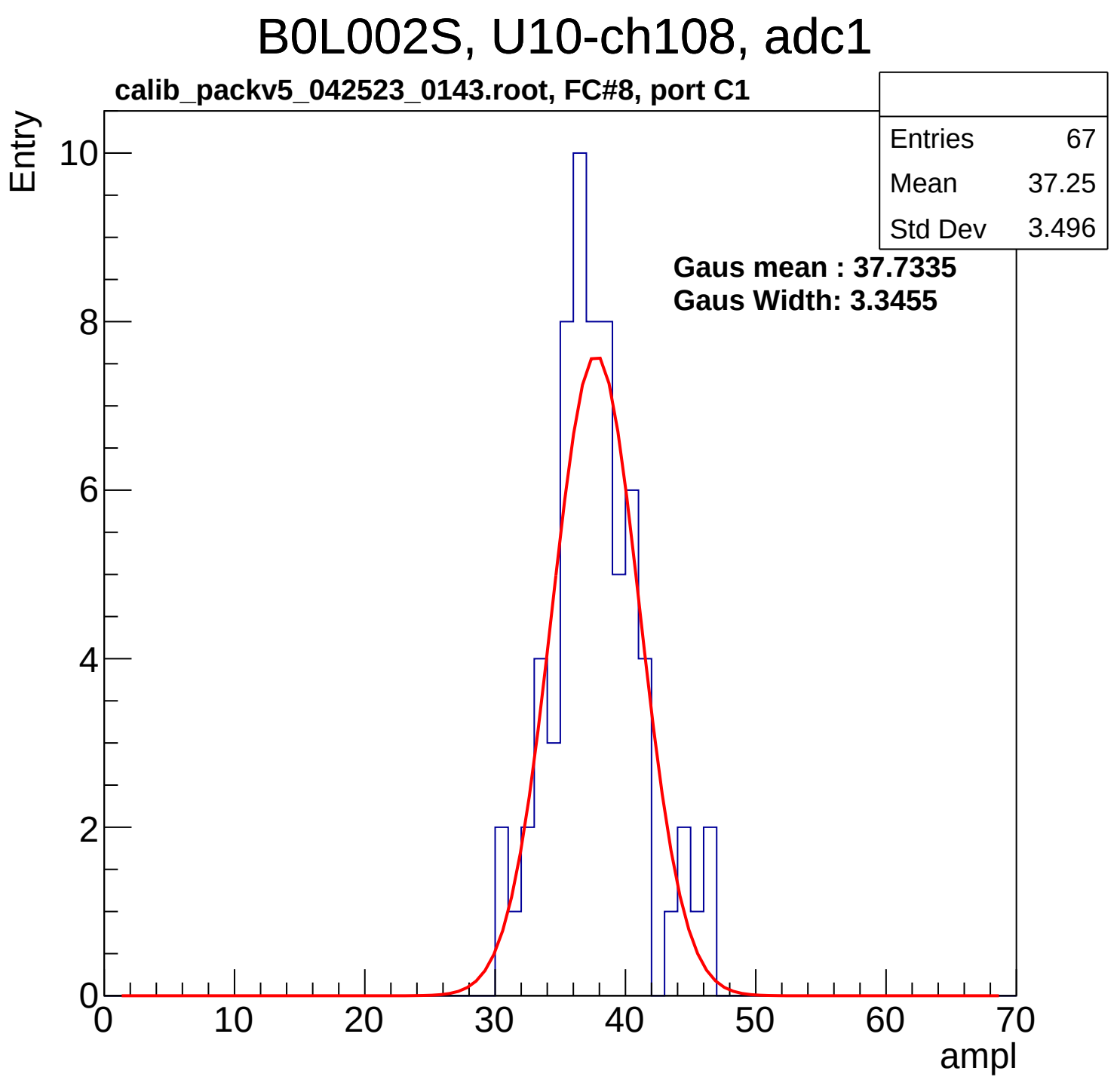

B0L002S, U10-ch107, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 57.21
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

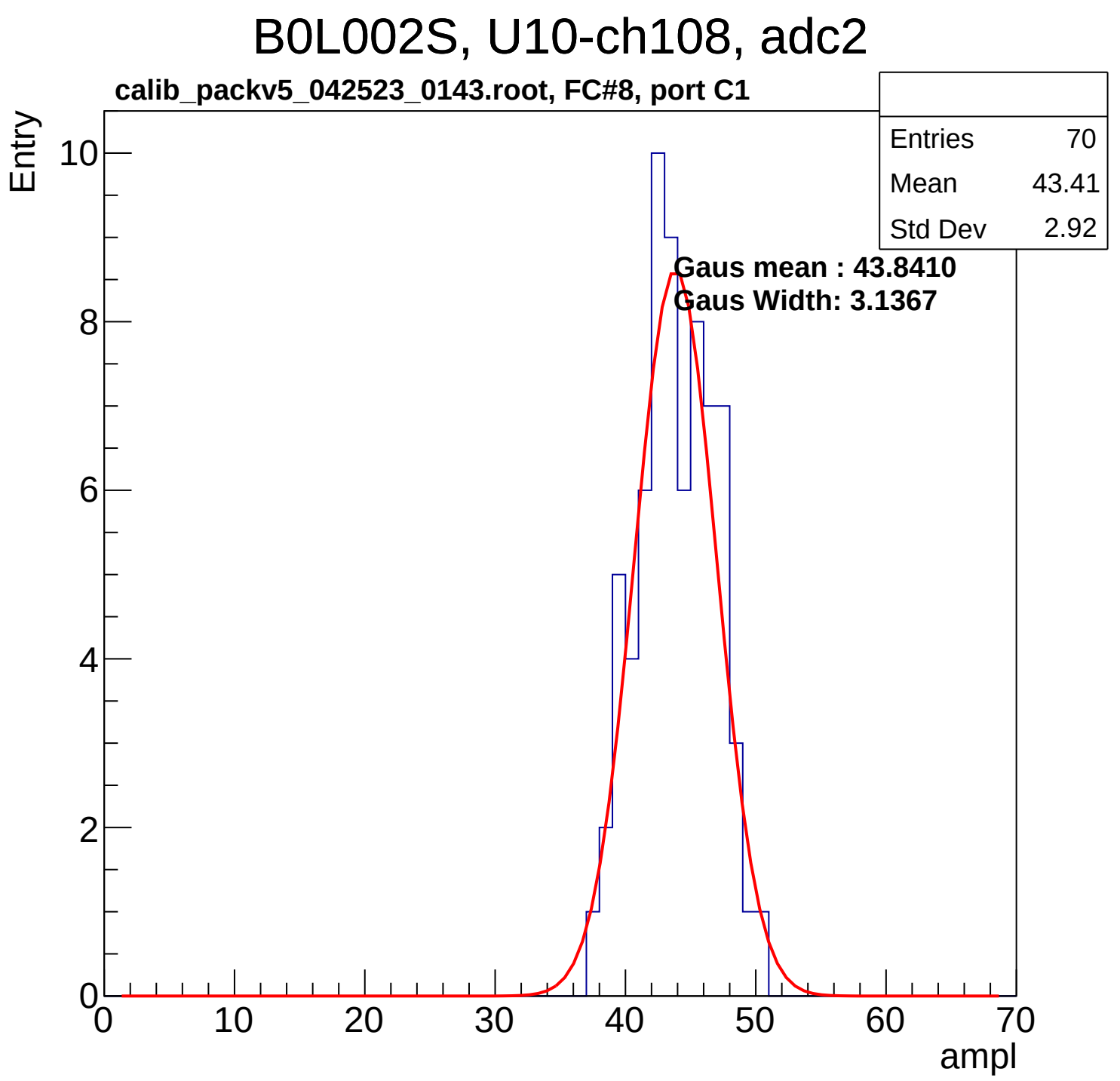

B0L002S, U10-ch107, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 30
Mean 58.87
Std Dev 11.08
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

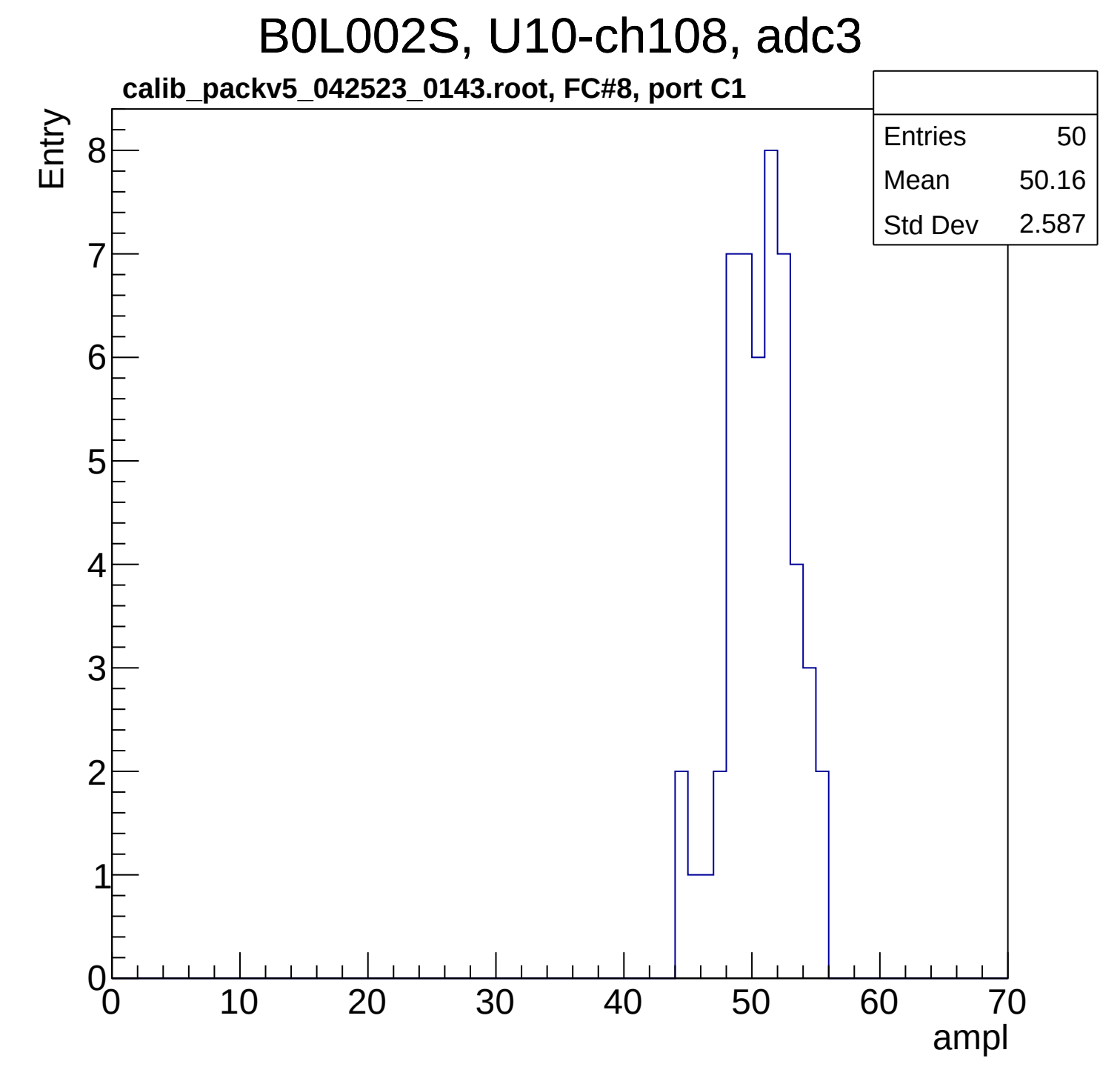

B0L002S, U10-ch107, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

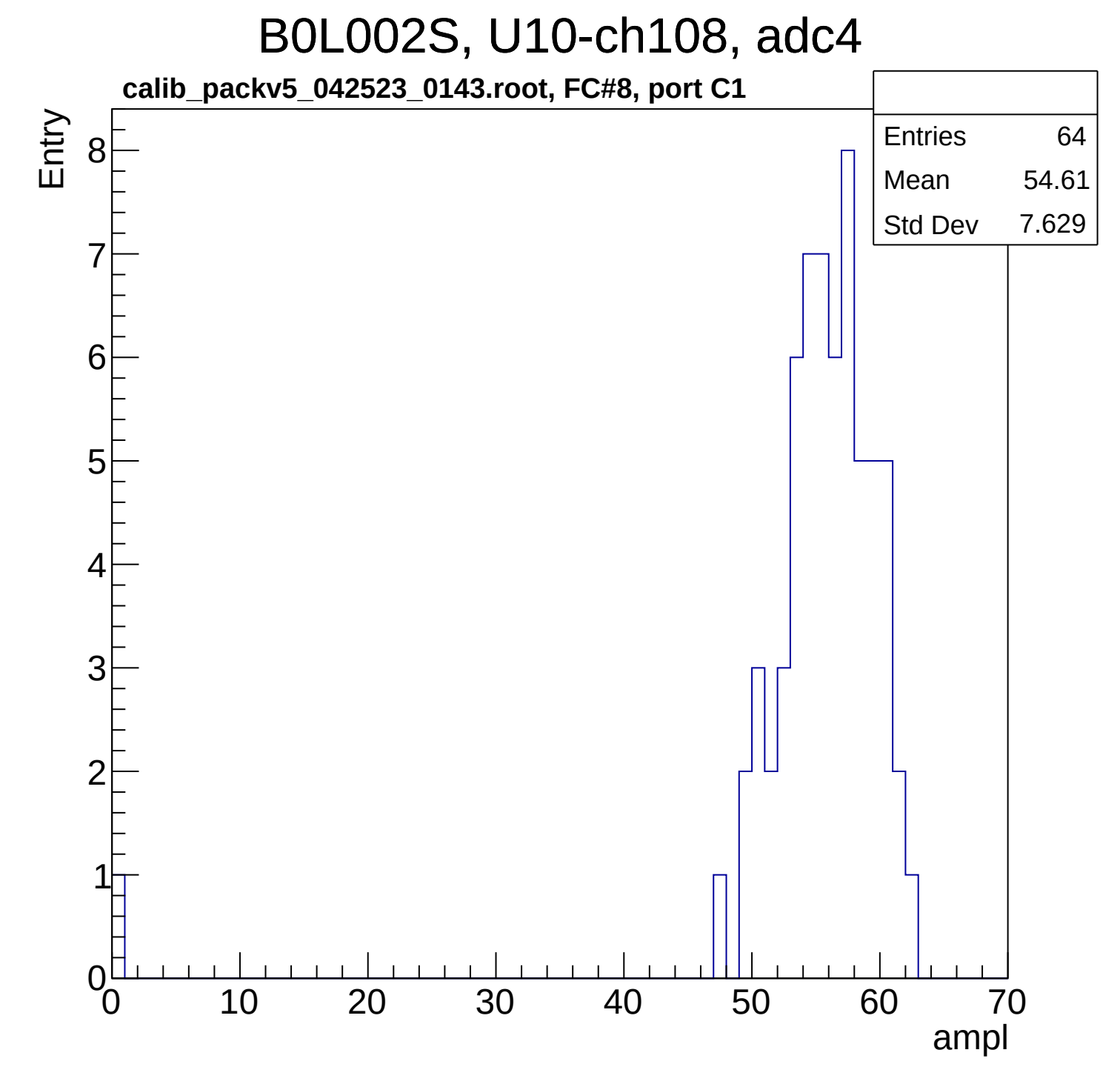

B0L002S, U10-ch107, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

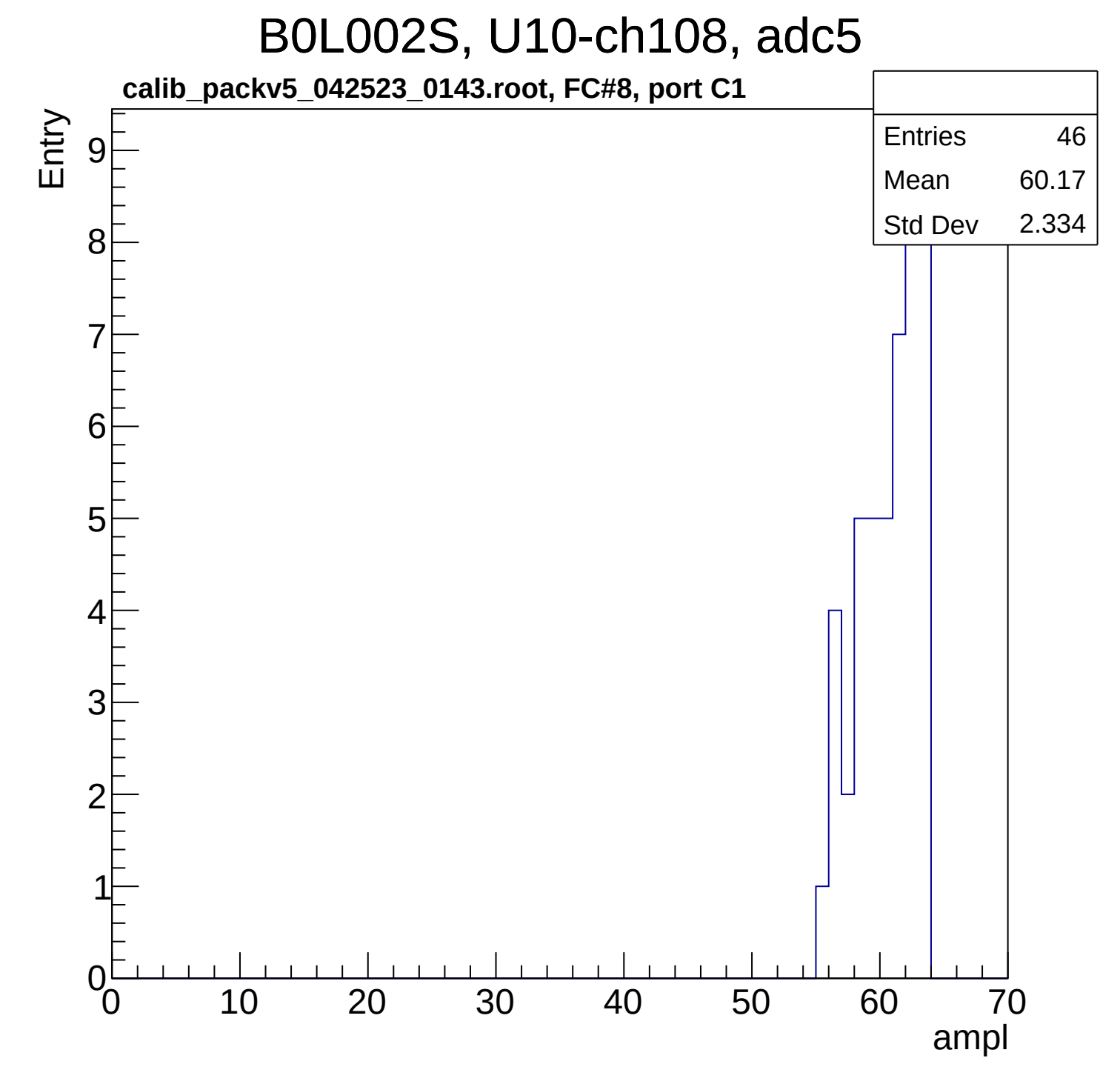

B0L002S, U10-ch108, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 74
Mean 29.88
Std Dev 2.913
Gaus mean : 30.3953
Gaus Width: 3.2778
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

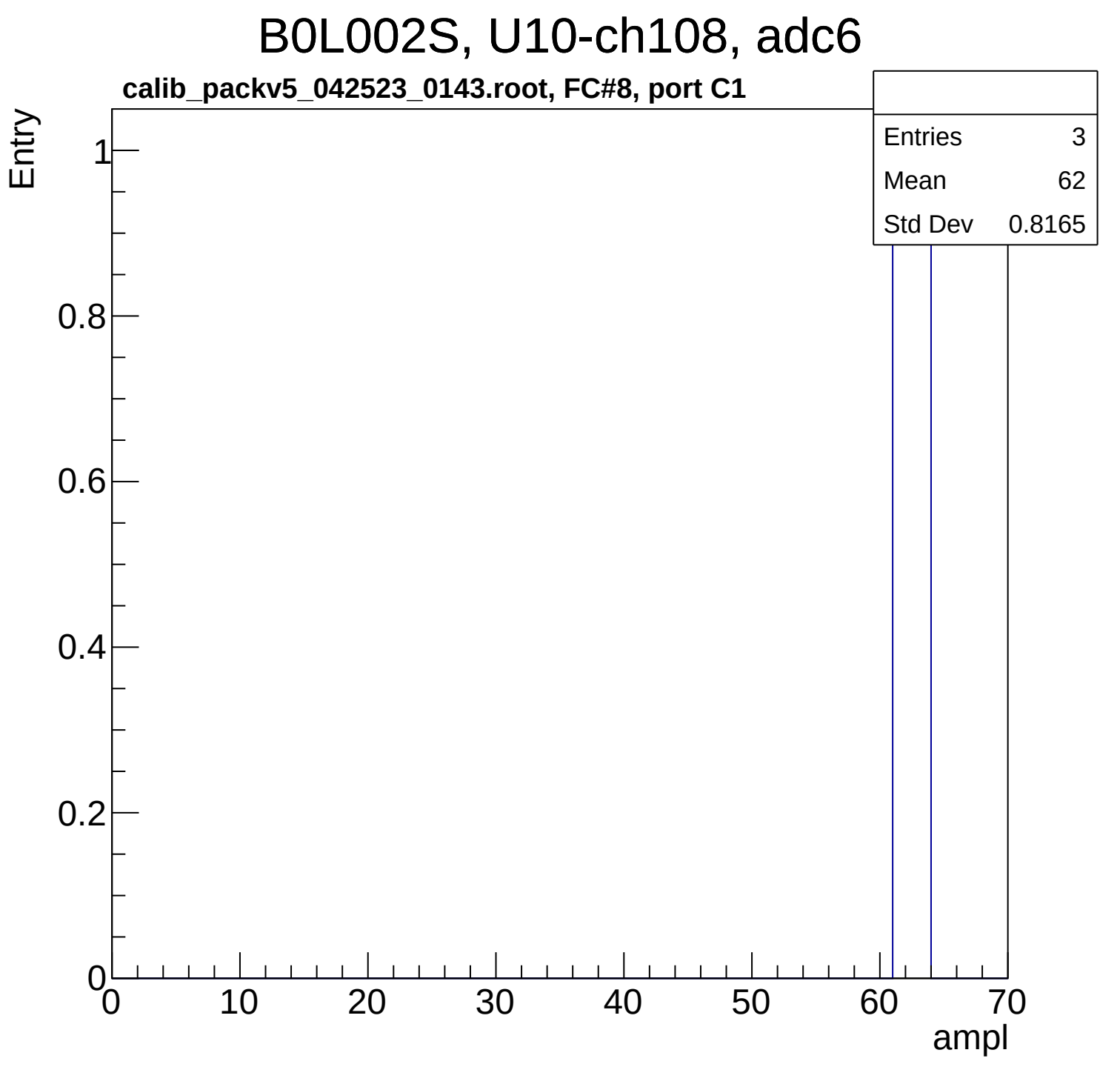

B0L002S, U10-ch108, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 37.25
Std Dev 3.496
Gaus mean : 37.7335
Gaus Width: 3.3455
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

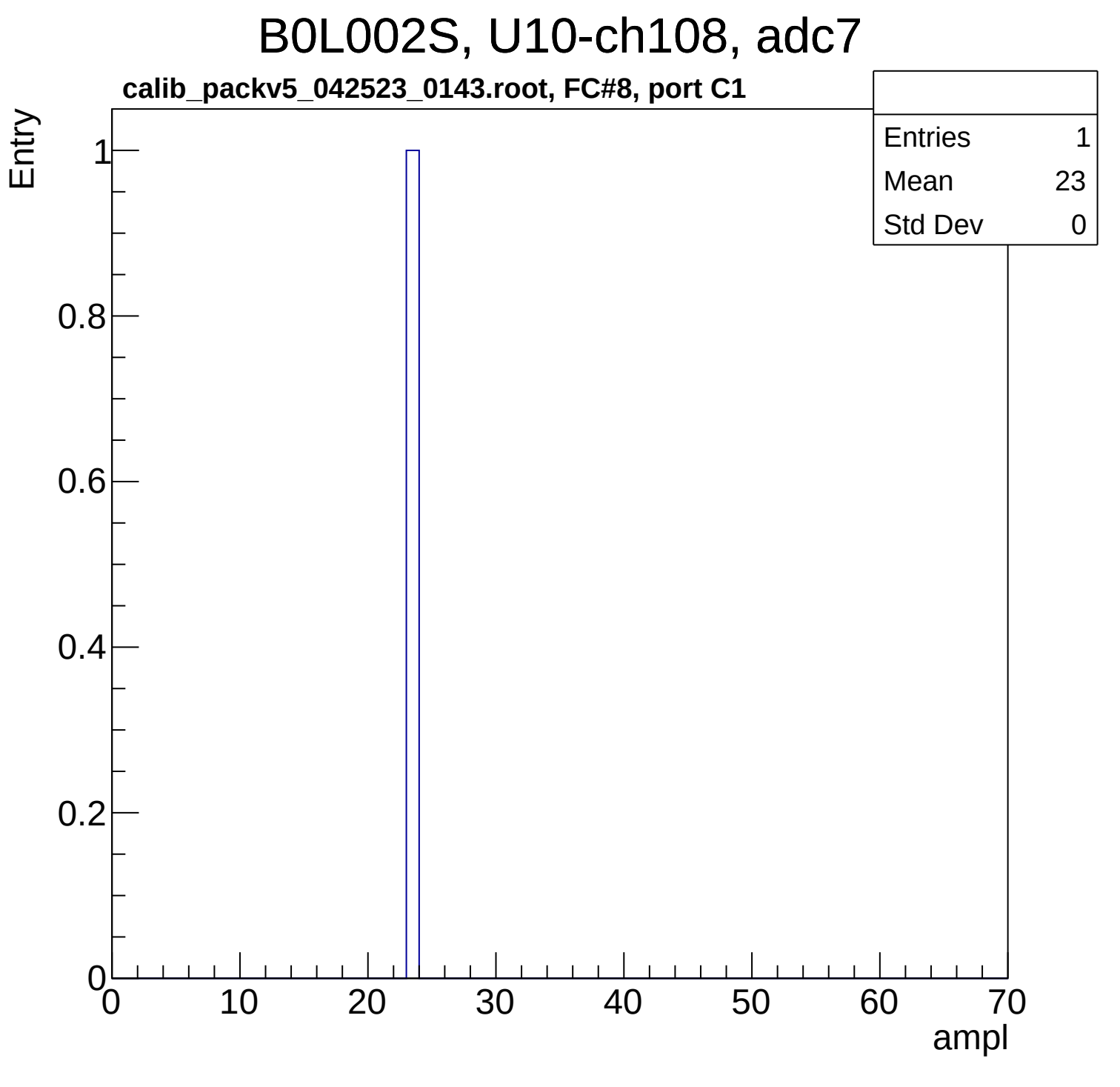

B0L002S, U10-ch108, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 43.41
Std Dev 2.92
Gaus mean : 43.8410
Gaus Width: 3.1367
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

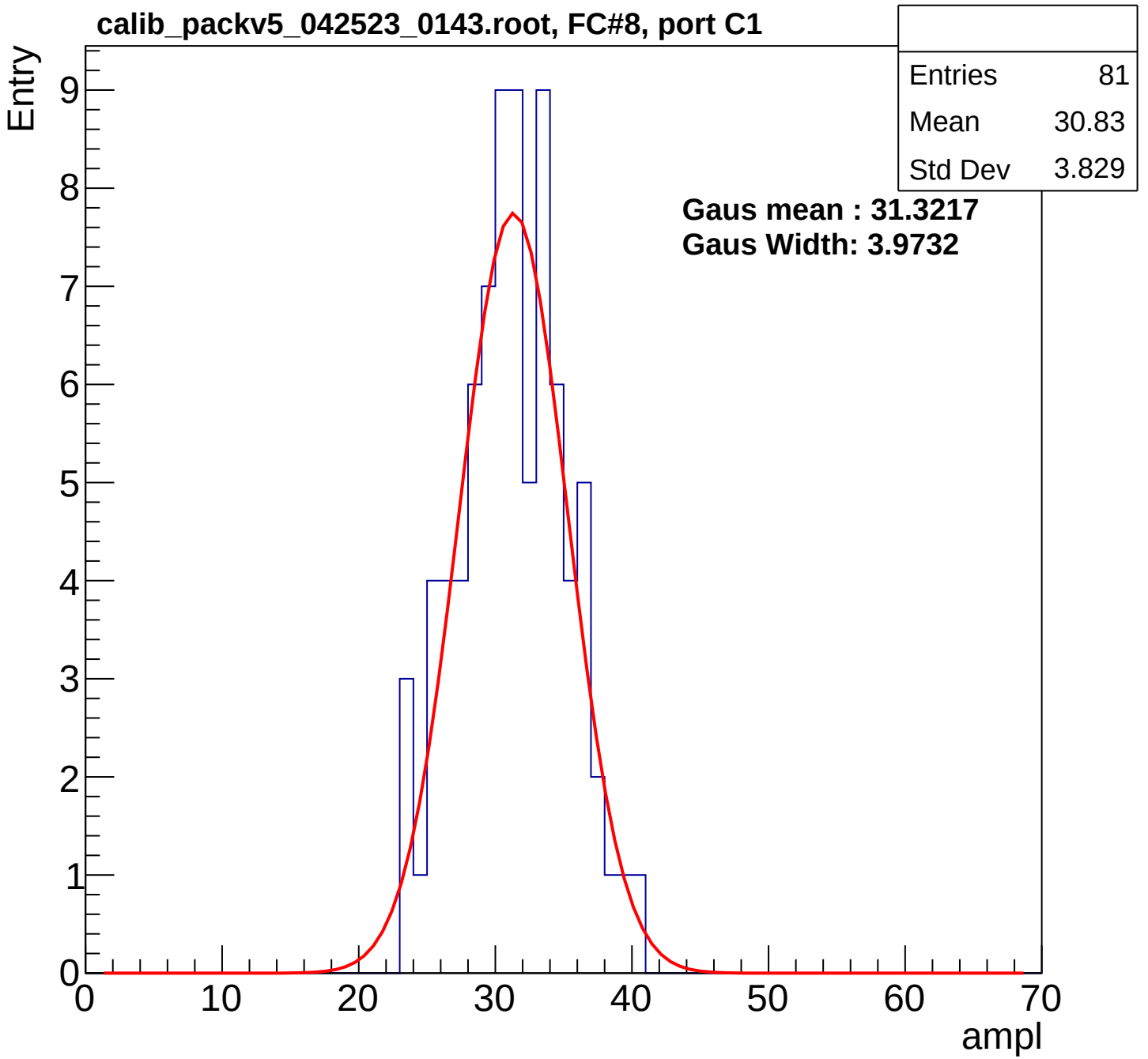

B0L002S, U10-ch108, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 50.16
Std Dev 2.587
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

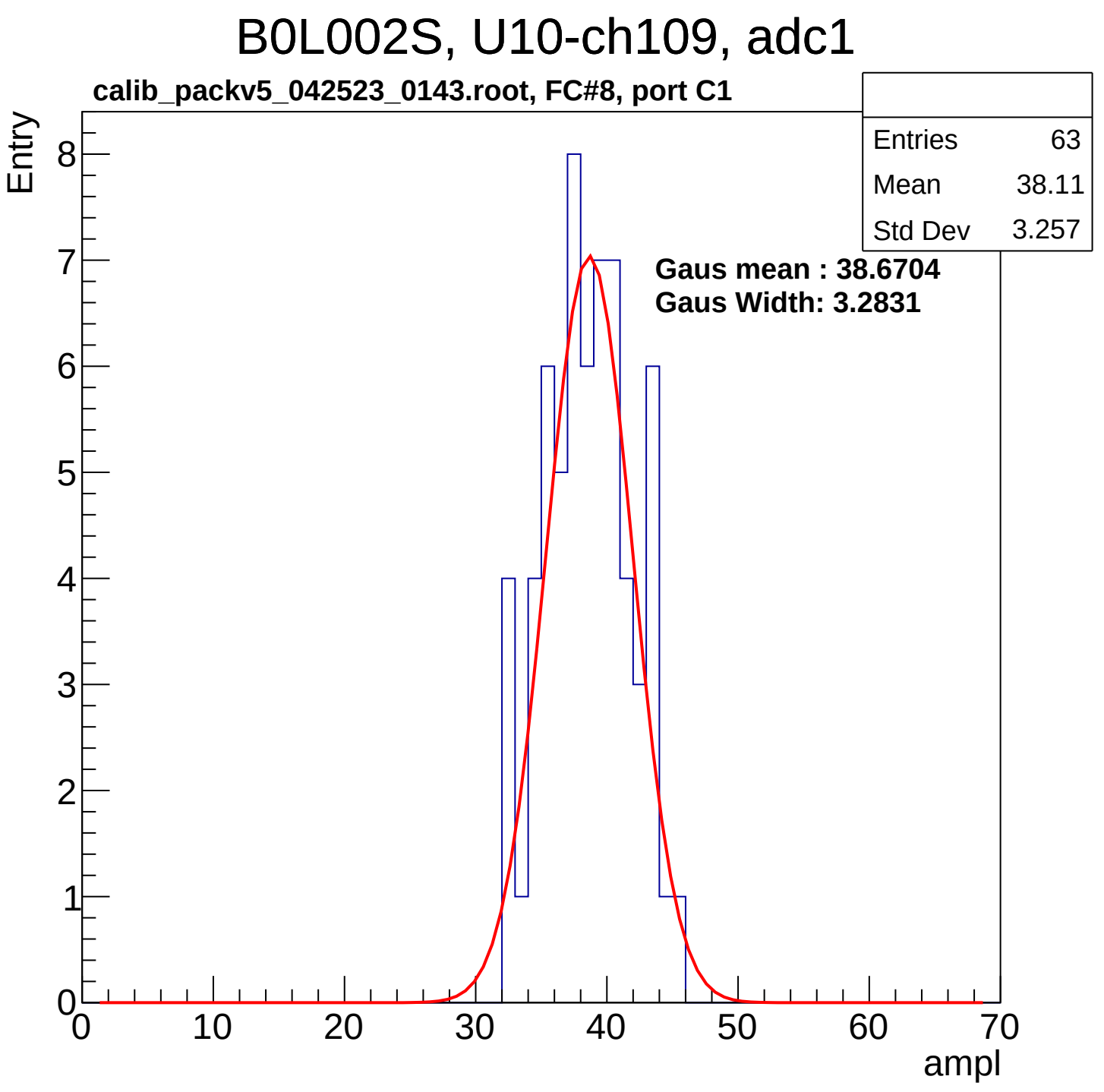

B0L002S, U10-ch108, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 54.61
Std Dev 7.629
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

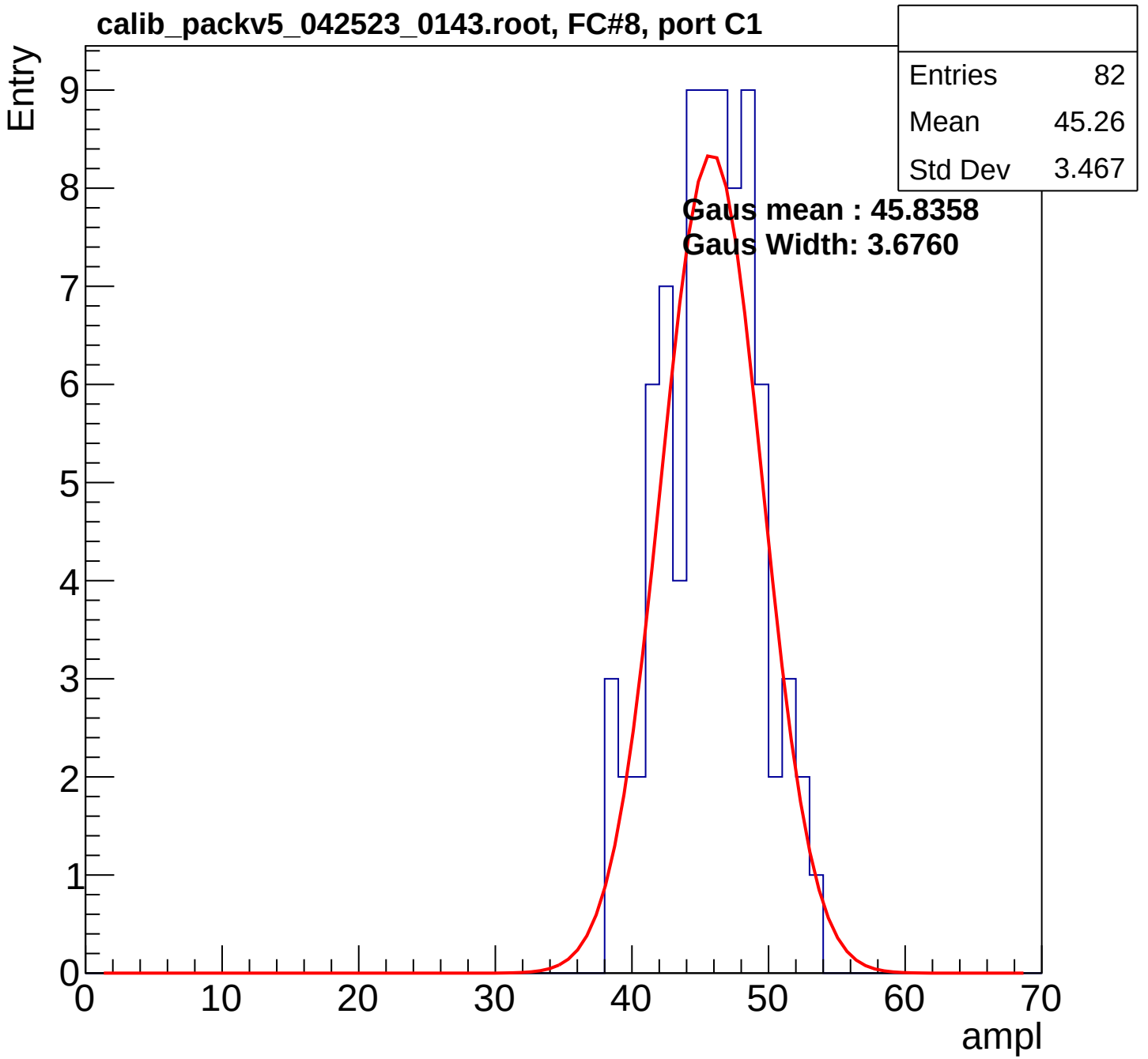

B0L002S, U10-ch108, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 60.17
Std Dev 2.334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

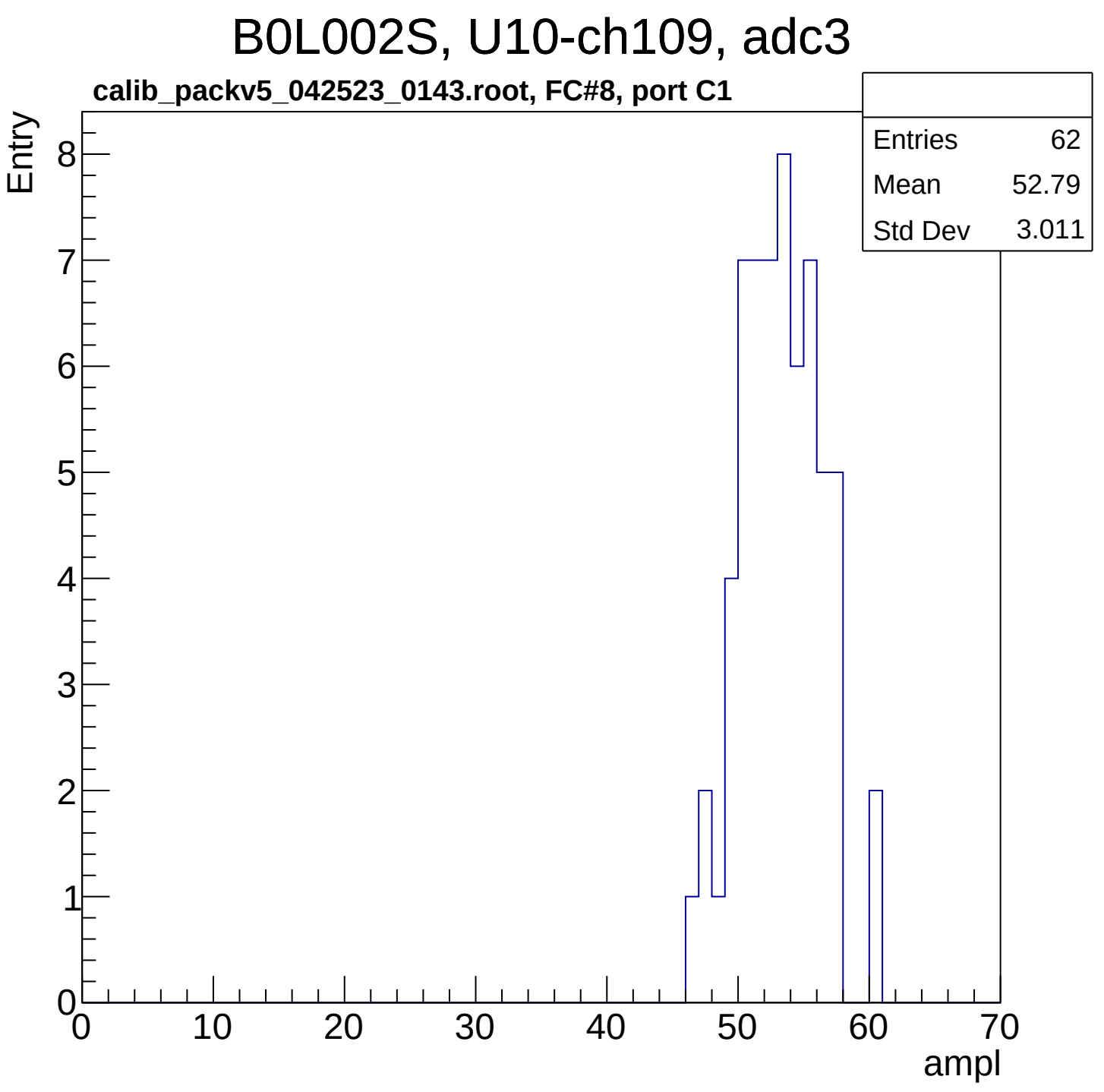

B0L002S, U10-ch108, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

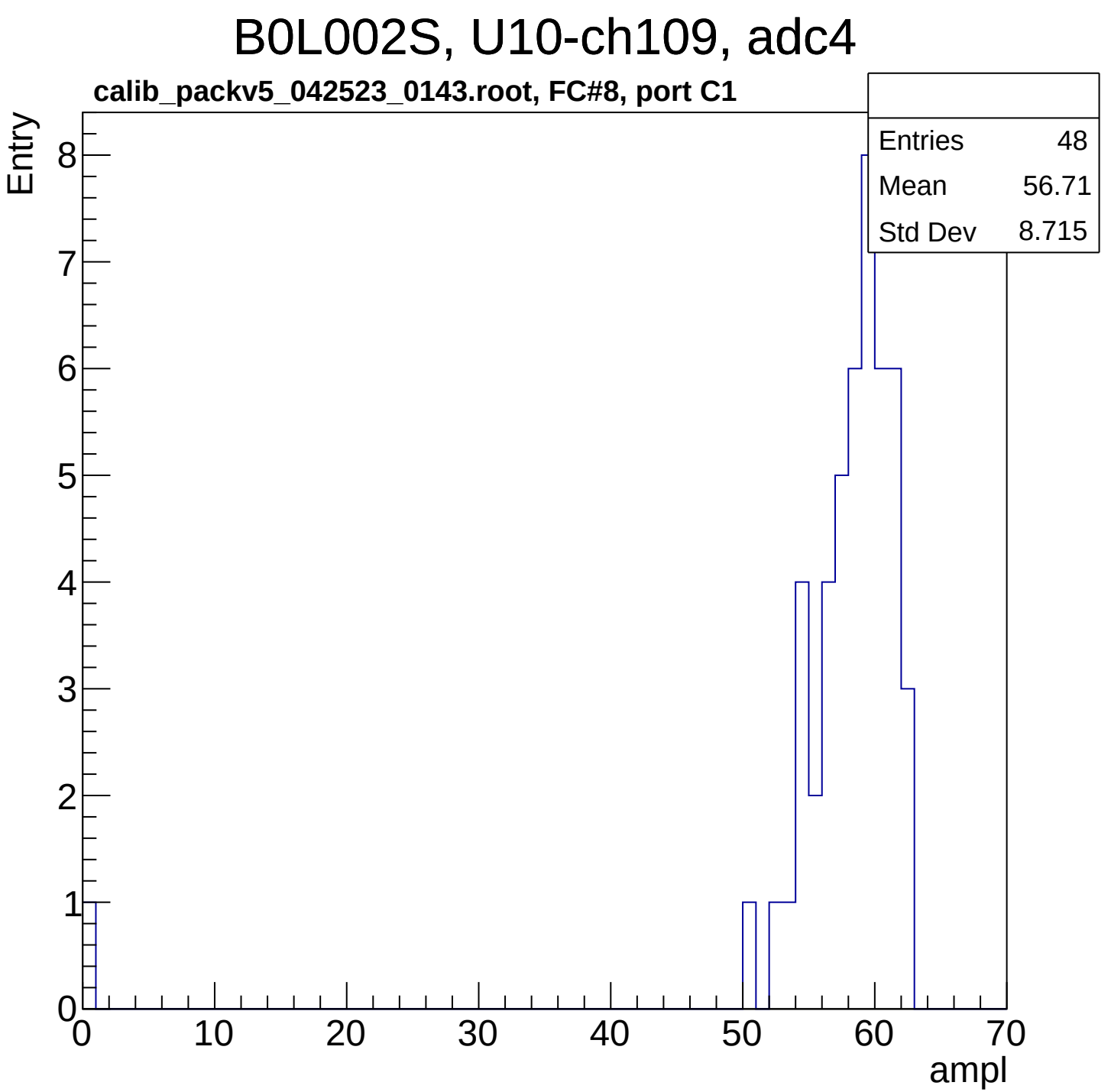

B0L002S, U10-ch108, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

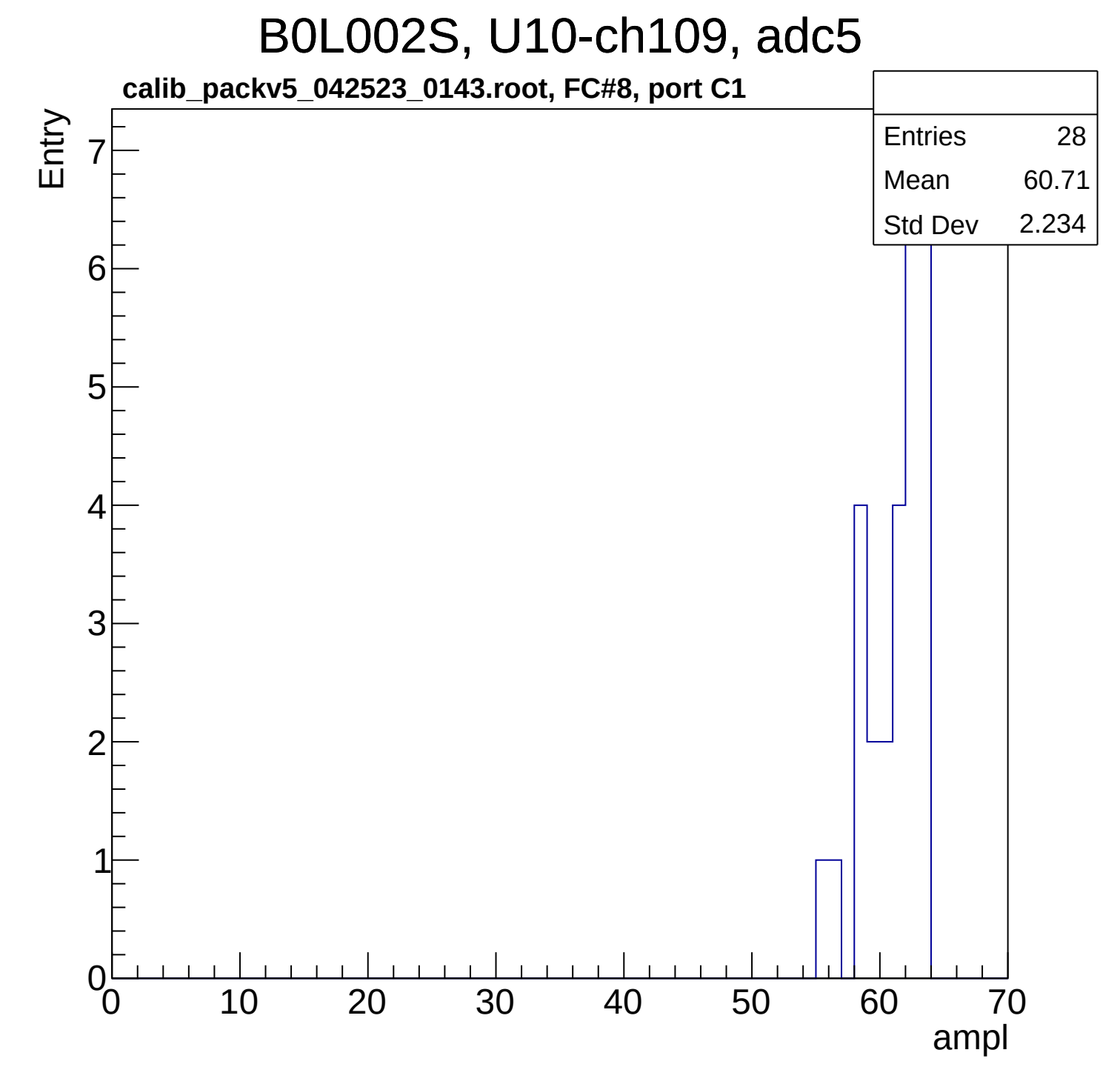

B0L002S, U10-ch109, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 30.83
Std Dev 3.829
Gaus mean : 31.3217
Gaus Width: 3.9732
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

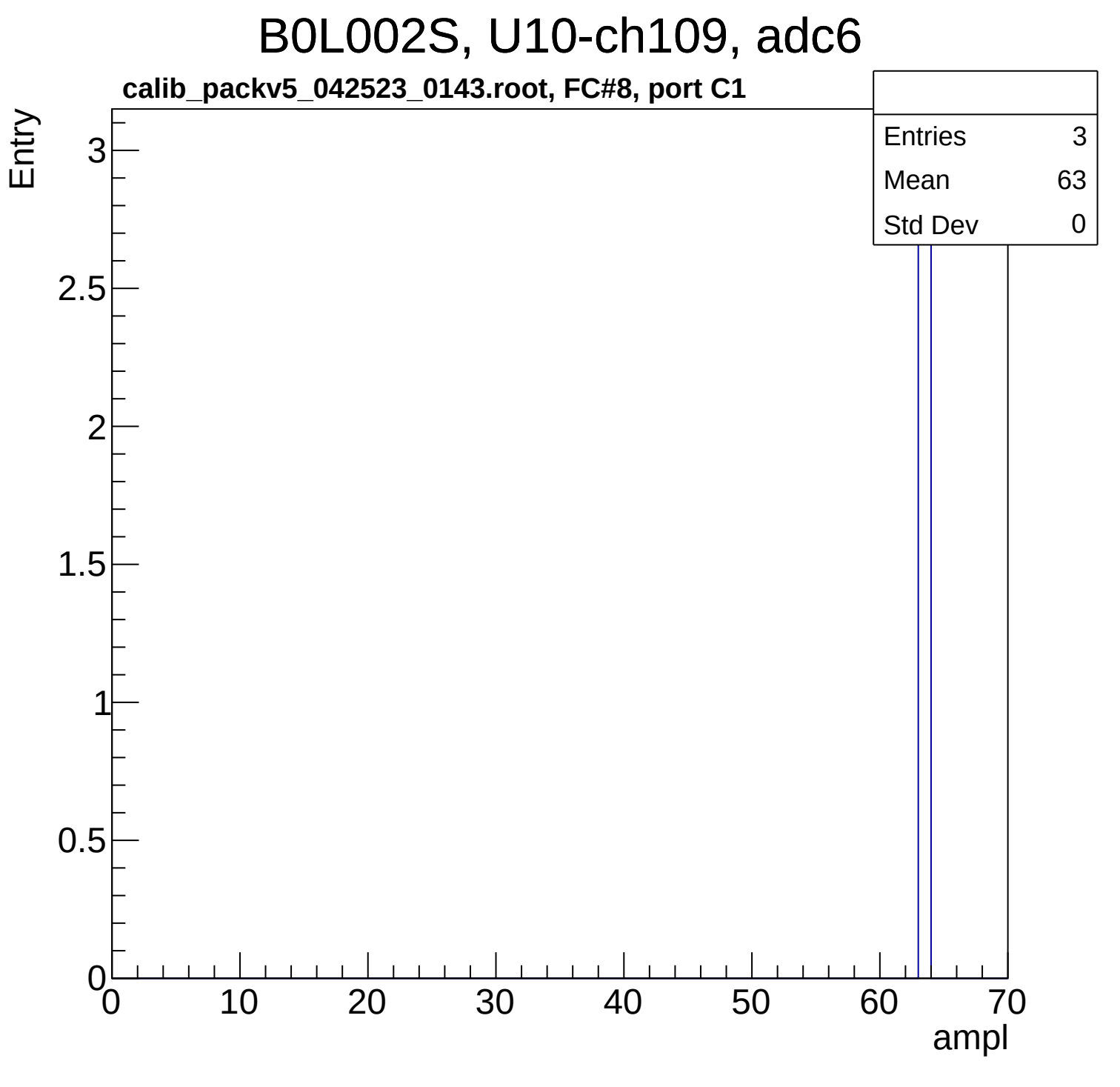

B0L002S, U10-ch109, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.11
Std Dev 3.257
Gaus mean : 38.6704
Gaus Width: 3.2831
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

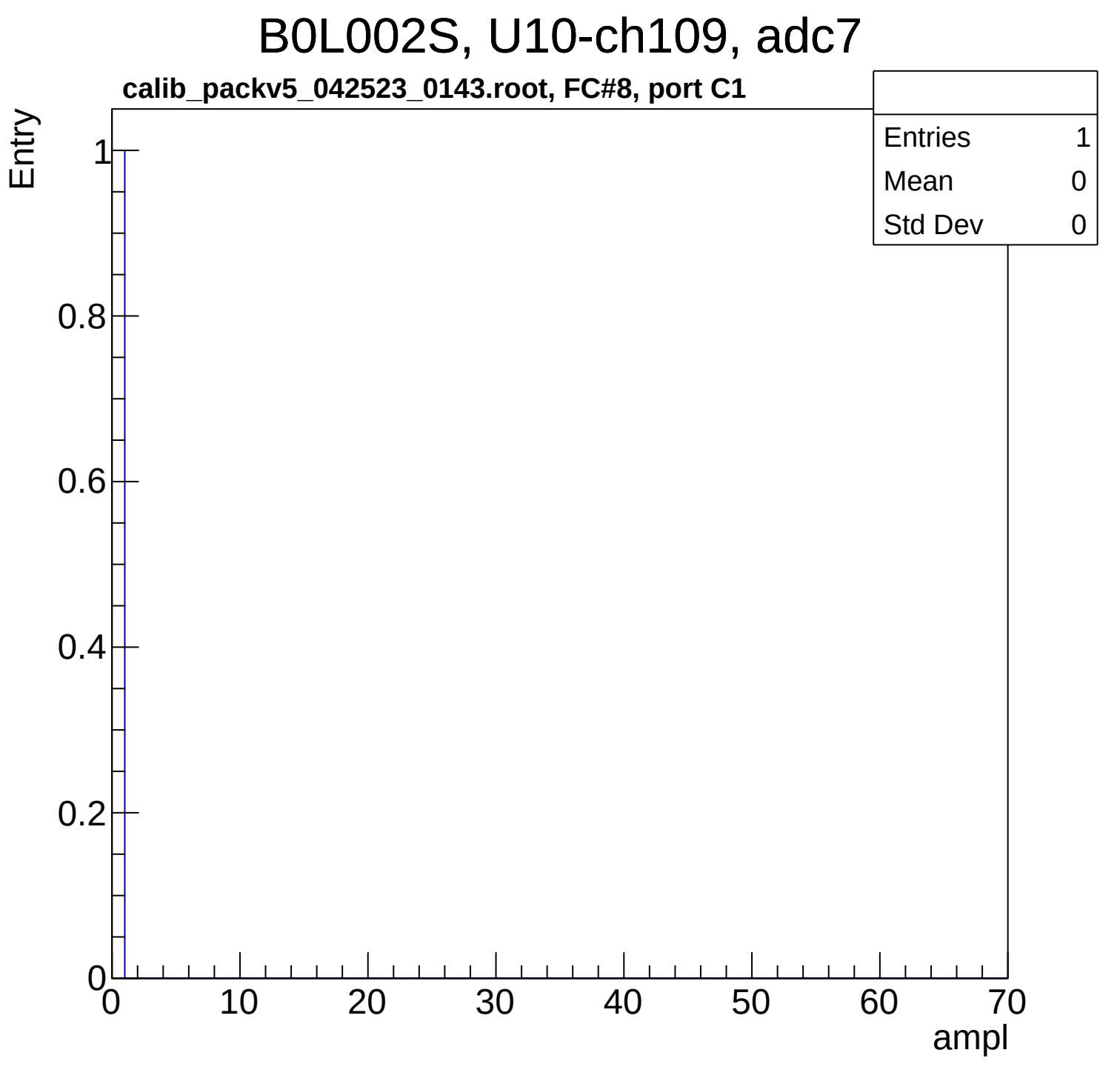

B0L002S, U10-ch109, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 82
Mean 45.26
Std Dev 3.467
Gaus mean : 45.8358
Gaus Width: 3.6760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

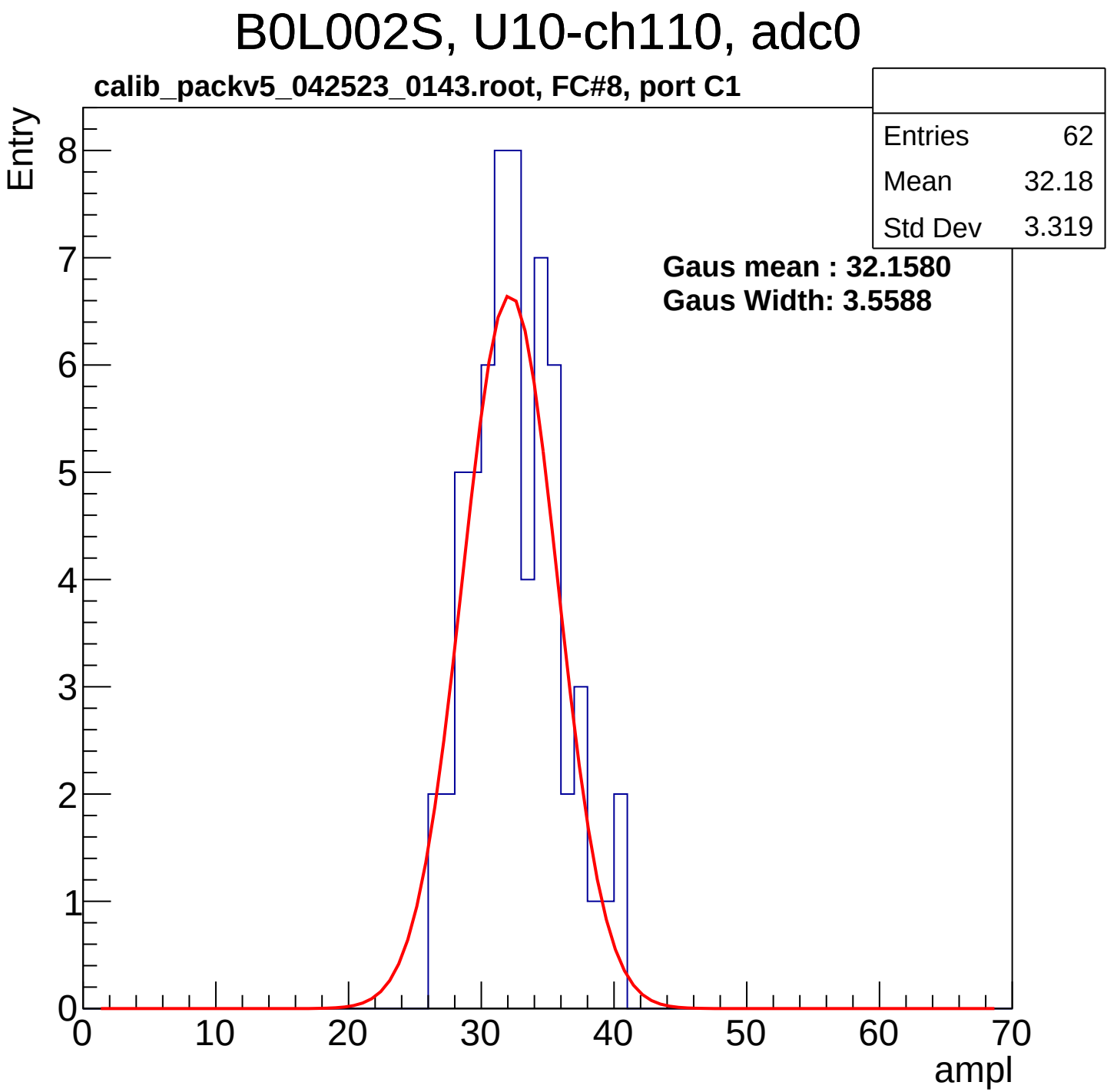

B0L002S, U10-ch109, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 52.79
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

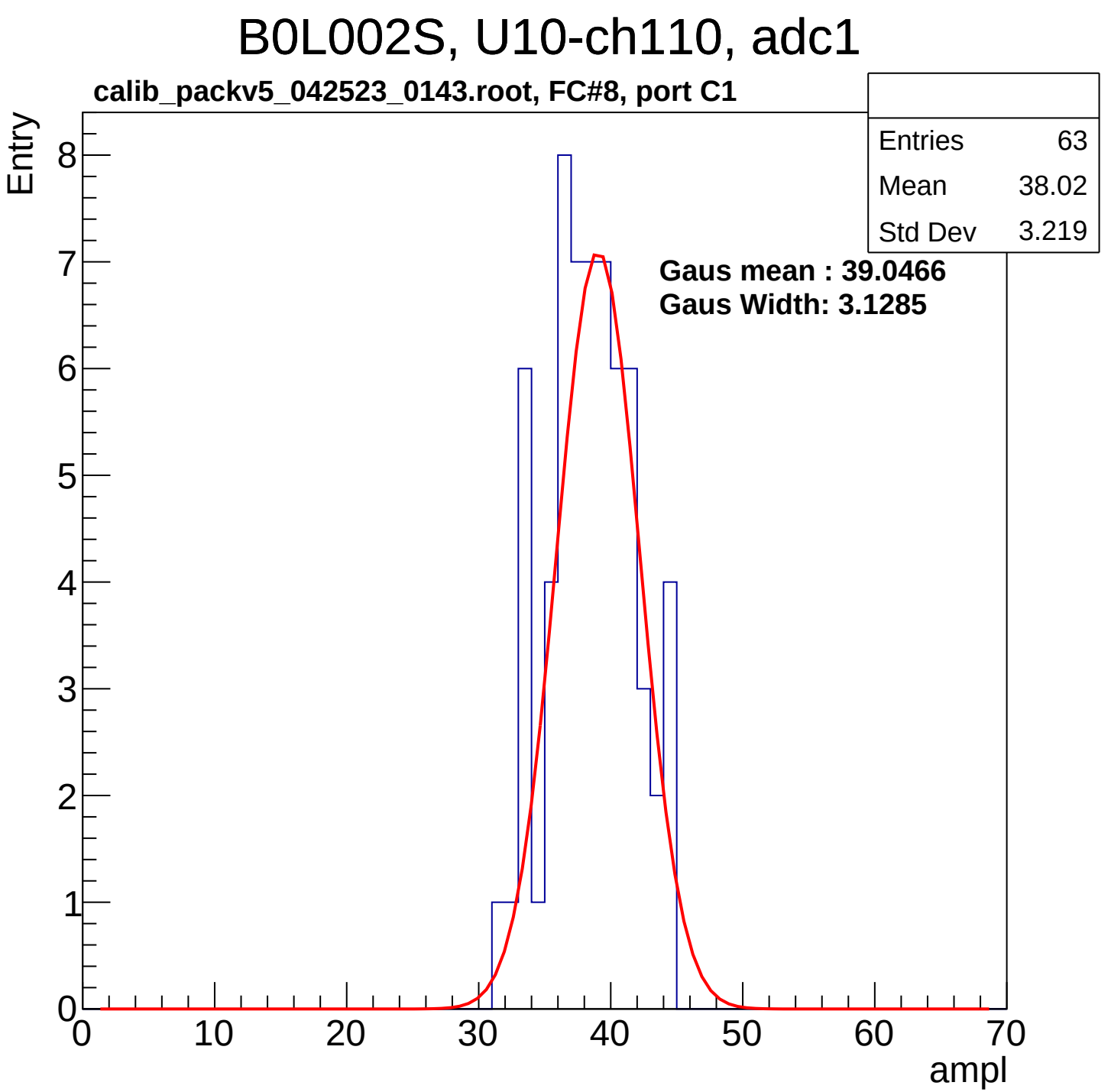

B0L002S, U10-ch109, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 56.71
Std Dev 8.715
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

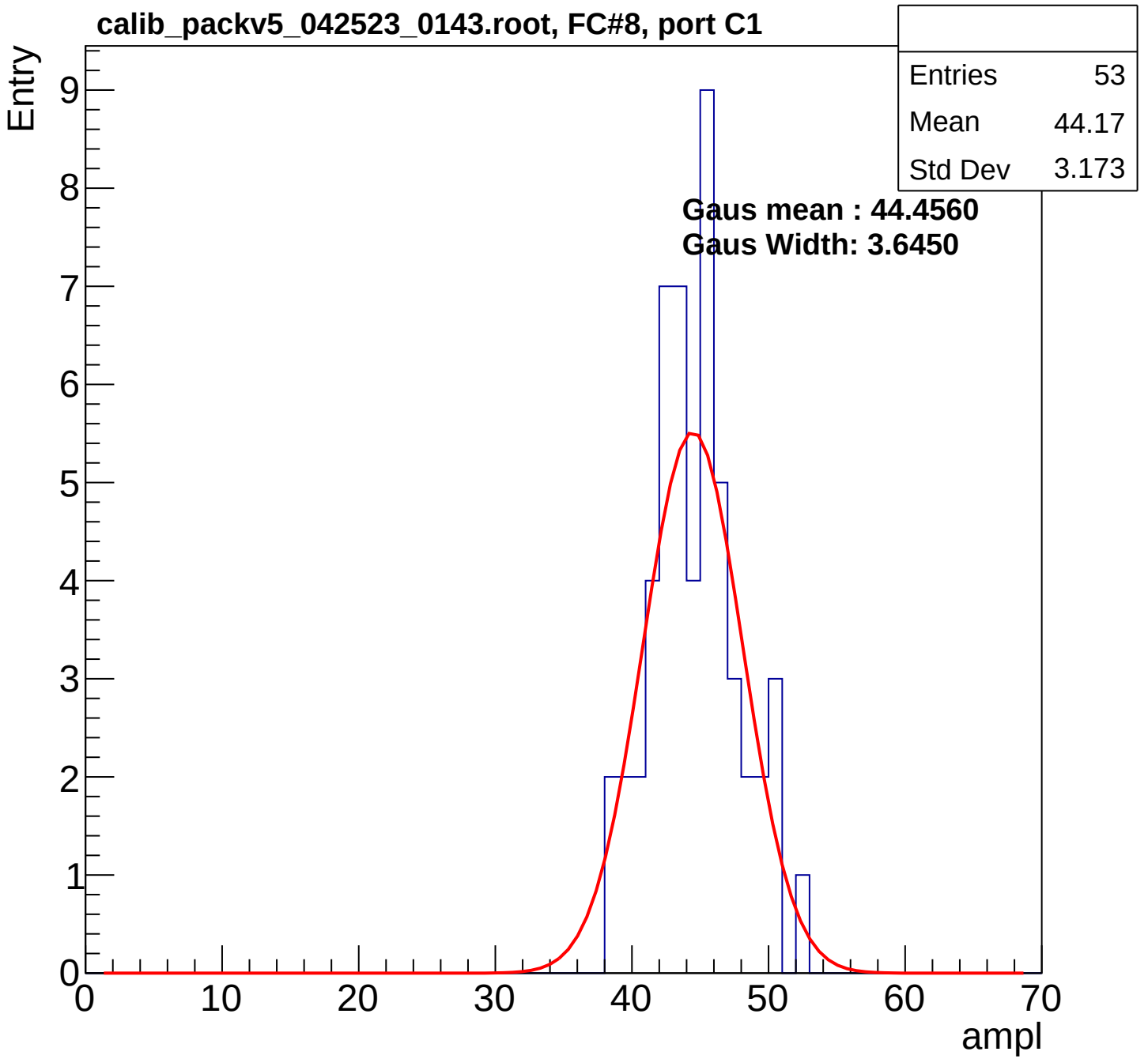

B0L002S, U10-ch109, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 28
Mean 60.71
Std Dev 2.234
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

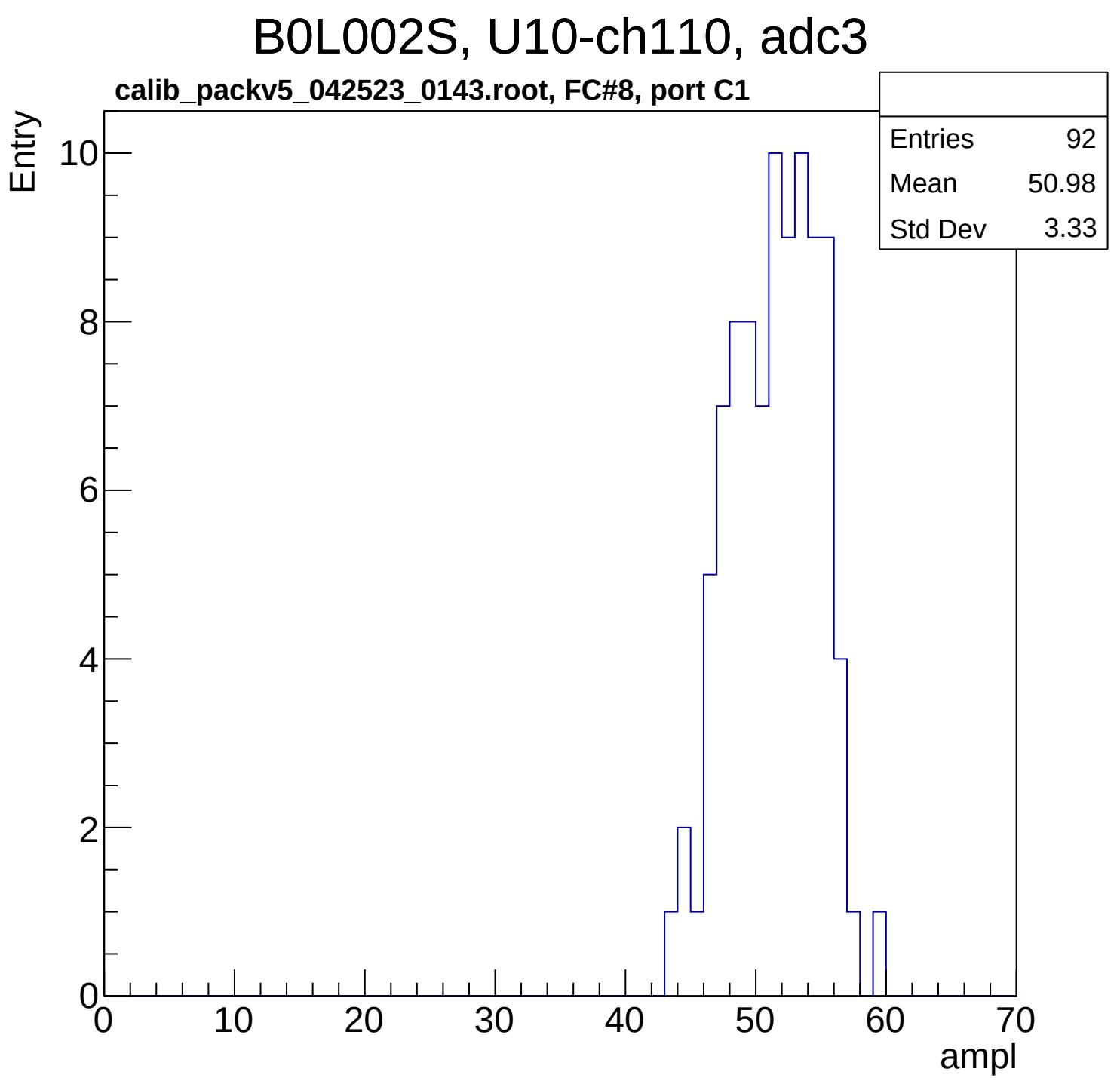

B0L002S, U10-ch109, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

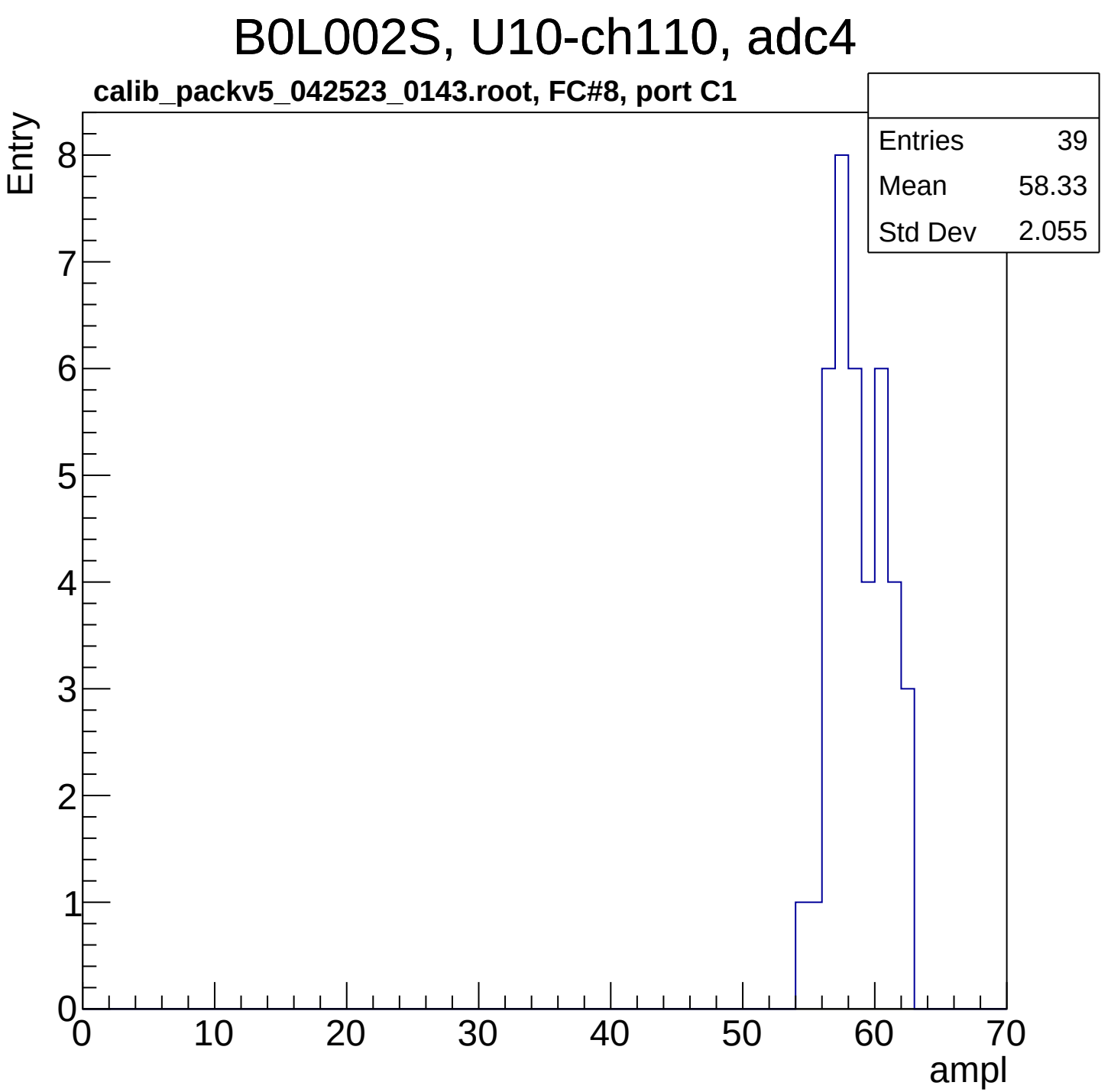

B0L002S, U10-ch109, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

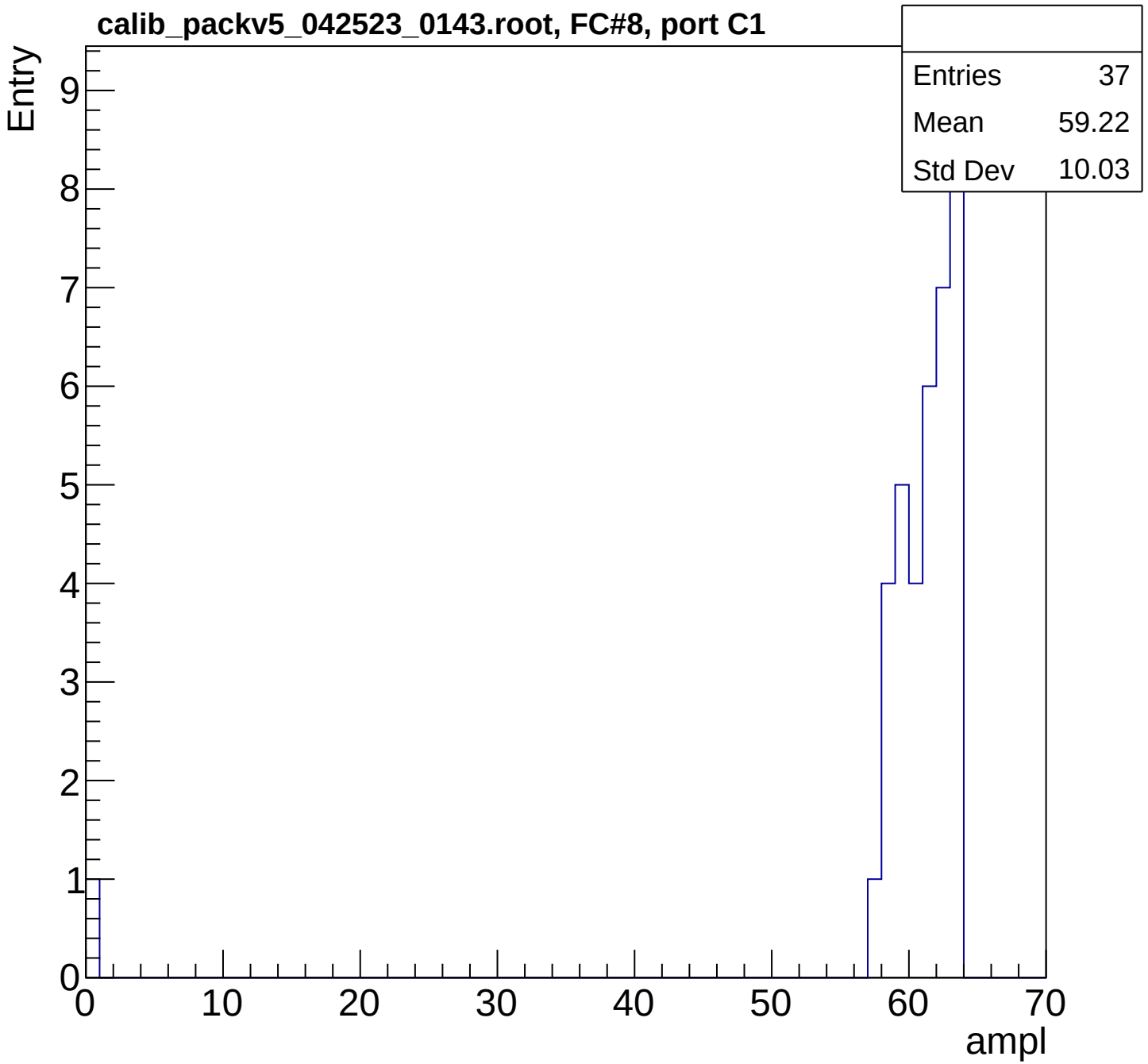

B0L002S, U10-ch110, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 32.18
Std Dev 3.319
Gaus mean : 32.1580
Gaus Width: 3.5588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

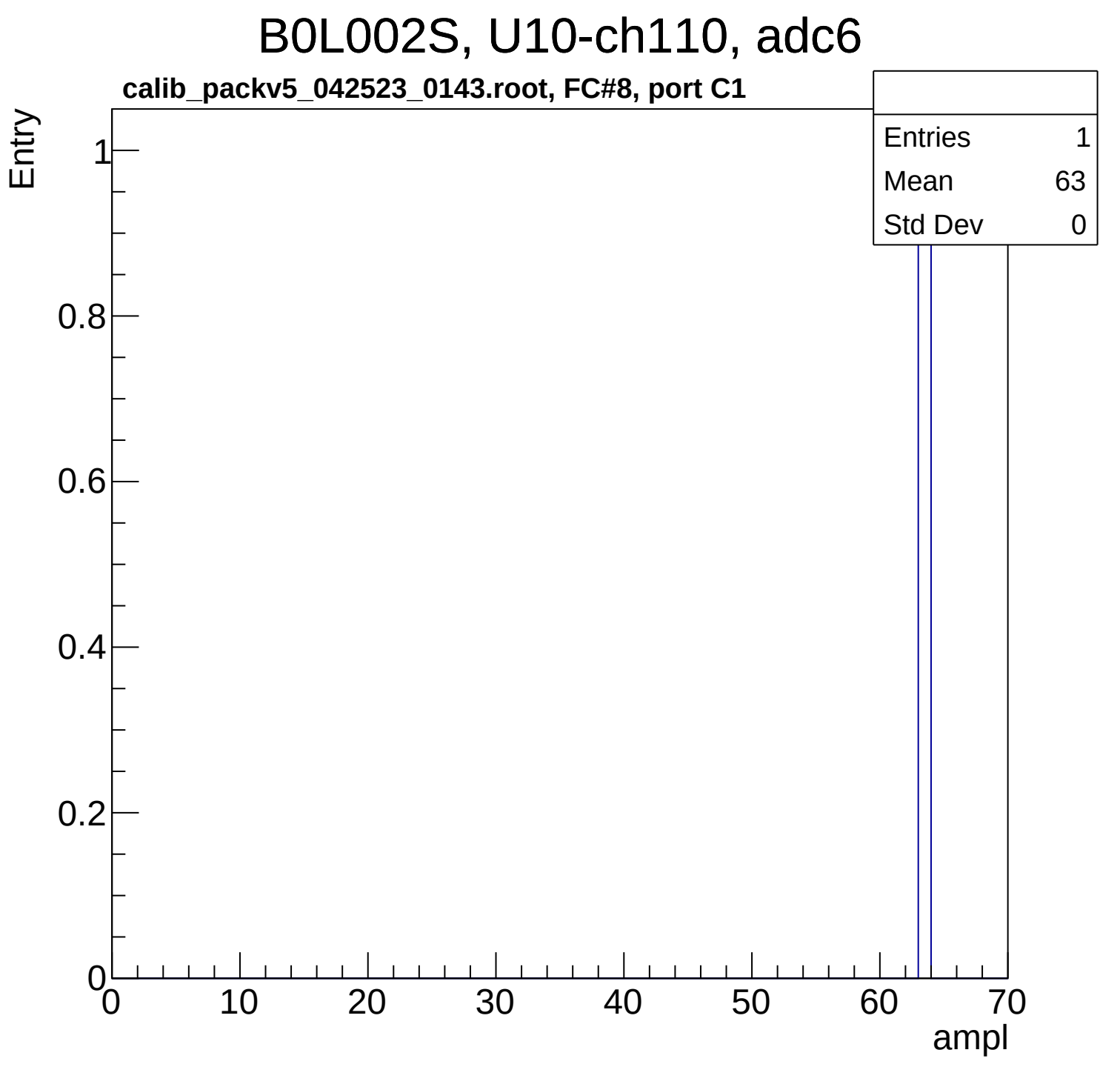

B0L002S, U10-ch110, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 38.02
Std Dev 3.219
Gaus mean : 39.0466
Gaus Width: 3.1285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

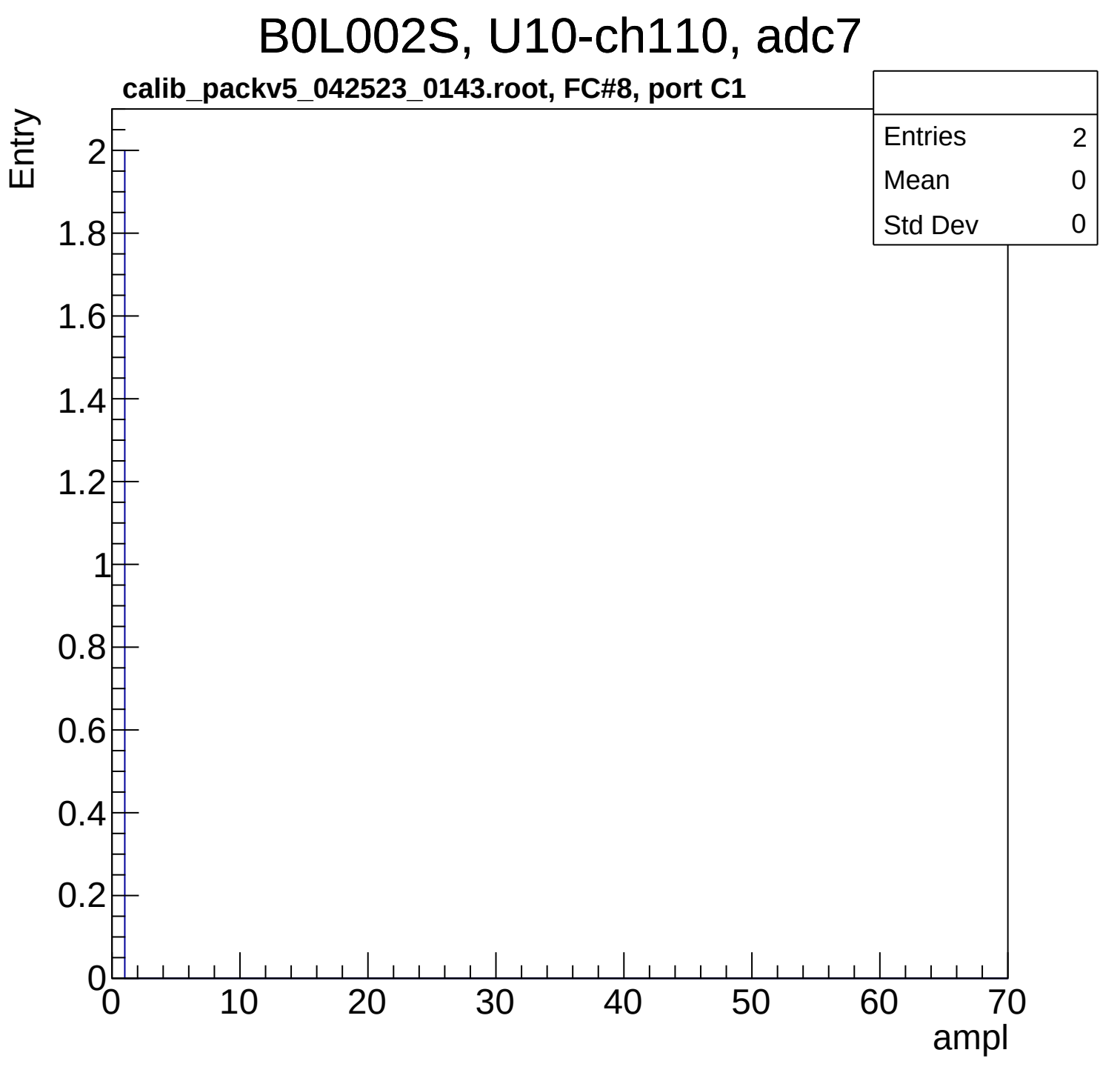

B0L002S, U10-ch110, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 44.17
Std Dev 3.173
Gaus mean : 44.4560
Gaus Width: 3.6450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

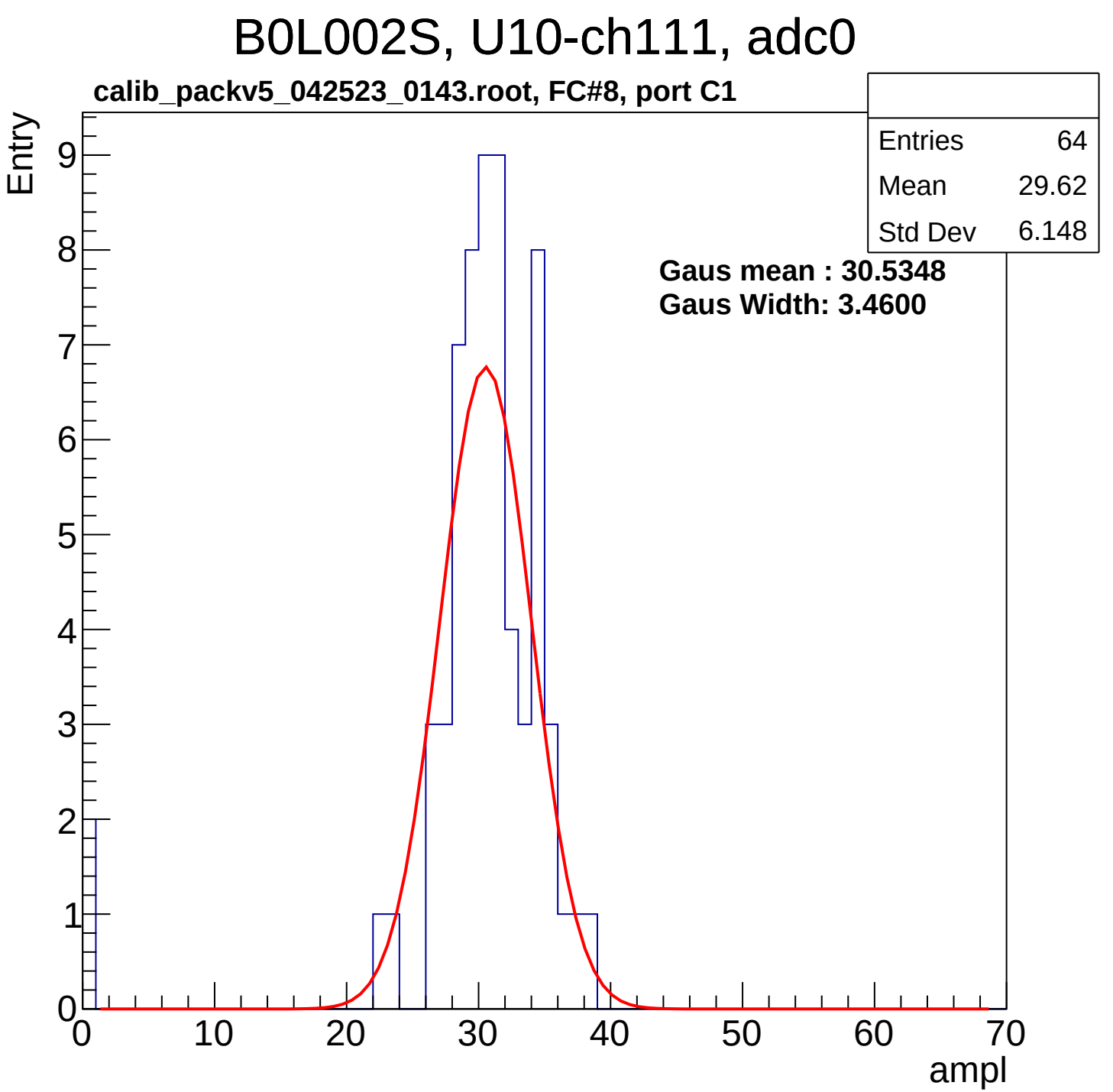

B0L002S, U10-ch110, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 92
Mean 50.98
Std Dev 3.33
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

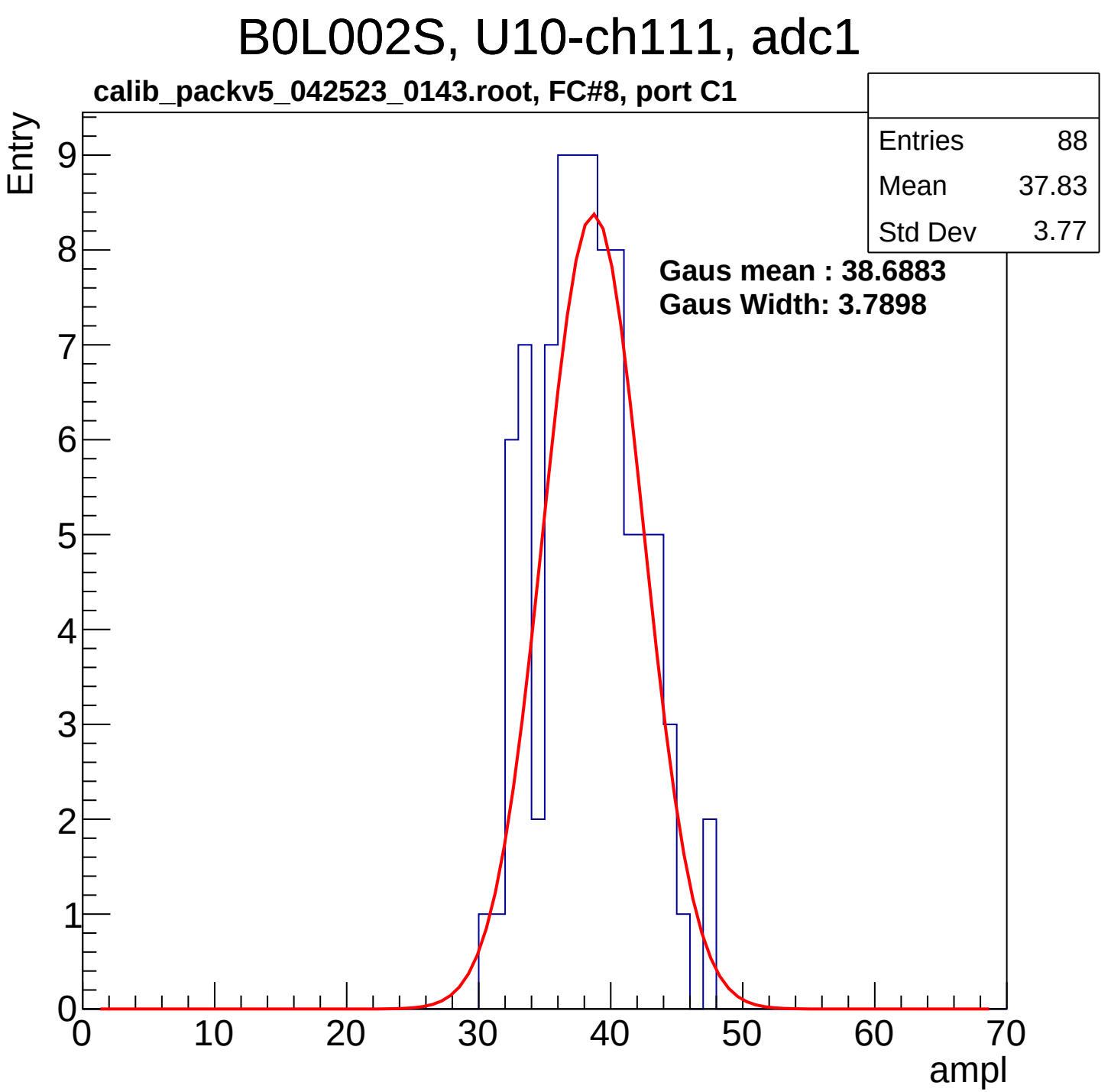

B0L002S, U10-ch110, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 58.33
Std Dev 2.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

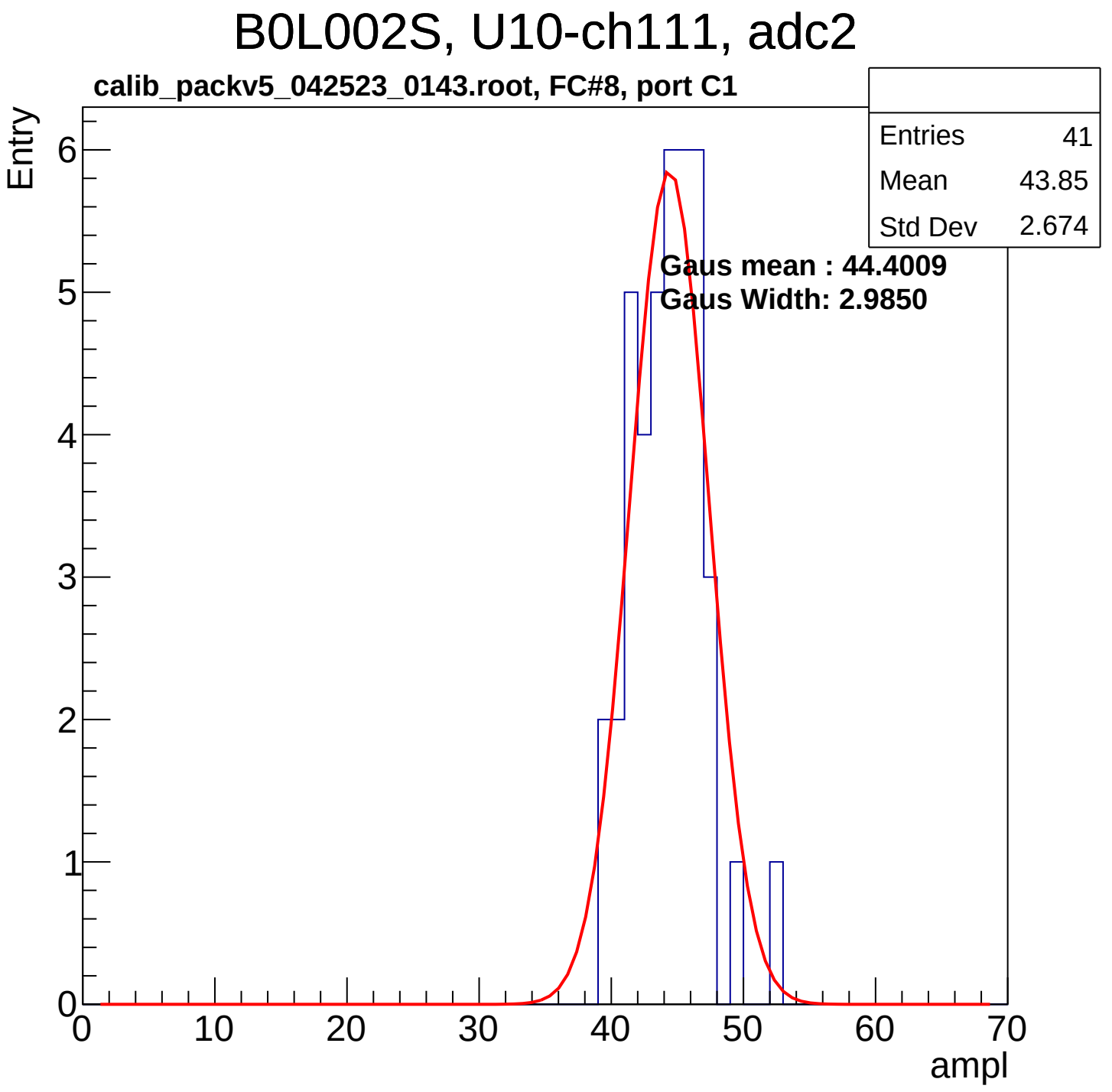

B0L002S, U10-ch110, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 37
Mean 59.22
Std Dev 10.03
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

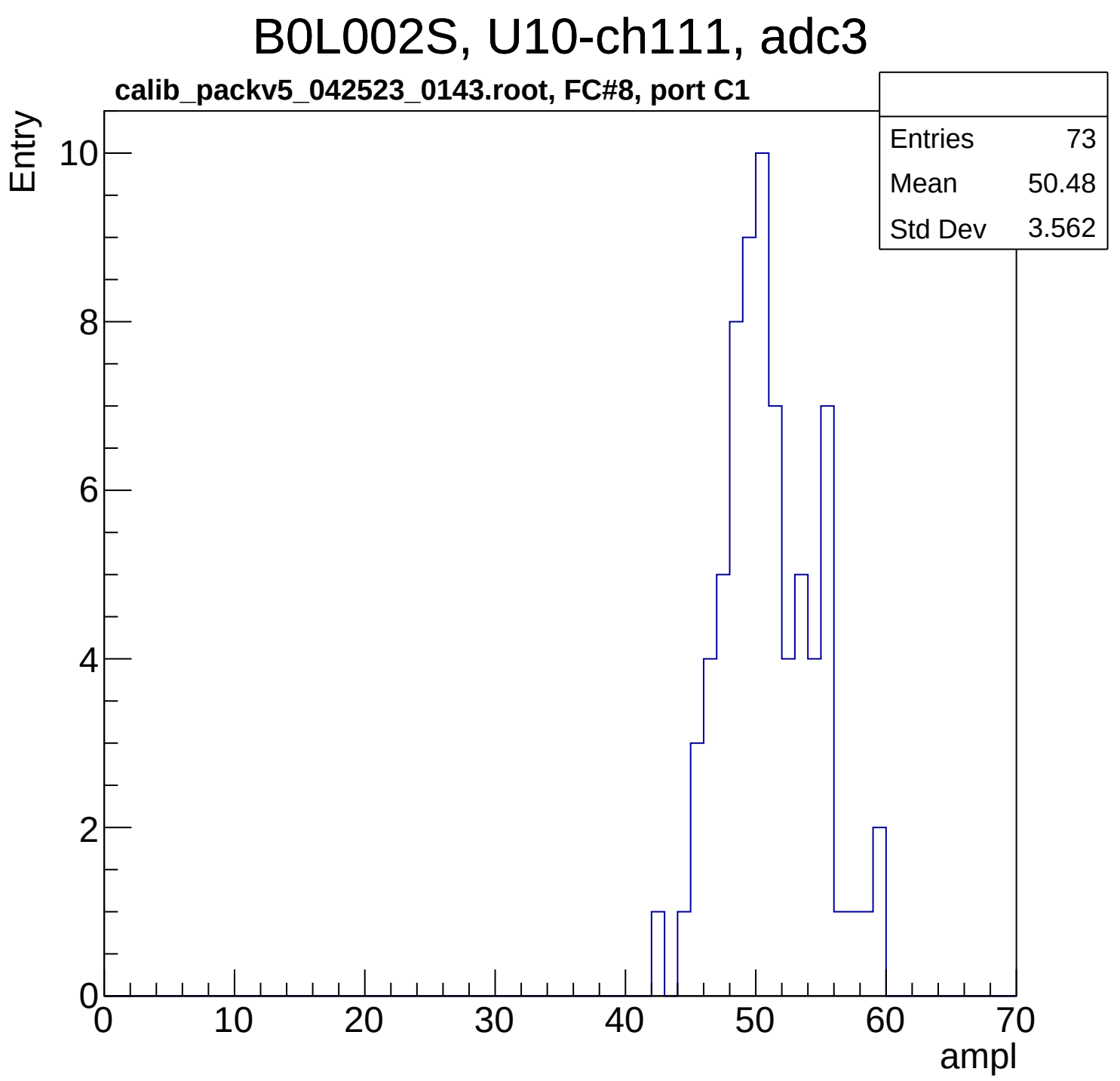

B0L002S, U10-ch110, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

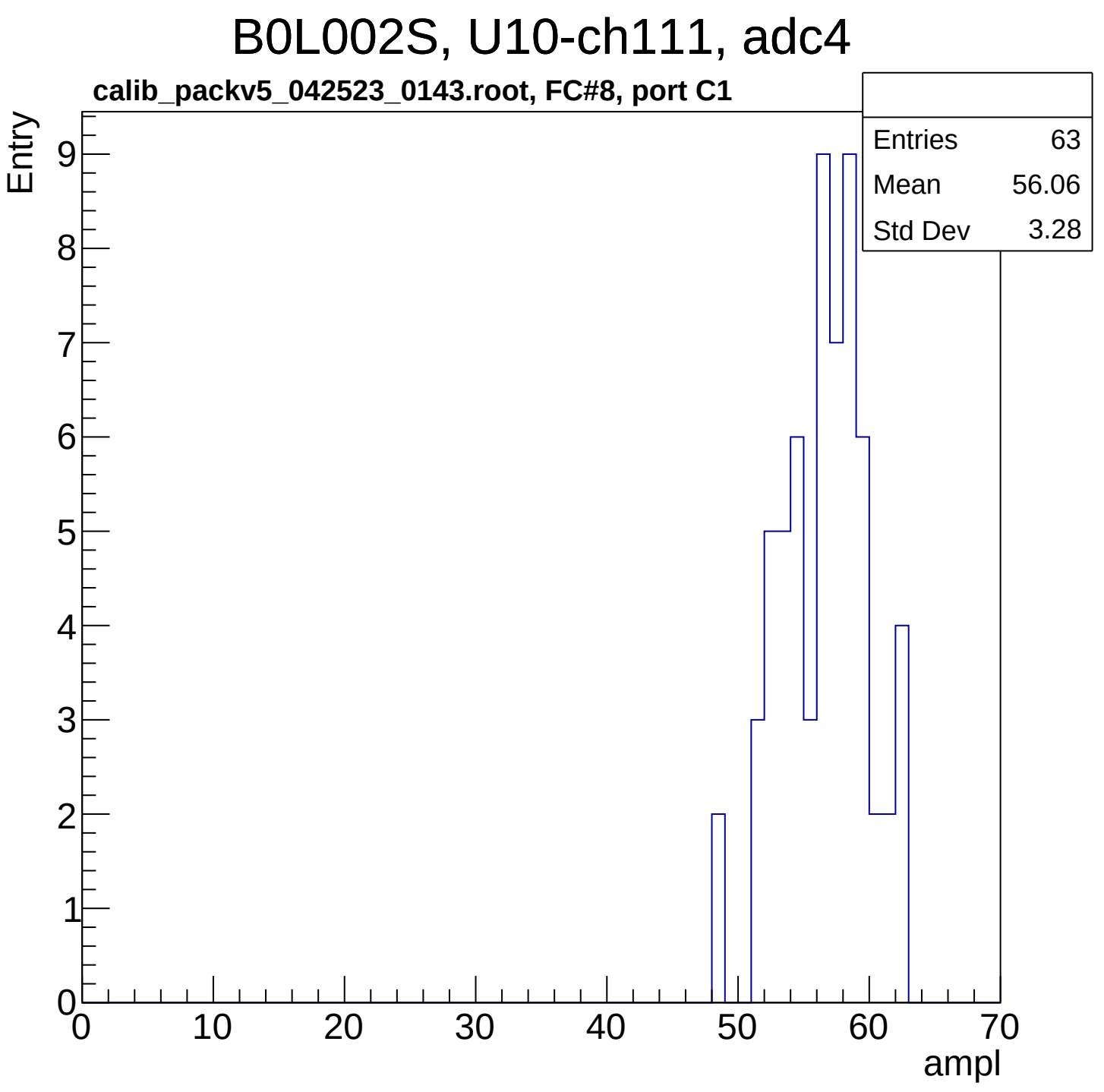

B0L002S, U10-ch110, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl

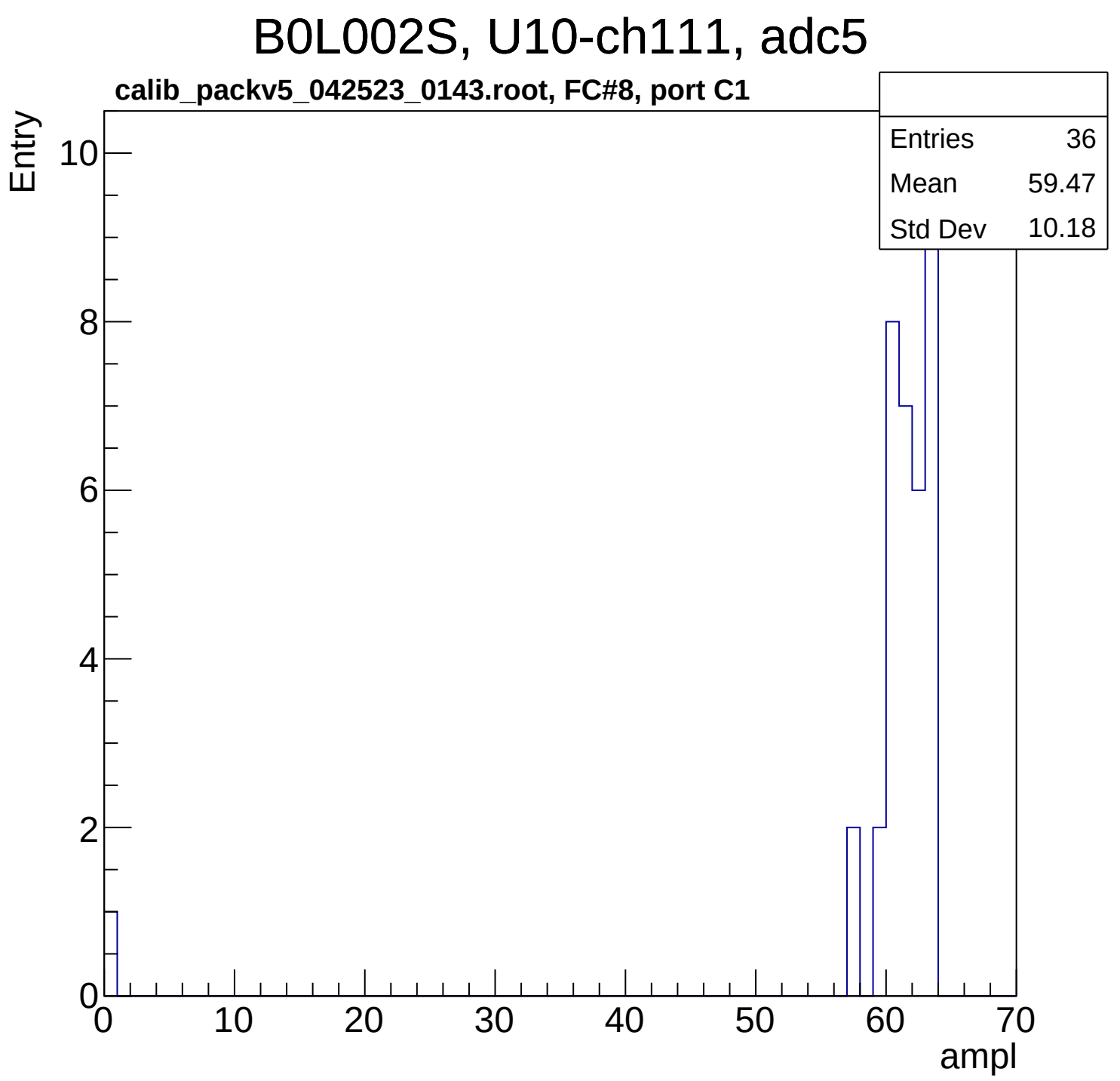

B0L002S, U10-ch111, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 29.62
Std Dev 6.148
Gaus mean : 30.5348
Gaus Width: 3.4600
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

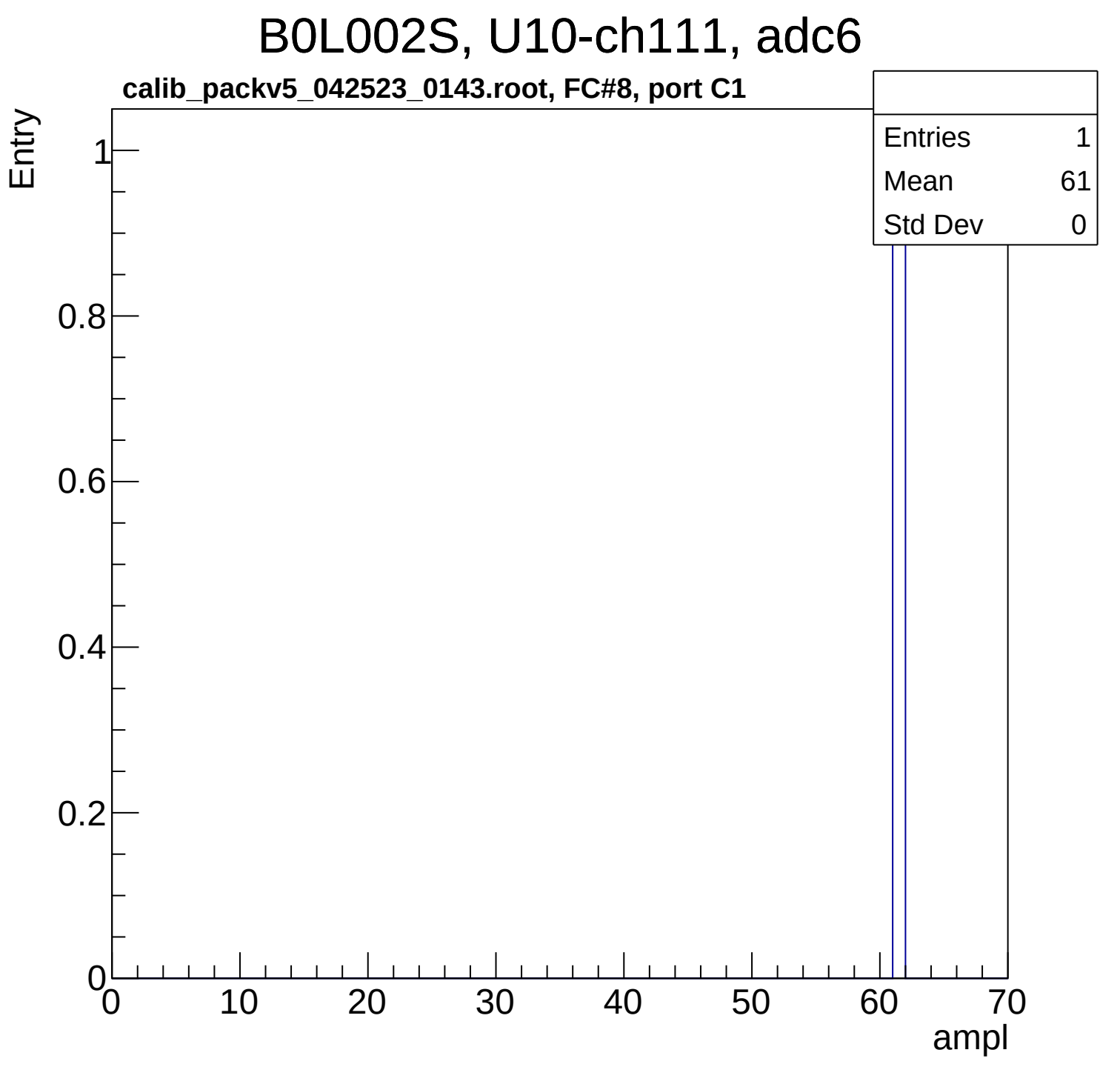

B0L002S, U10-ch111, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 88
Mean 37.83
Std Dev 3.77
Gaus mean : 38.6883
Gaus Width: 3.7898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

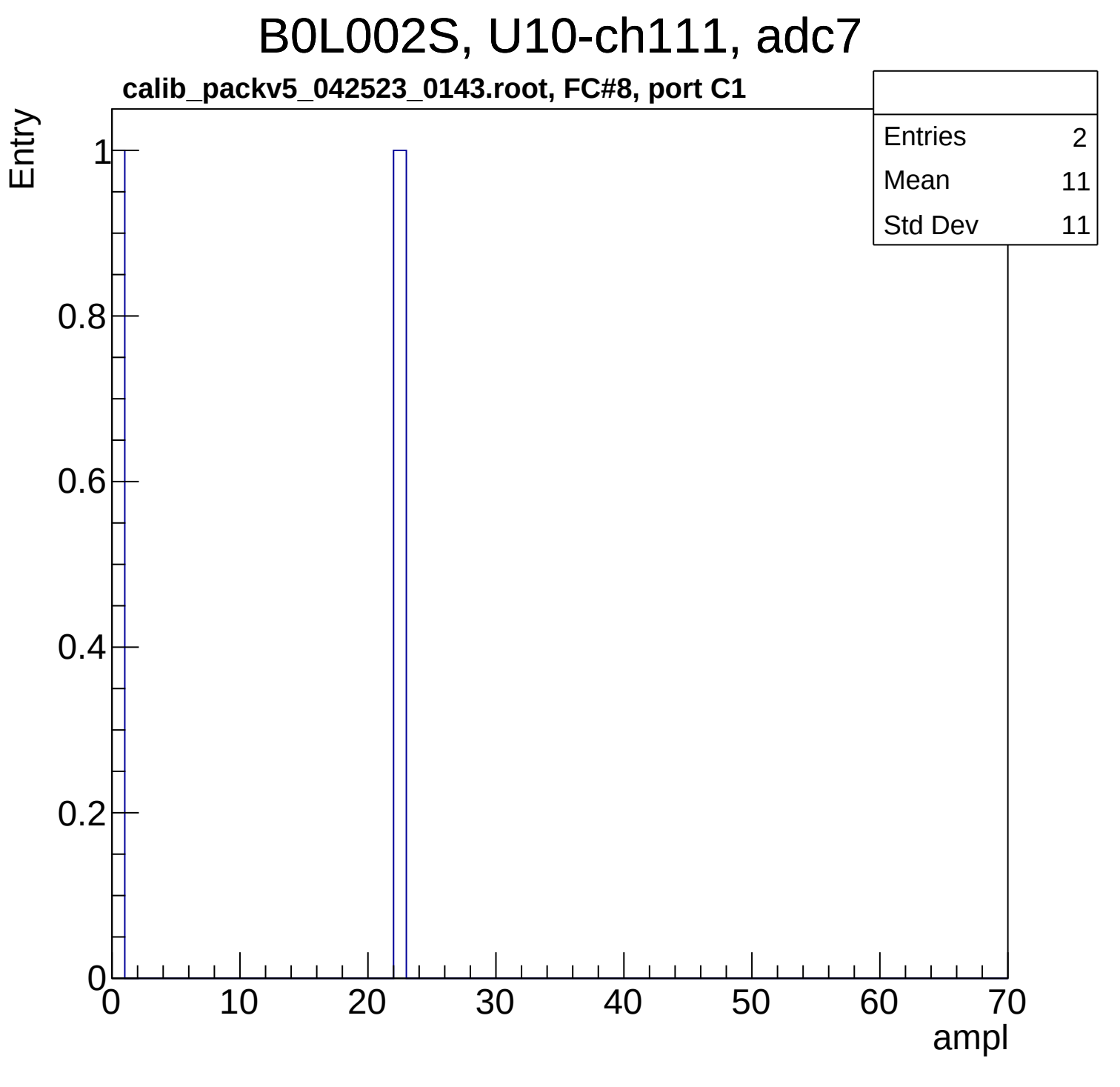

B0L002S, U10-ch111, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 43.85
Std Dev 2.674
Gaus mean : 44.4009
Gaus Width: 2.9850
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

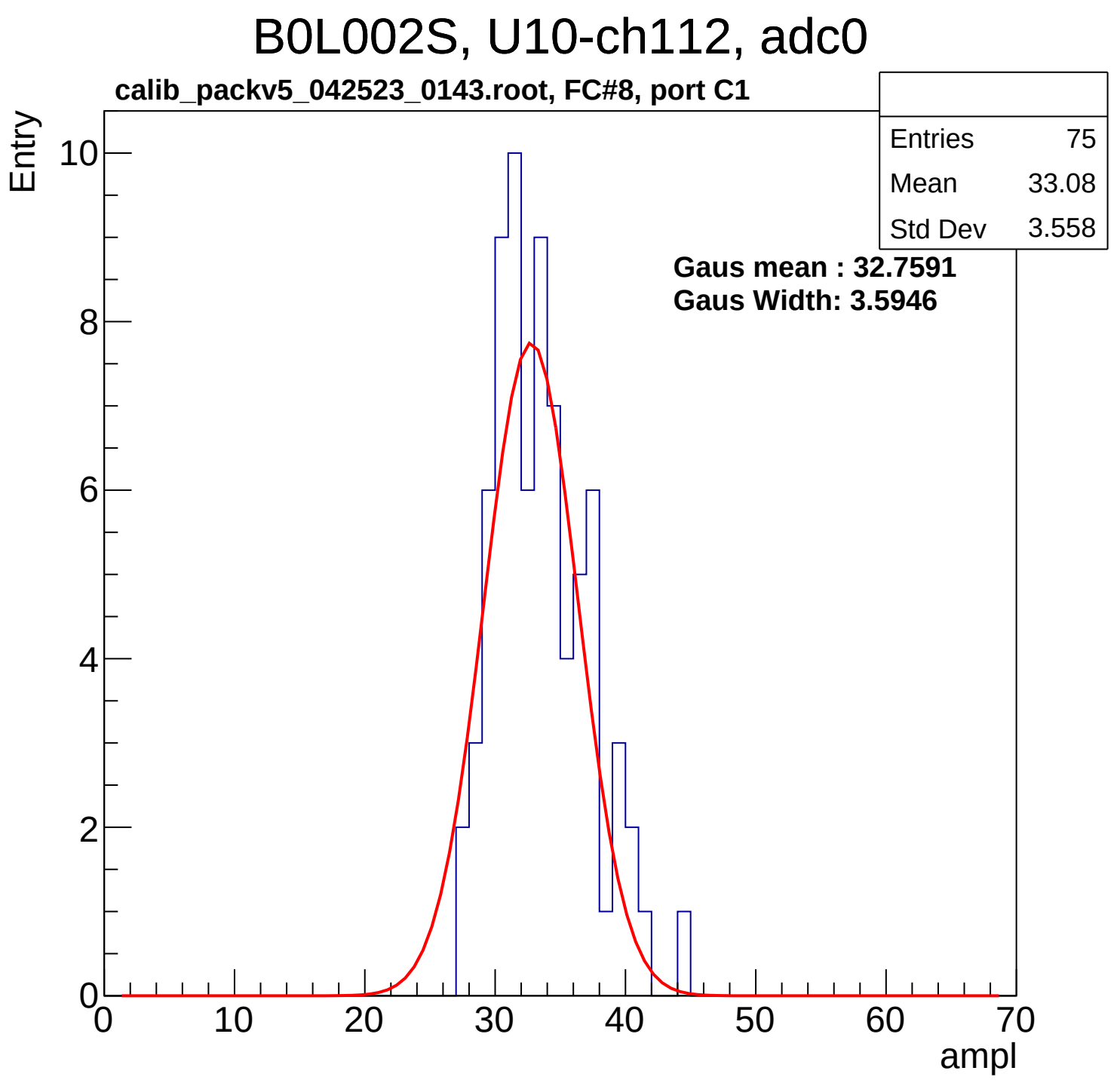

B0L002S, U10-ch111, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 50.48
Std Dev 3.562
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

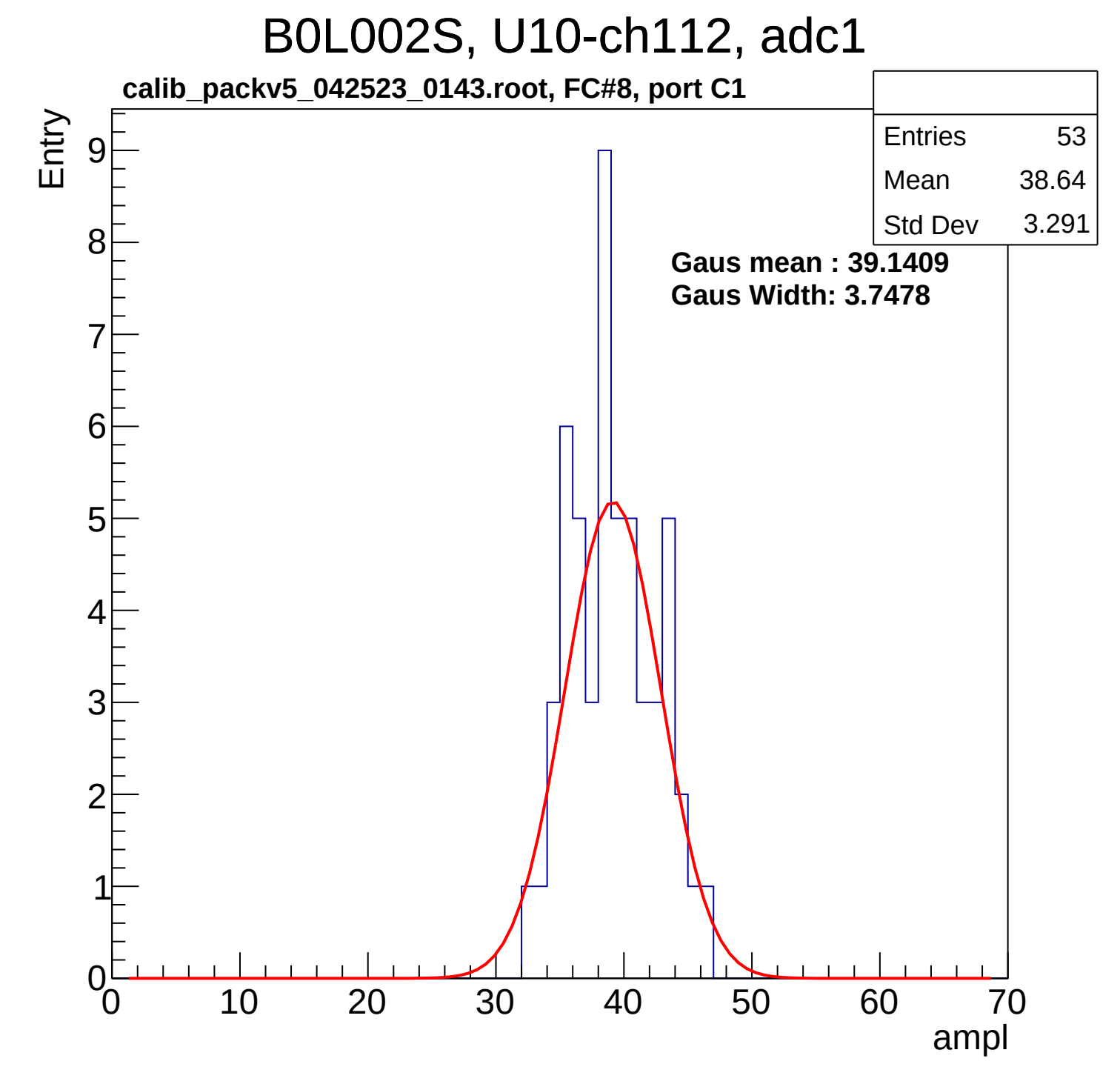

B0L002S, U10-ch111, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 56.06
Std Dev 3.28
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

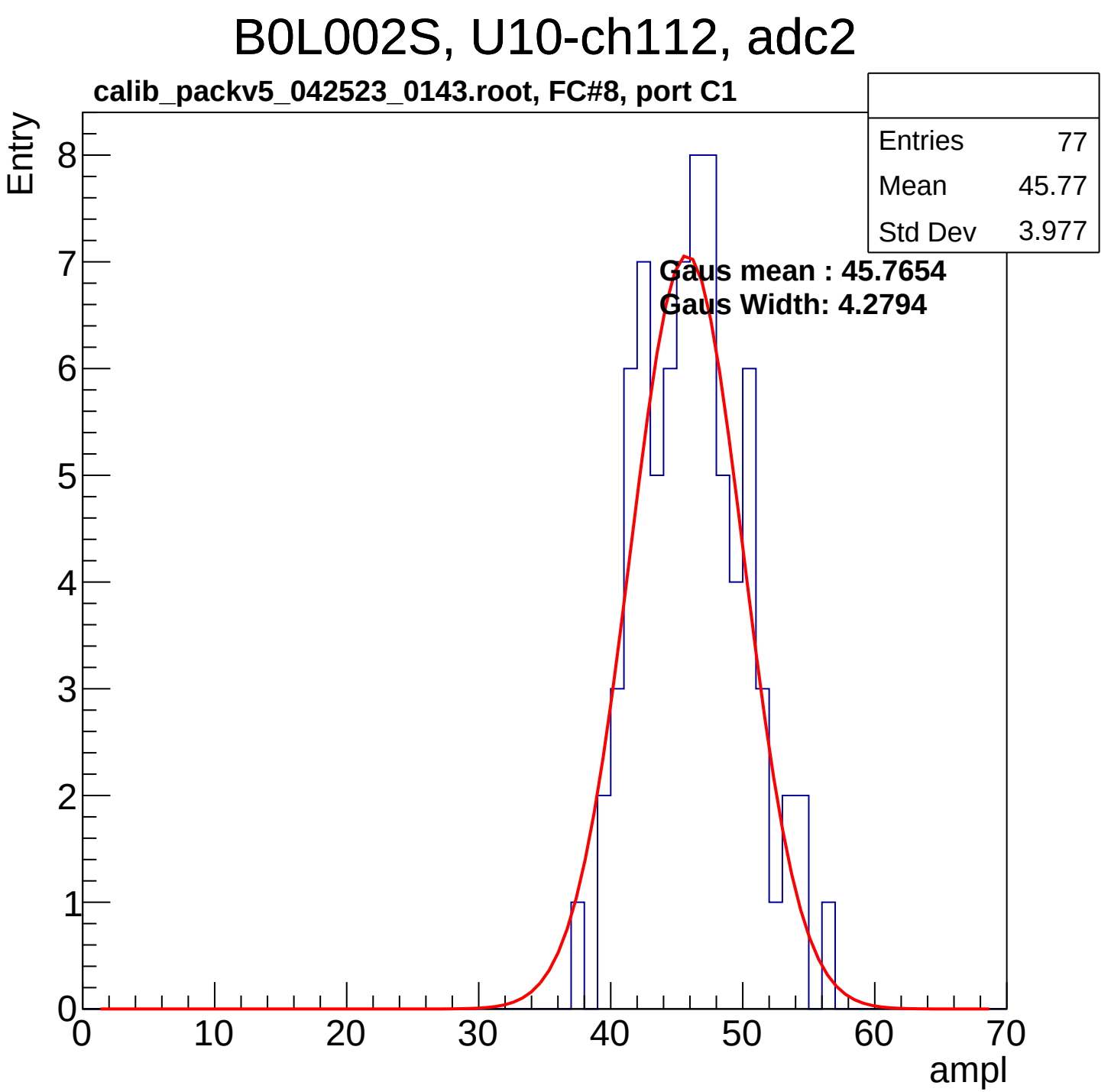

B0L002S, U10-ch111, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 59.47
Std Dev 10.18
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

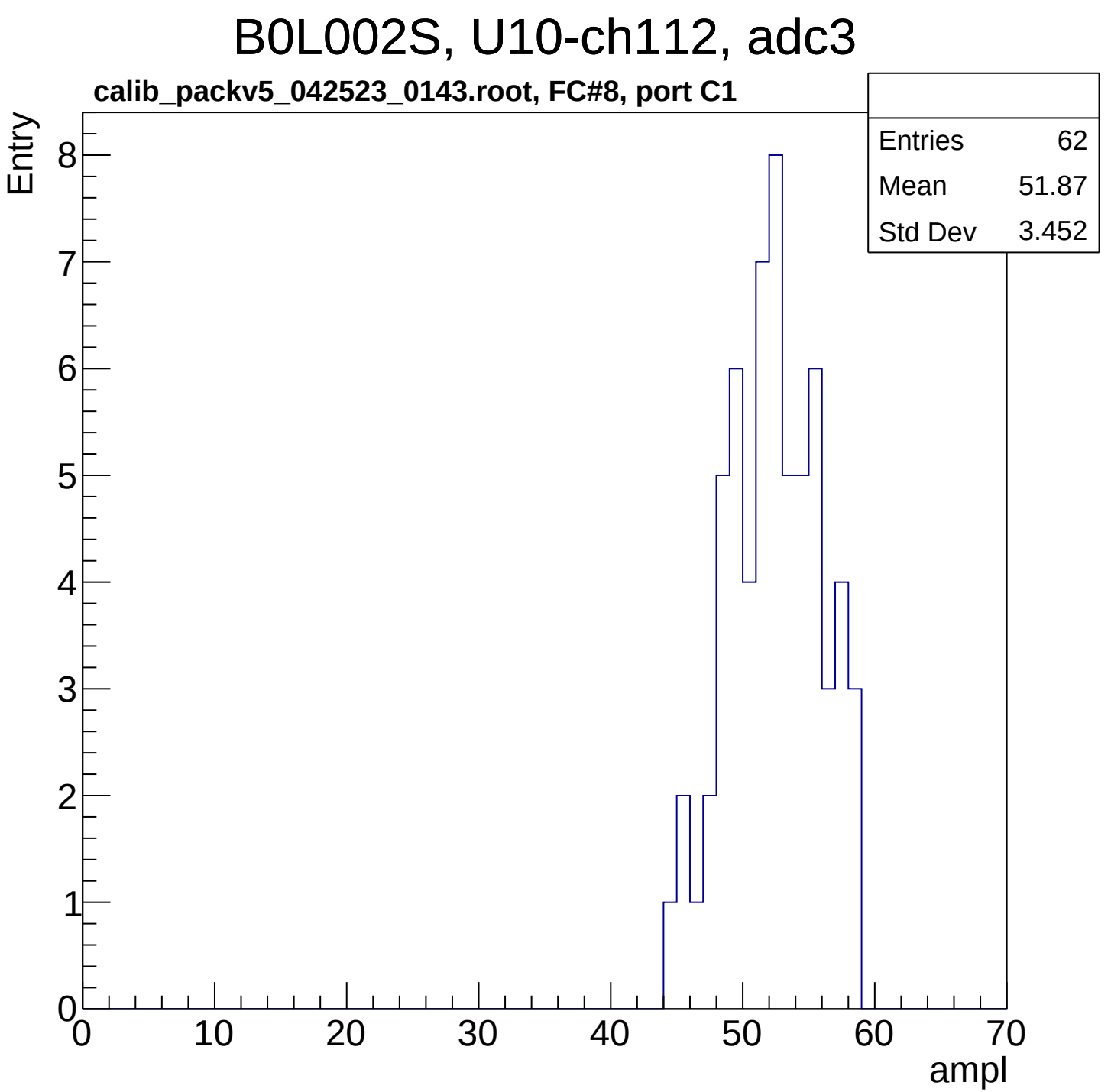

B0L002S, U10-ch111, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

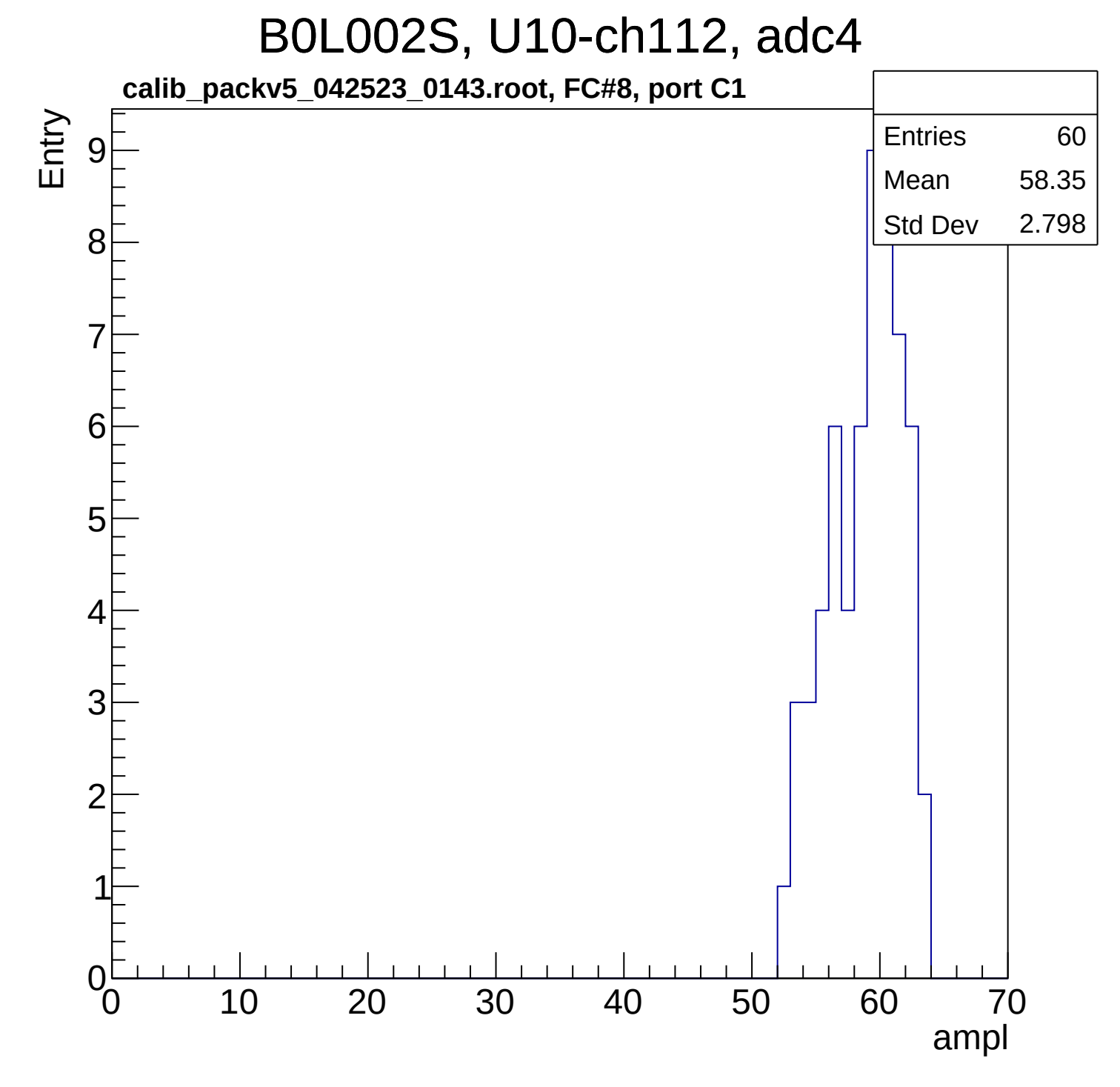

B0L002S, U10-ch111, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

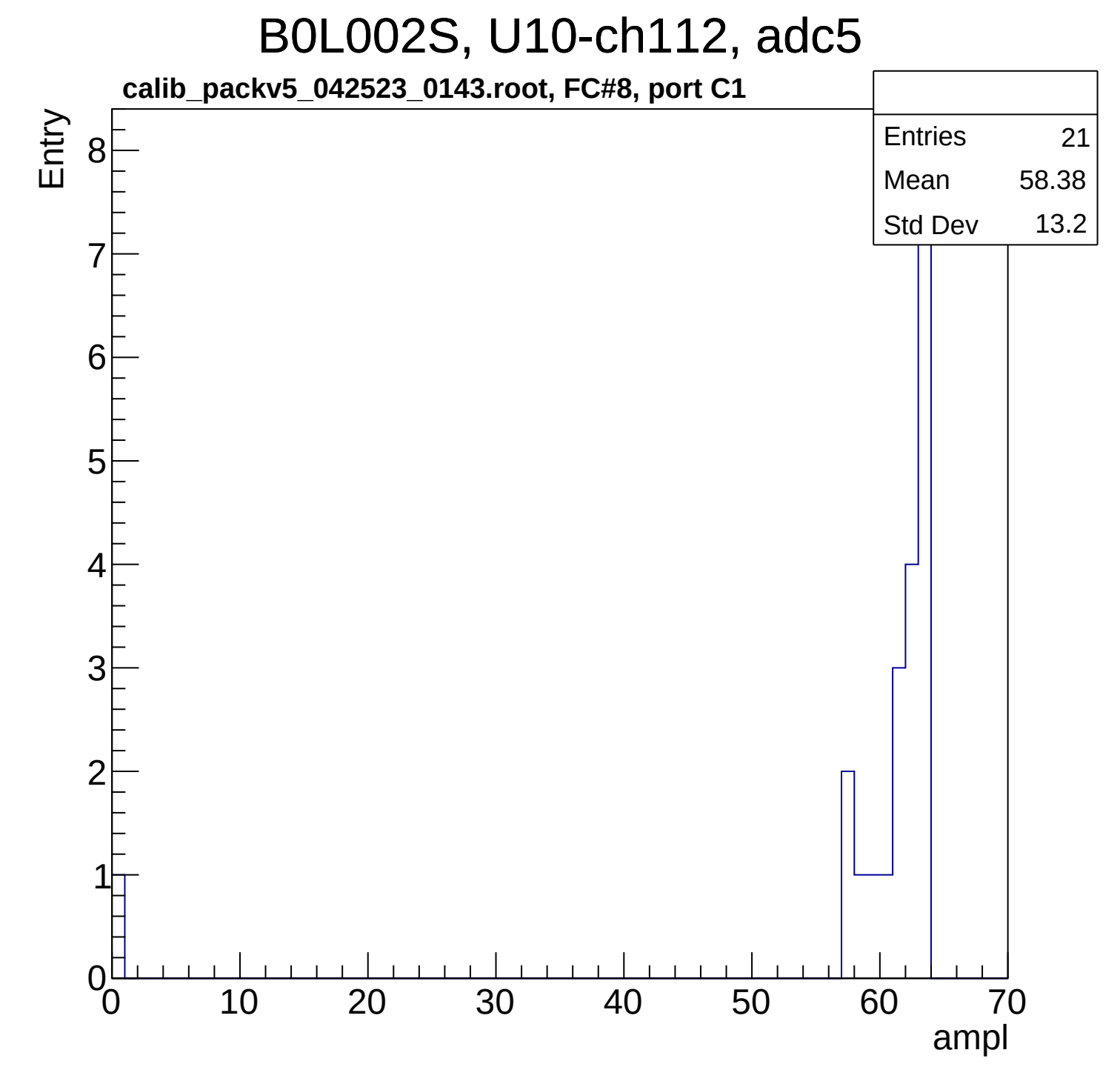

B0L002S, U10-ch112, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 33.08
Std Dev 3.558
Gaus mean : 32.7591
Gaus Width: 3.5946
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

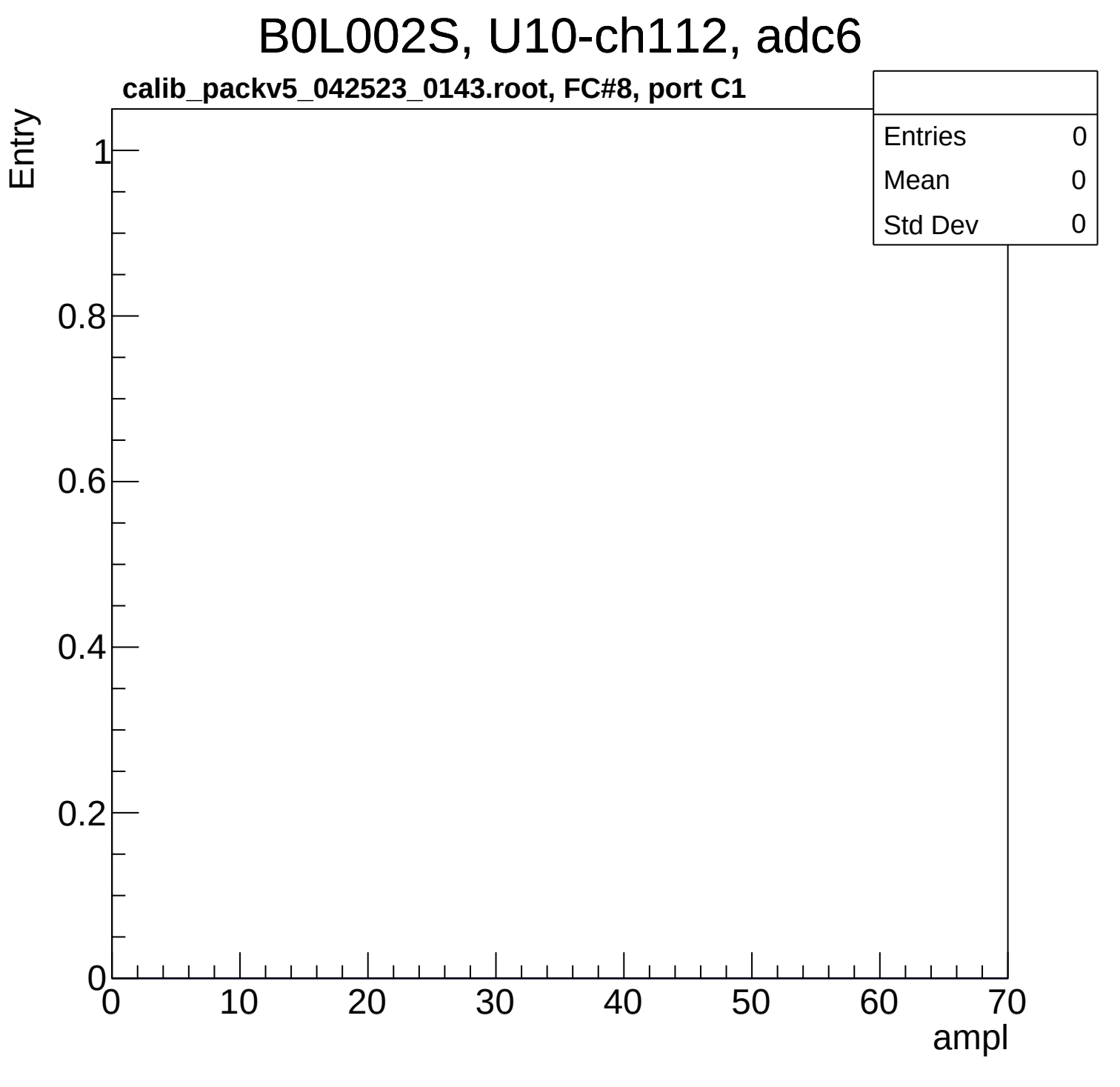

B0L002S, U10-ch112, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 38.64
Std Dev 3.291
Gaus mean : 39.1409
Gaus Width: 3.7478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

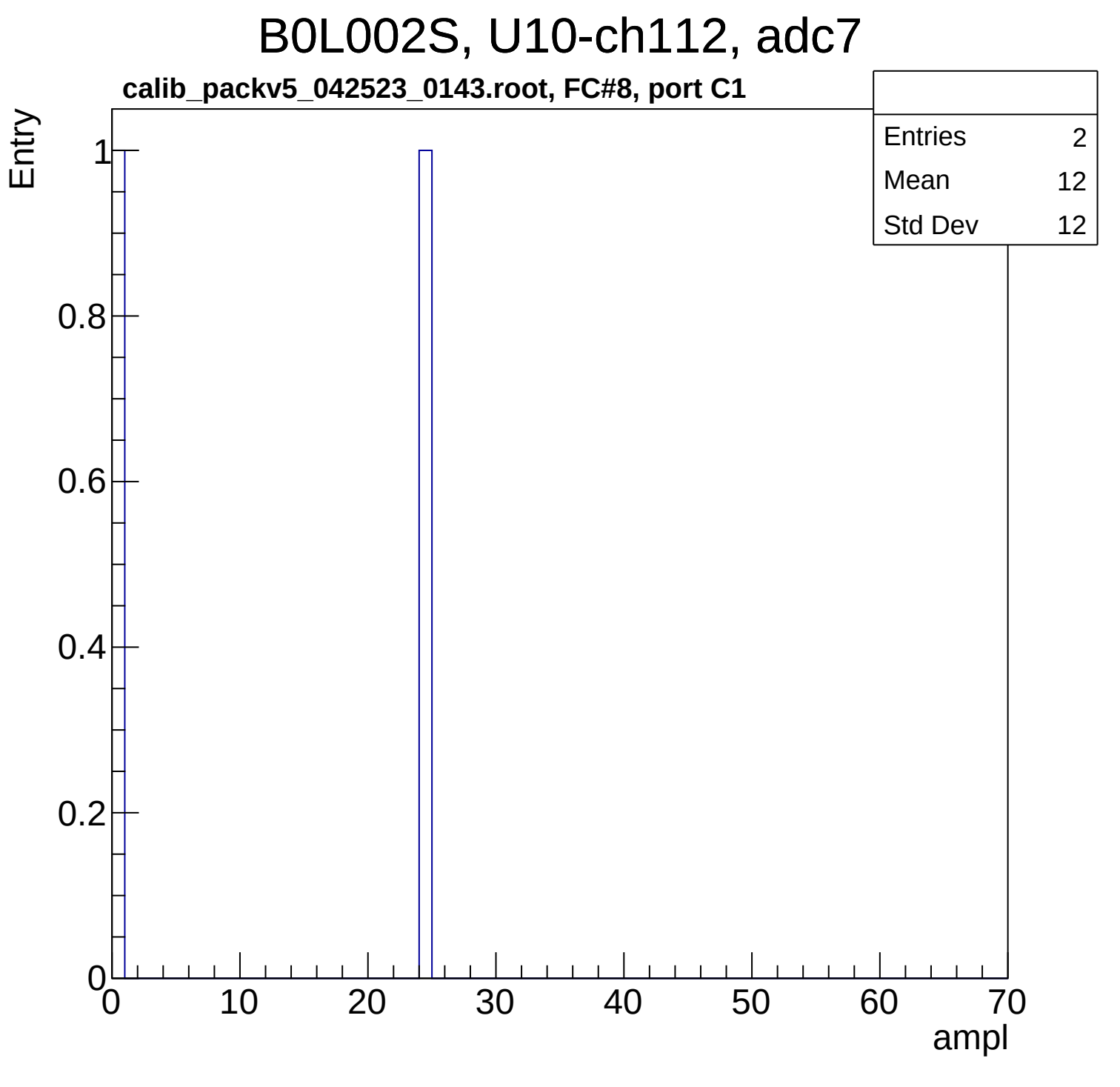

B0L002S, U10-ch112, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 45.77
Std Dev 3.977
Gaus mean : 45.7654
Gaus Width: 4.2794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

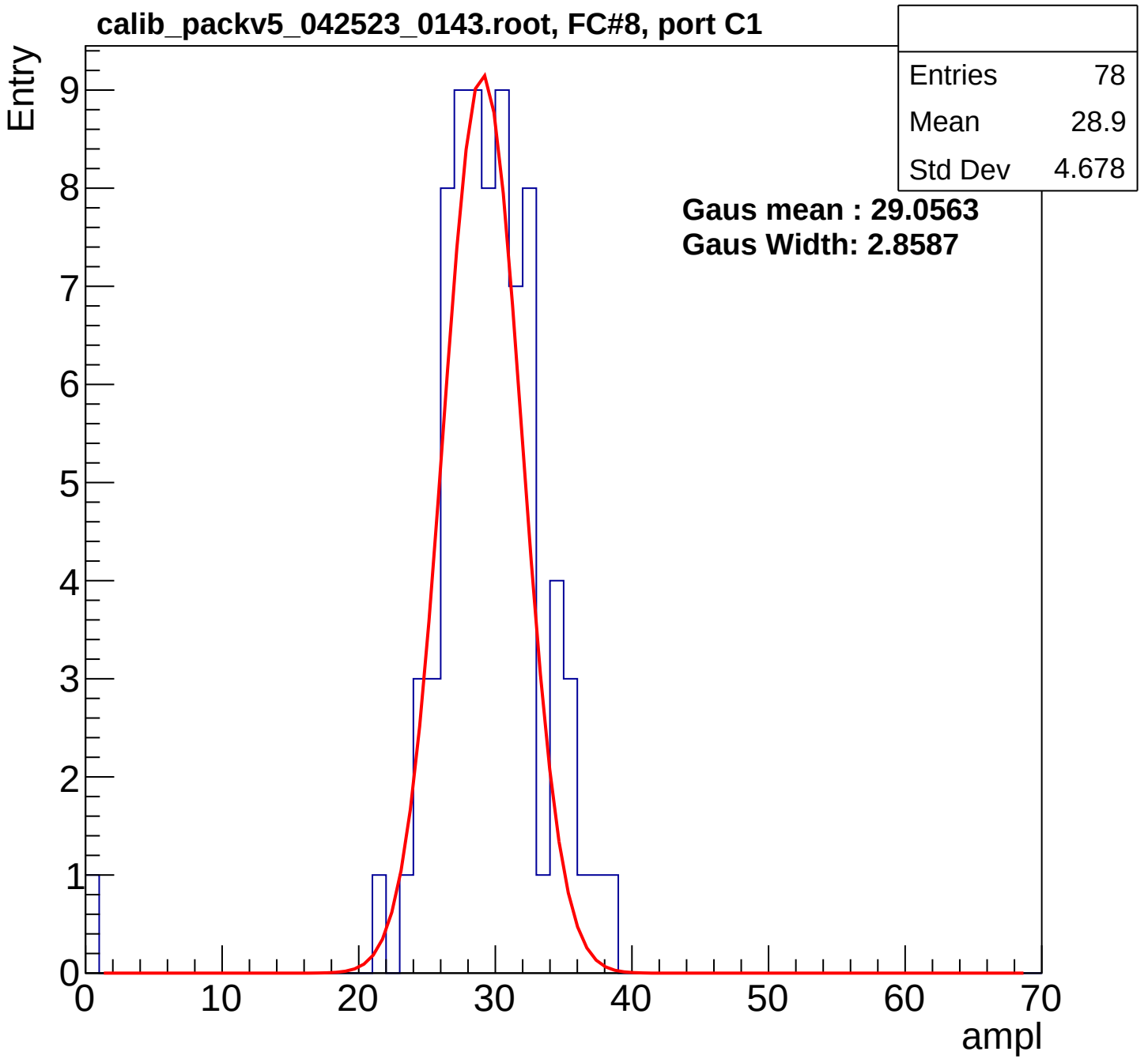

B0L002S, U10-ch112, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 51.87
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

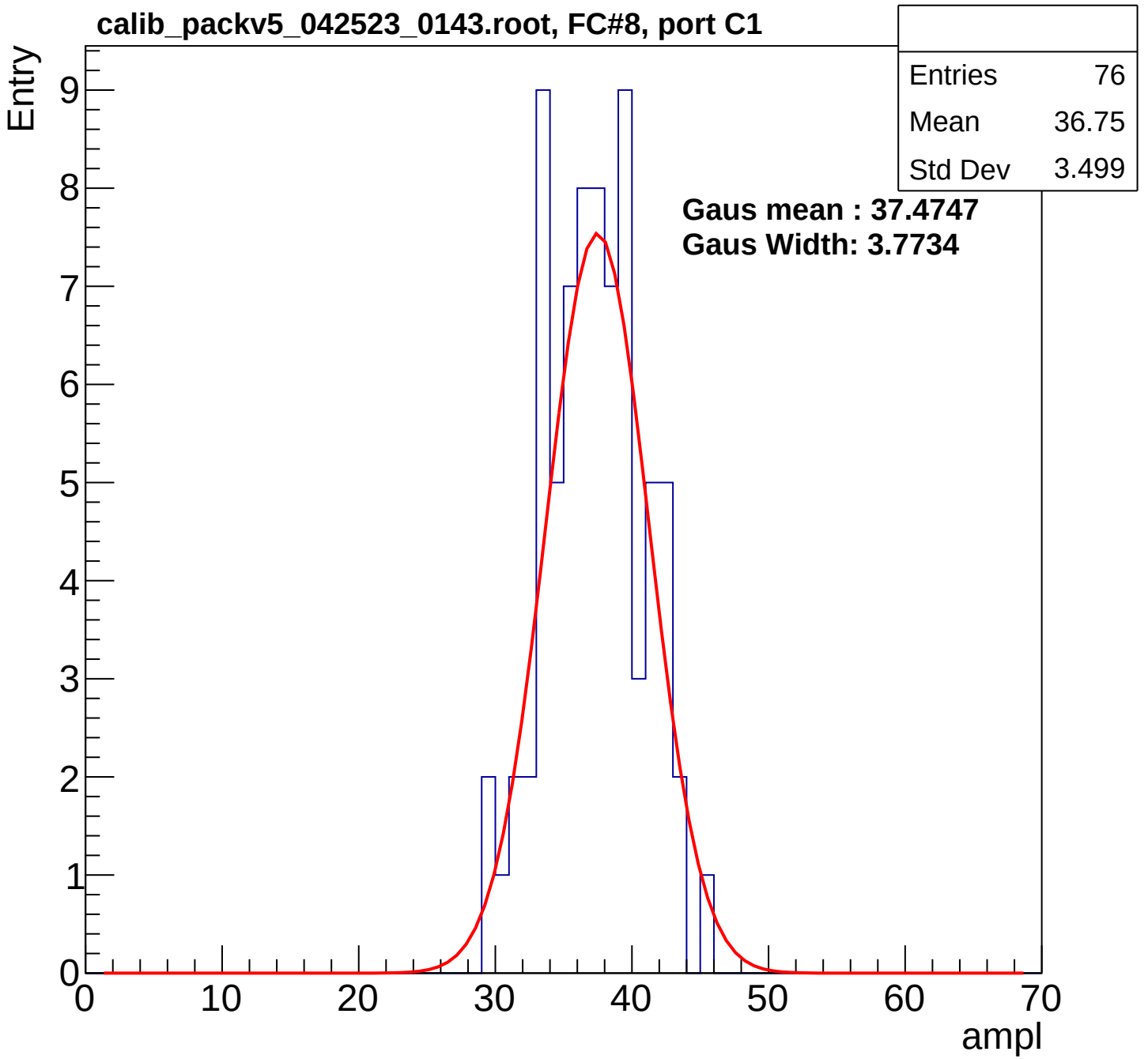

B0L002S, U10-ch112, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 58.35
Std Dev 2.798
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

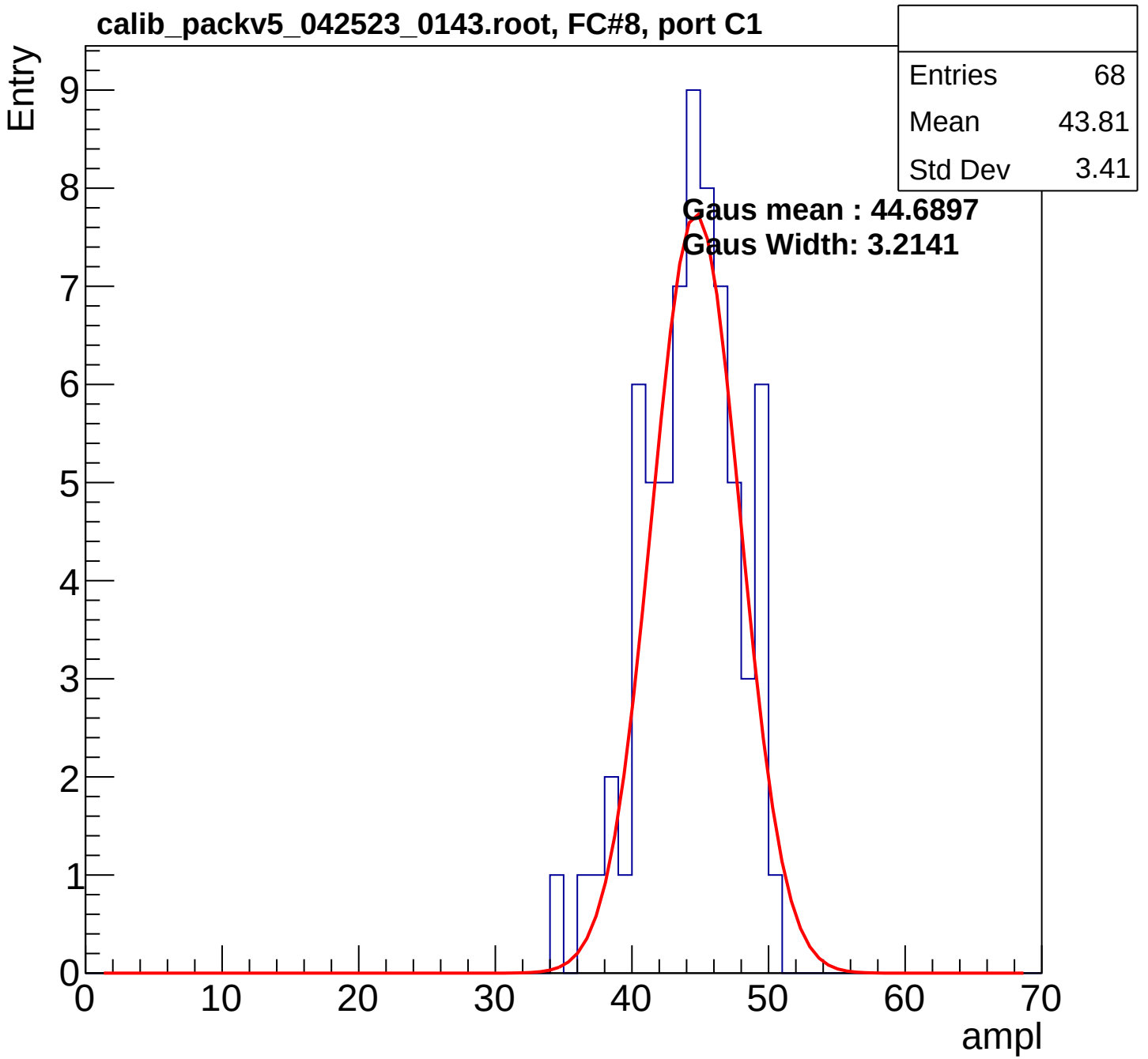

B0L002S, U10-ch112, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.38
Std Dev 13.2
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

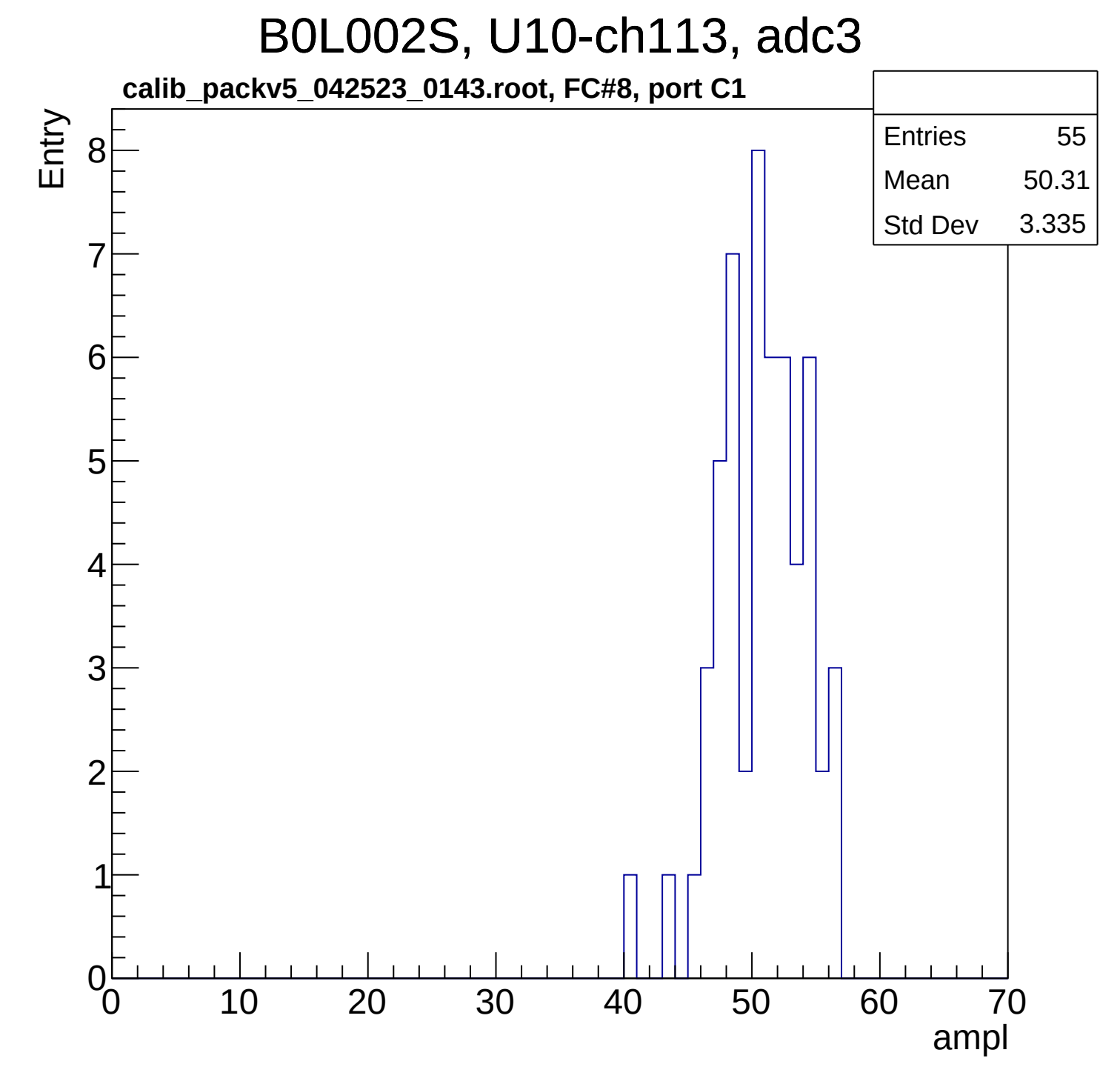

B0L002S, U10-ch112, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

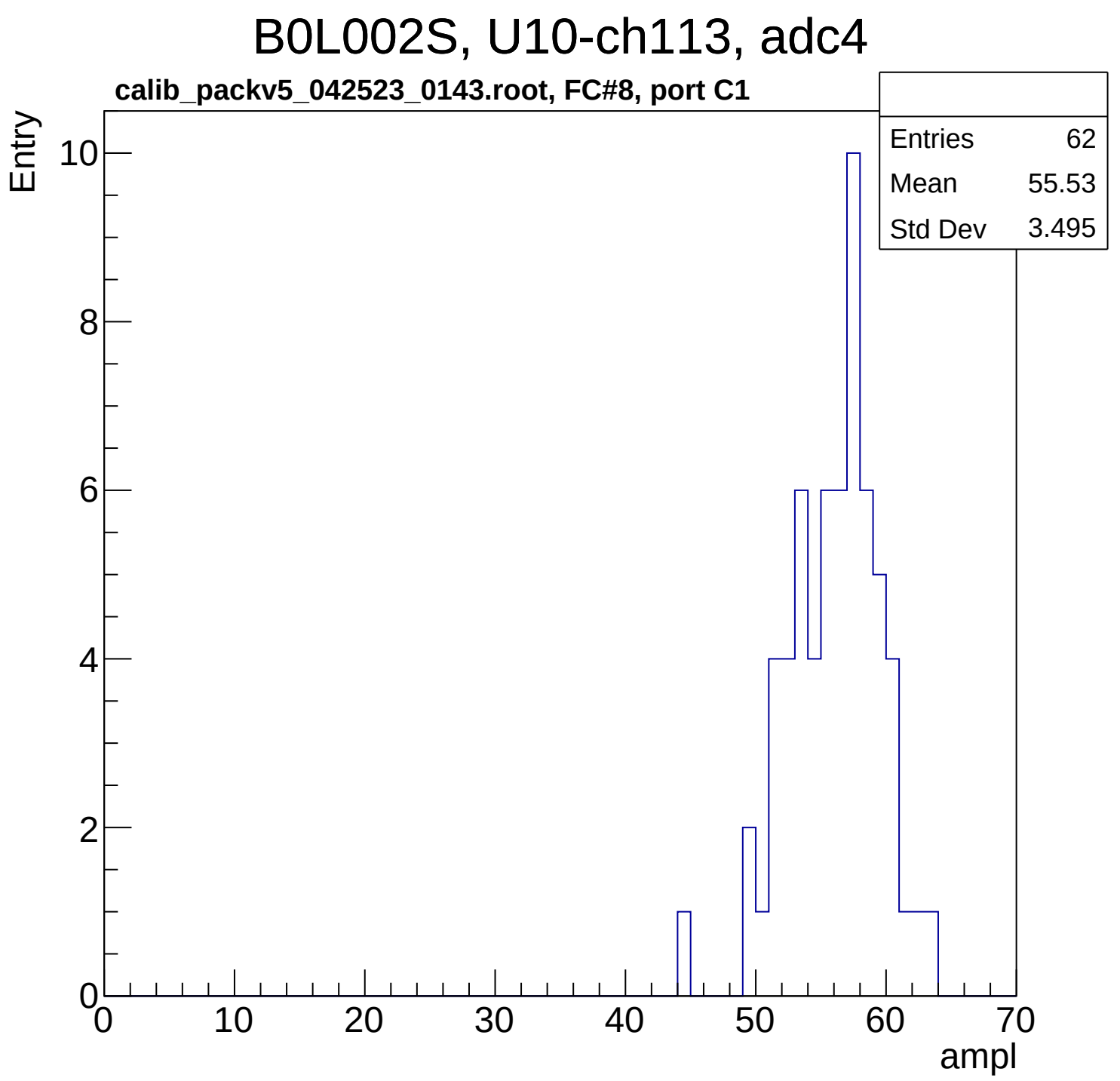

B0L002S, U10-ch112, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

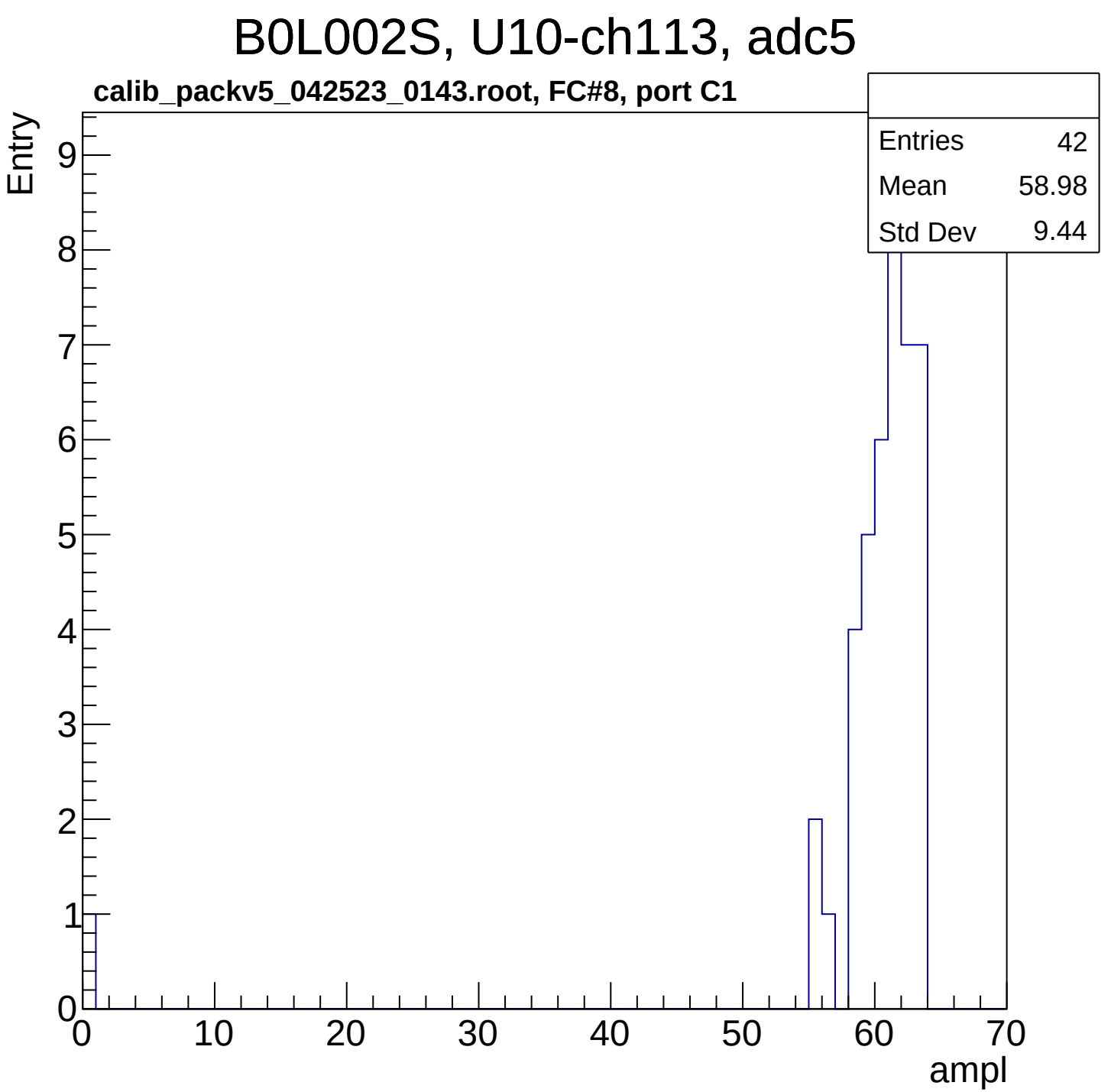

B0L002S, U10-ch113, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 28.9
Std Dev 4.678
Gaus mean : 29.0563
Gaus Width: 2.8587
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

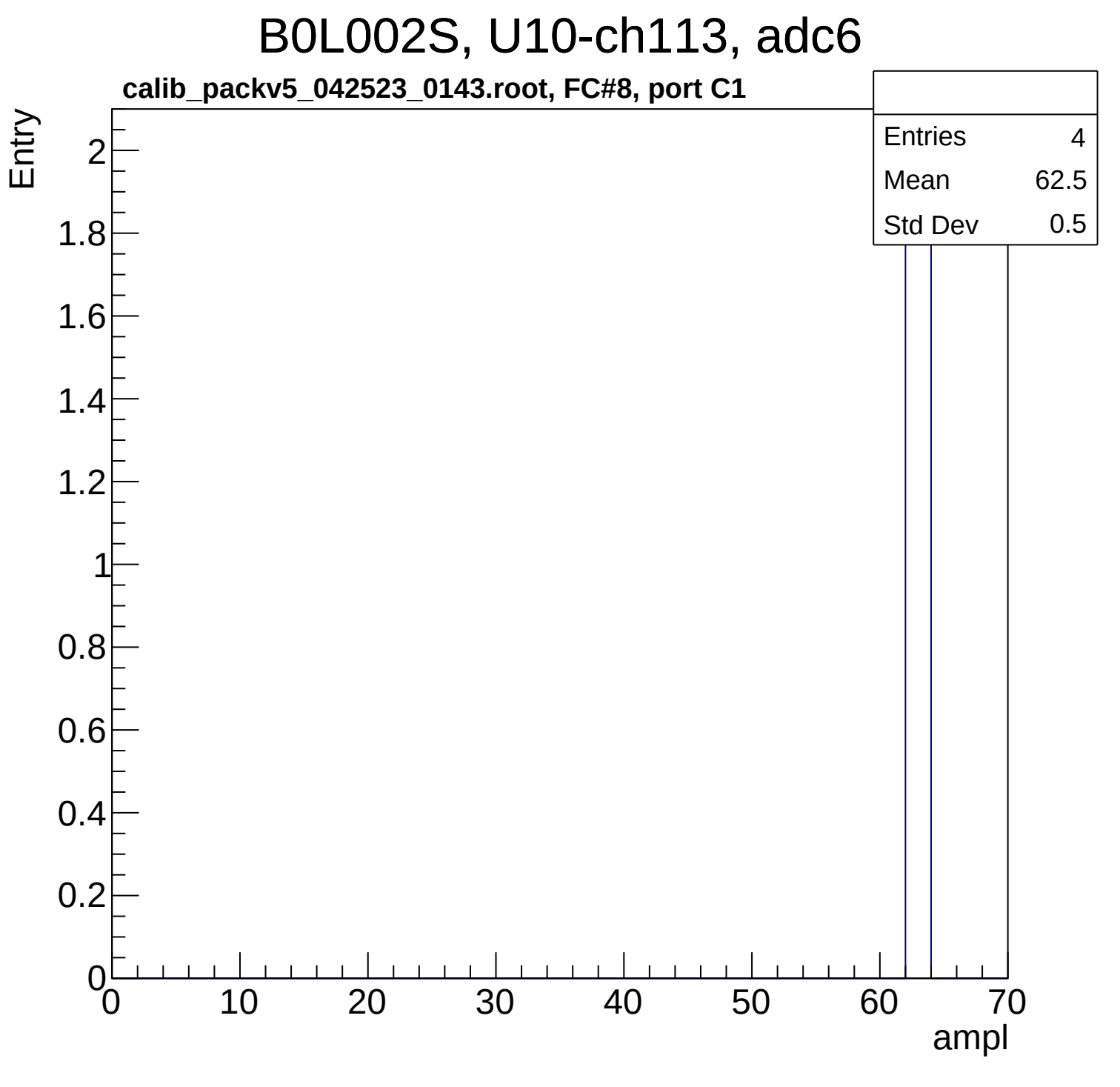

B0L002S, U10-ch113, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 76
Mean 36.75
Std Dev 3.499
Gaus mean : 37.4747
Gaus Width: 3.7734
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

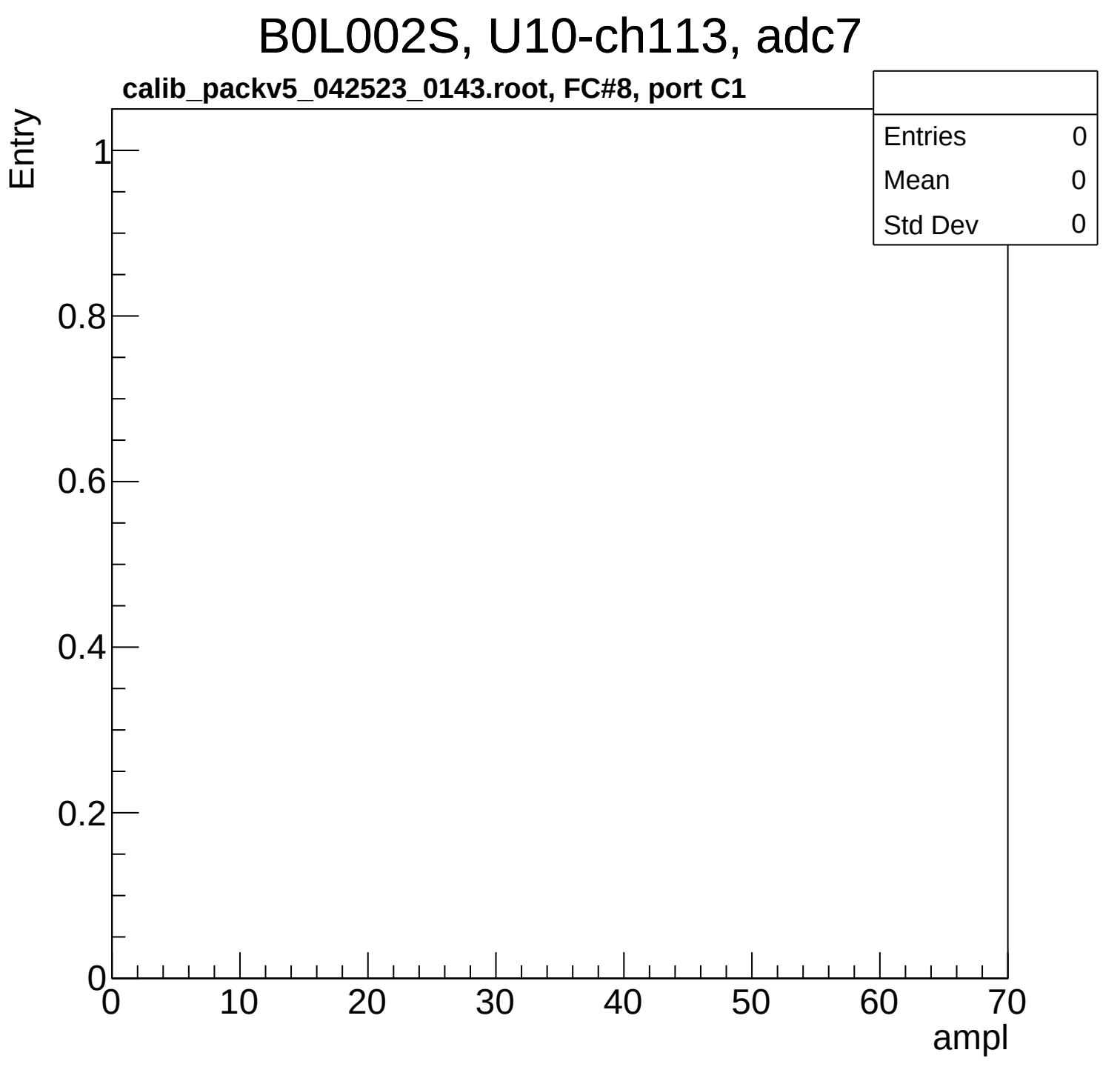

B0L002S, U10-ch113, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 43.81
Std Dev 3.41
Gaus mean : 44.6897
Gaus Width: 3.2141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

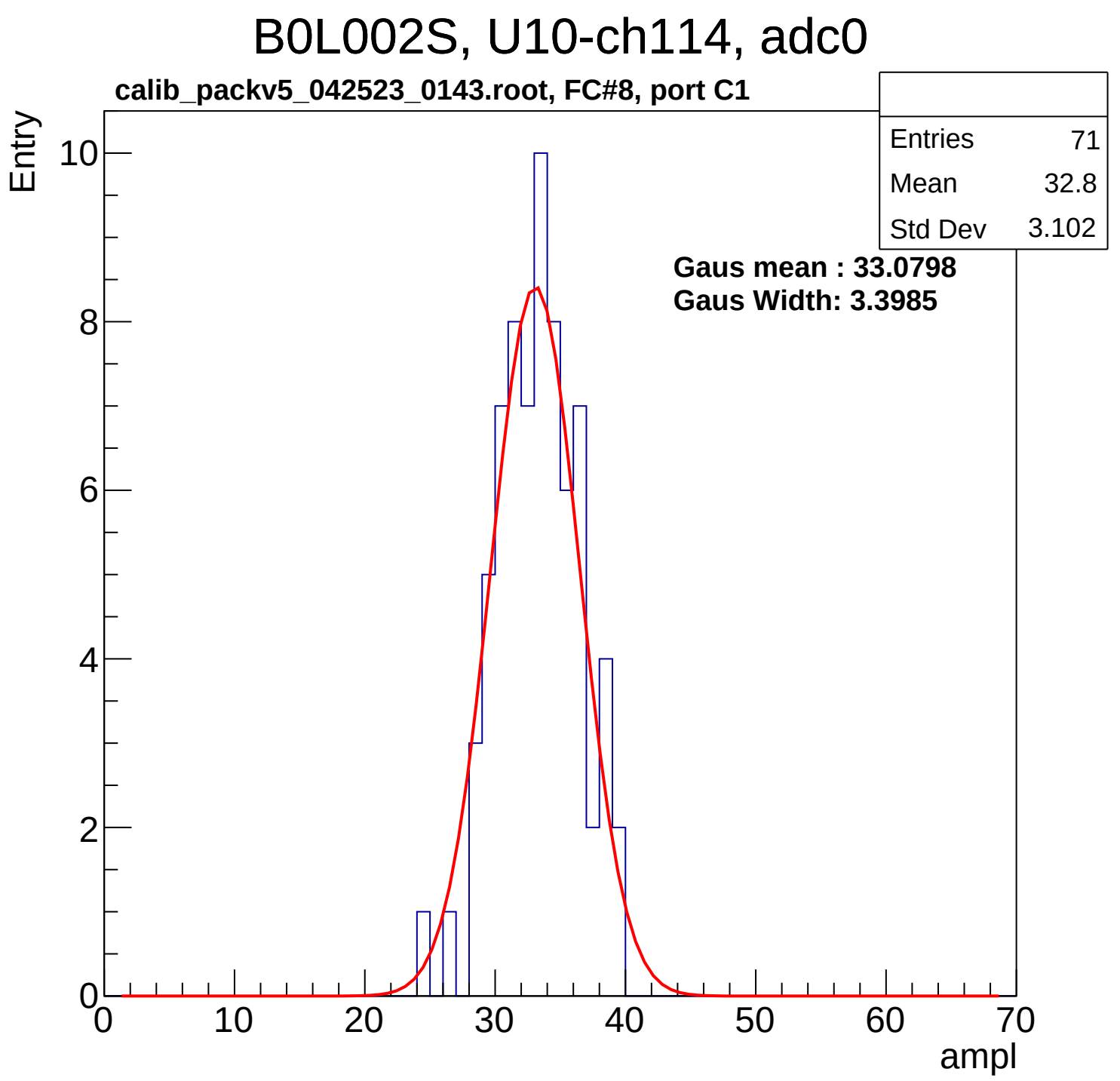

B0L002S, U10-ch113, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 50.31
Std Dev 3.335
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

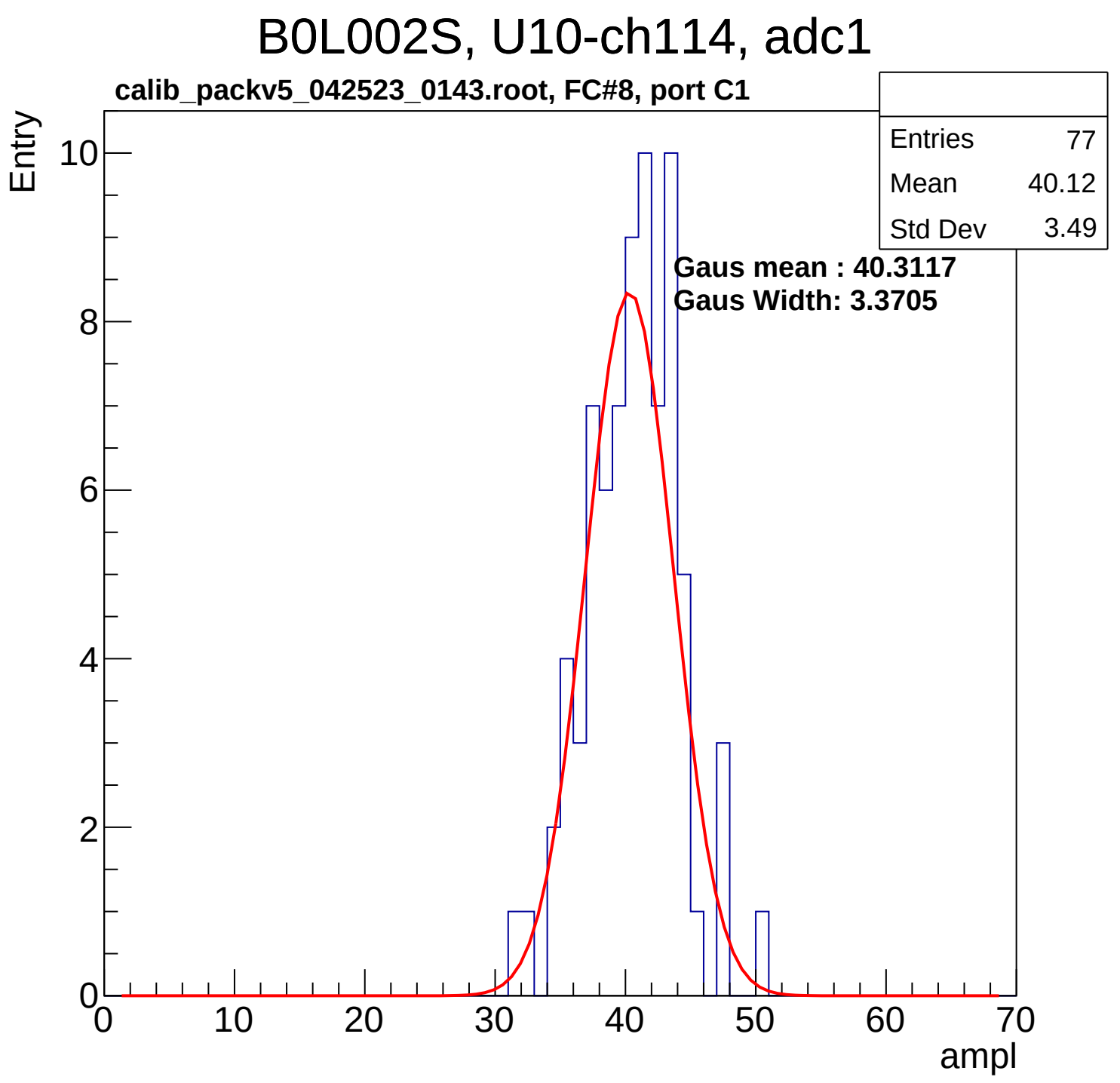

B0L002S, U10-ch113, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 55.53
Std Dev 3.495
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

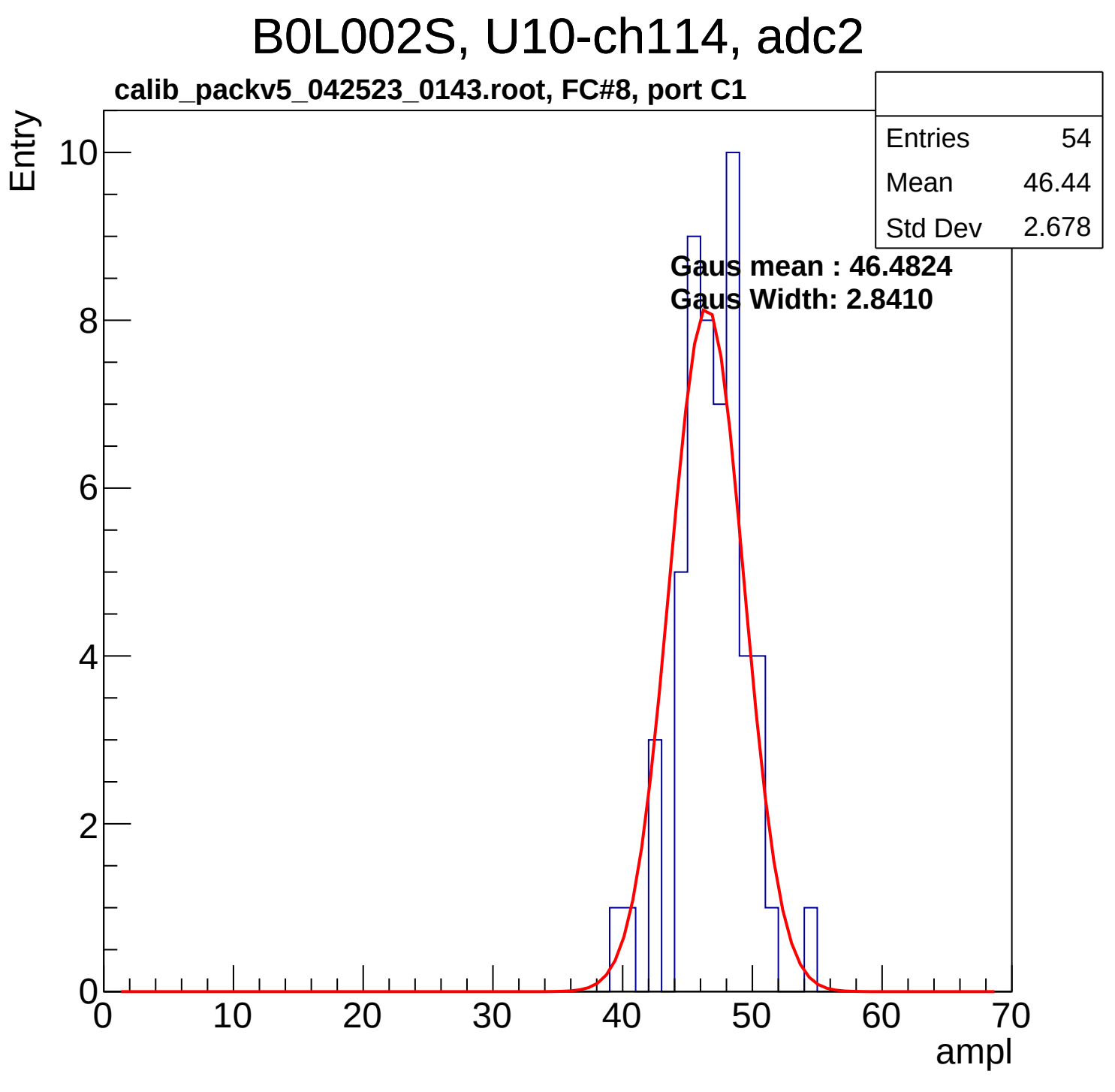

B0L002S, U10-ch113, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 42
Mean 58.98
Std Dev 9.44
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

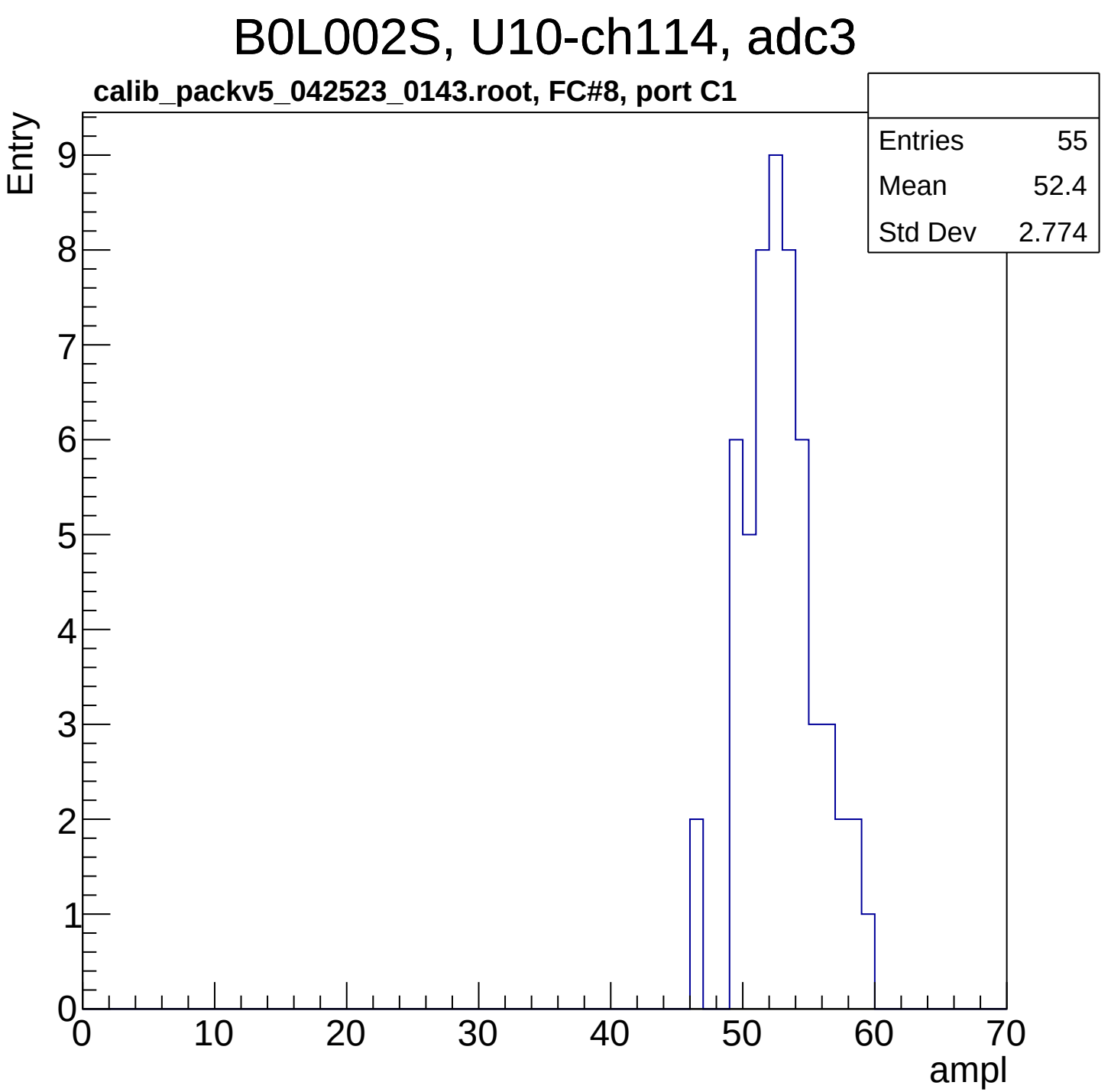

B0L002S, U10-ch113, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

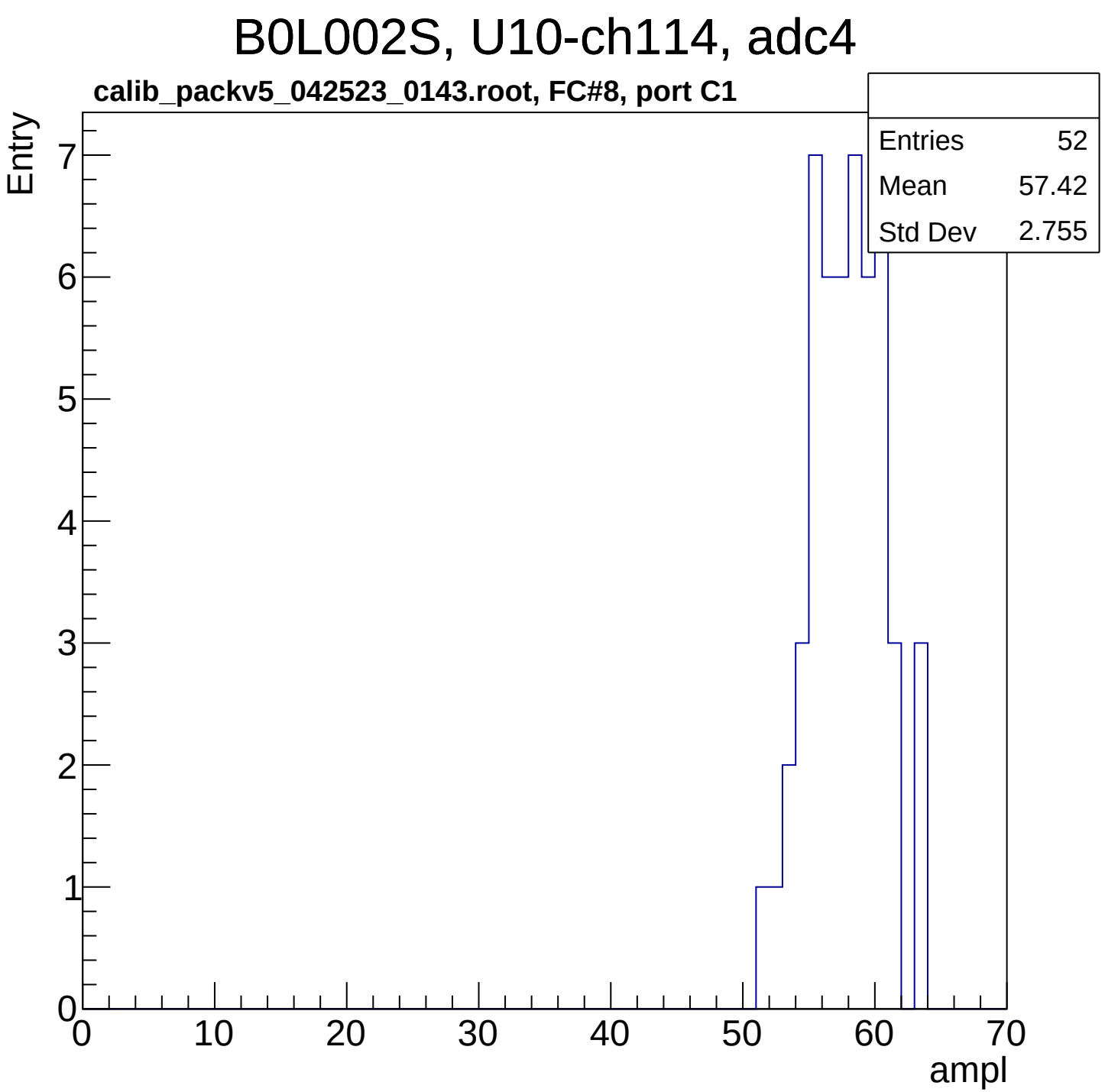

B0L002S, U10-ch113, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

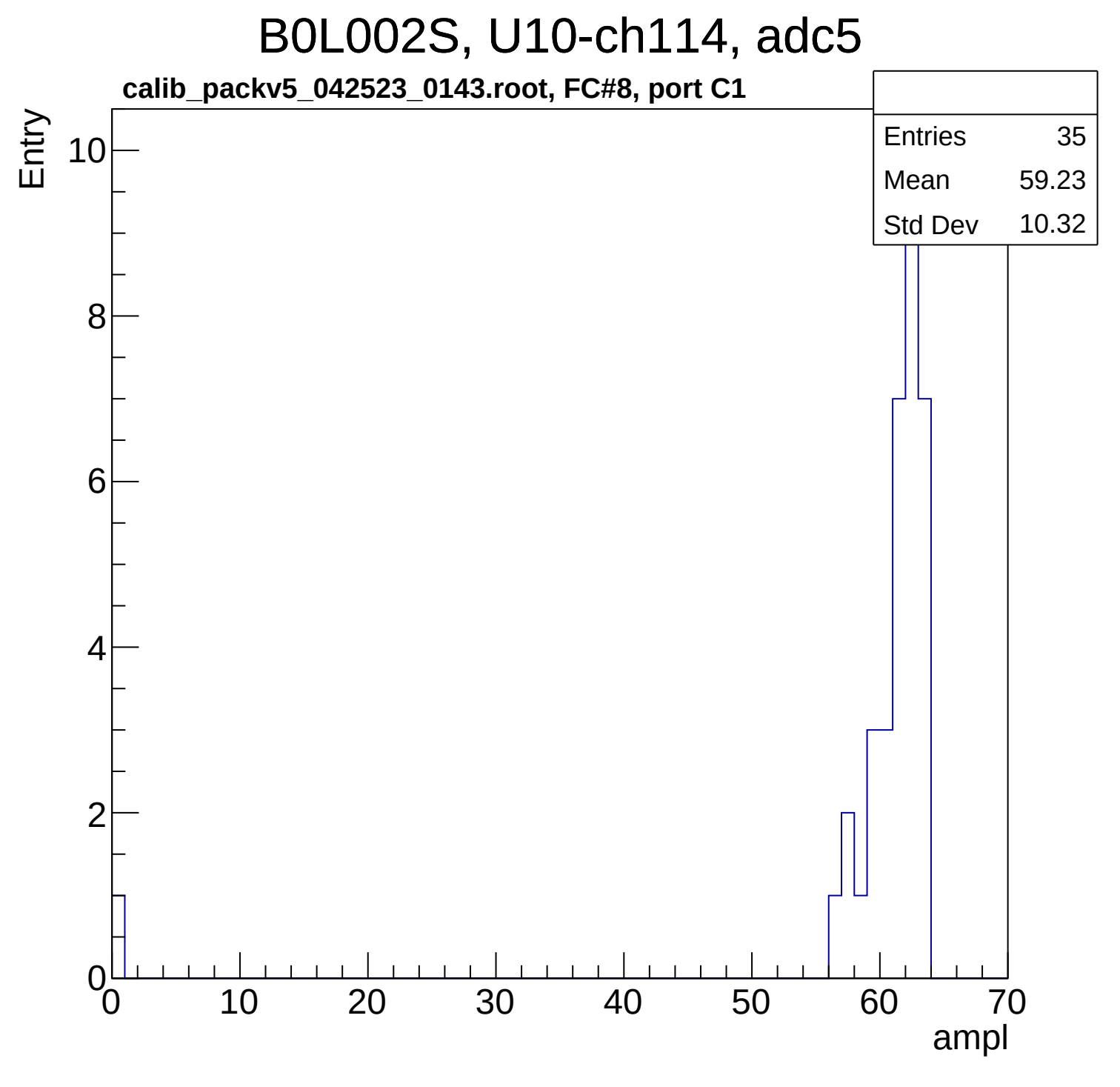

B0L002S, U10-ch114, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 32.8
Std Dev 3.102
Gaus mean : 33.0798
Gaus Width: 3.3985
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

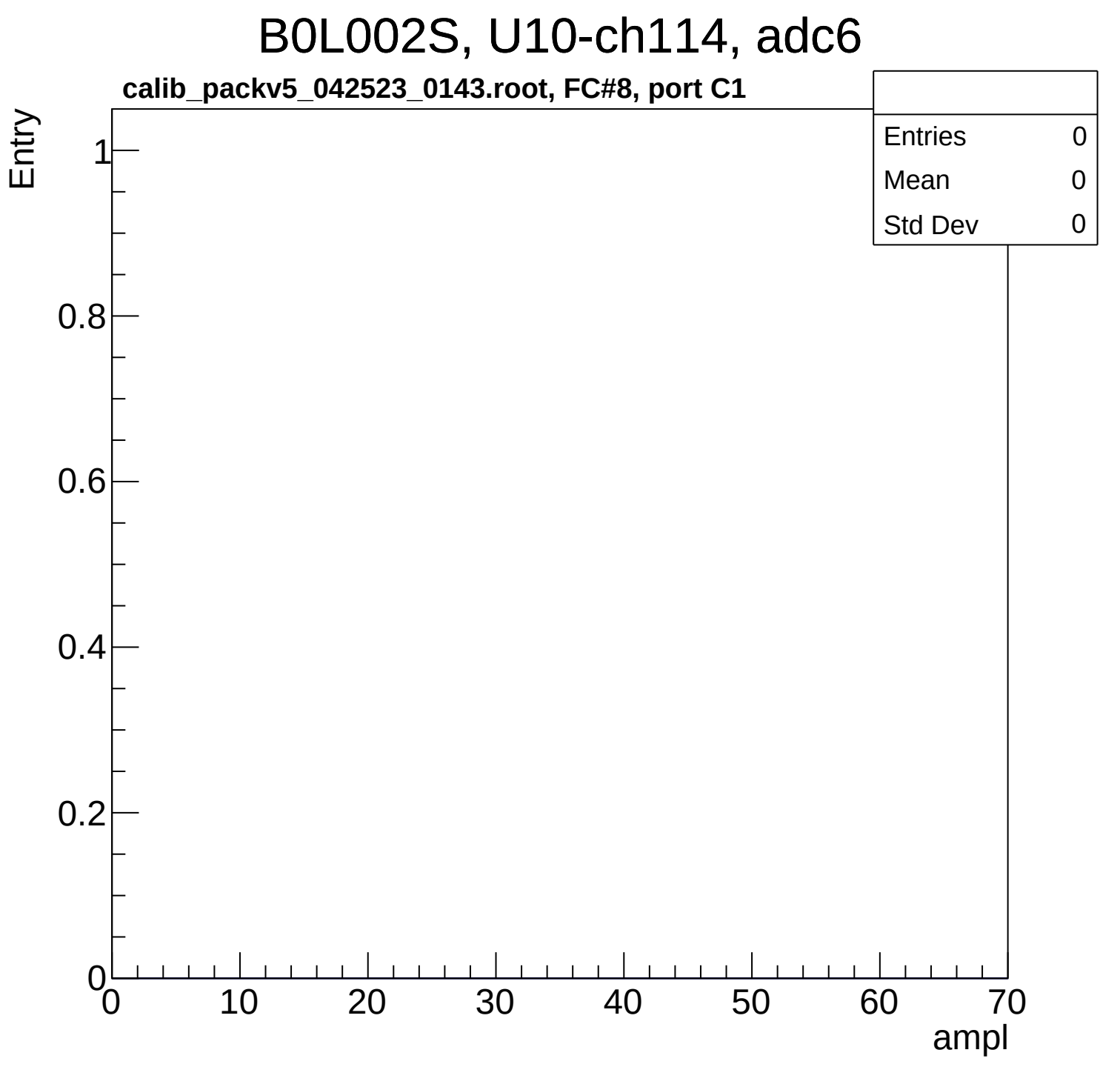

B0L002S, U10-ch114, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 40.12
Std Dev 3.49
Gaus mean : 40.3117
Gaus Width: 3.3705
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

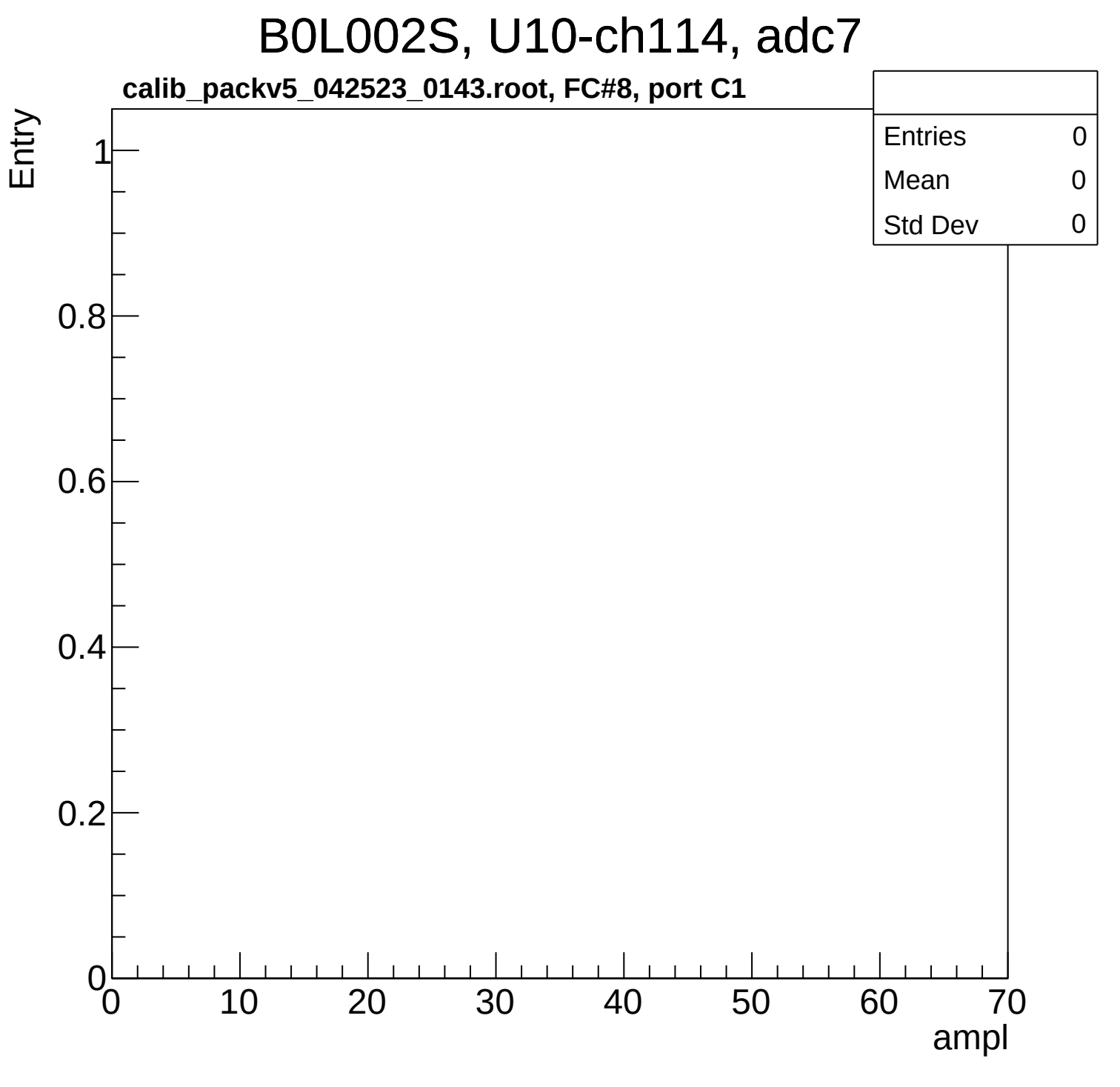

B0L002S, U10-ch114, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 46.44
Std Dev 2.678
Gaus mean : 46.4824
Gaus Width: 2.8410
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

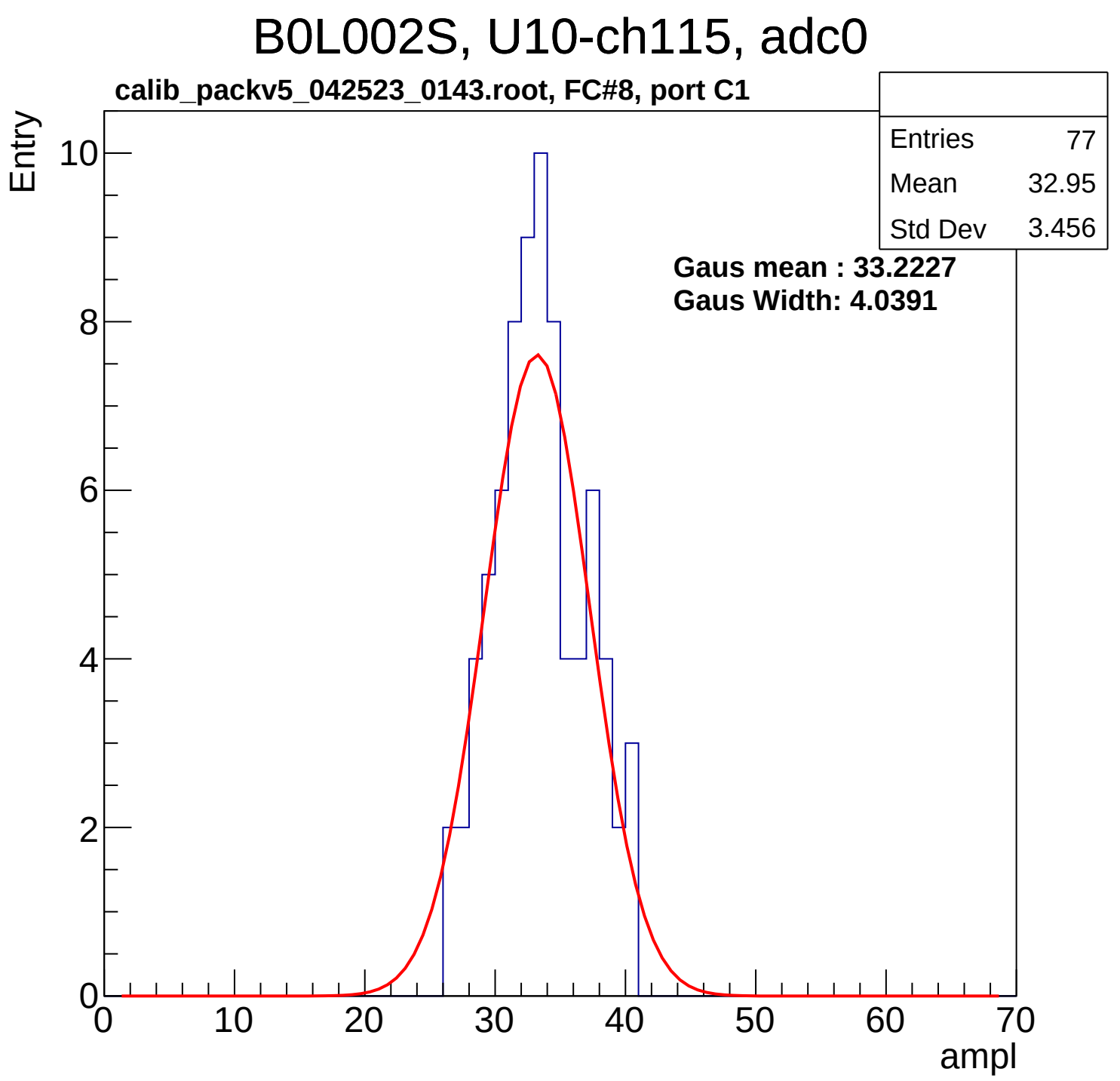

B0L002S, U10-ch114, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 52.4
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

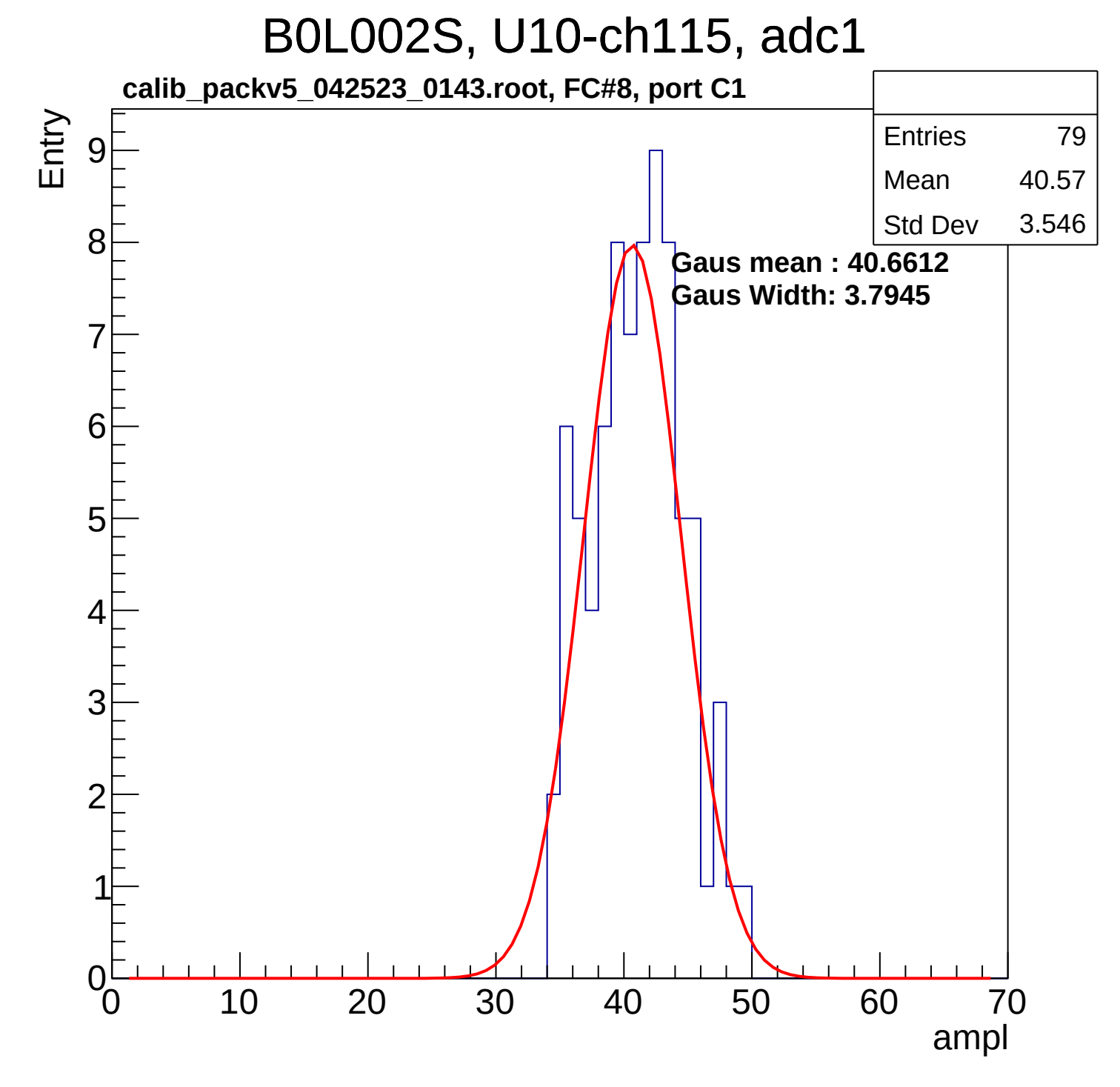

B0L002S, U10-ch114, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 57.42
Std Dev 2.755
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

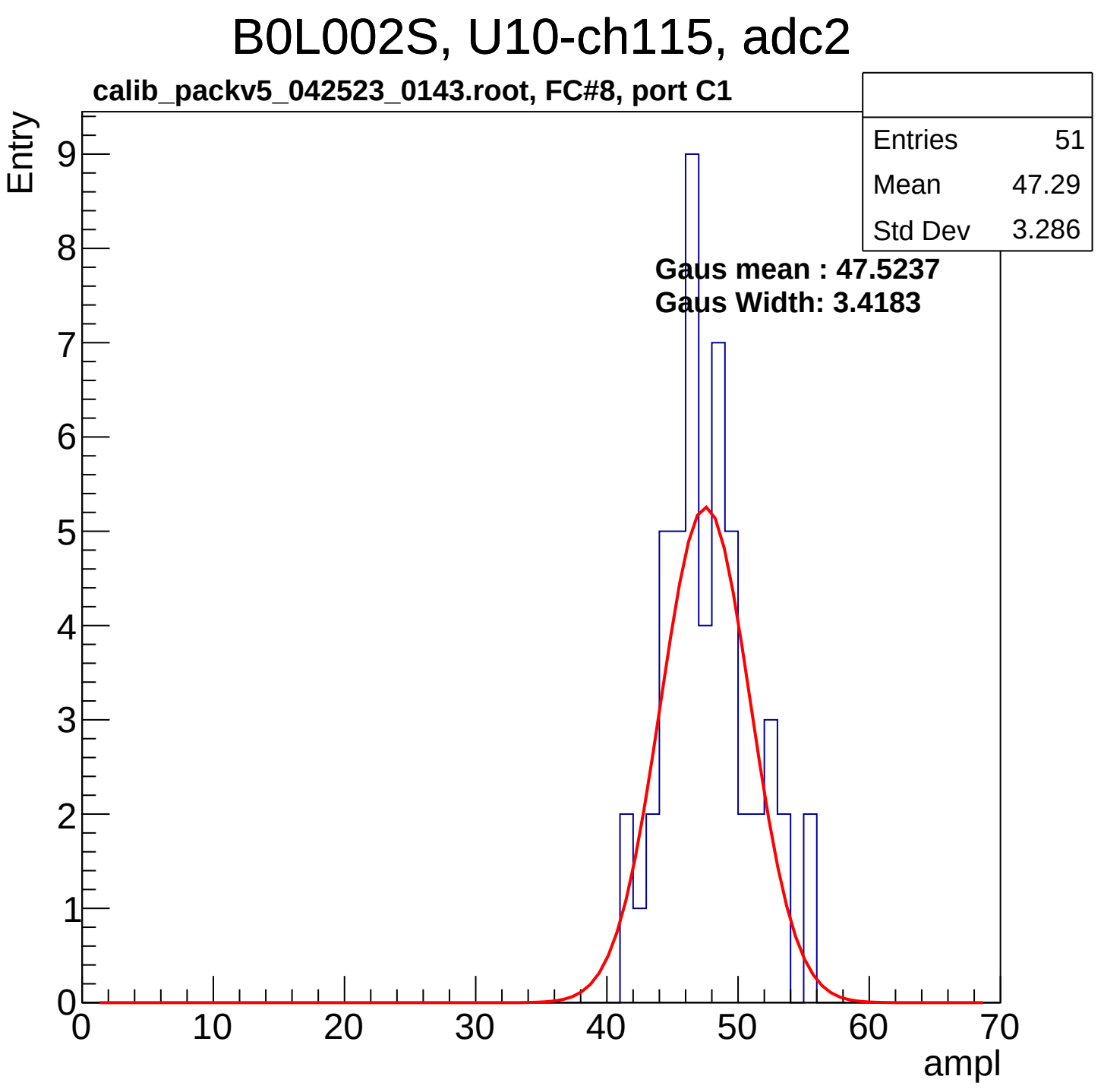

B0L002S, U10-ch114, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 35
Mean 59.23
Std Dev 10.32
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

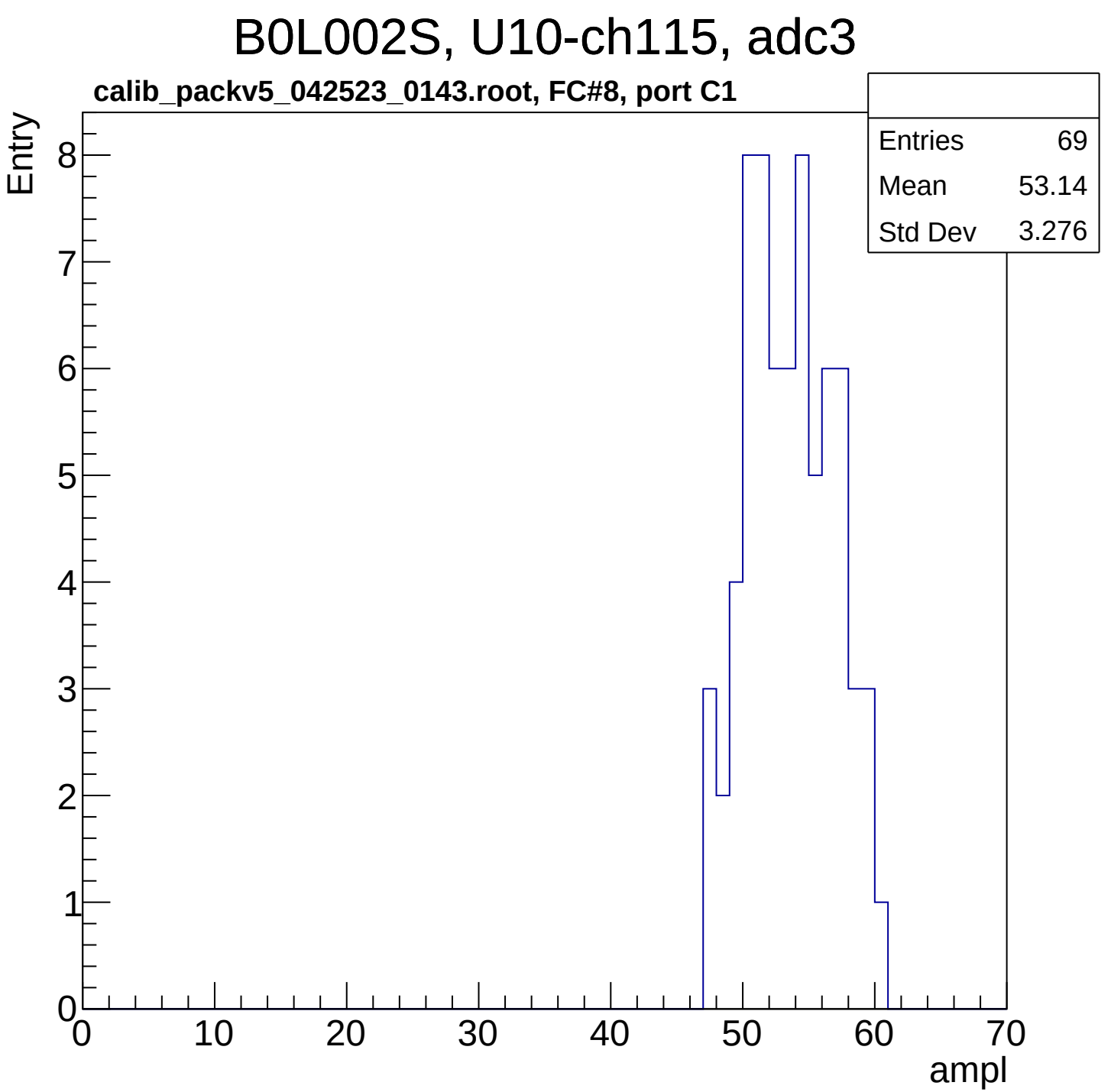

B0L002S, U10-ch114, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

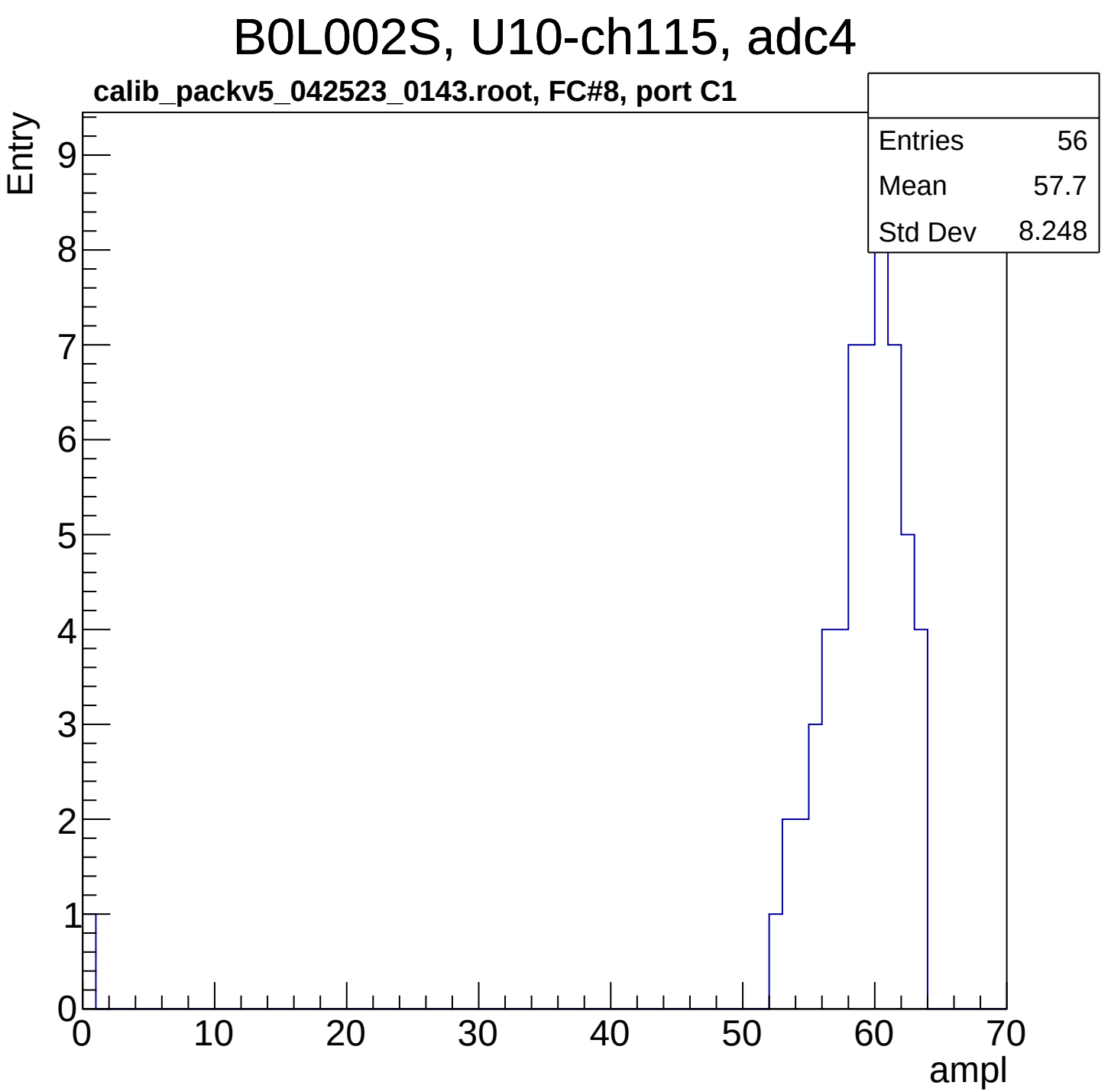

B0L002S, U10-ch114, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

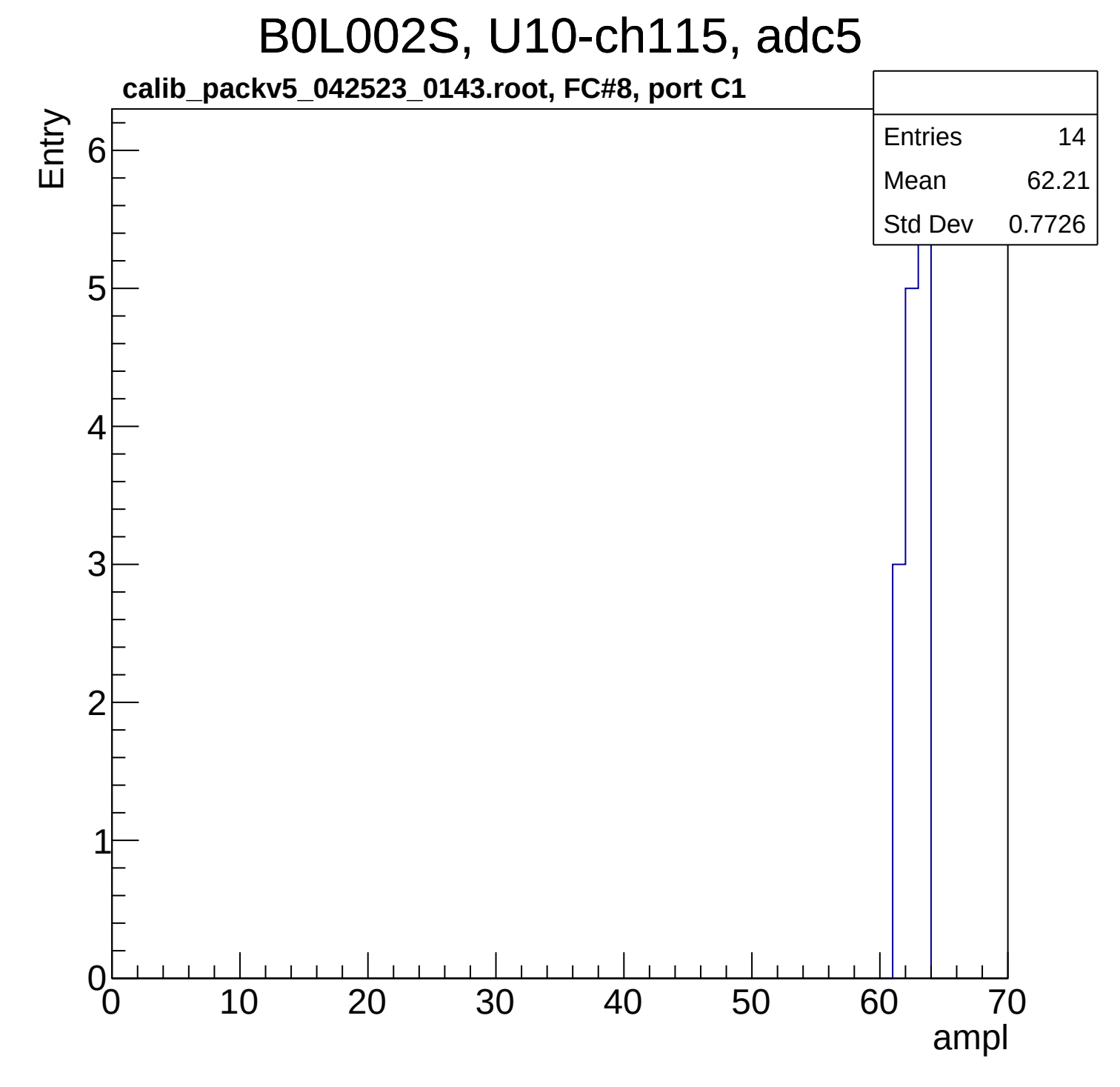

B0L002S, U10-ch115, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 32.95
Std Dev 3.456
Gaus mean : 33.2227
Gaus Width: 4.0391
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

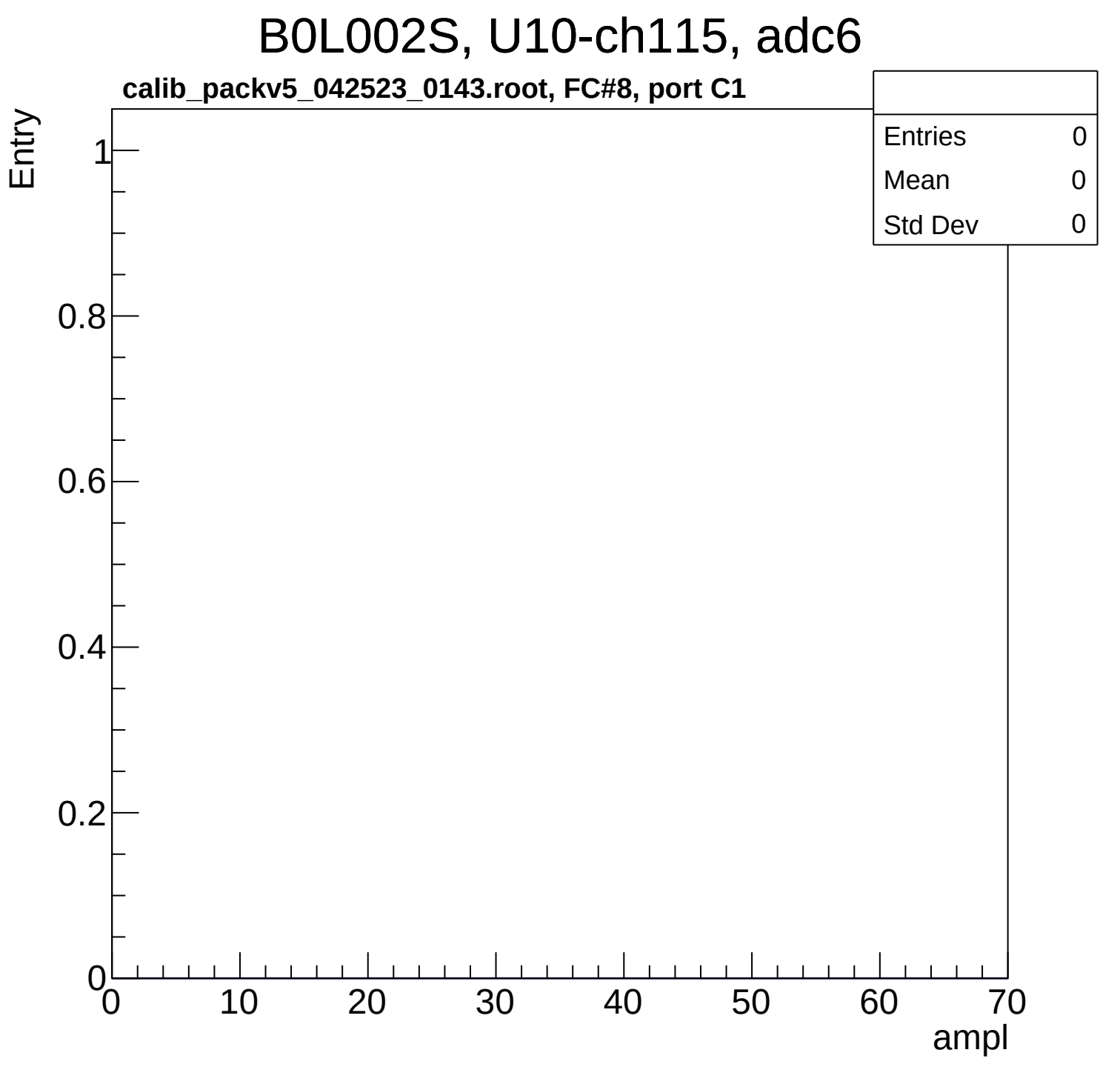

B0L002S, U10-ch115, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 79
Mean 40.57
Std Dev 3.546
Gaus mean : 40.6612
Gaus Width: 3.7945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

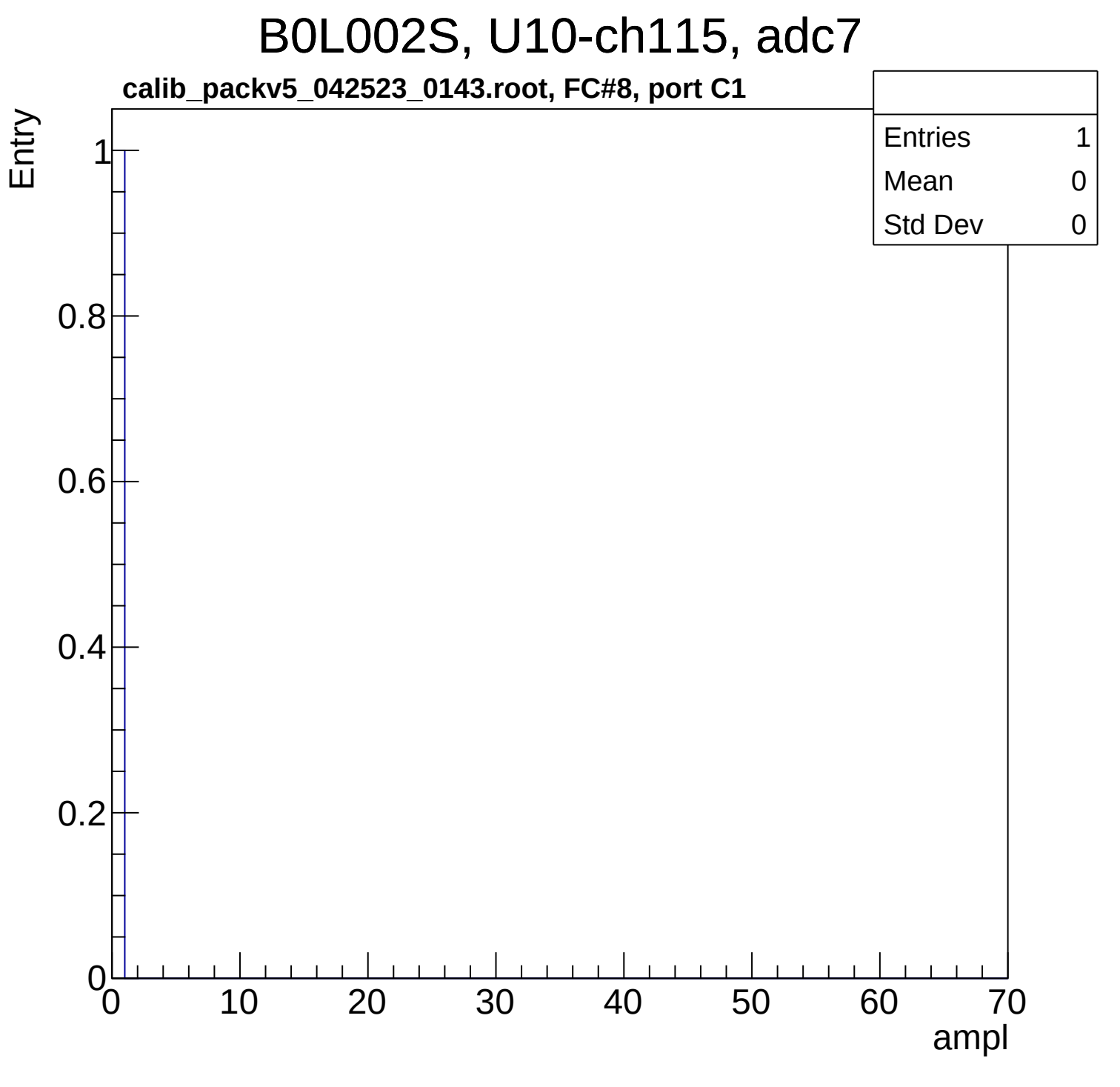

B0L002S, U10-ch115, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 51
Mean 47.29
Std Dev 3.286
Gaus mean : 47.5237
Gaus Width: 3.4183
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

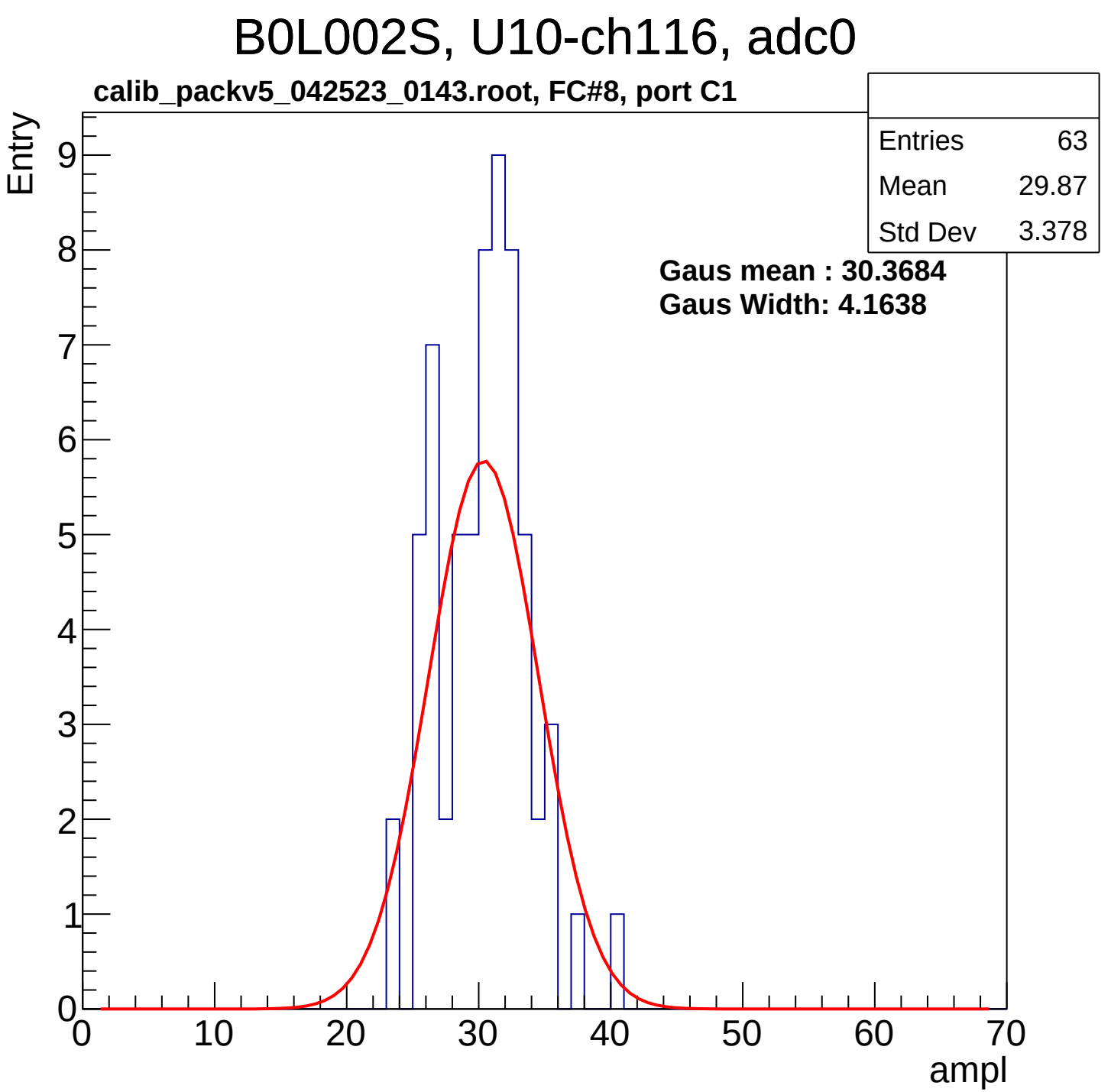

B0L002S, U10-ch115, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 53.14
Std Dev 3.276
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

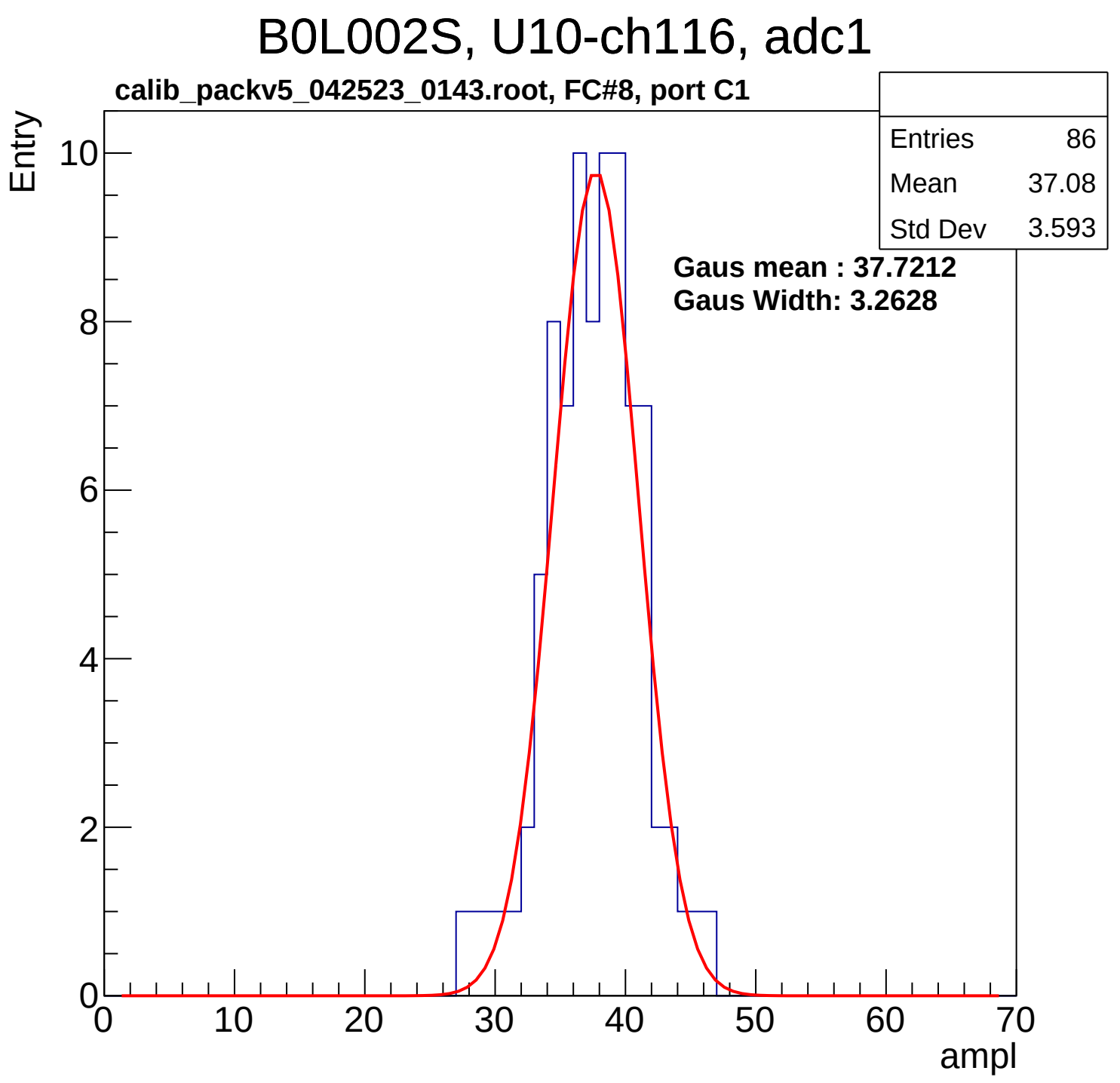

B0L002S, U10-ch115, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57.7
Std Dev 8.248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

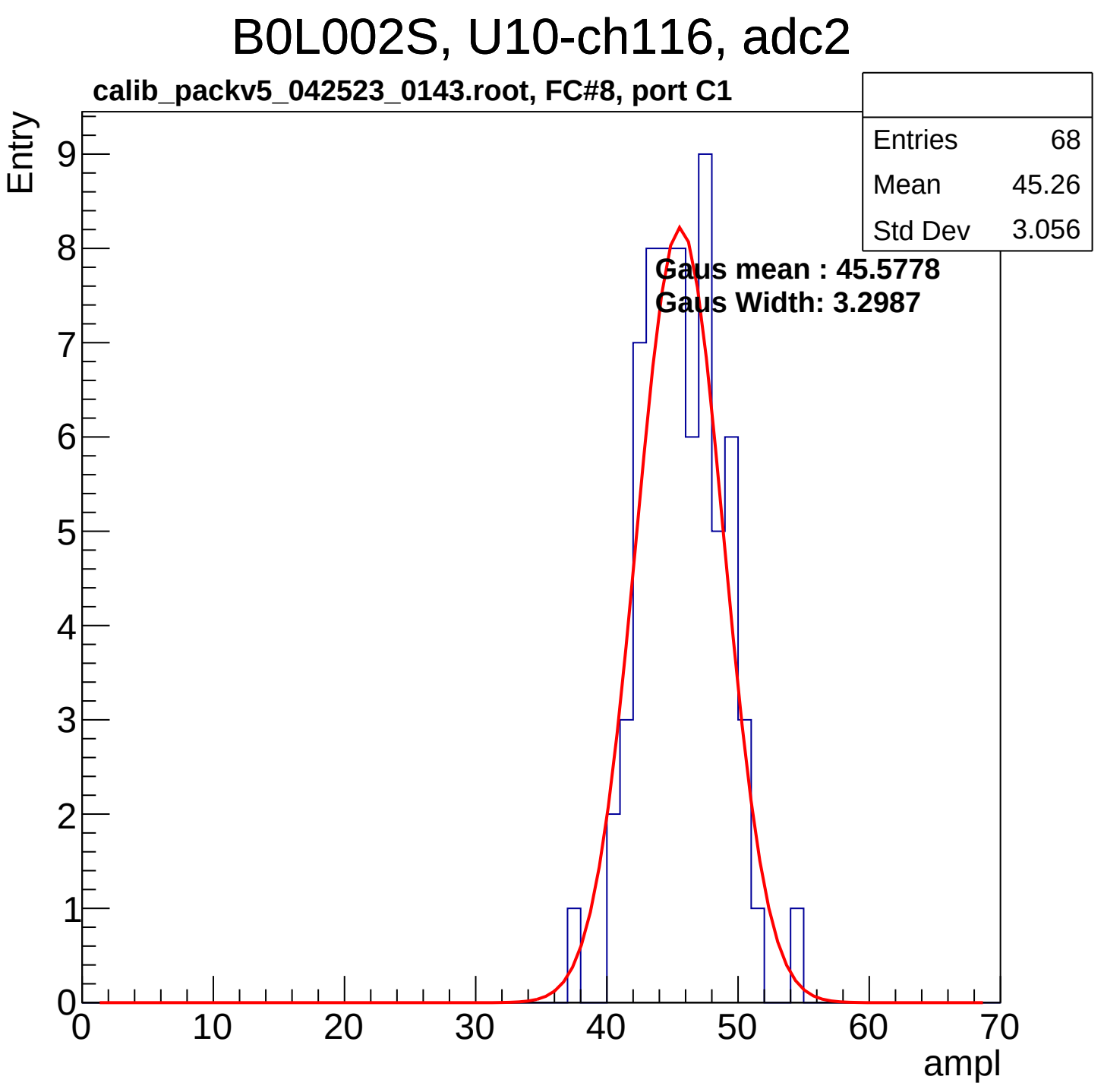

B0L002S, U10-ch115, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 14
Mean 62.21
Std Dev 0.7726
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

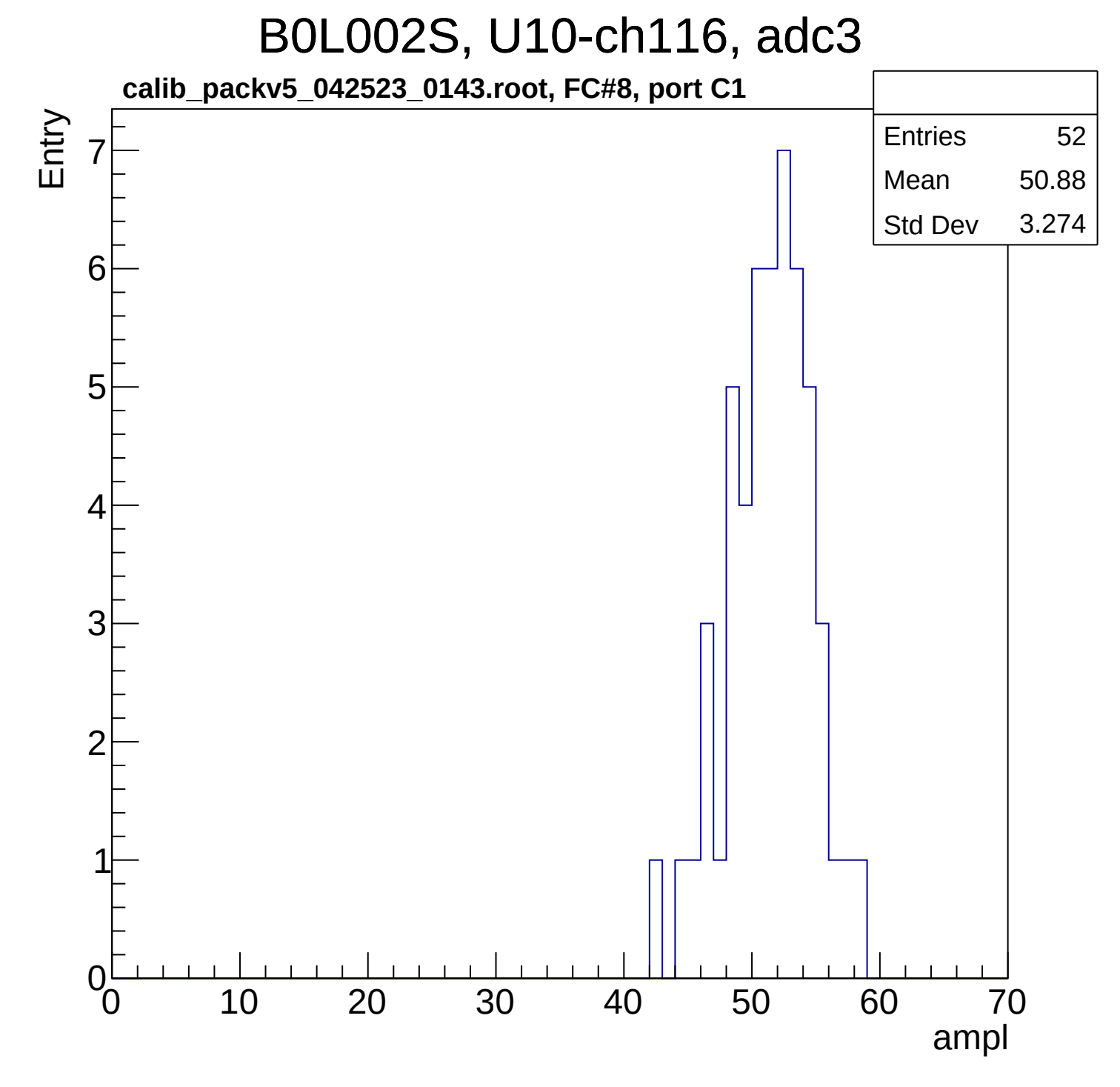

B0L002S, U10-ch115, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

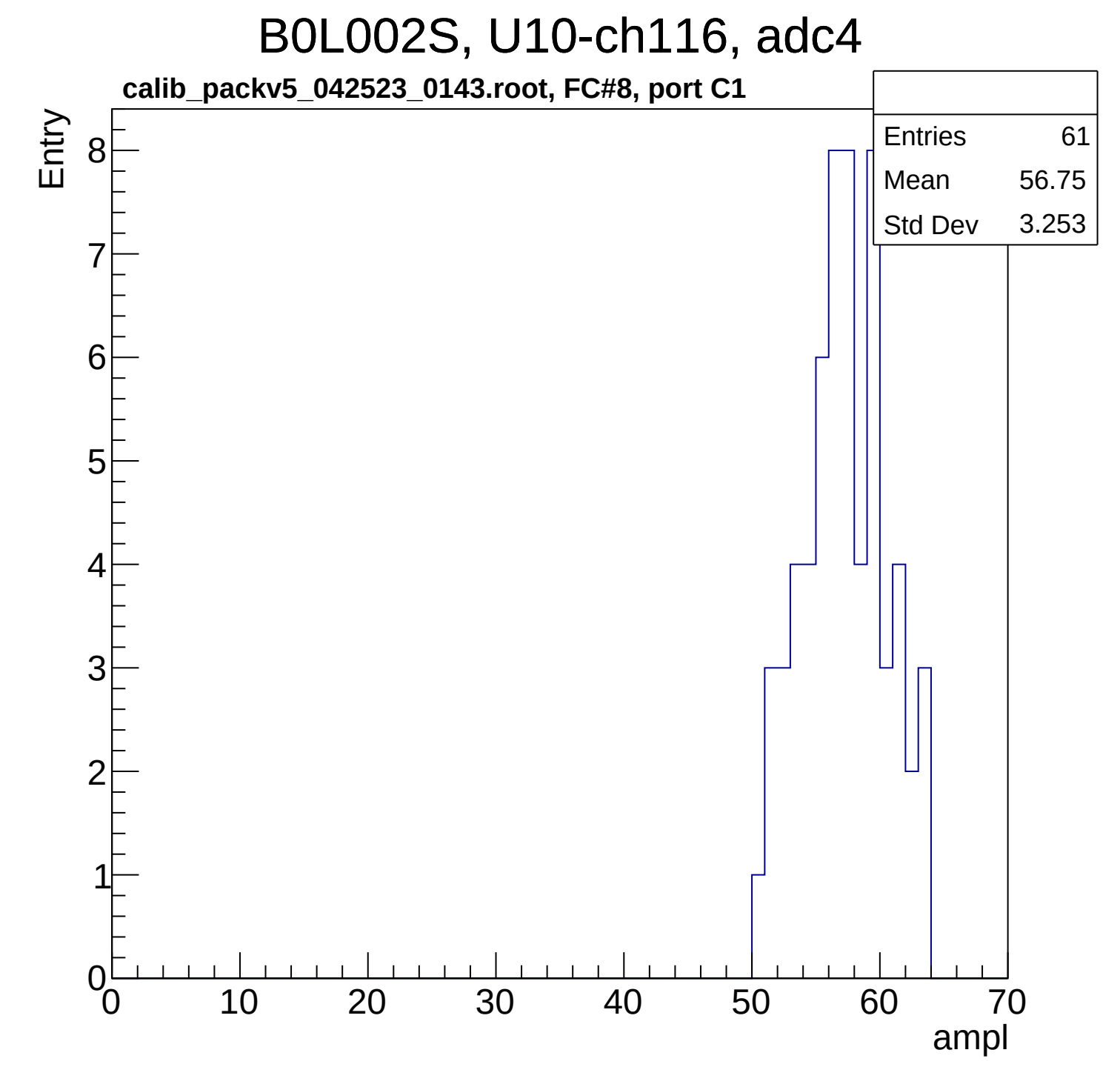

B0L002S, U10-ch115, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

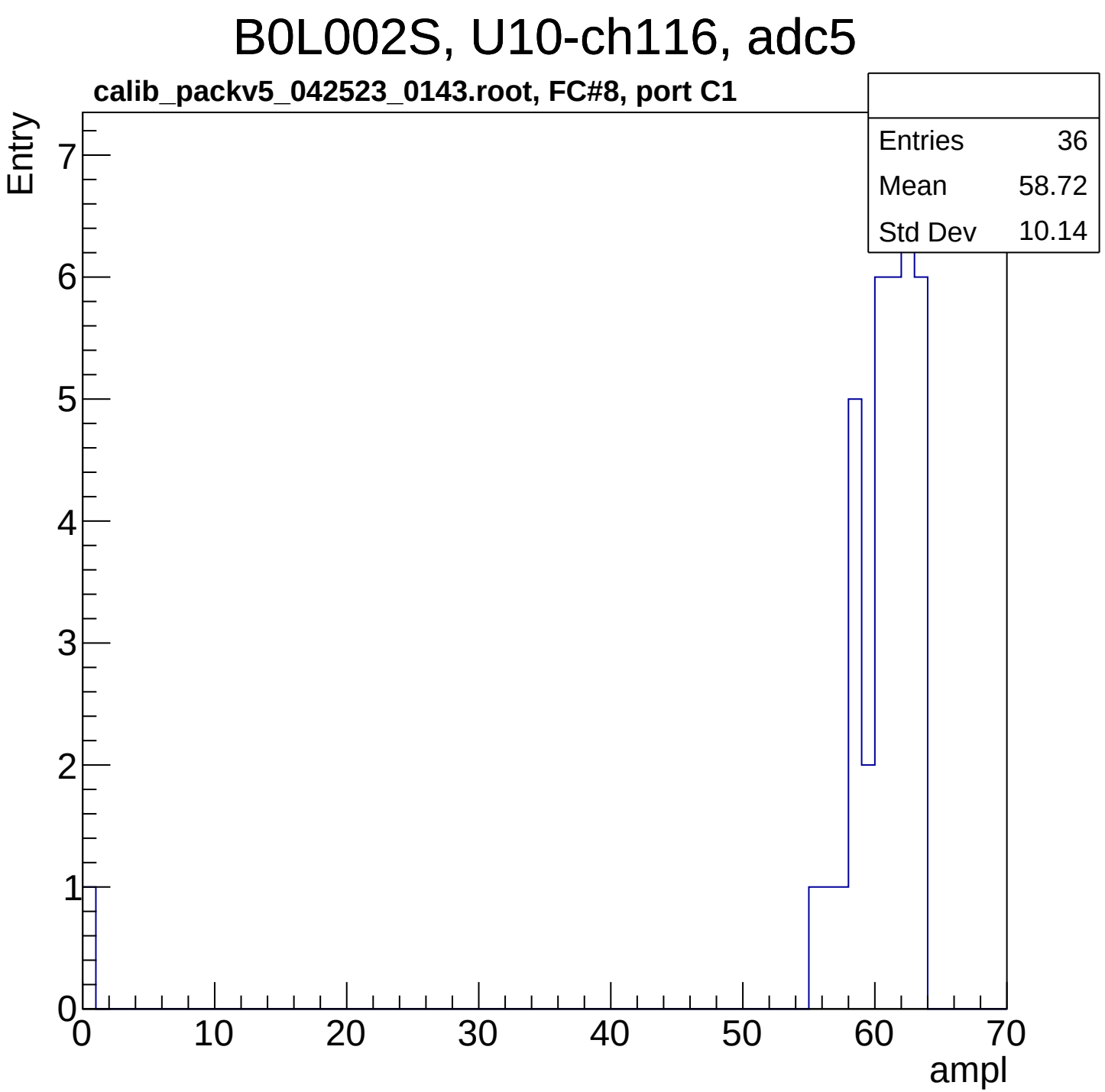

B0L002S, U10-ch116, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 29.87
Std Dev 3.378
Gaus mean : 30.3684
Gaus Width: 4.1638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

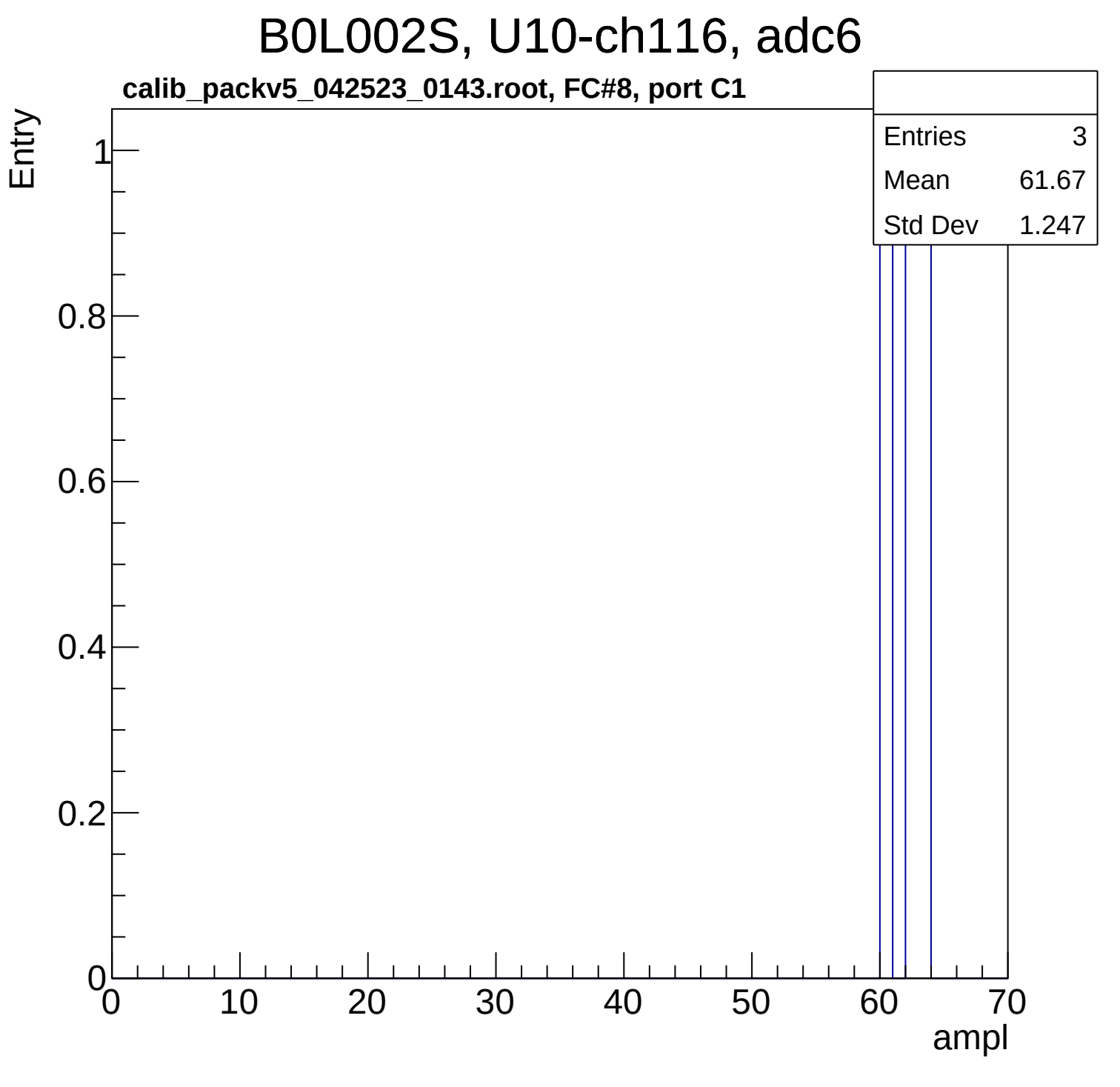

B0L002S, U10-ch116, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 86
Mean 37.08
Std Dev 3.593
Gaus mean : 37.7212
Gaus Width: 3.2628
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

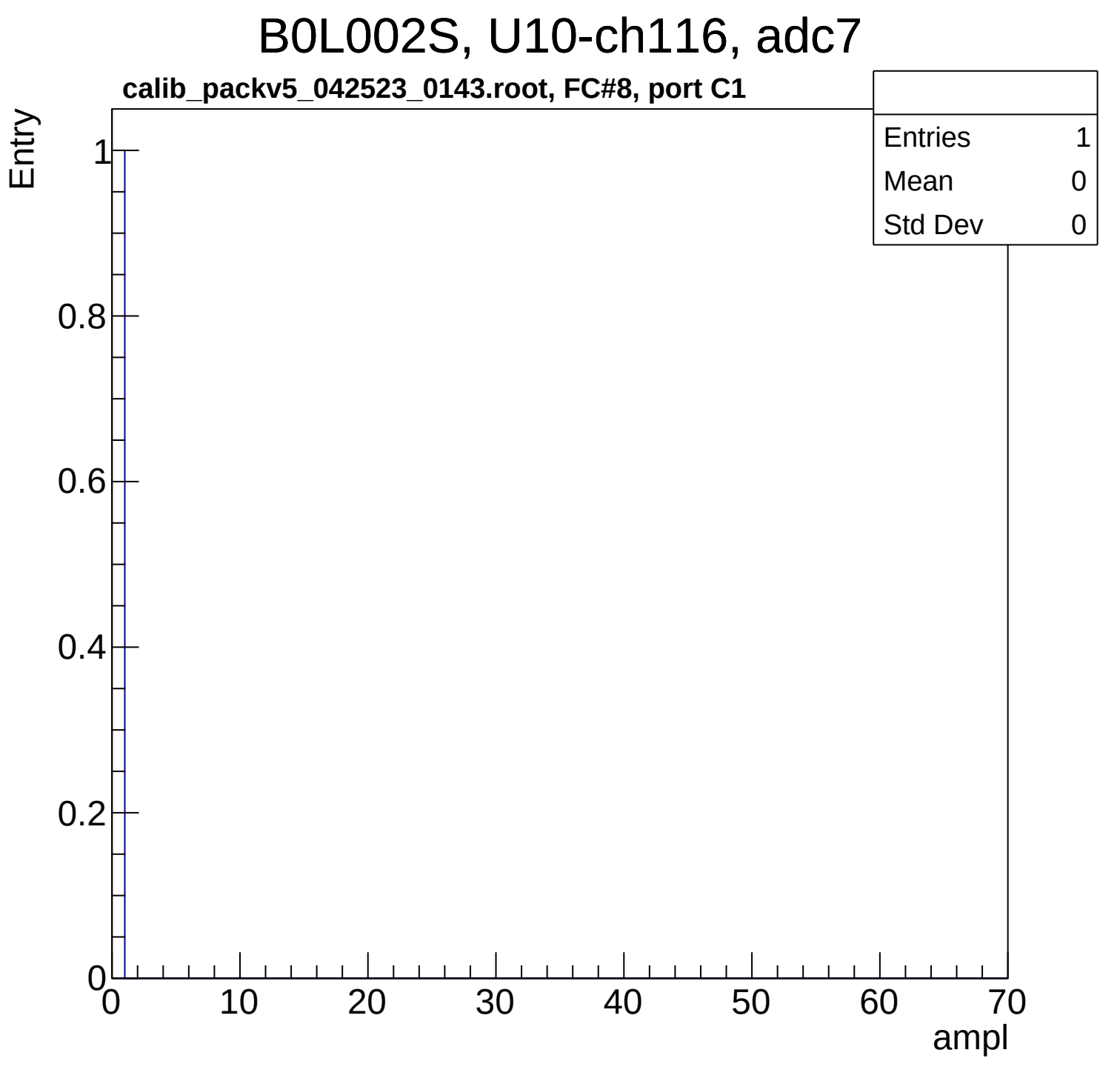

B0L002S, U10-ch116, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 45.26
Std Dev 3.056
Gaus mean : 45.5778
Gaus Width: 3.2987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

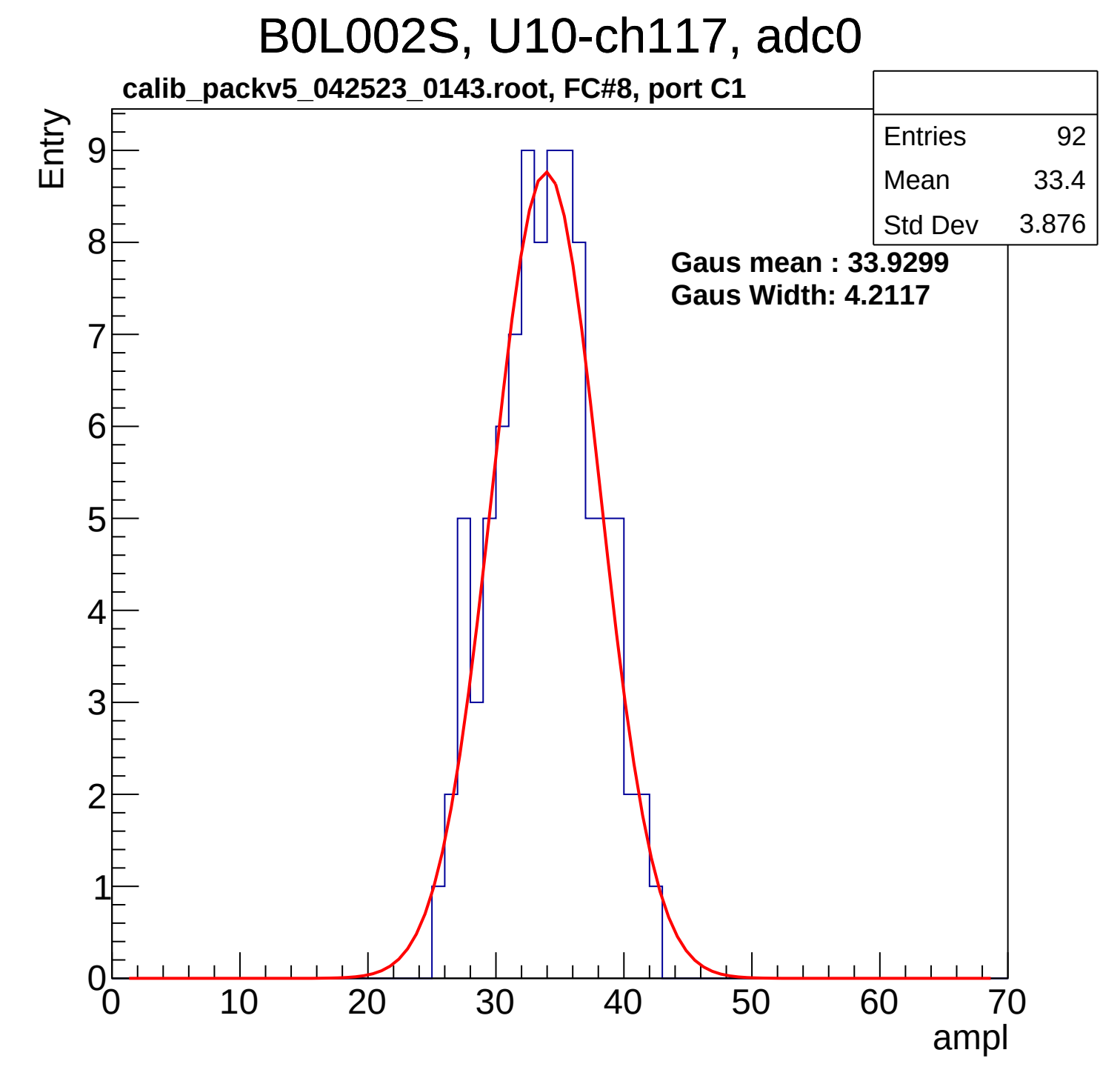

B0L002S, U10-ch116, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 50.88
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

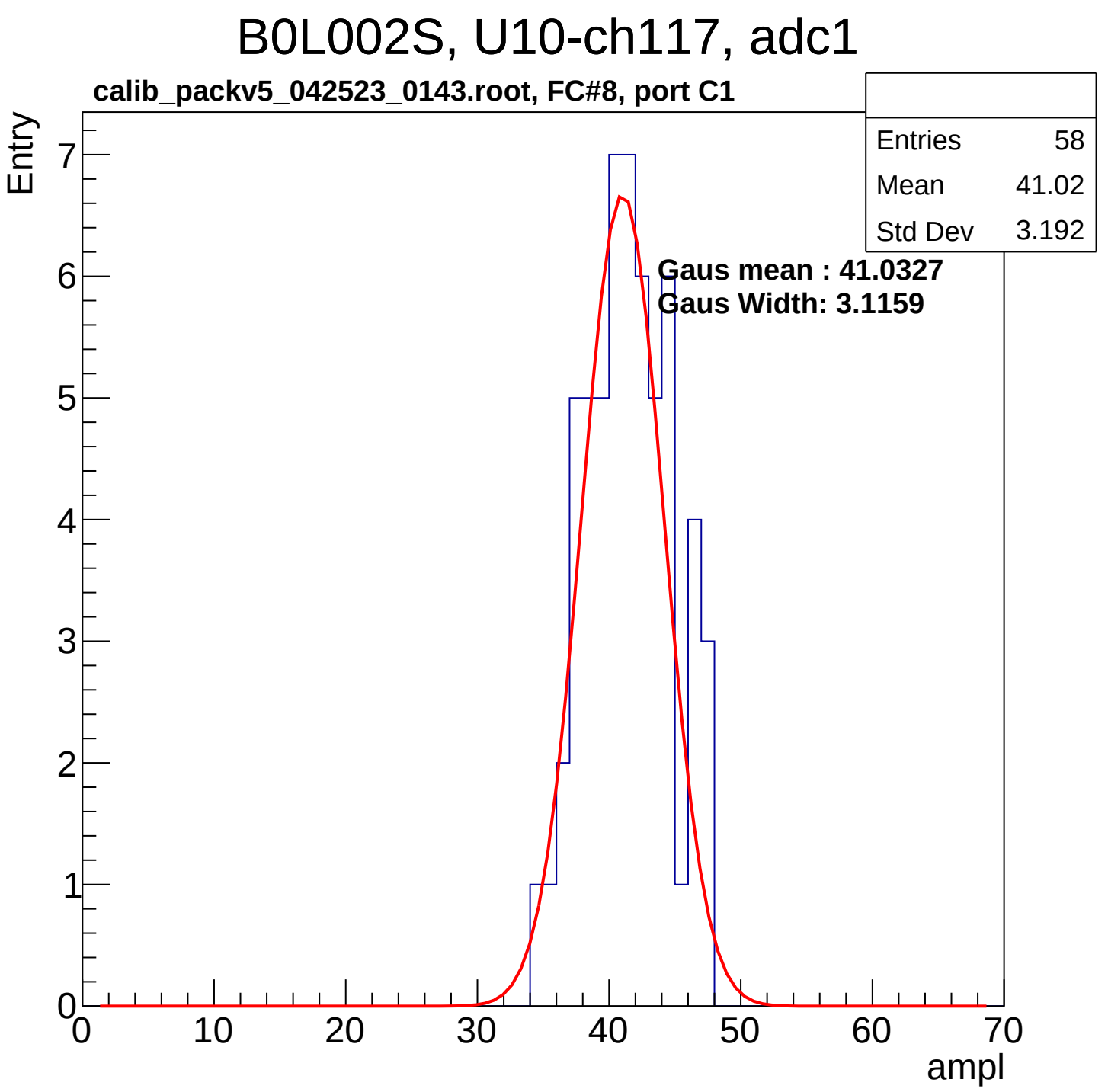

B0L002S, U10-ch116, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 56.75
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

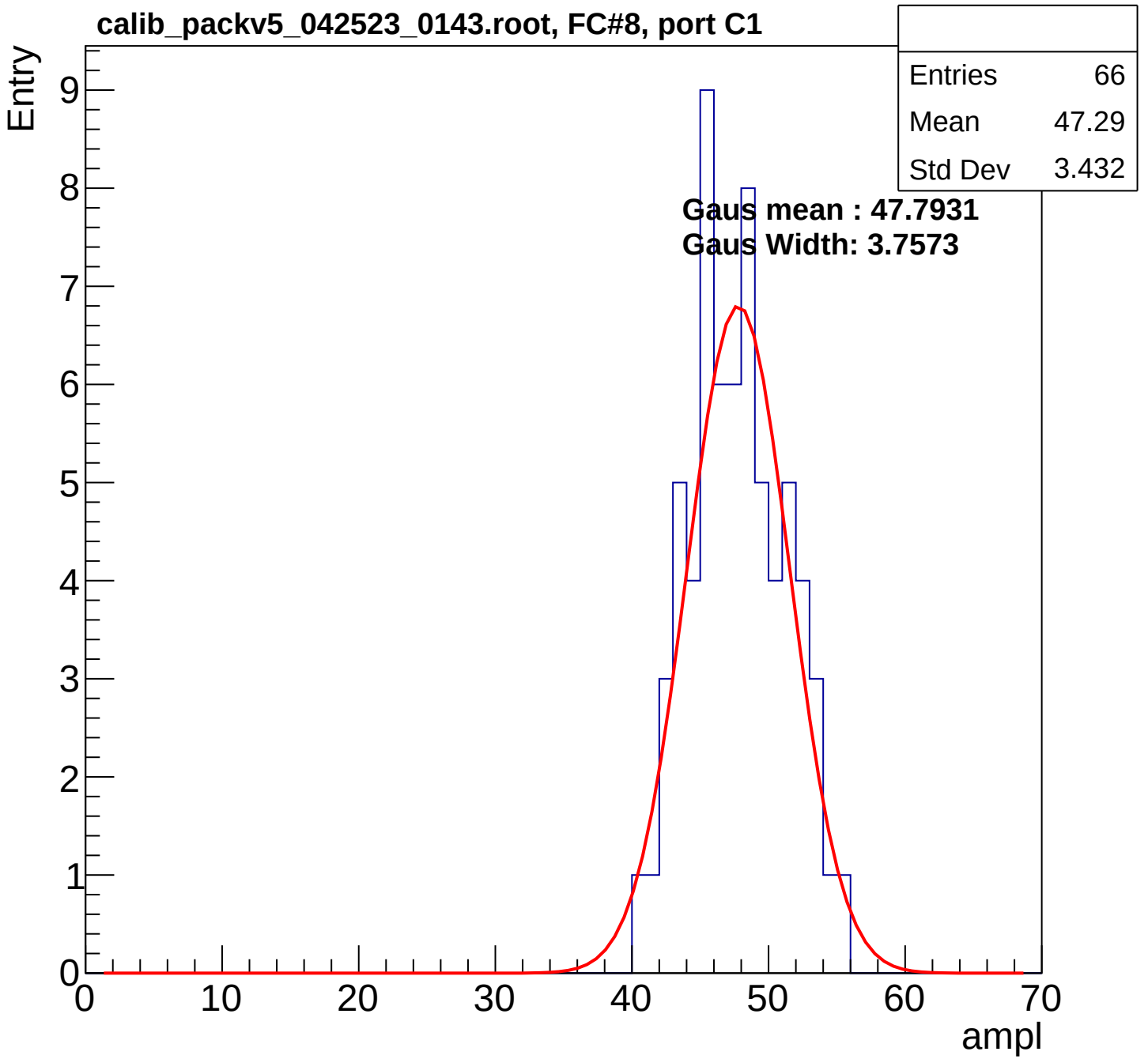

B0L002S, U10-ch116, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 36
Mean 58.72
Std Dev 10.14
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

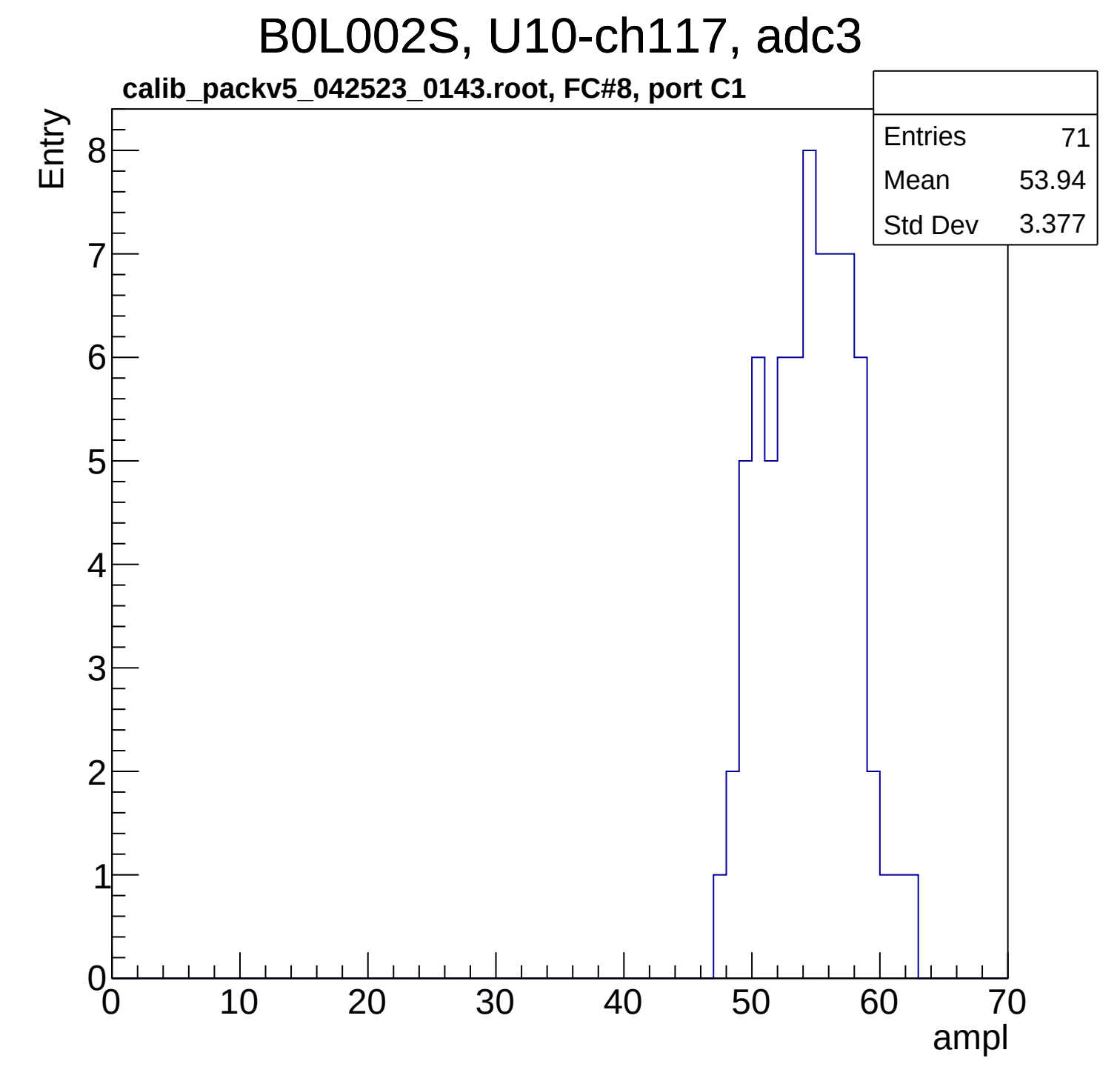

B0L002S, U10-ch116, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

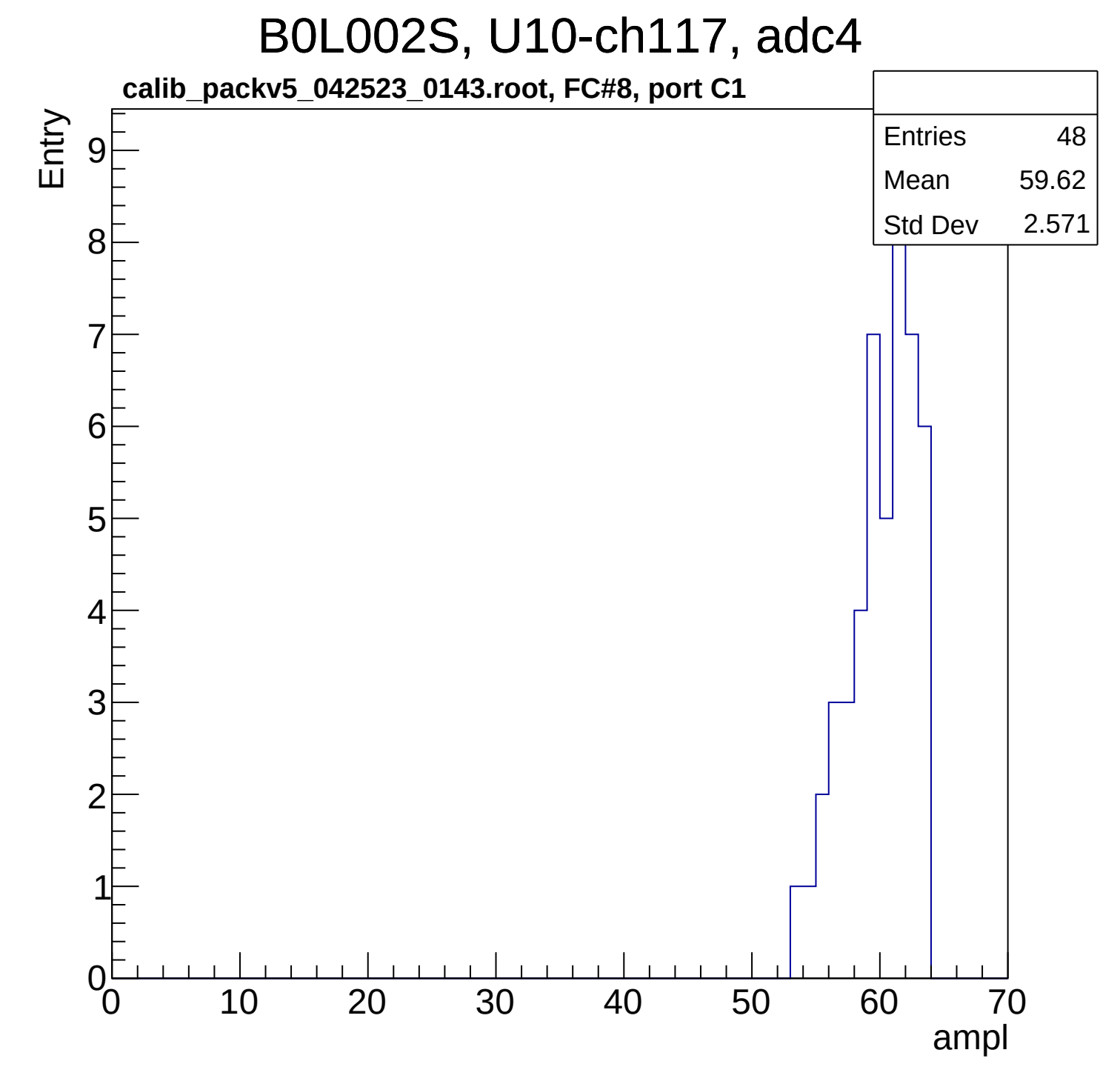

B0L002S, U10-ch116, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

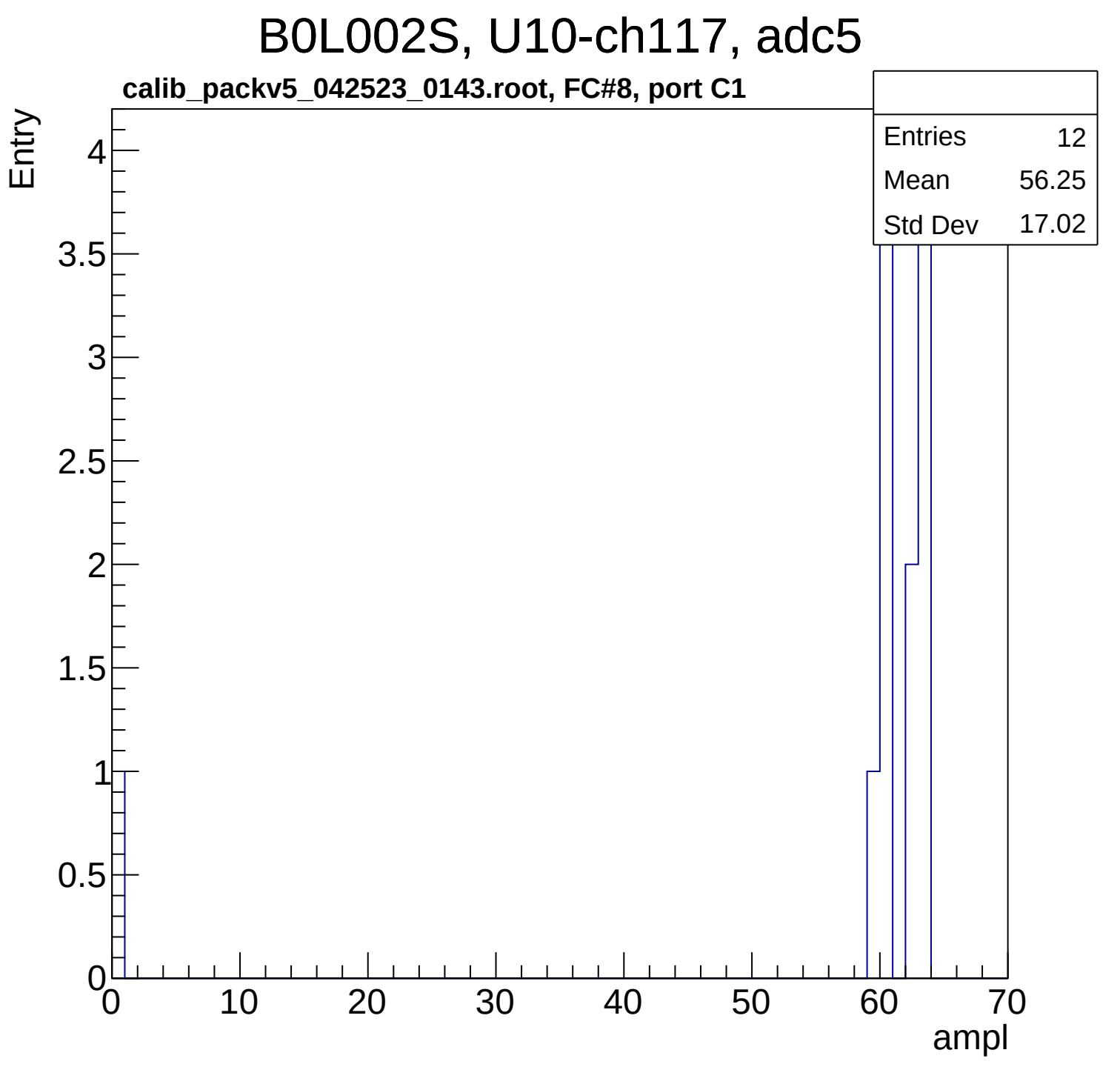

B0L002S, U10-ch117, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 92
Mean 33.4
Std Dev 3.876
Gaus mean : 33.9299
Gaus Width: 4.2117
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

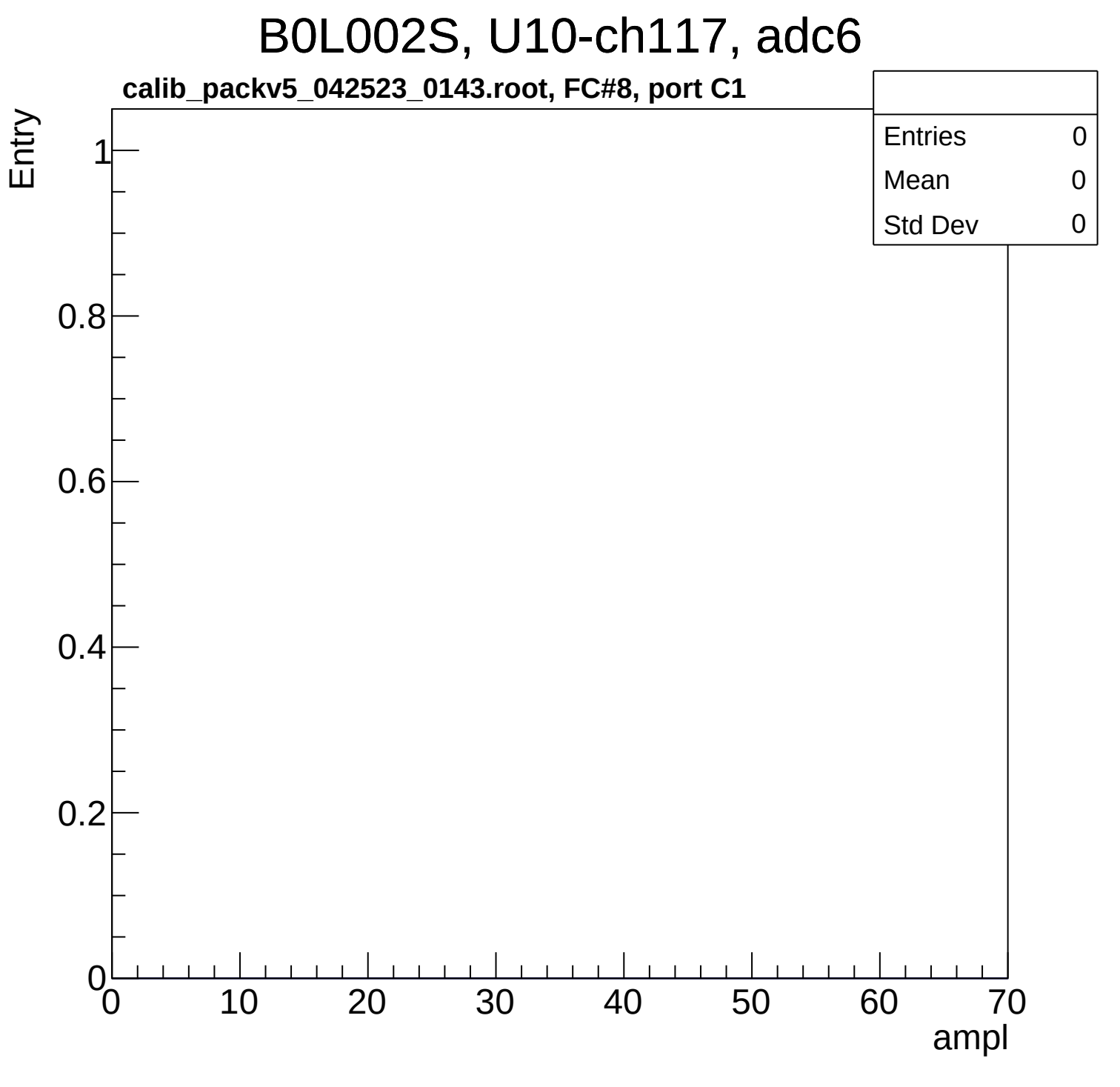

B0L002S, U10-ch117, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 41.02
Std Dev 3.192
Gaus mean : 41.0327
Gaus Width: 3.1159
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

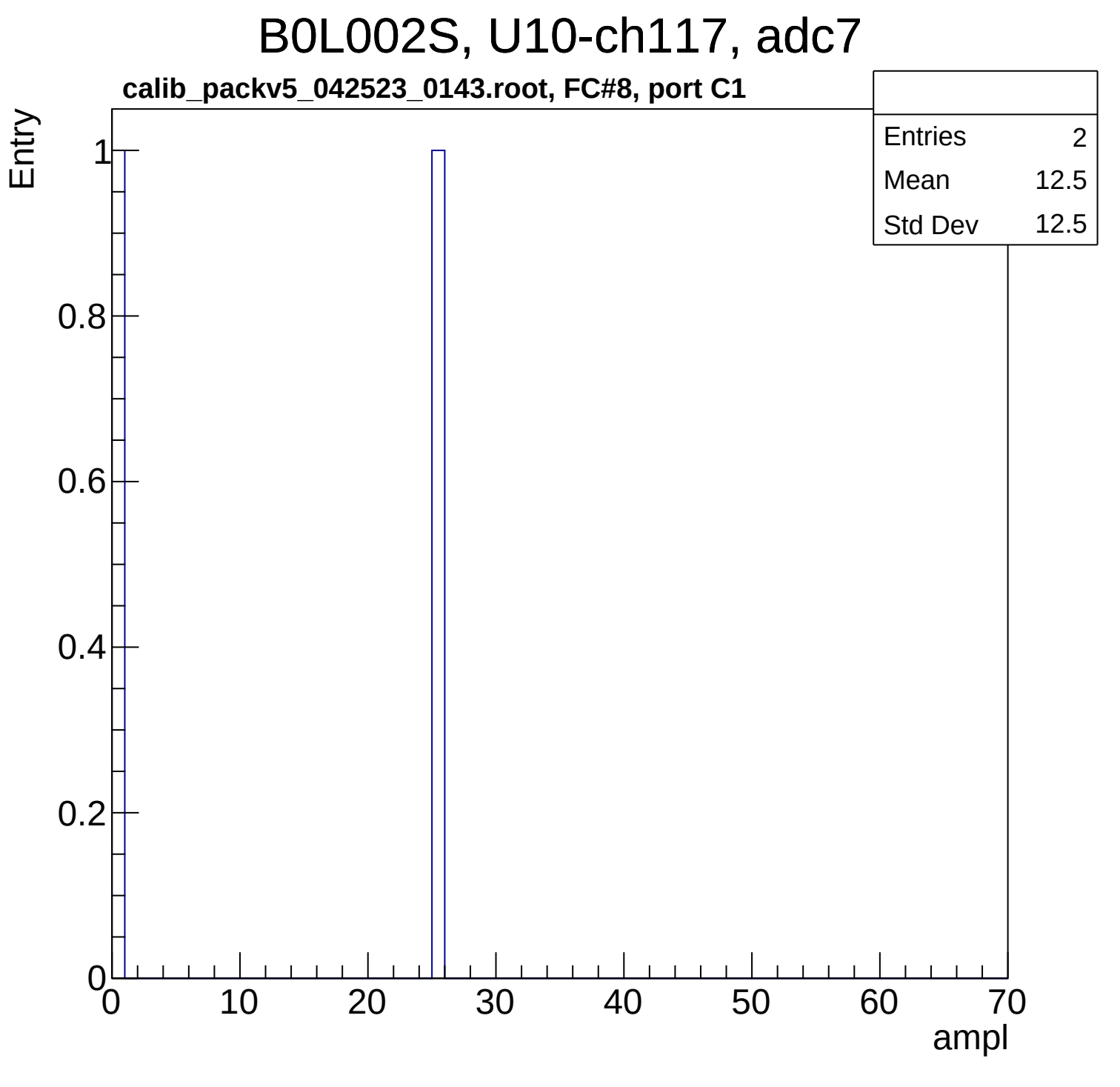

B0L002S, U10-ch117, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 47.29
Std Dev 3.432
Gaus mean : 47.7931
Gaus Width: 3.7573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

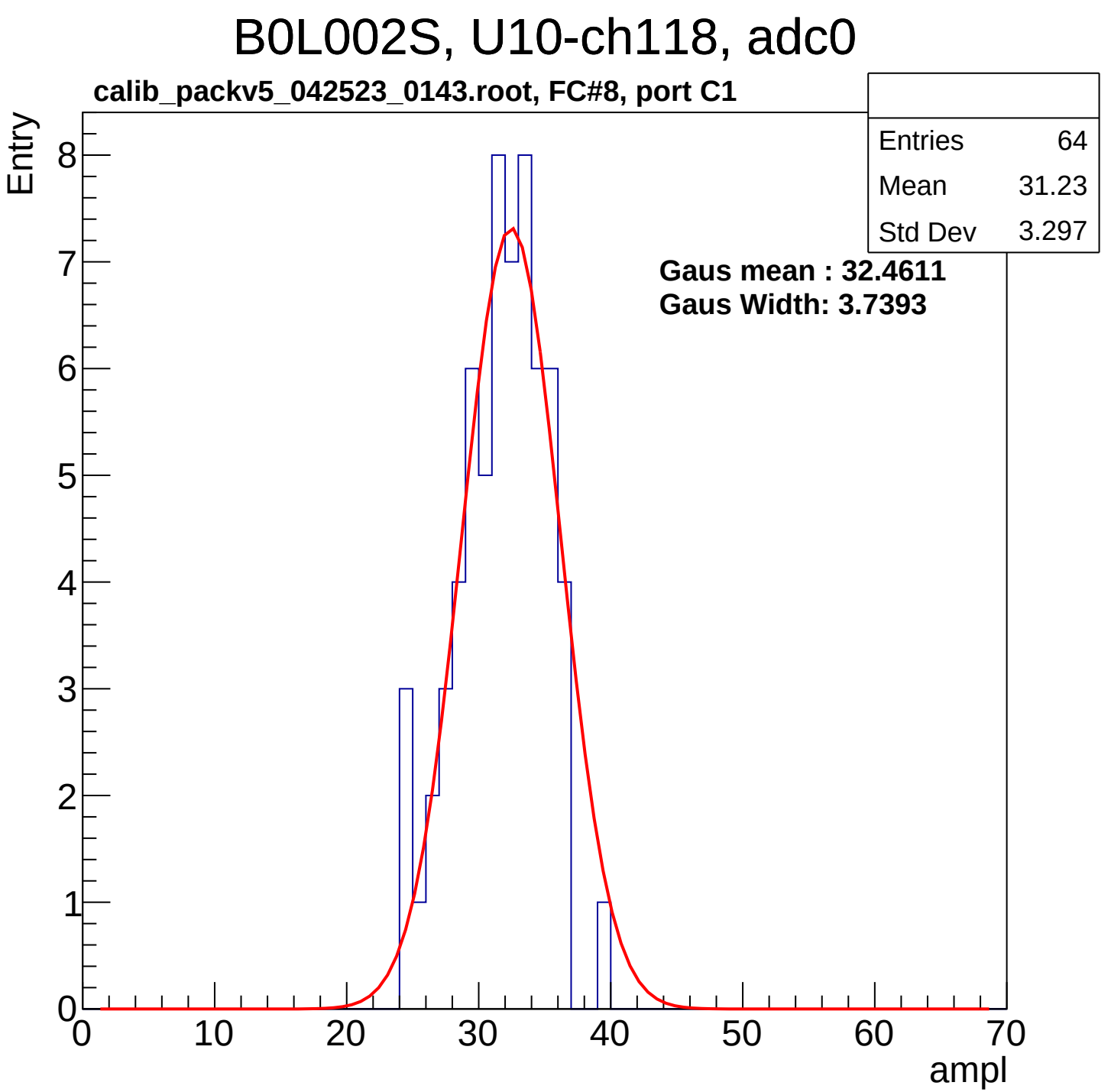

B0L002S, U10-ch117, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 71
Mean 53.94
Std Dev 3.377
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

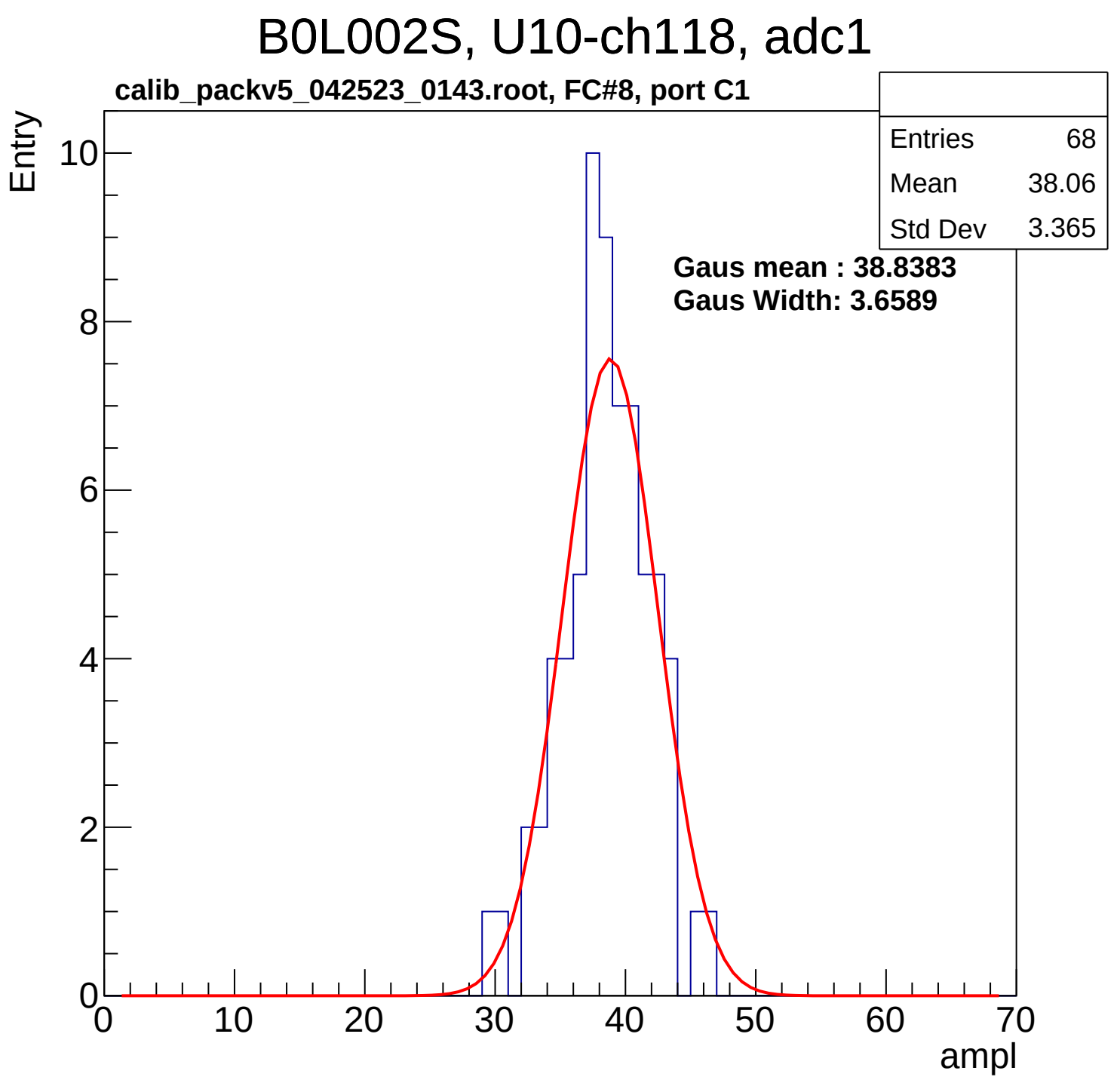

B0L002S, U10-ch117, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 59.62
Std Dev 2.571
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

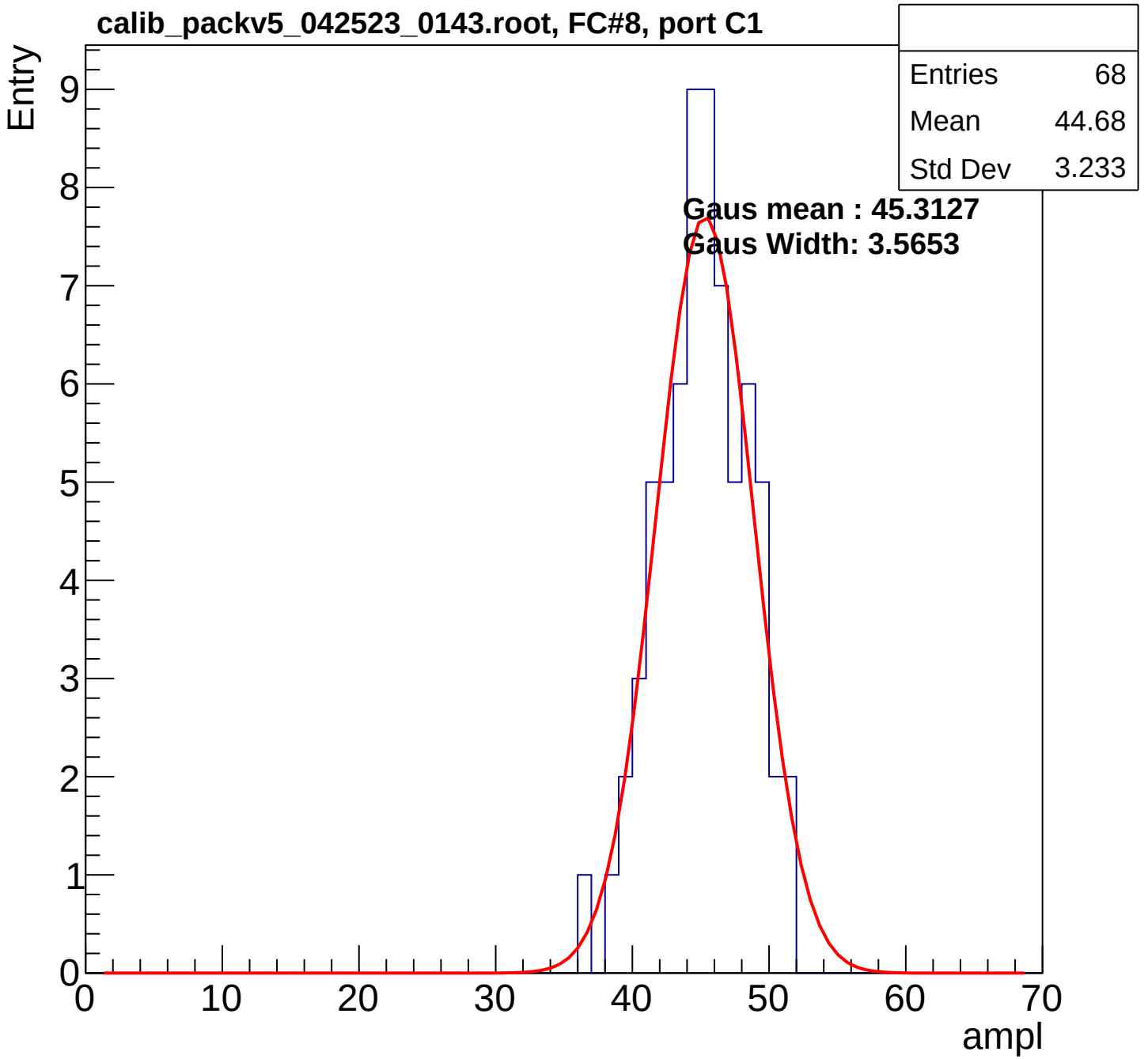

B0L002S, U10-ch117, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 12
Mean 56.25
Std Dev 17.02
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

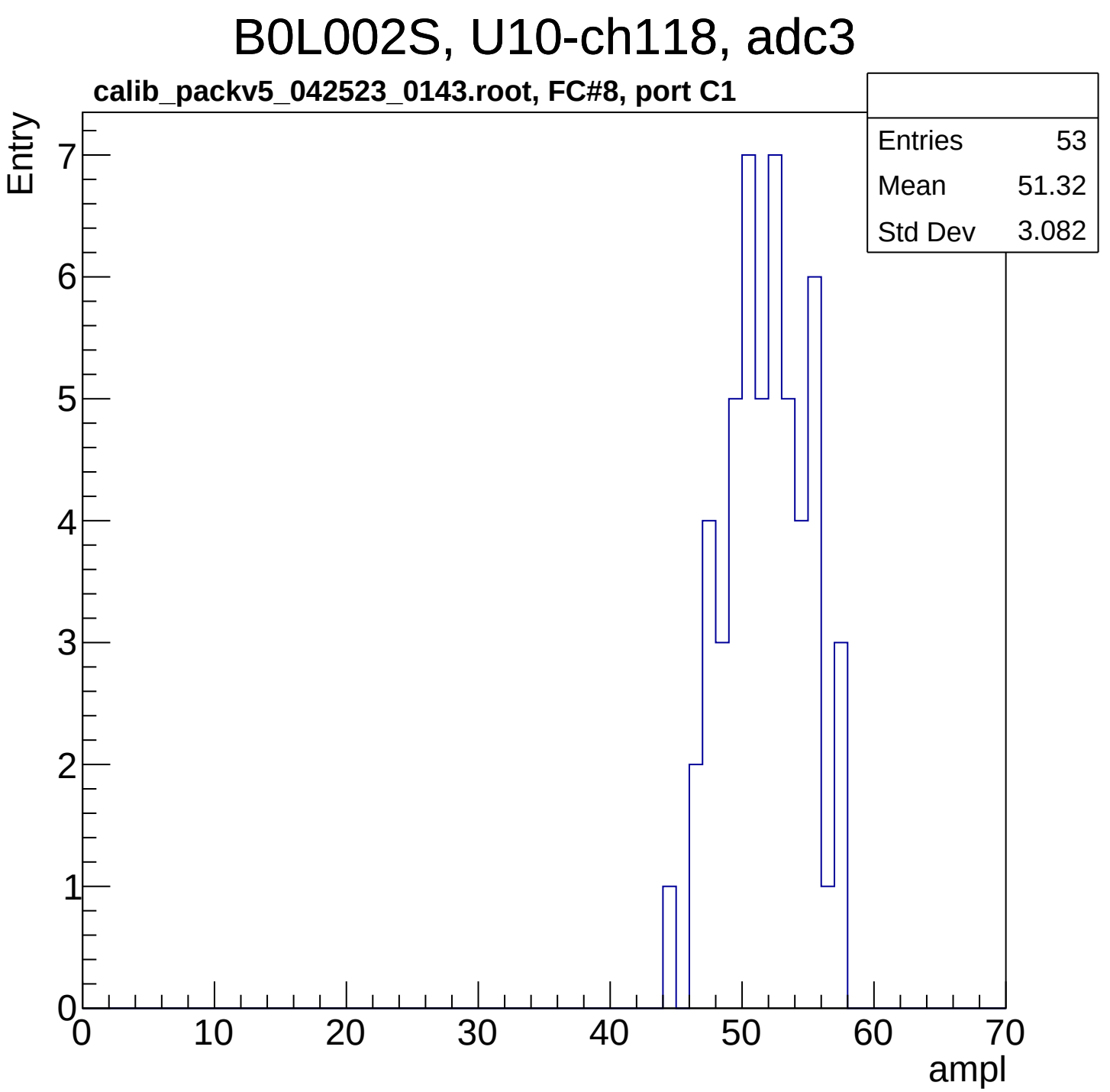

B0L002S, U10-ch117, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

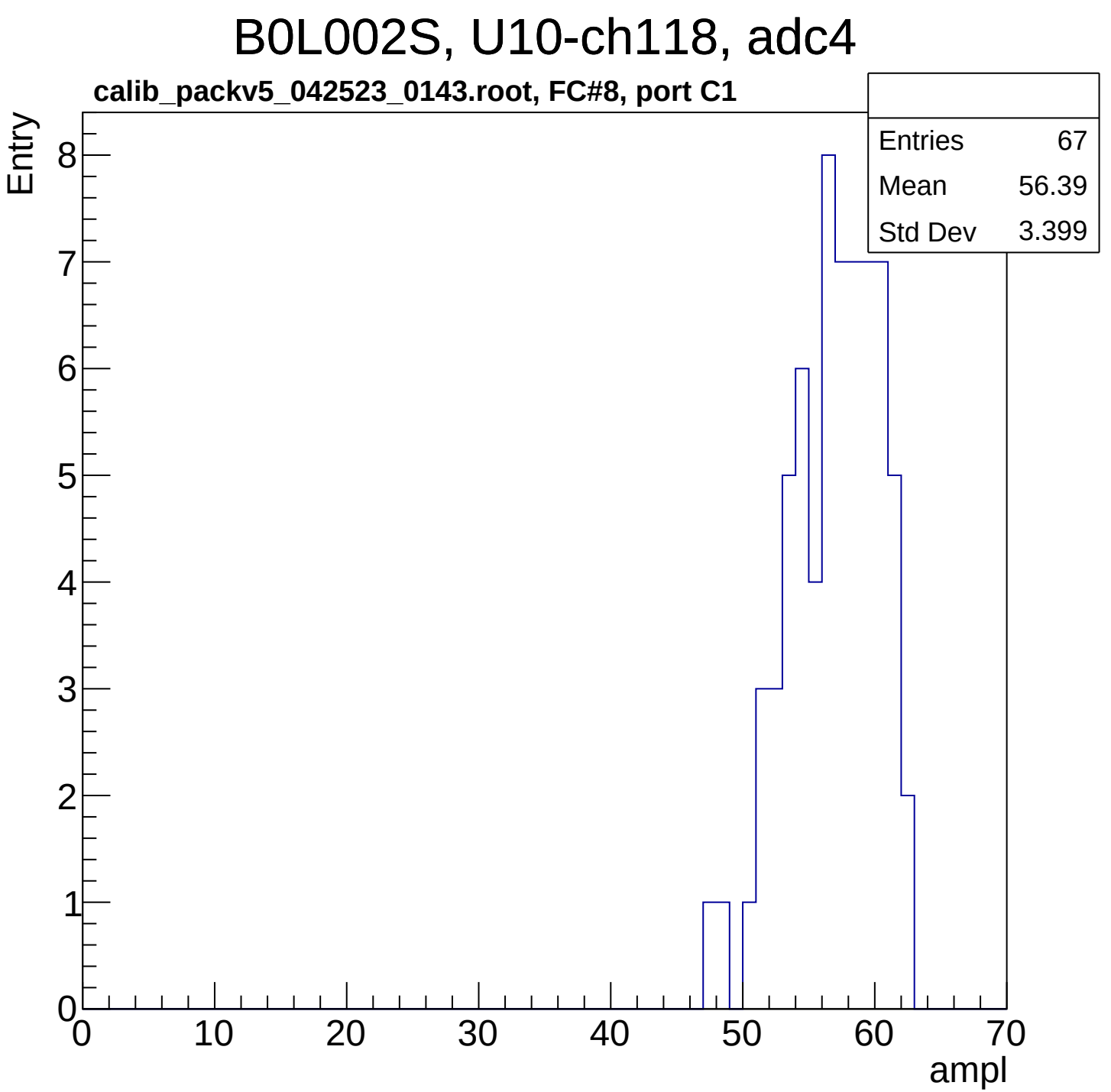

B0L002S, U10-ch117, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

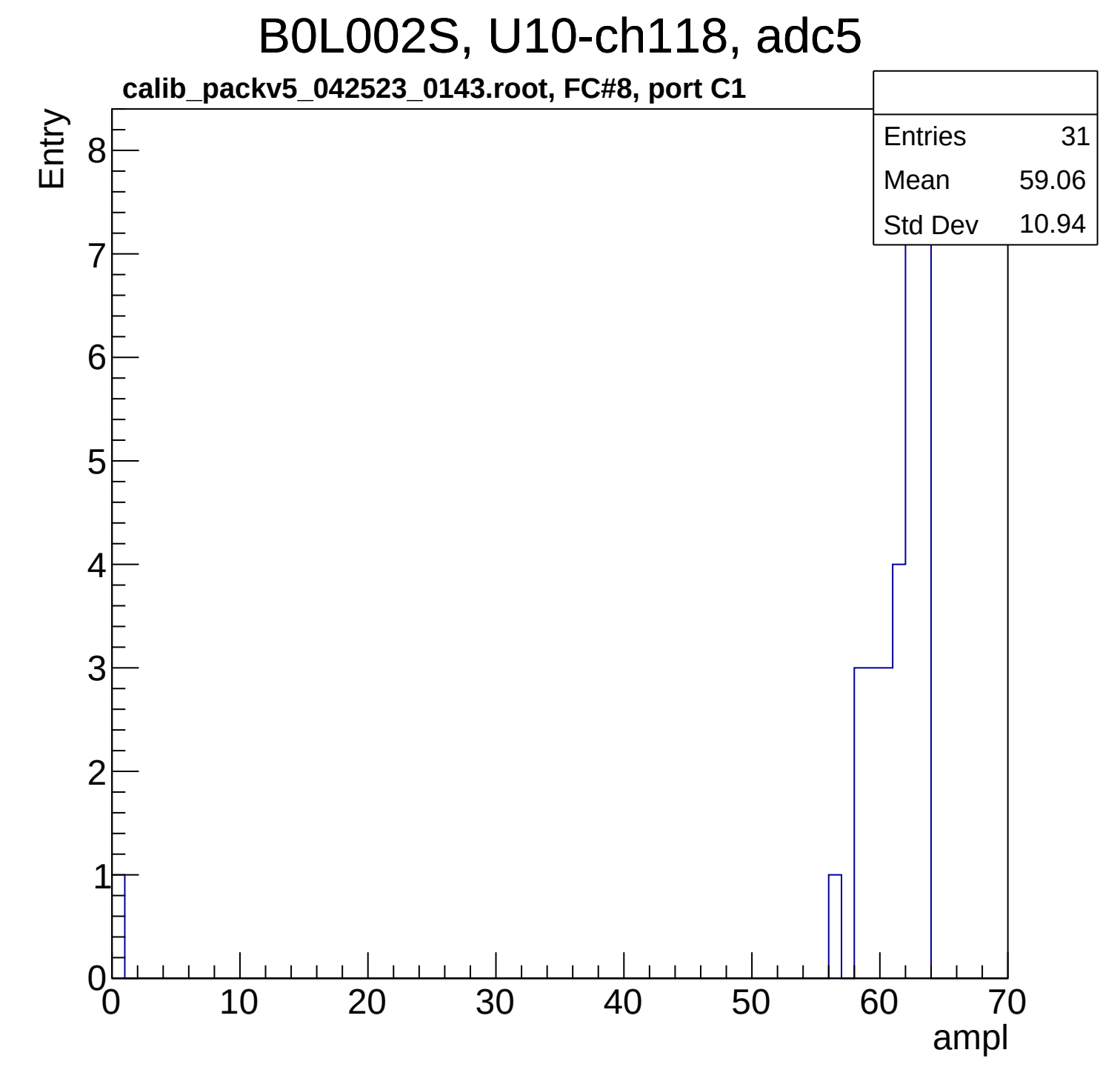

B0L002S, U10-ch118, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 31.23
Std Dev 3.297
Gaus mean : 32.4611
Gaus Width: 3.7393
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

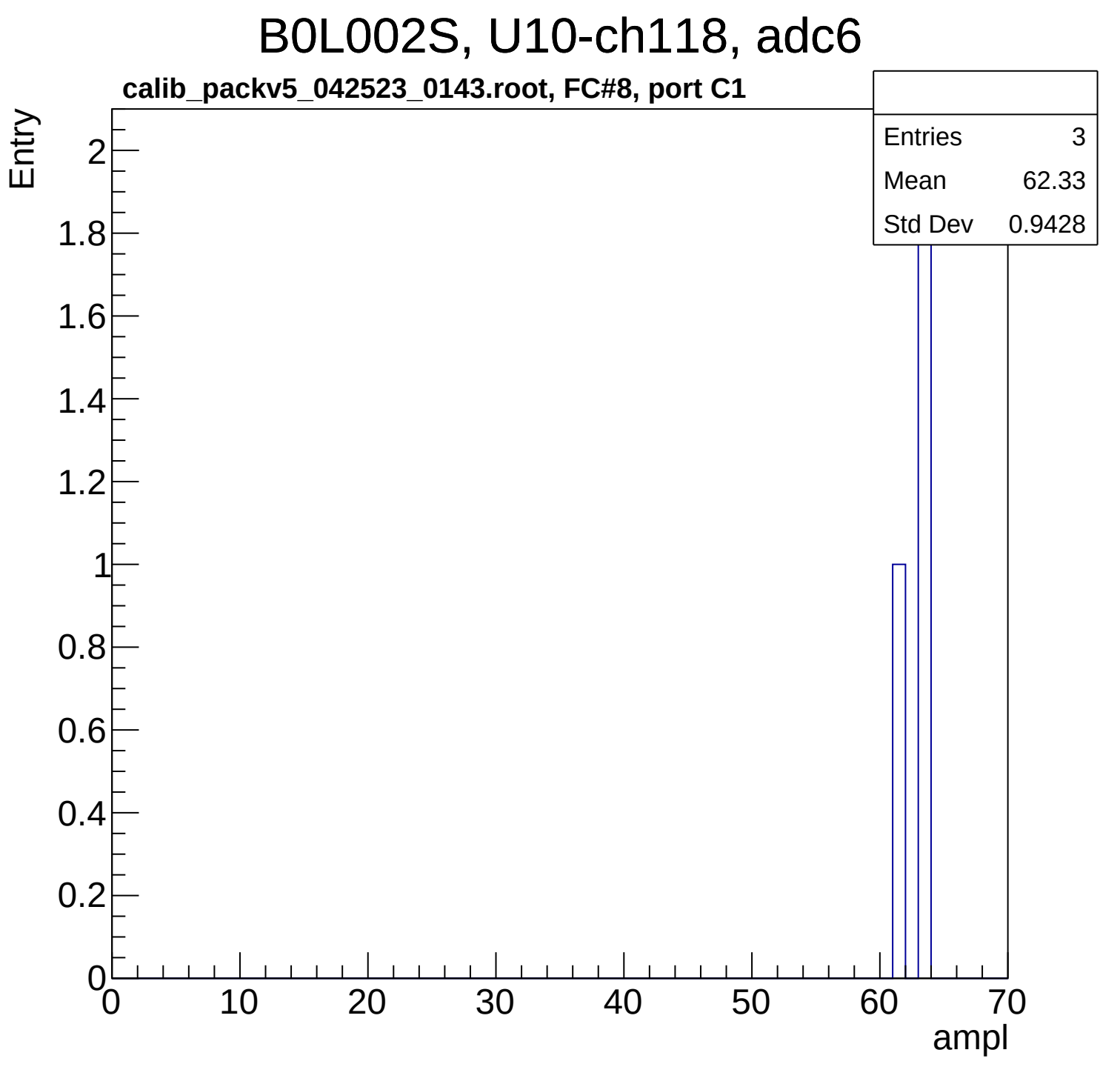

B0L002S, U10-ch118, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 38.06
Std Dev 3.365
Gaus mean : 38.8383
Gaus Width: 3.6589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

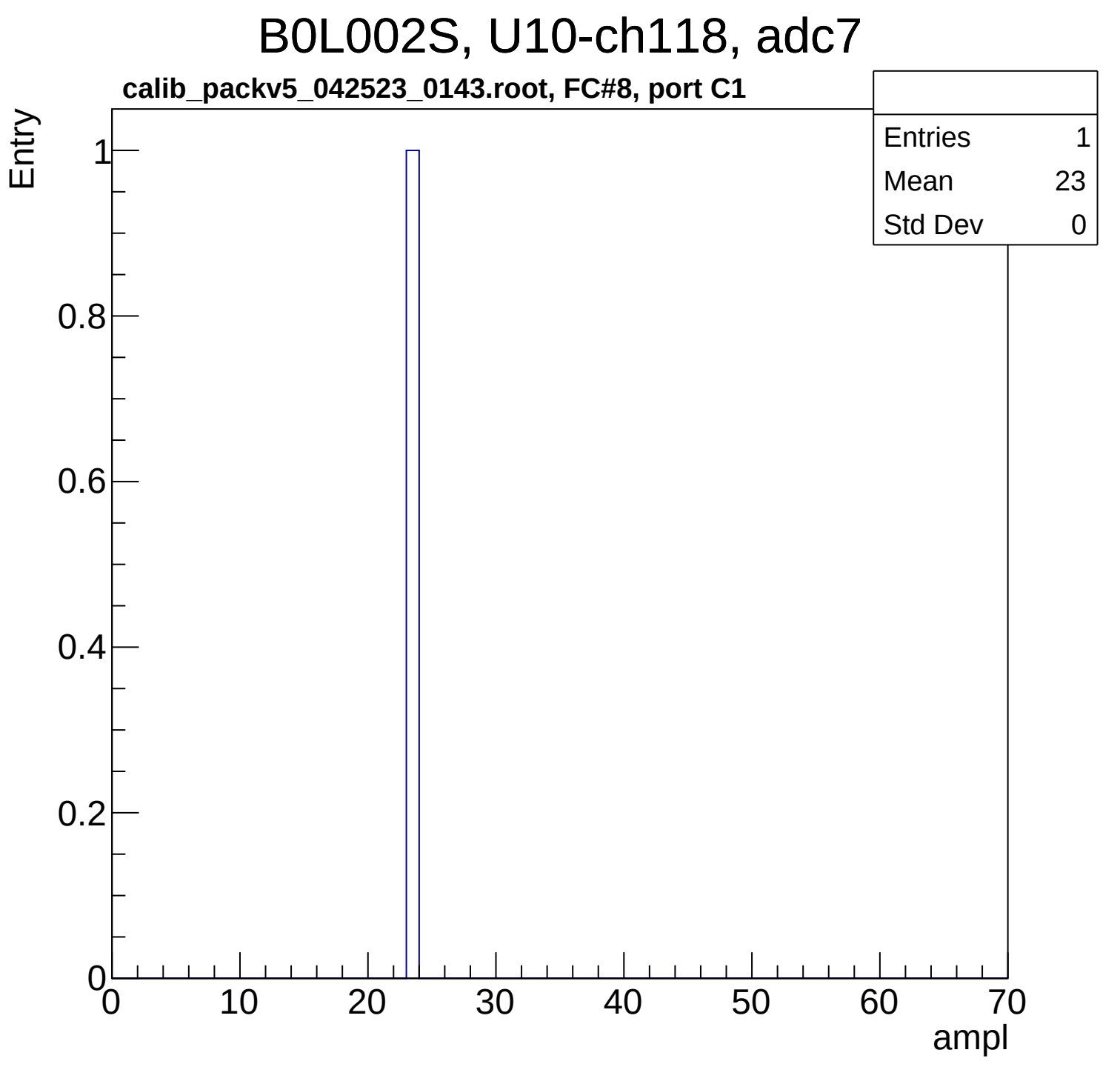

B0L002S, U10-ch118, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.68
Std Dev 3.233
Gaus mean : 45.3127
Gaus Width: 3.5653
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

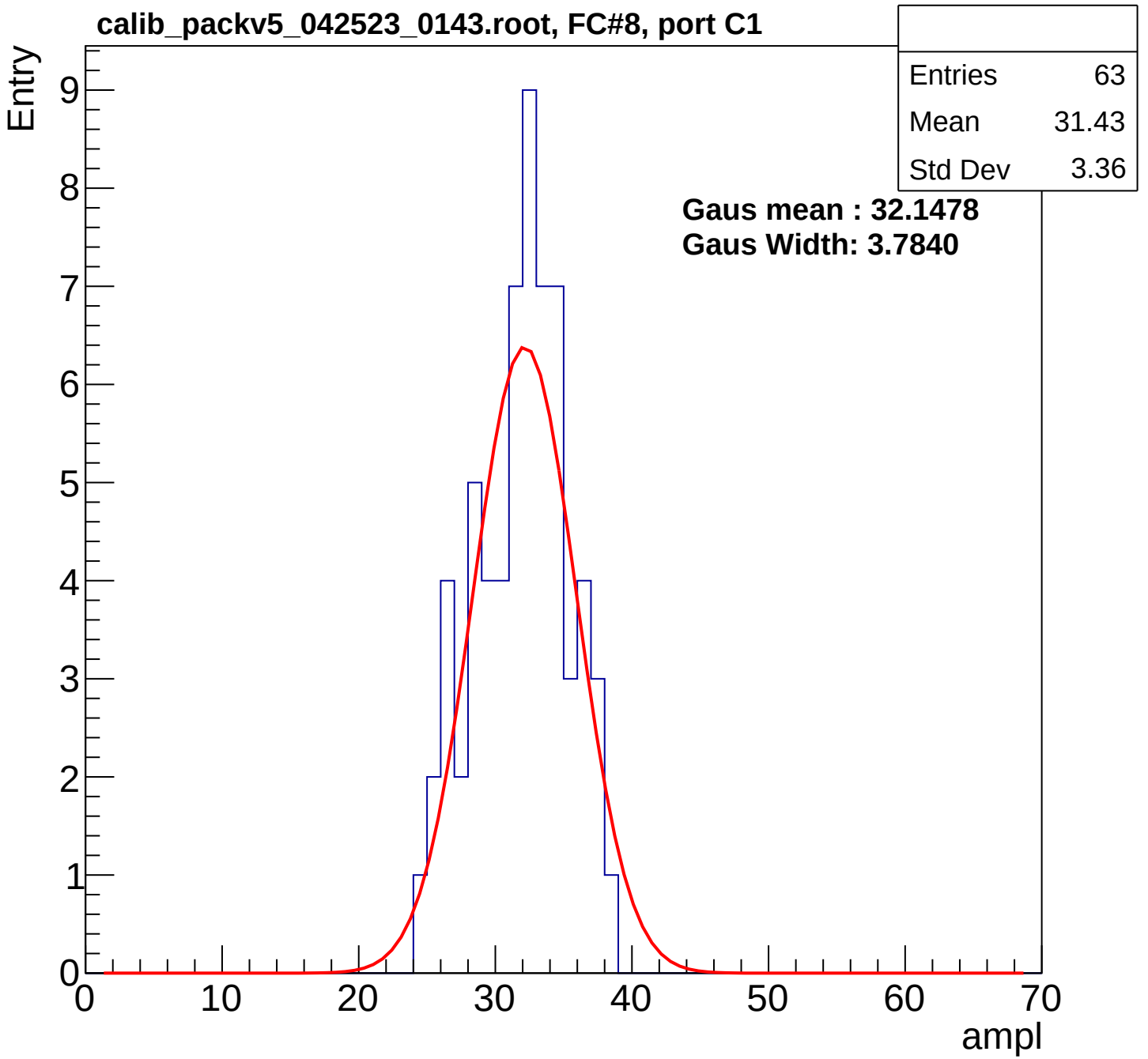

B0L002S, U10-ch118, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 53
Mean 51.32
Std Dev 3.082
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

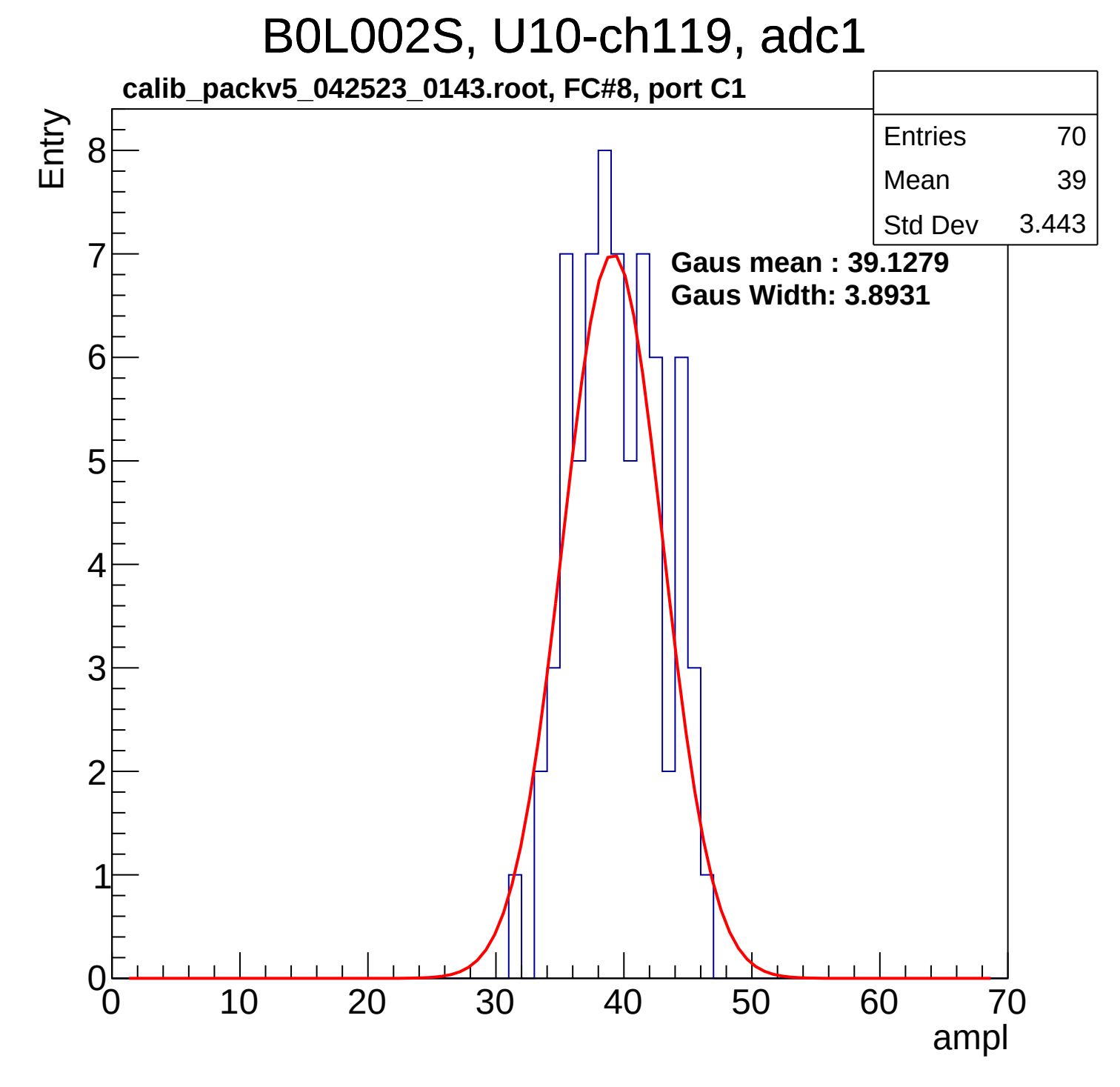

B0L002S, U10-ch118, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 56.39
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

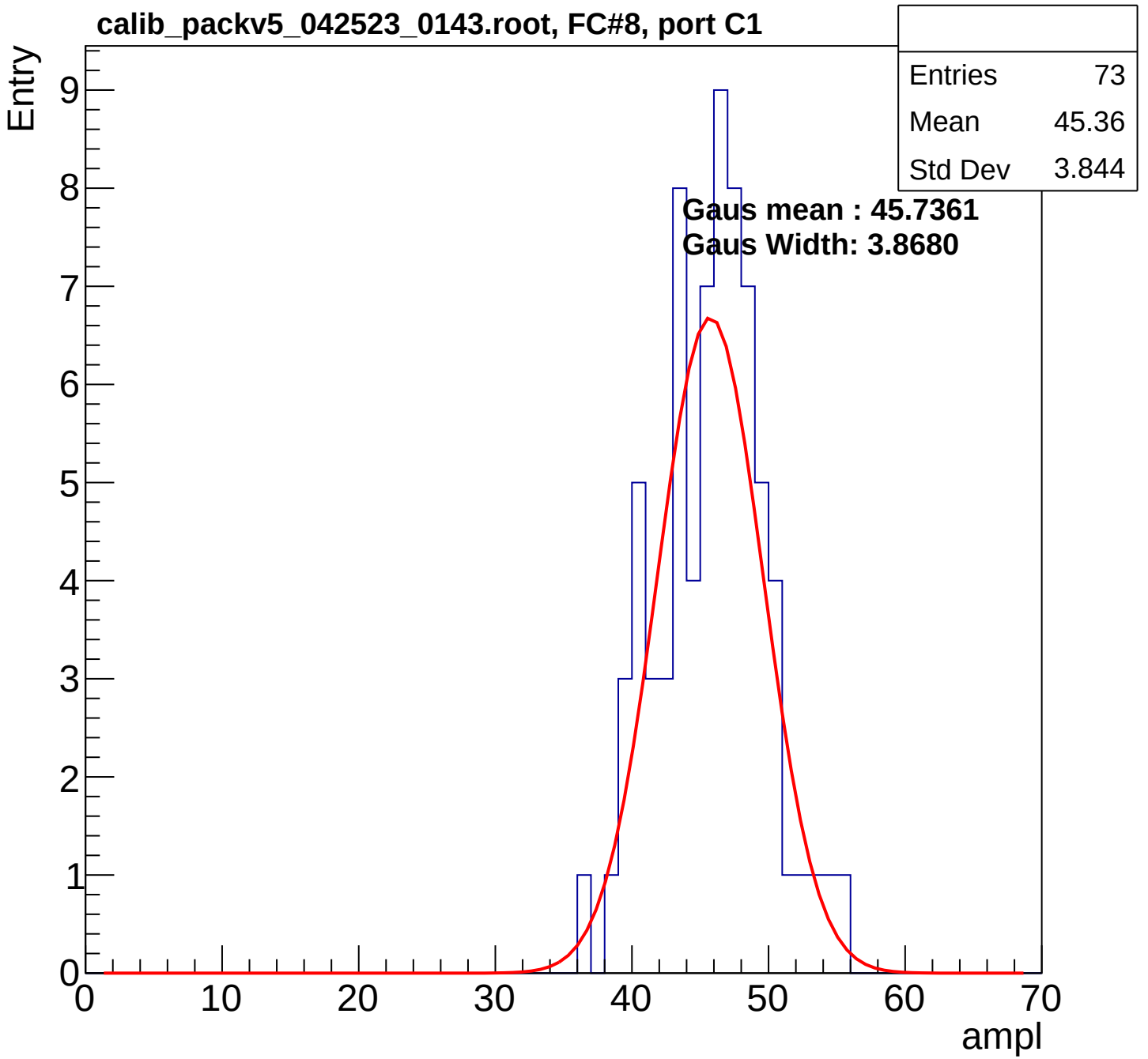

B0L002S, U10-ch118, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 31
Mean 59.06
Std Dev 10.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

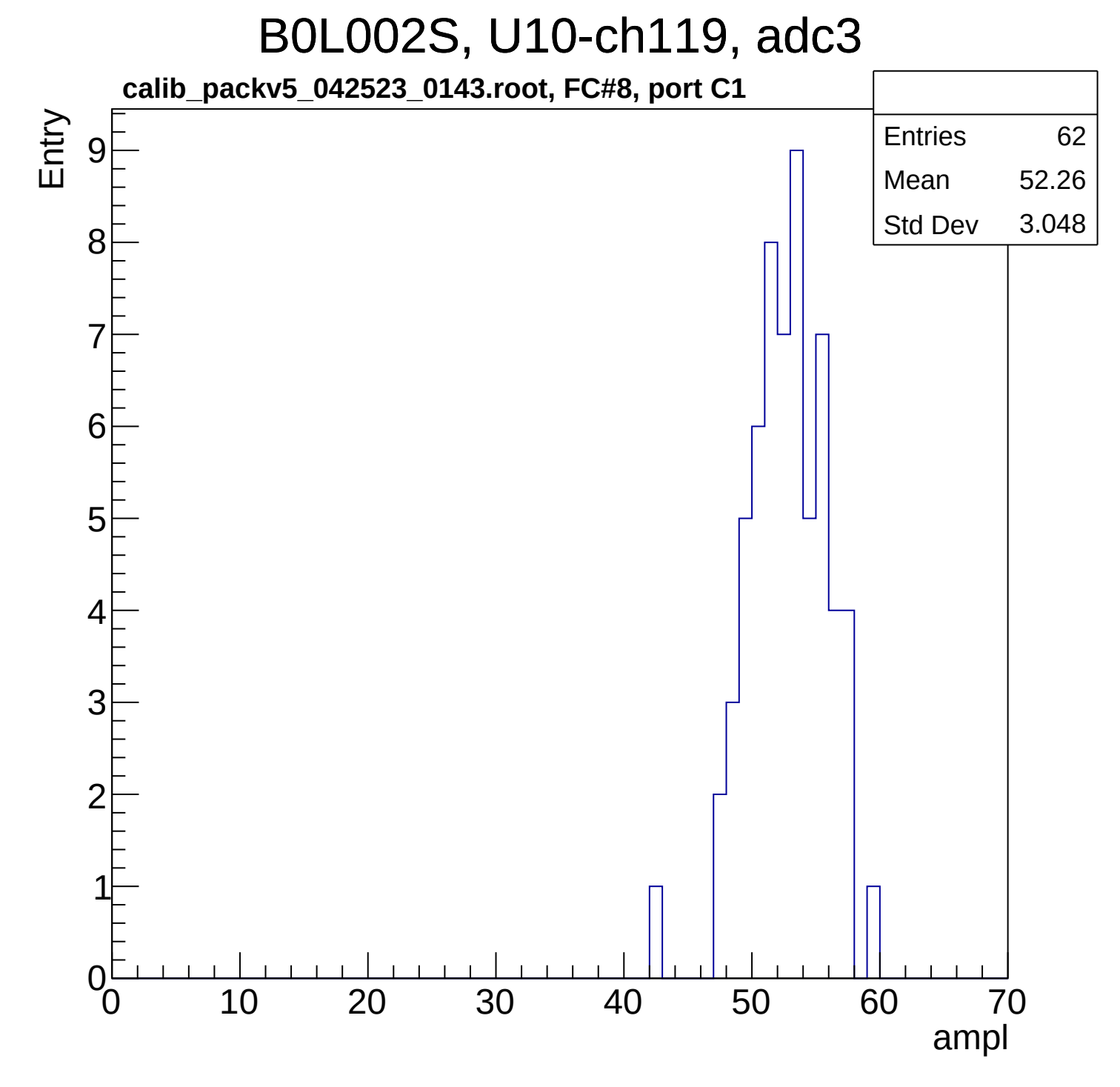

B0L002S, U10-ch118, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

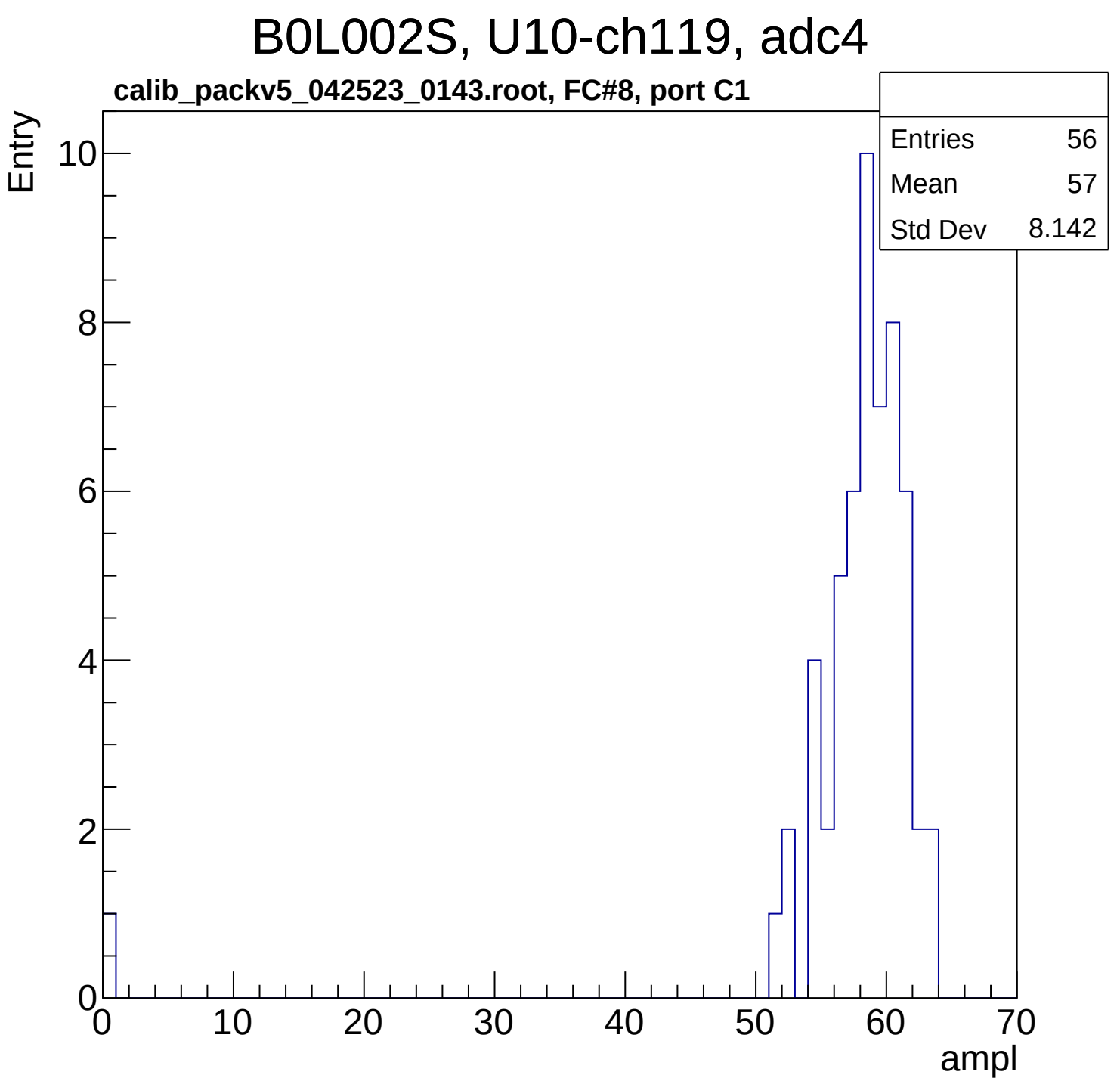

B0L002S, U10-ch118, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

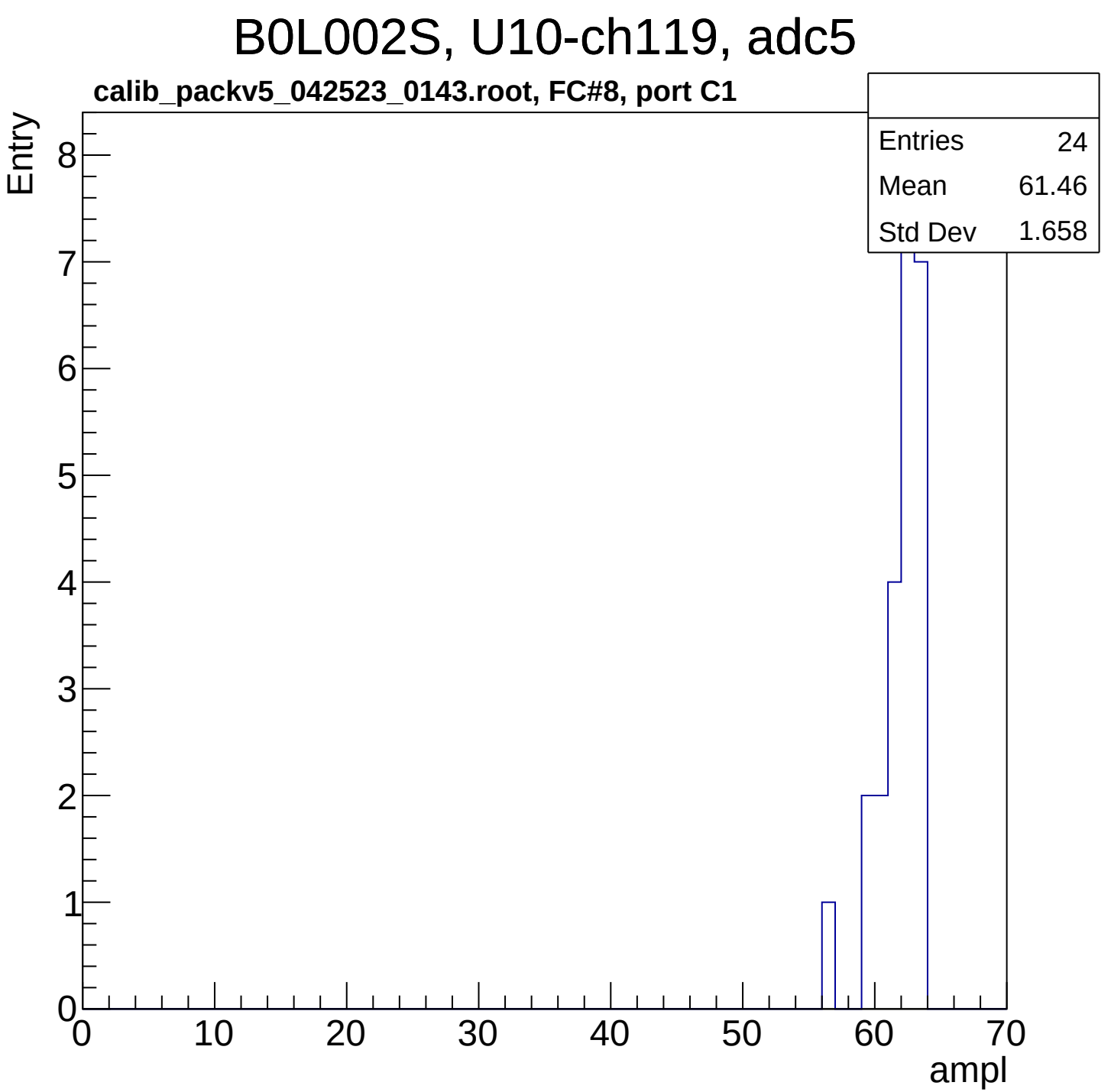

B0L002S, U10-ch119, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 31.43
Std Dev 3.36
Gaus mean : 32.1478
Gaus Width: 3.7840
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

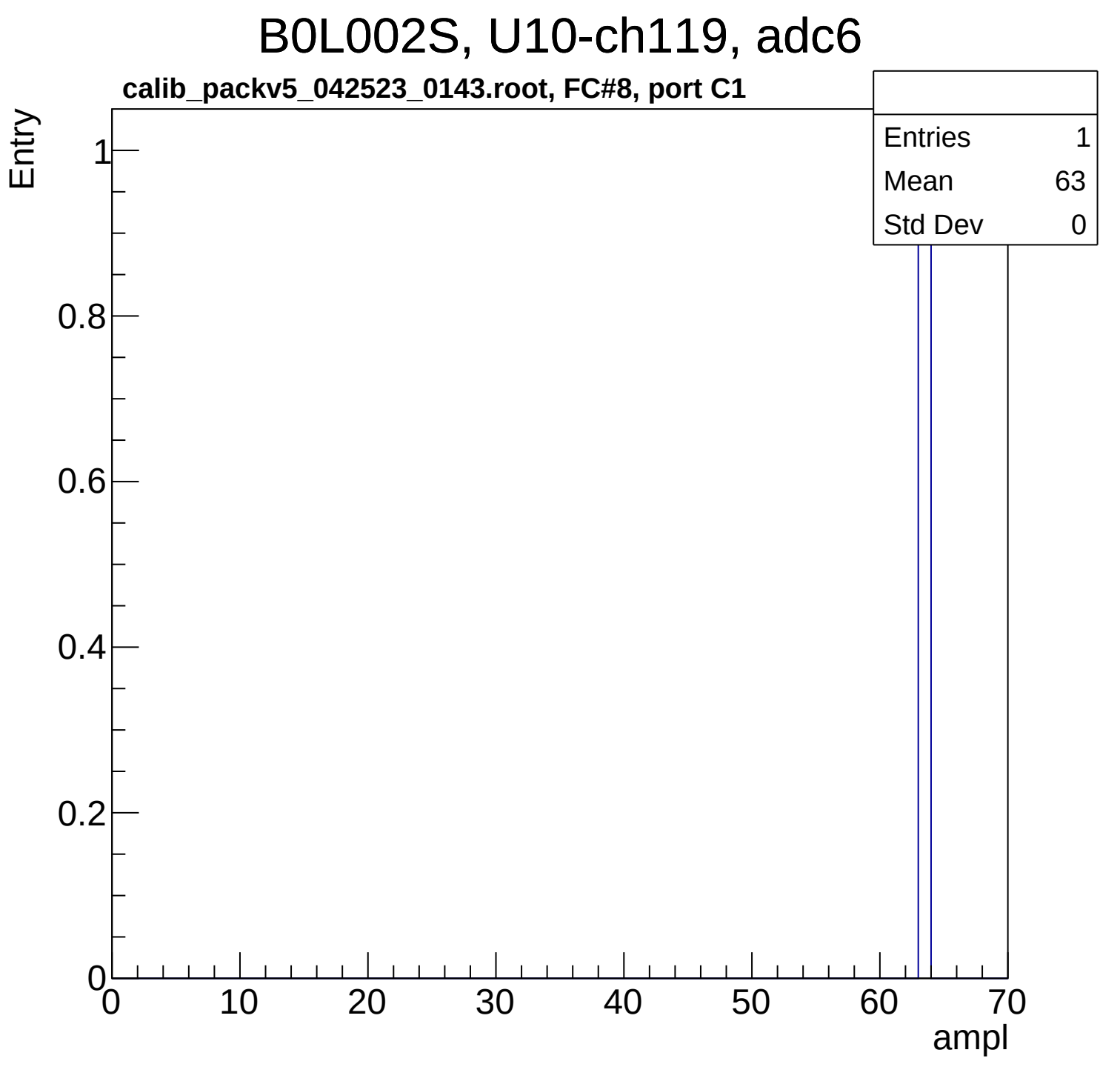

B0L002S, U10-ch119, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 70
Mean 39
Std Dev 3.443
Gaus mean : 39.1279
Gaus Width: 3.8931
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

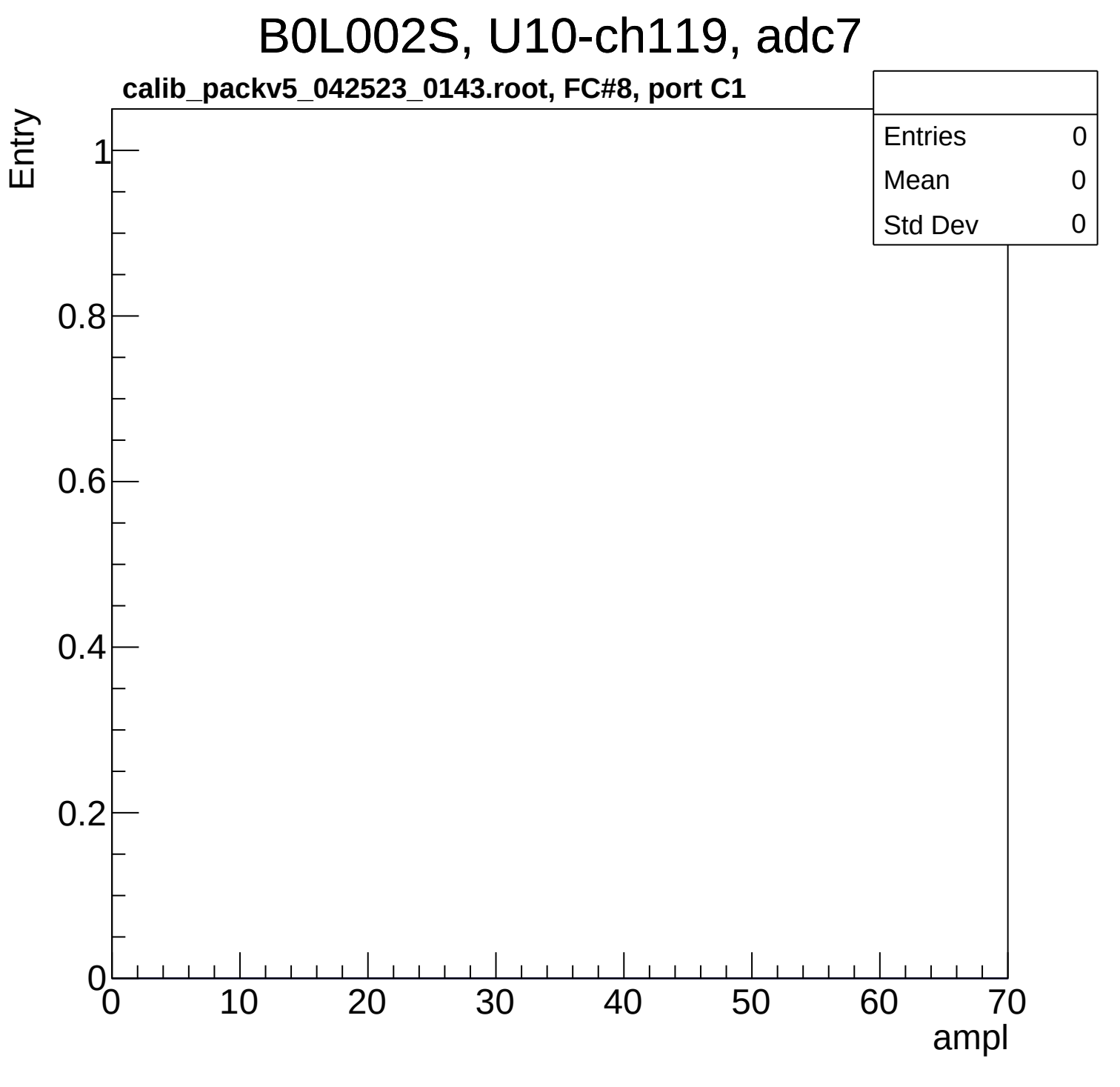

B0L002S, U10-ch119, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 73
Mean 45.36
Std Dev 3.844
Gaus mean : 45.7361
Gaus Width: 3.8680
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

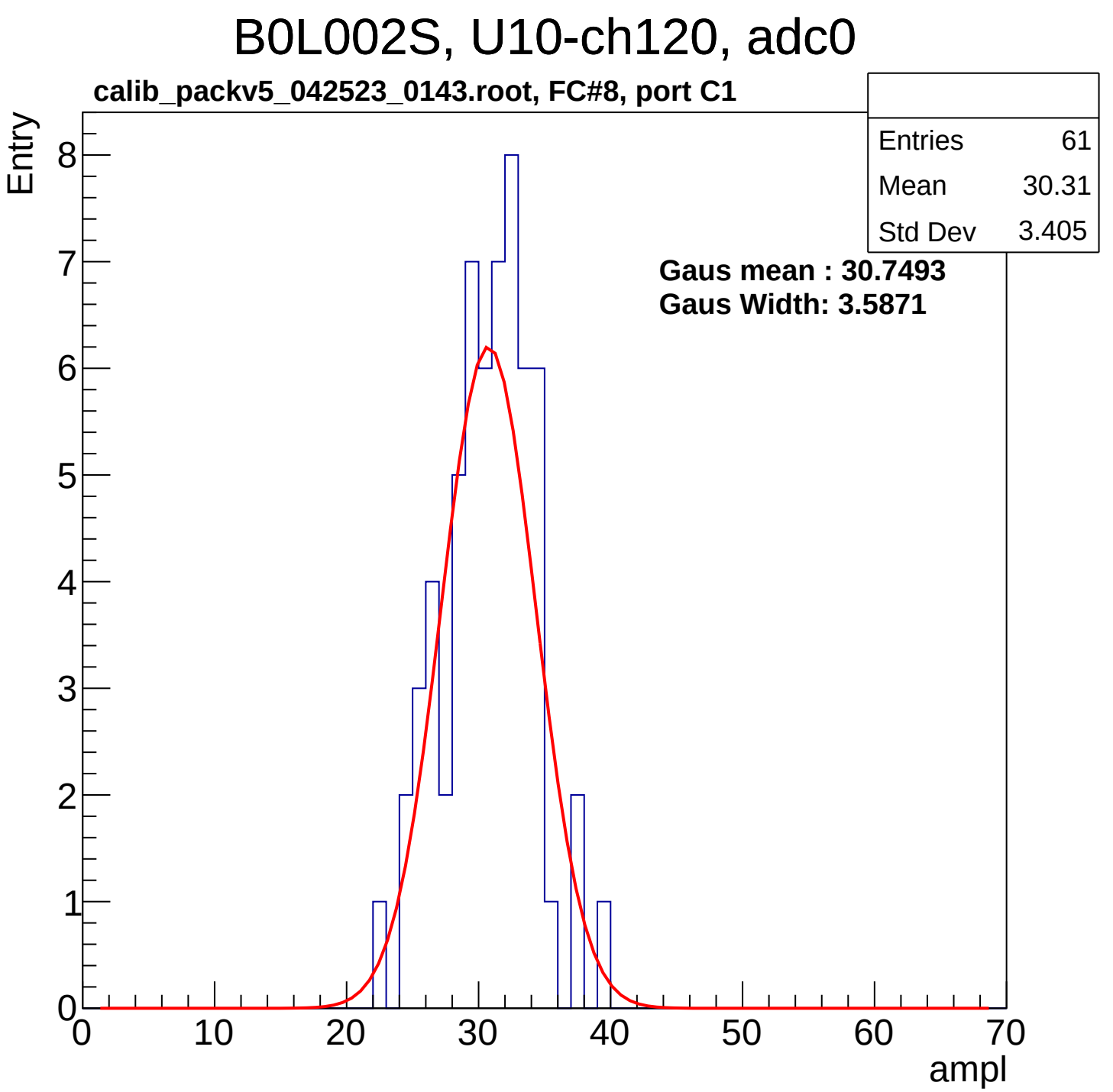

B0L002S, U10-ch119, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 62
Mean 52.26
Std Dev 3.048
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

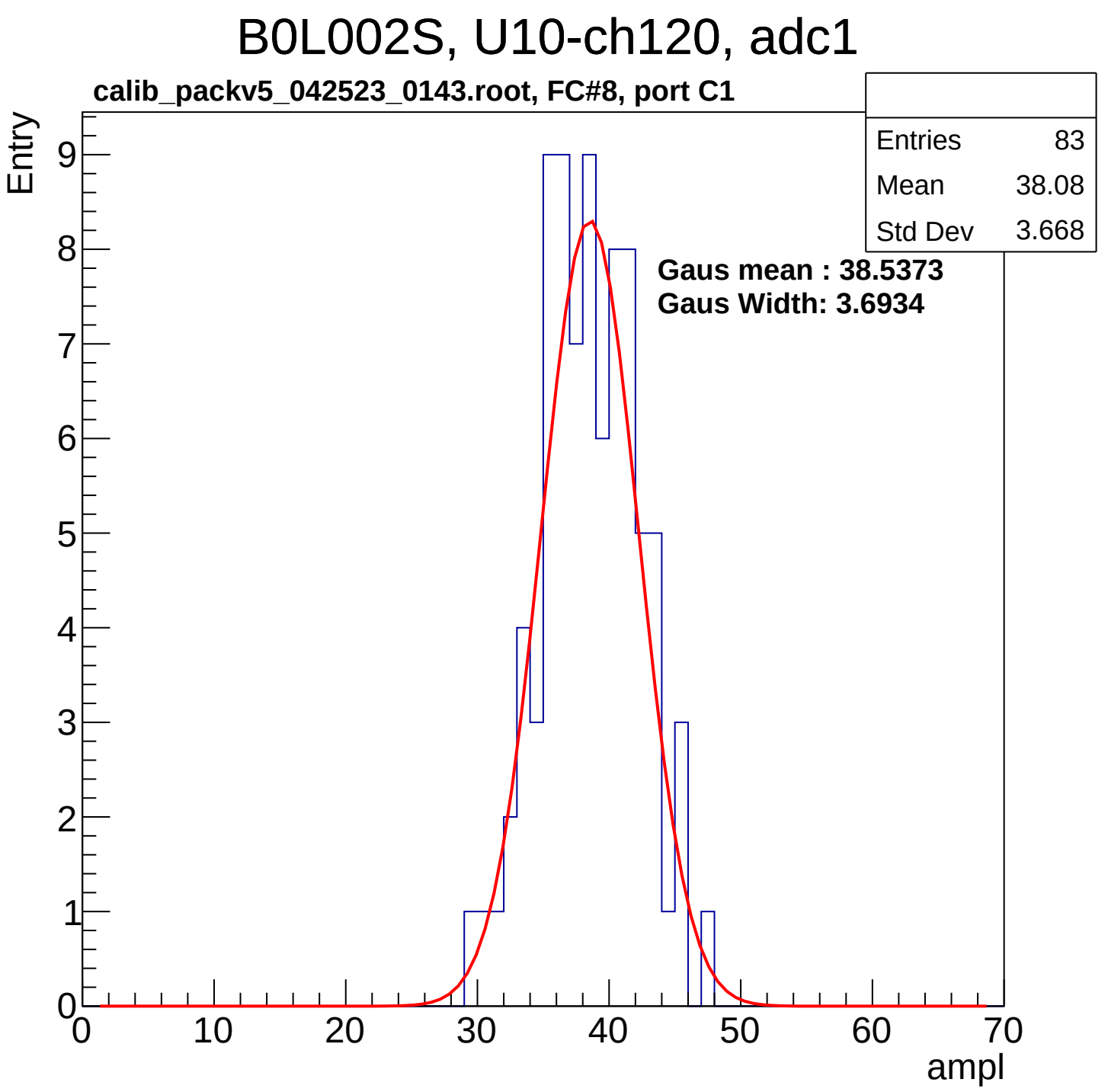

B0L002S, U10-ch119, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 57
Std Dev 8.142
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

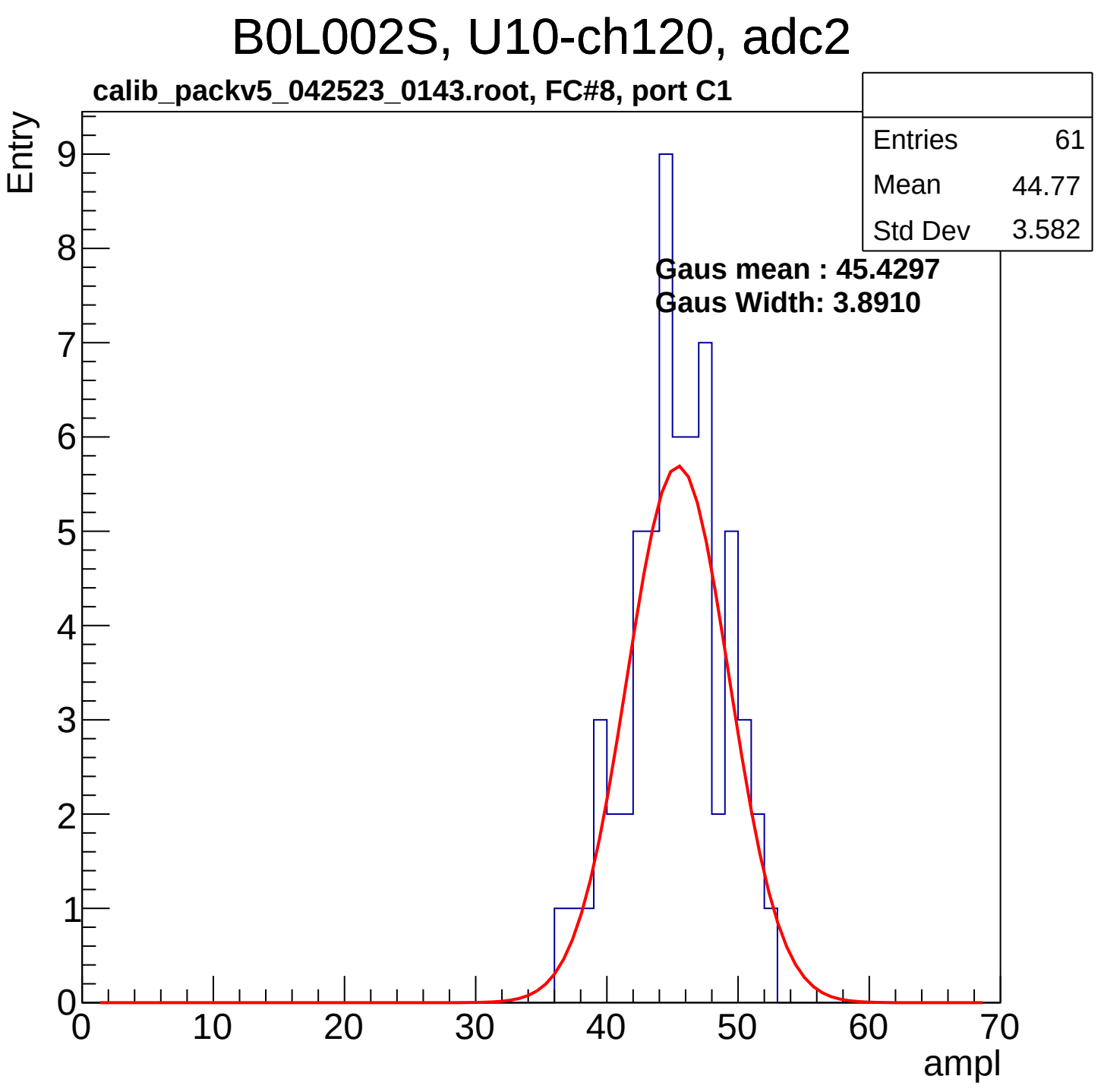

B0L002S, U10-ch119, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 24
Mean 61.46
Std Dev 1.658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

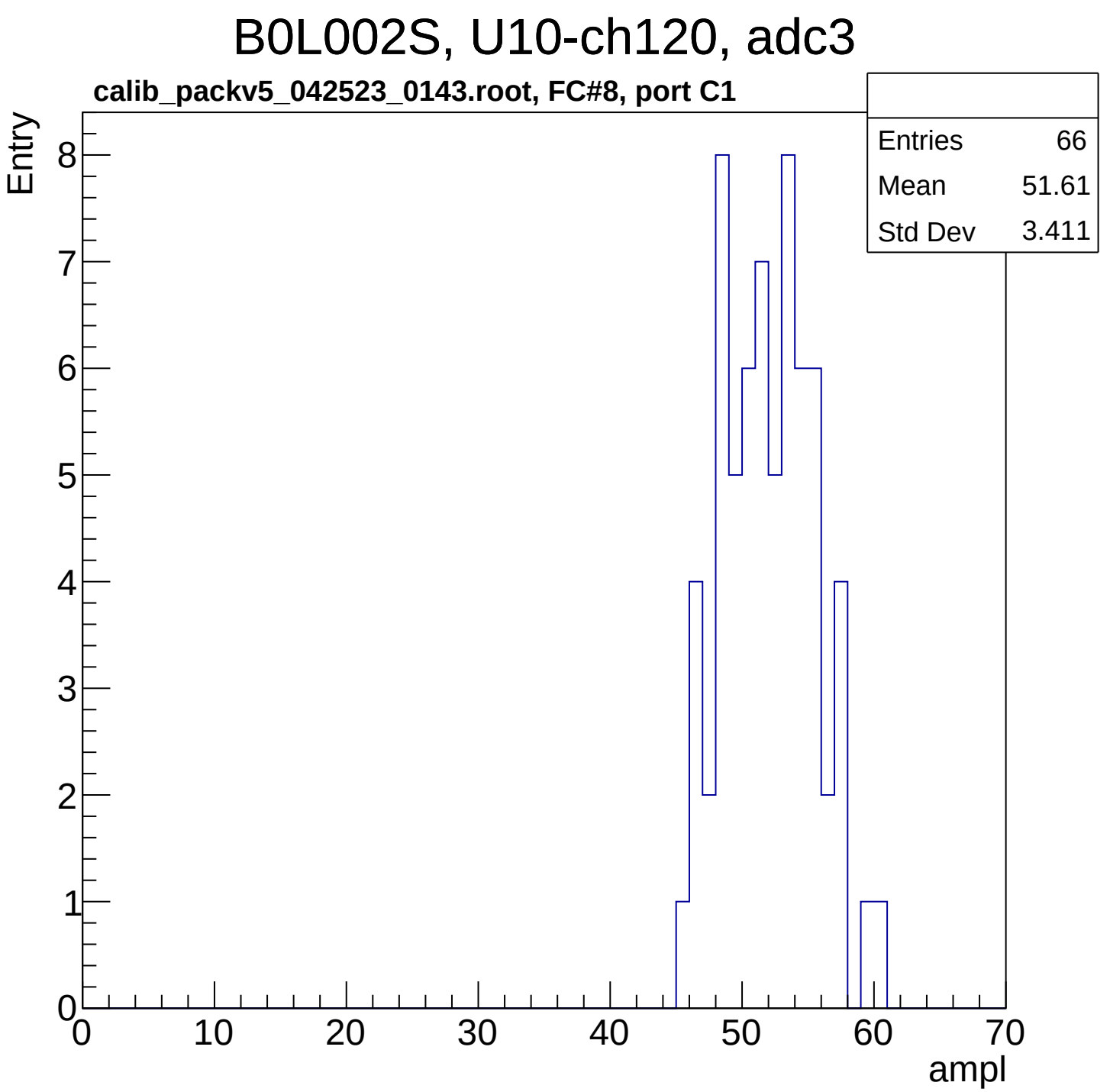

B0L002S, U10-ch119, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

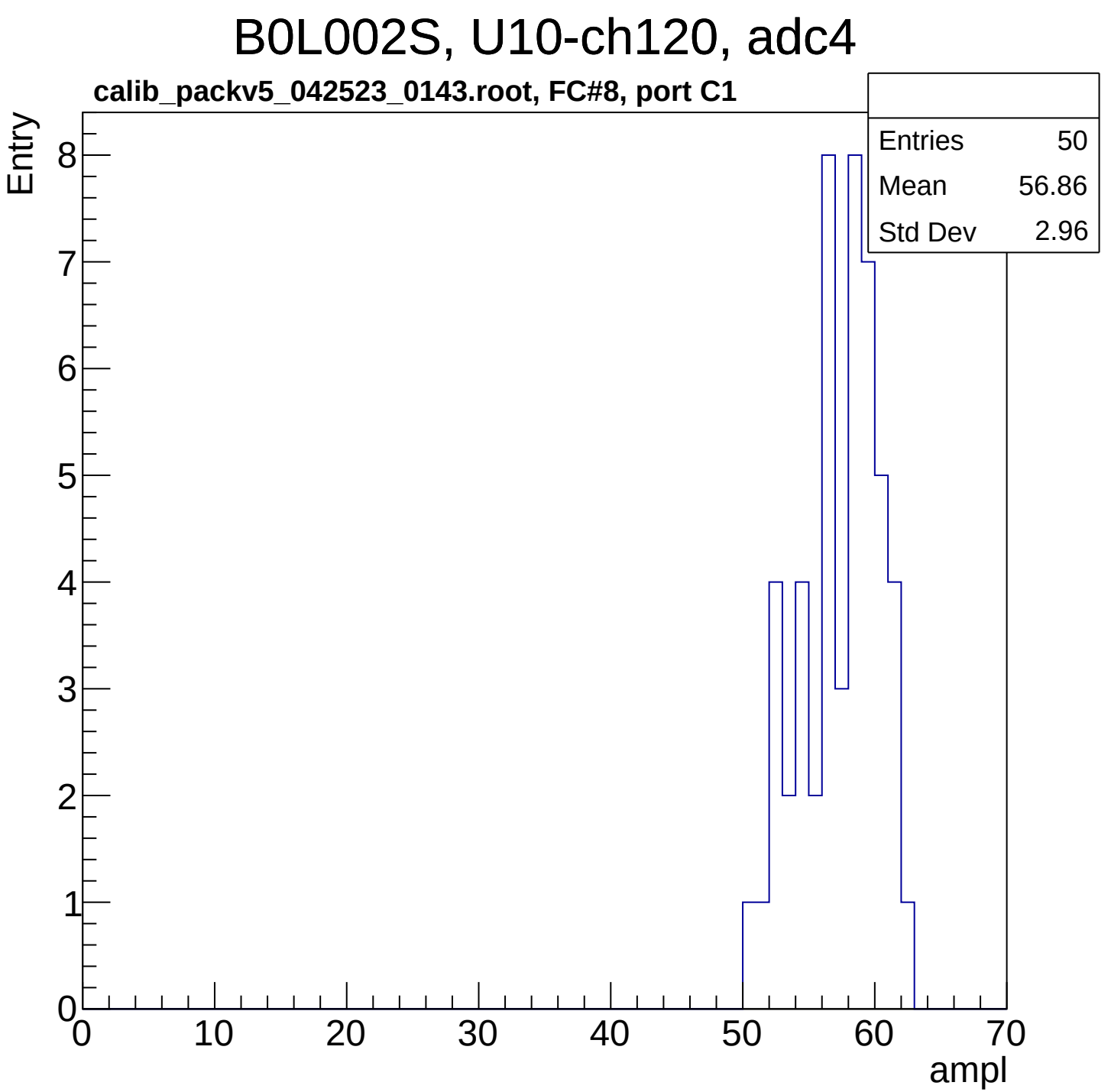

B0L002S, U10-ch119, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

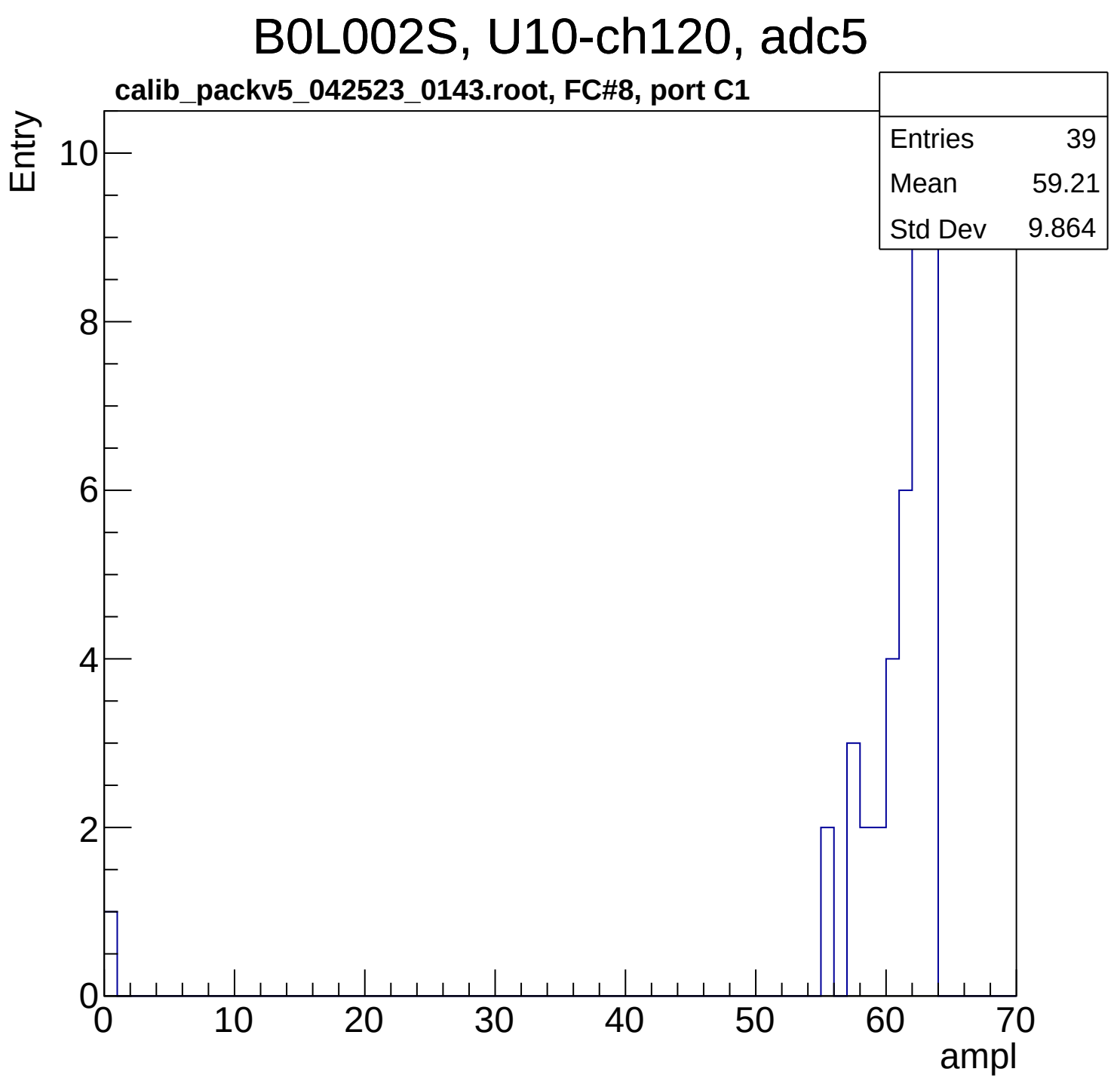

B0L002S, U10-ch120, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 30.31
Std Dev 3.405
Gaus mean : 30.7493
Gaus Width: 3.5871
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

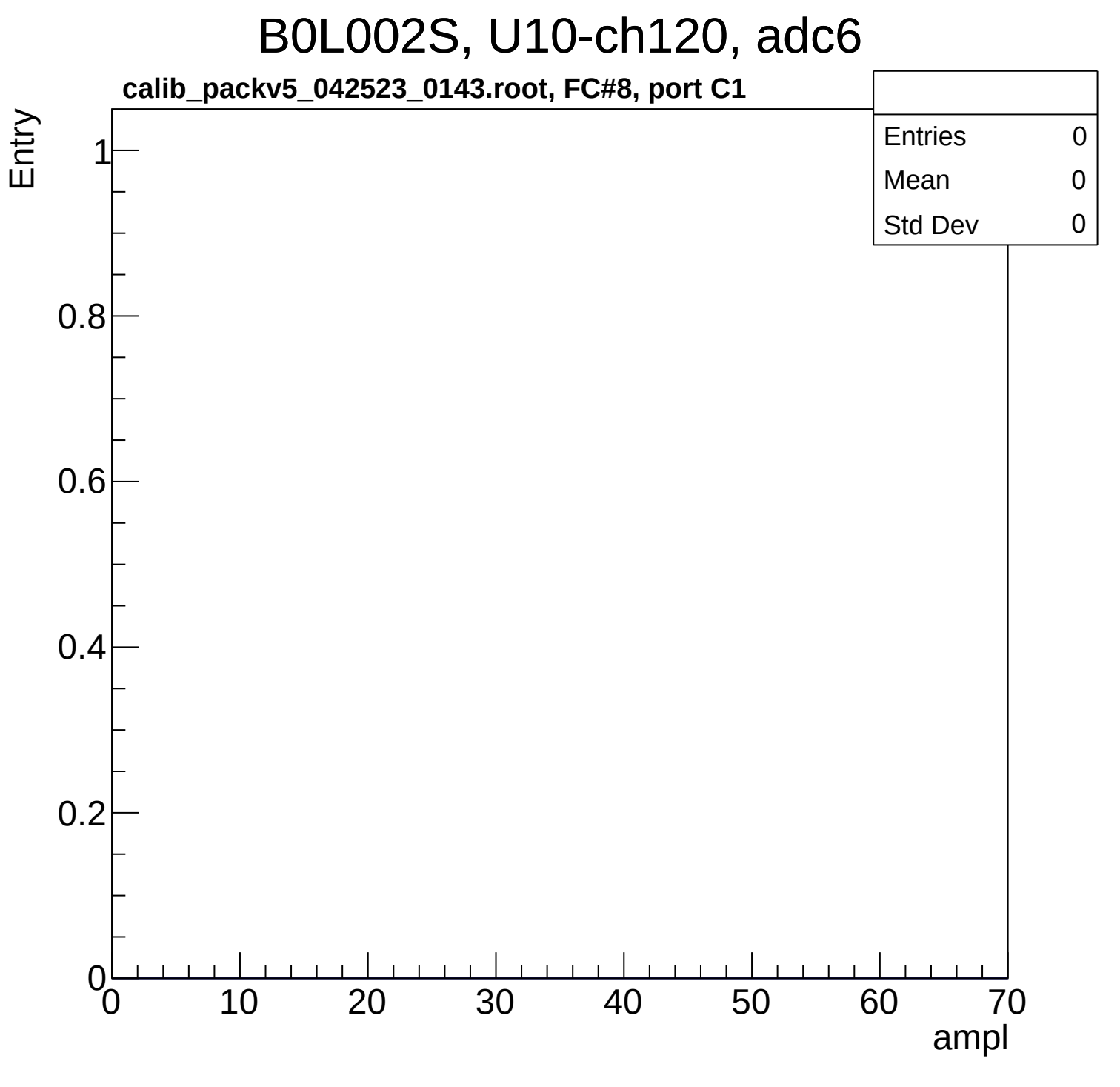

B0L002S, U10-ch120, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 83
Mean 38.08
Std Dev 3.668
Gaus mean : 38.5373
Gaus Width: 3.6934
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

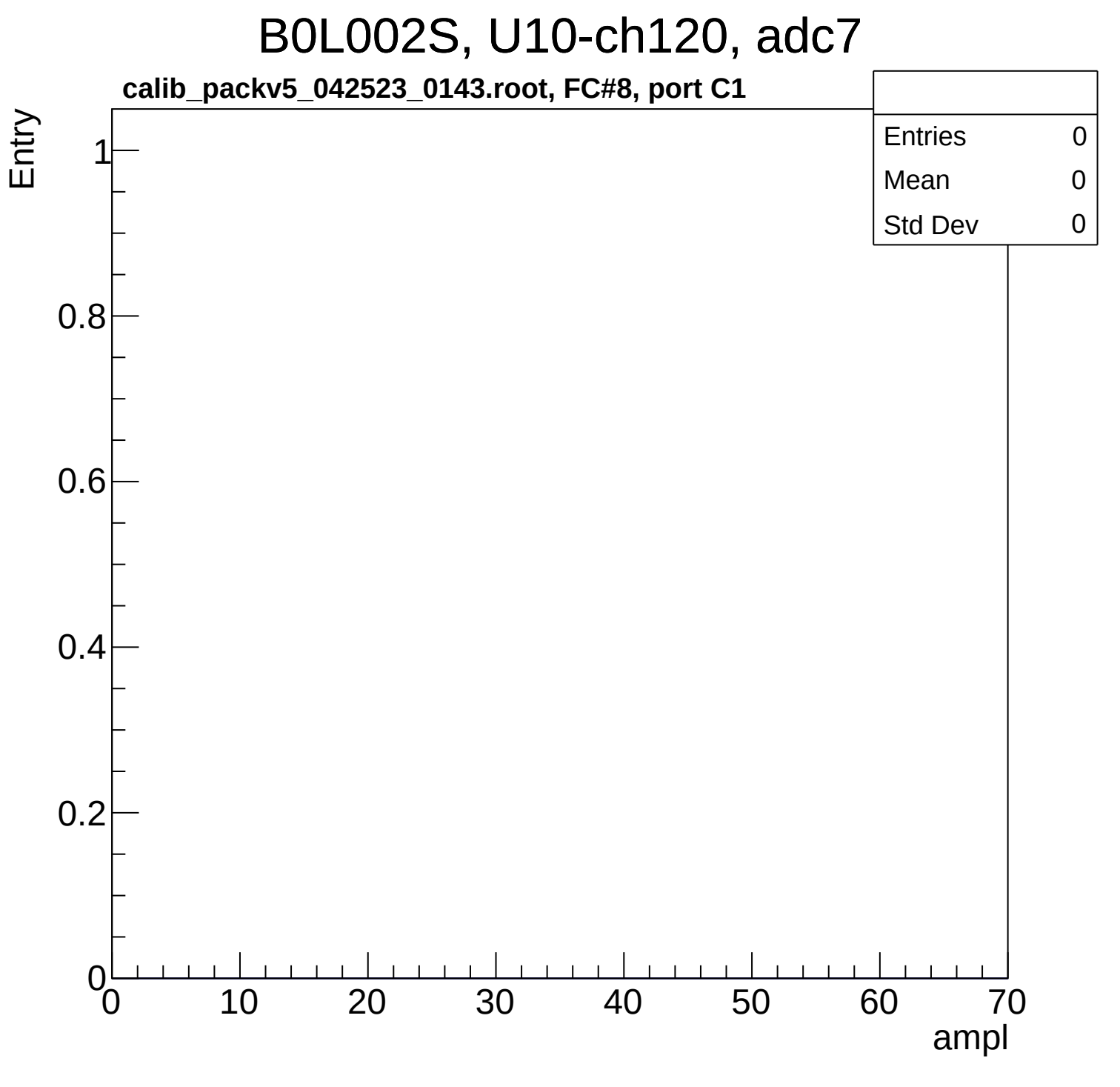

B0L002S, U10-ch120, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 44.77
Std Dev 3.582
Gaus mean : 45.4297
Gaus Width: 3.8910
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

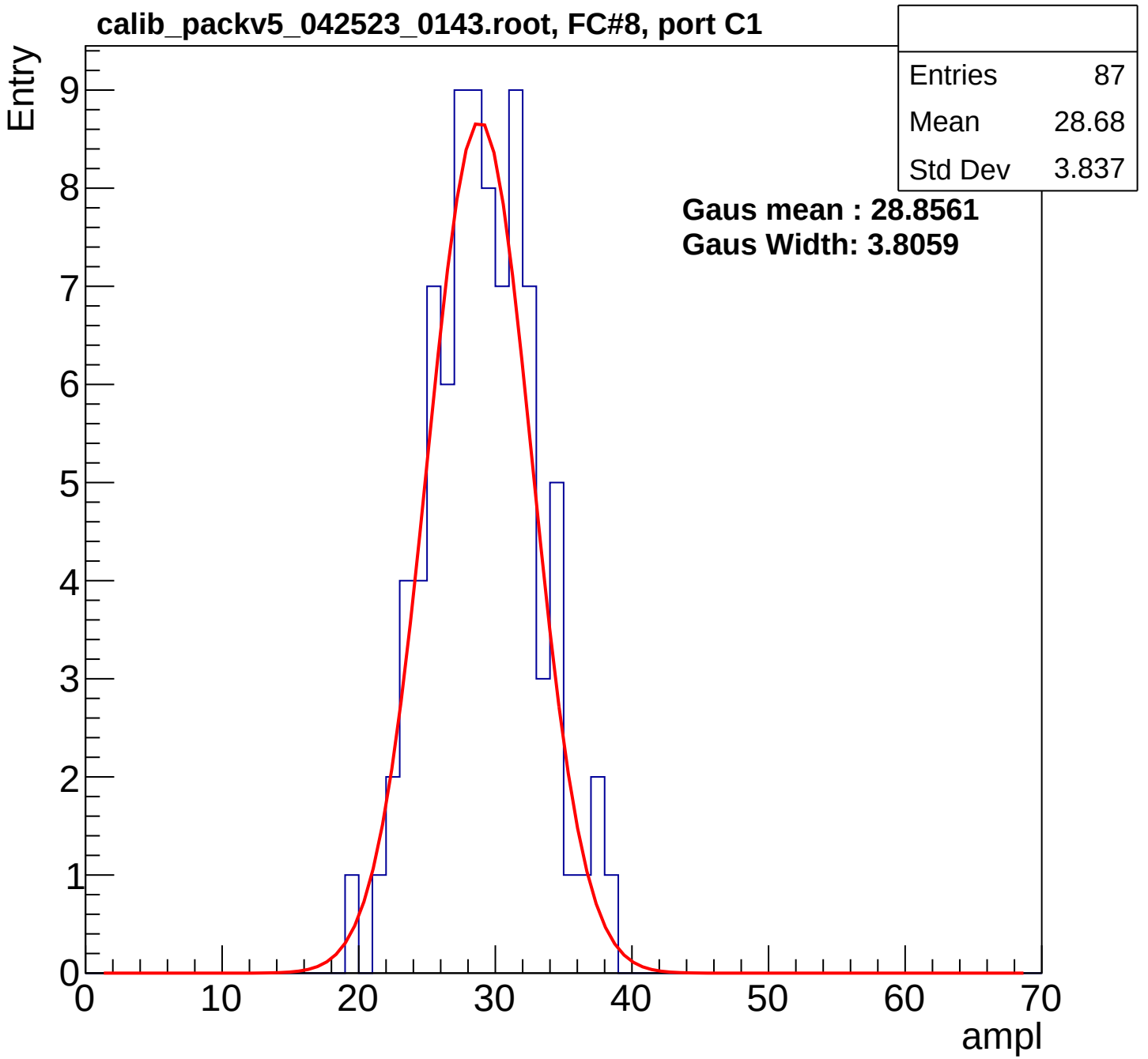

B0L002S, U10-ch120, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 66
Mean 51.61
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

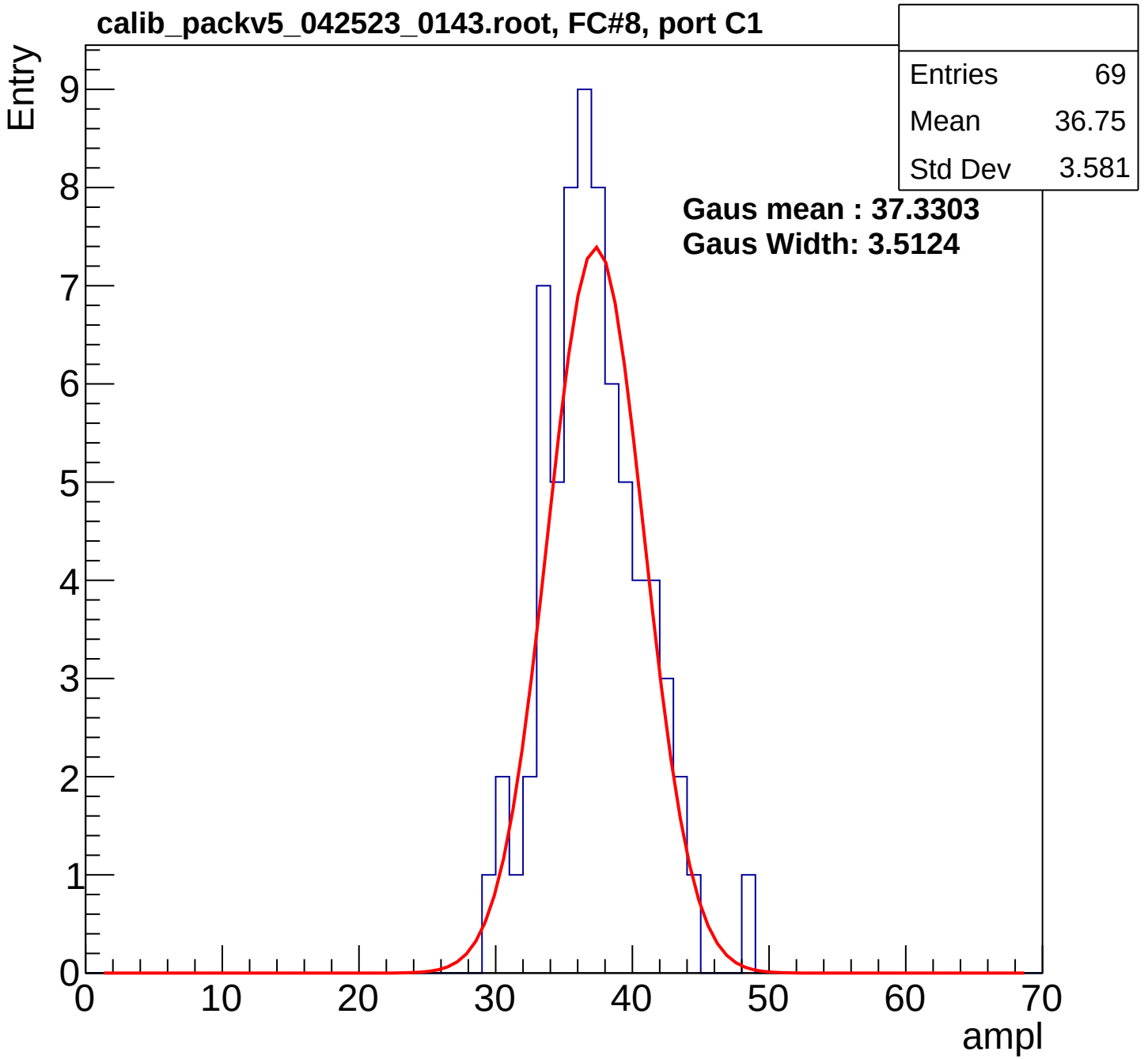

B0L002S, U10-ch120, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.86
Std Dev 2.96
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

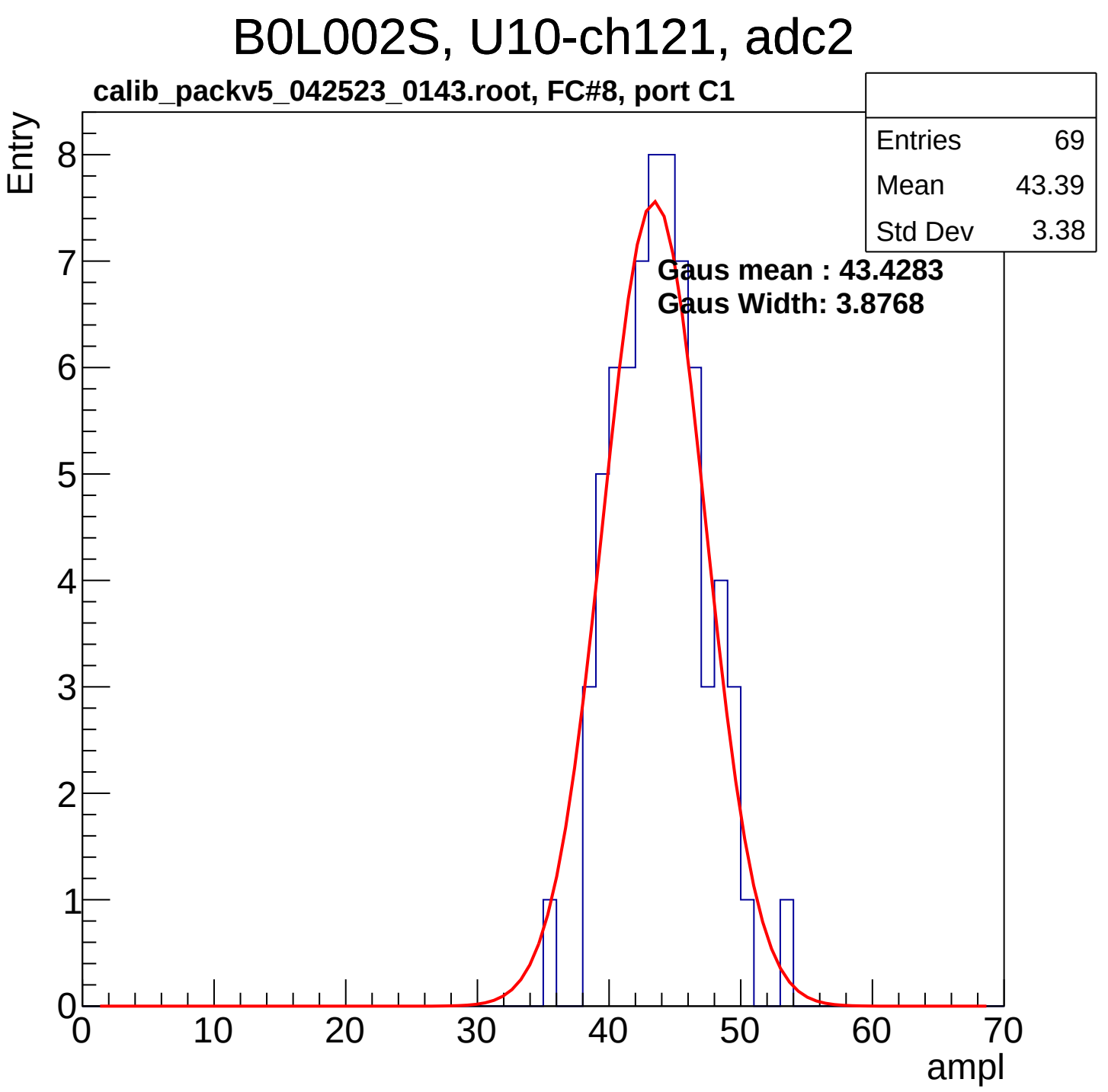

B0L002S, U10-ch120, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 39
Mean 59.21
Std Dev 9.864
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

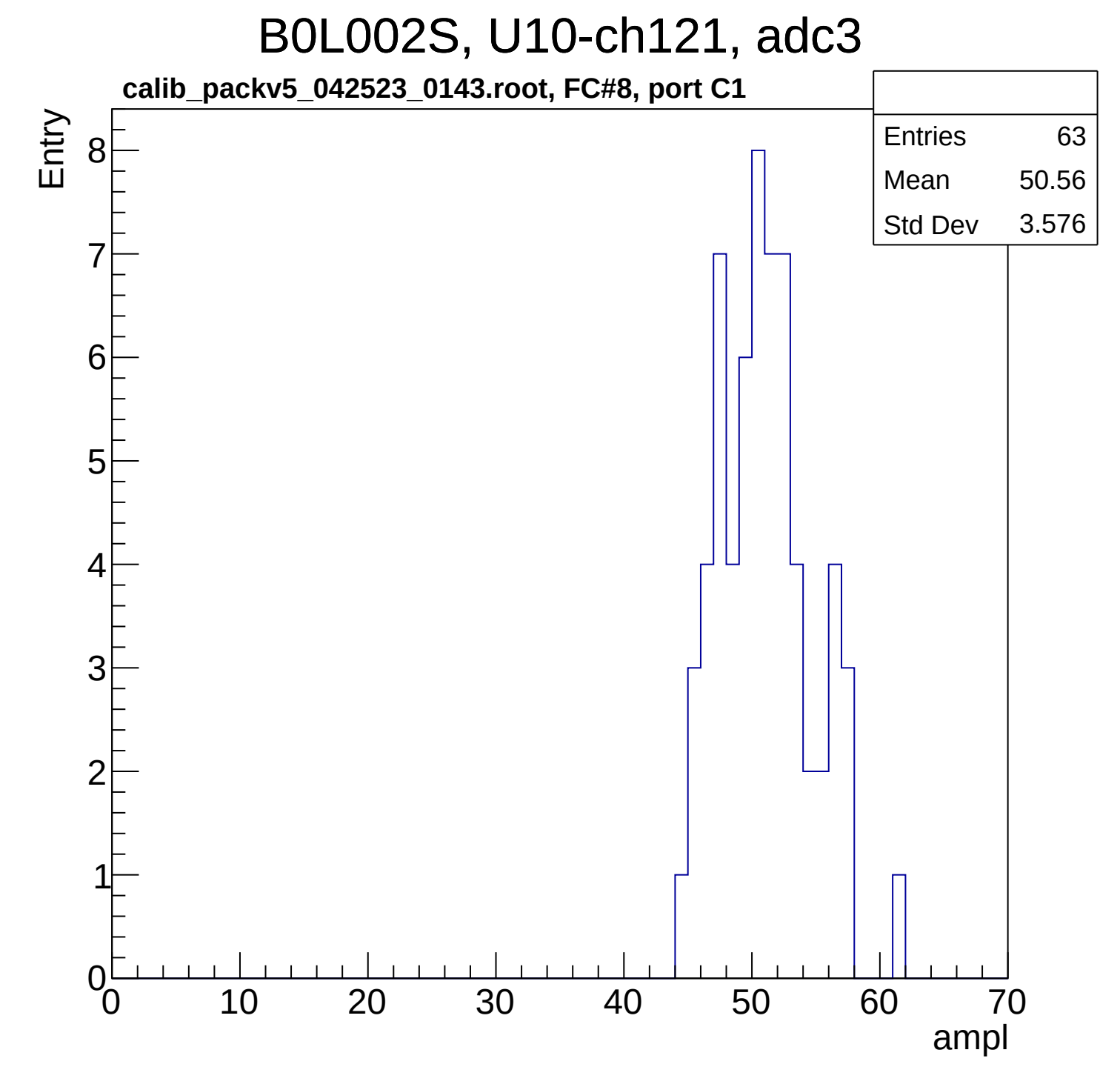

B0L002S, U10-ch120, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

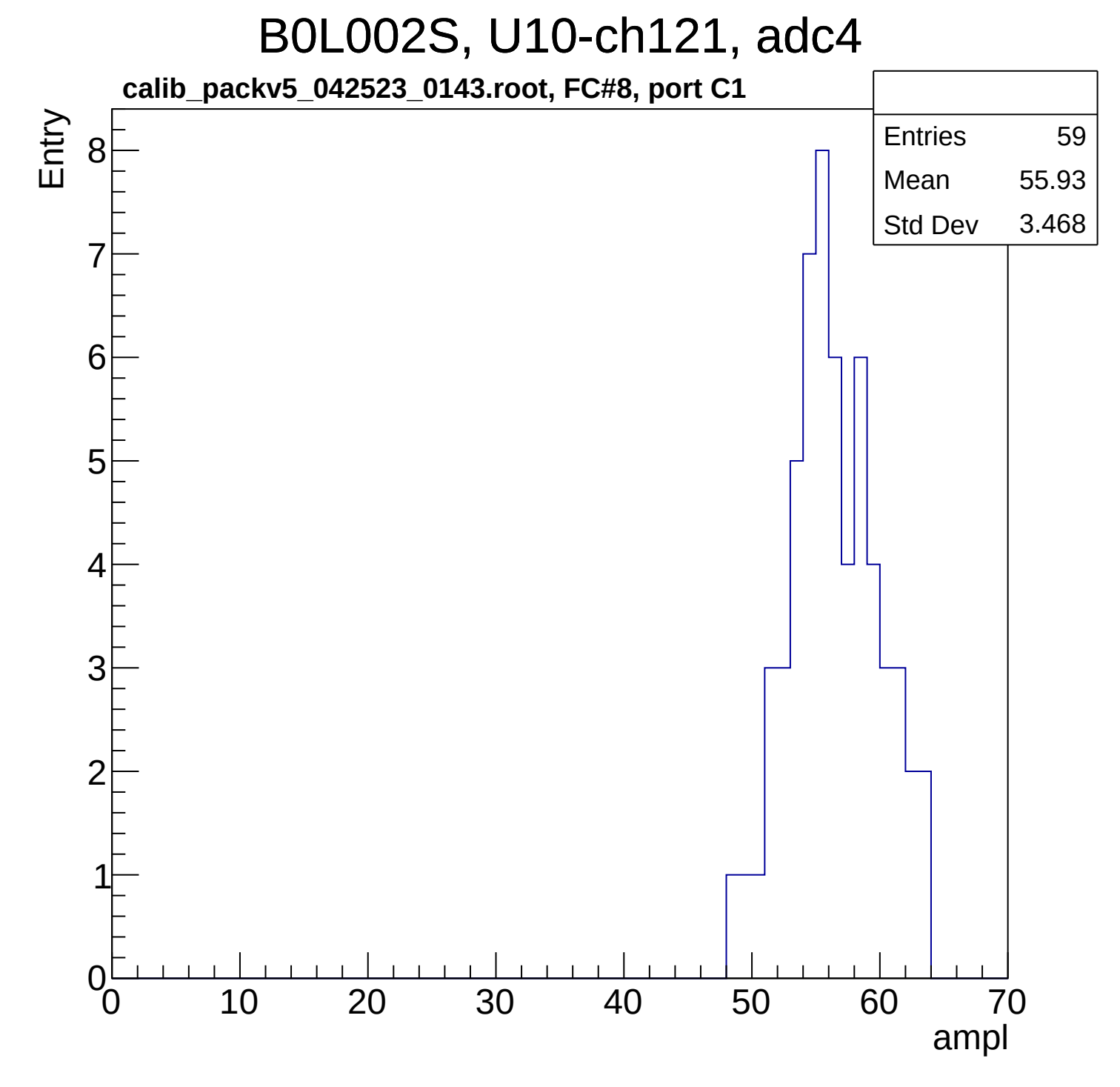

B0L002S, U10-ch120, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

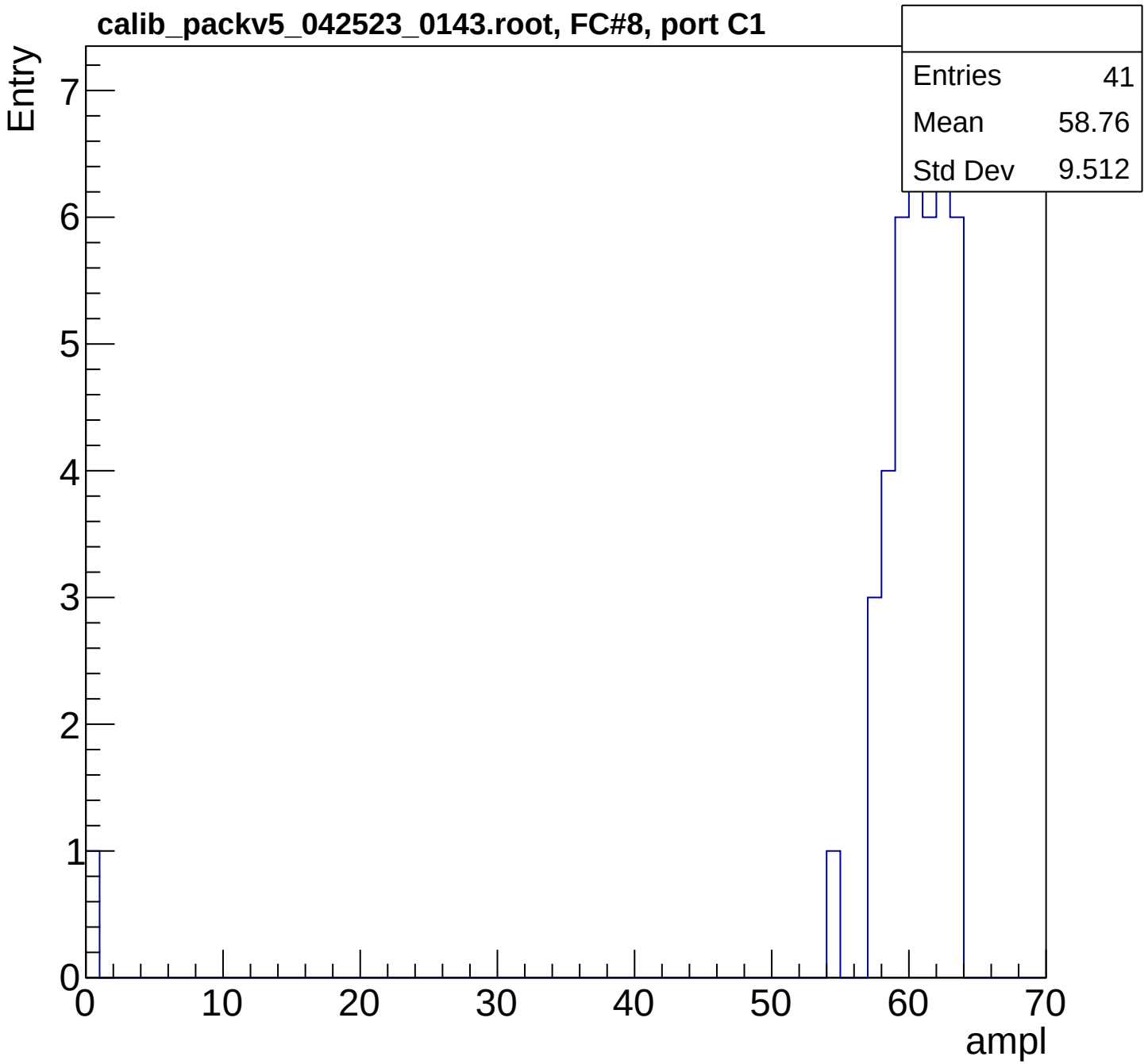

B0L002S, U10-ch121, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 87
Mean 28.68
Std Dev 3.837
Gaus mean : 28.8561
Gaus Width: 3.8059
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

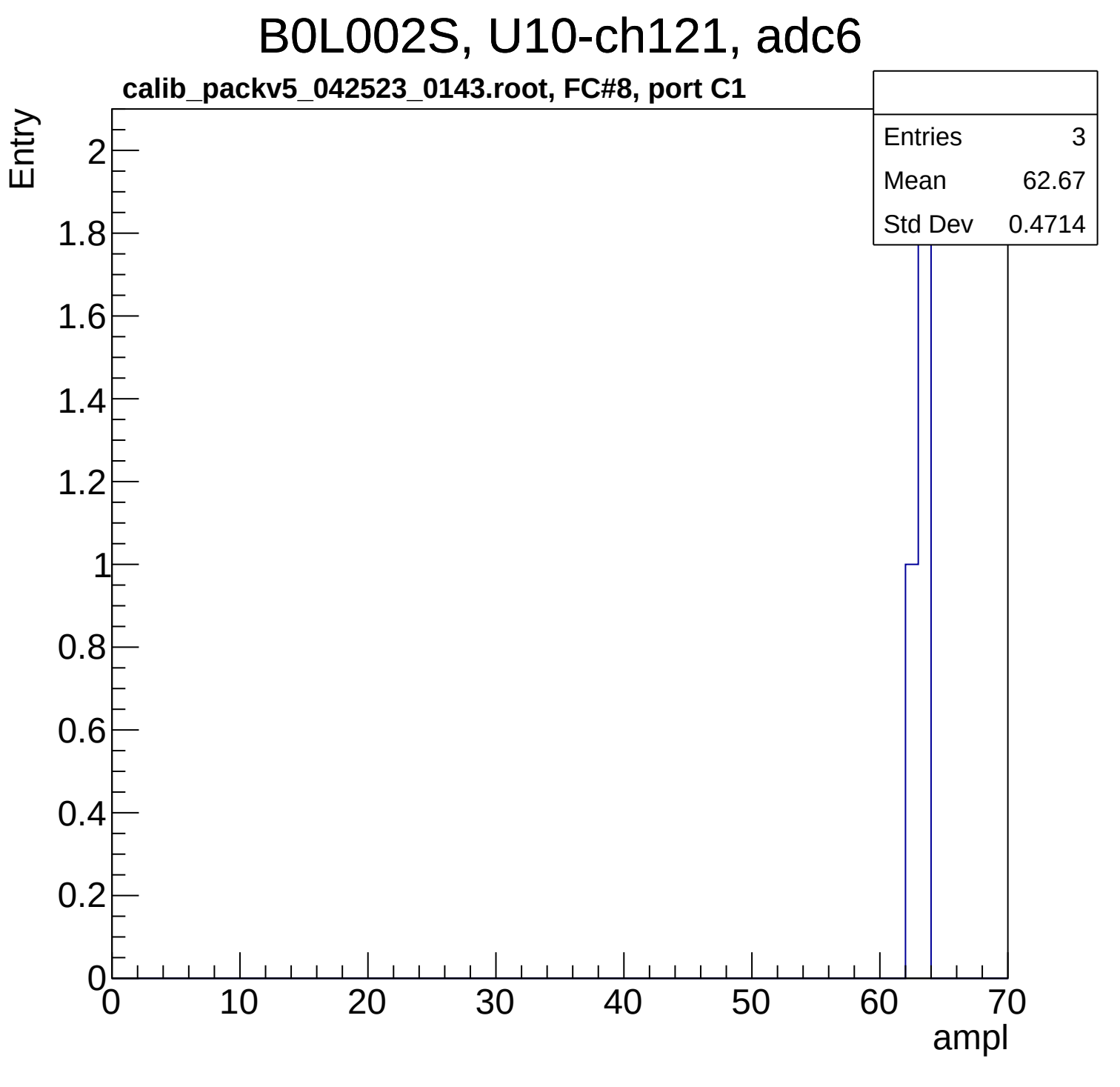

B0L002S, U10-ch121, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 36.75
Std Dev 3.581
Gaus mean : 37.3303
Gaus Width: 3.5124
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

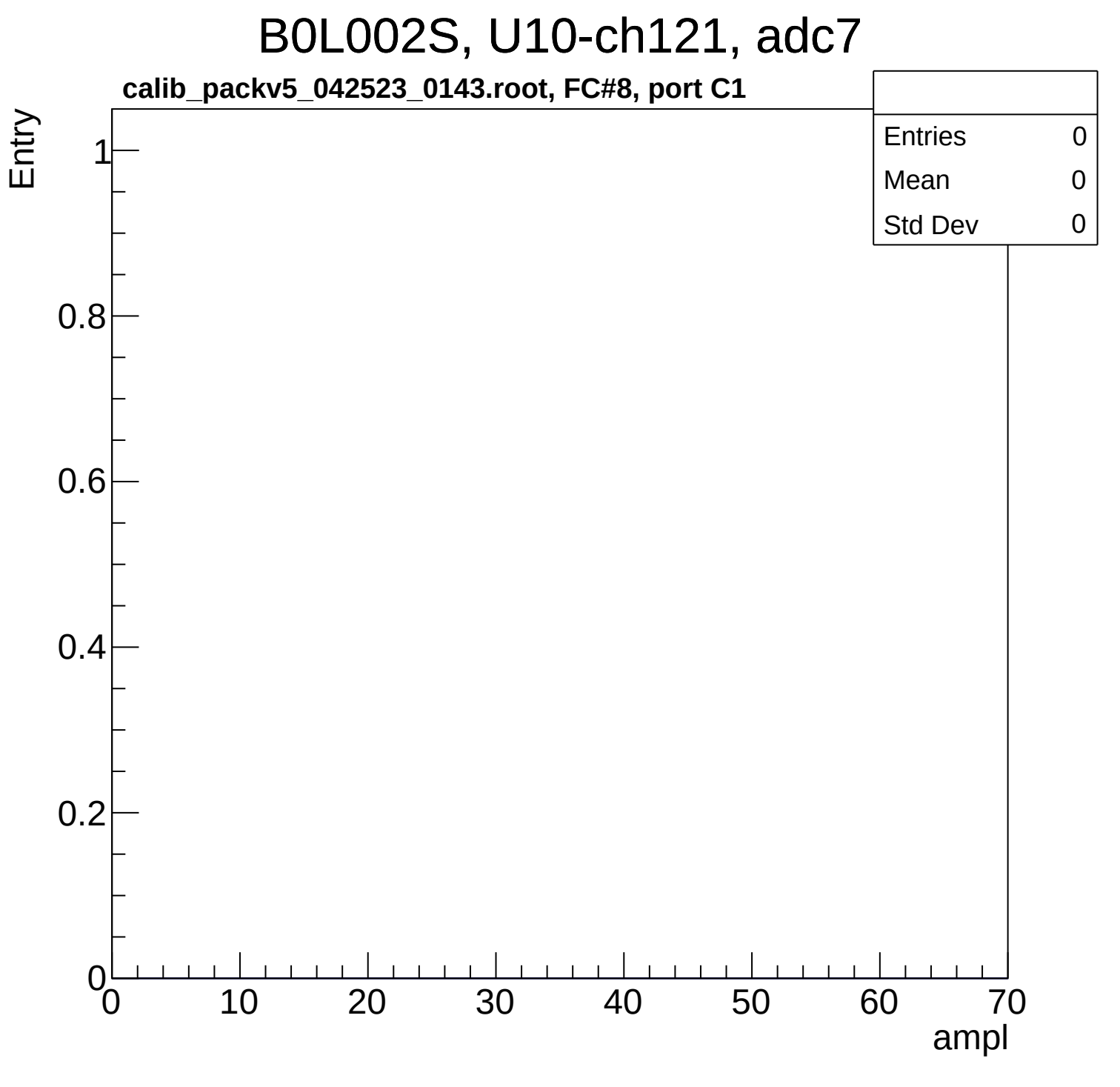

B0L002S, U10-ch121, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 69
Mean 43.39
Std Dev 3.38
Gaus mean : 43.4283
Gaus Width: 3.8768
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

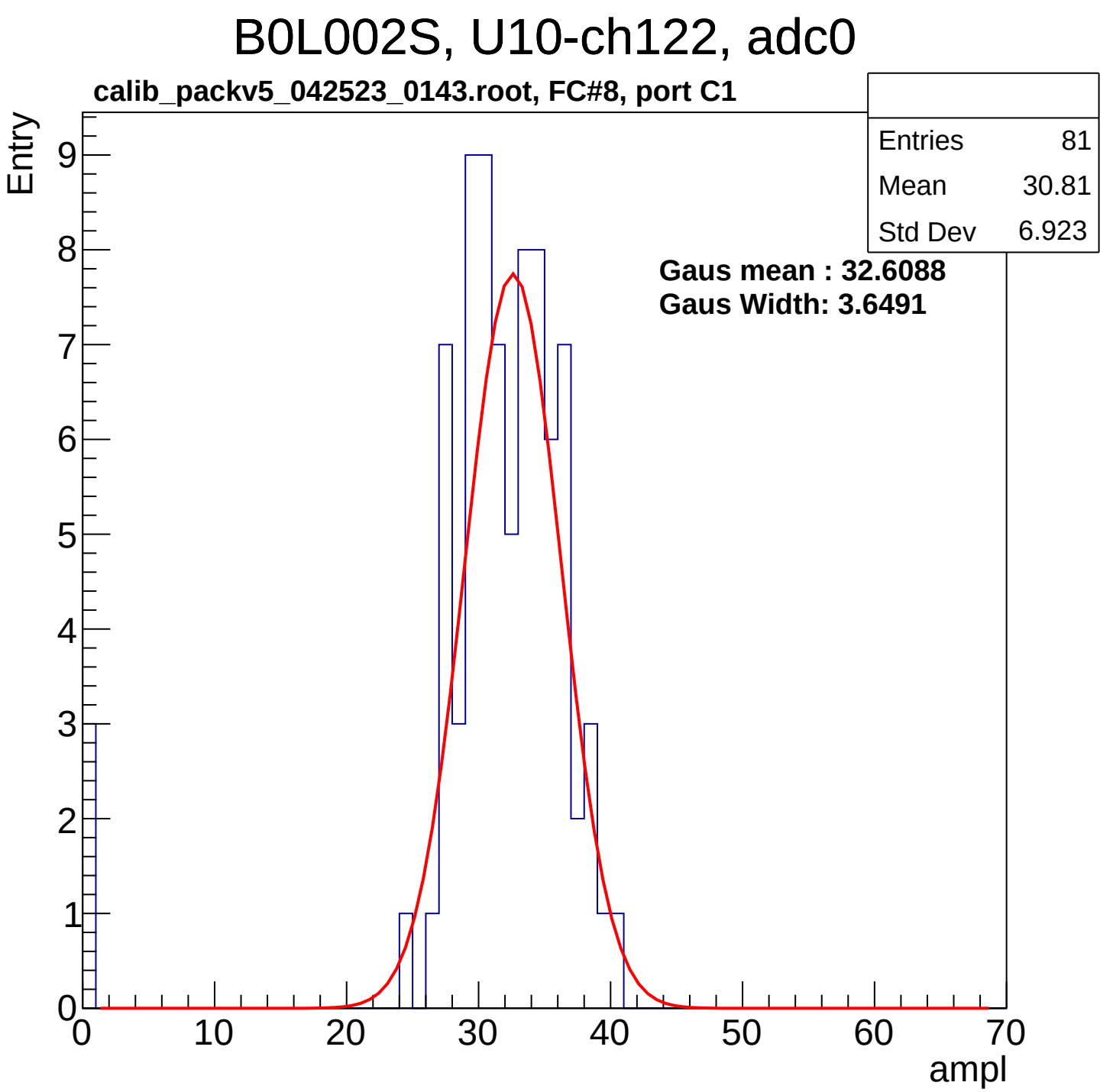

B0L002S, U10-ch121, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 63
Mean 50.56
Std Dev 3.576
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

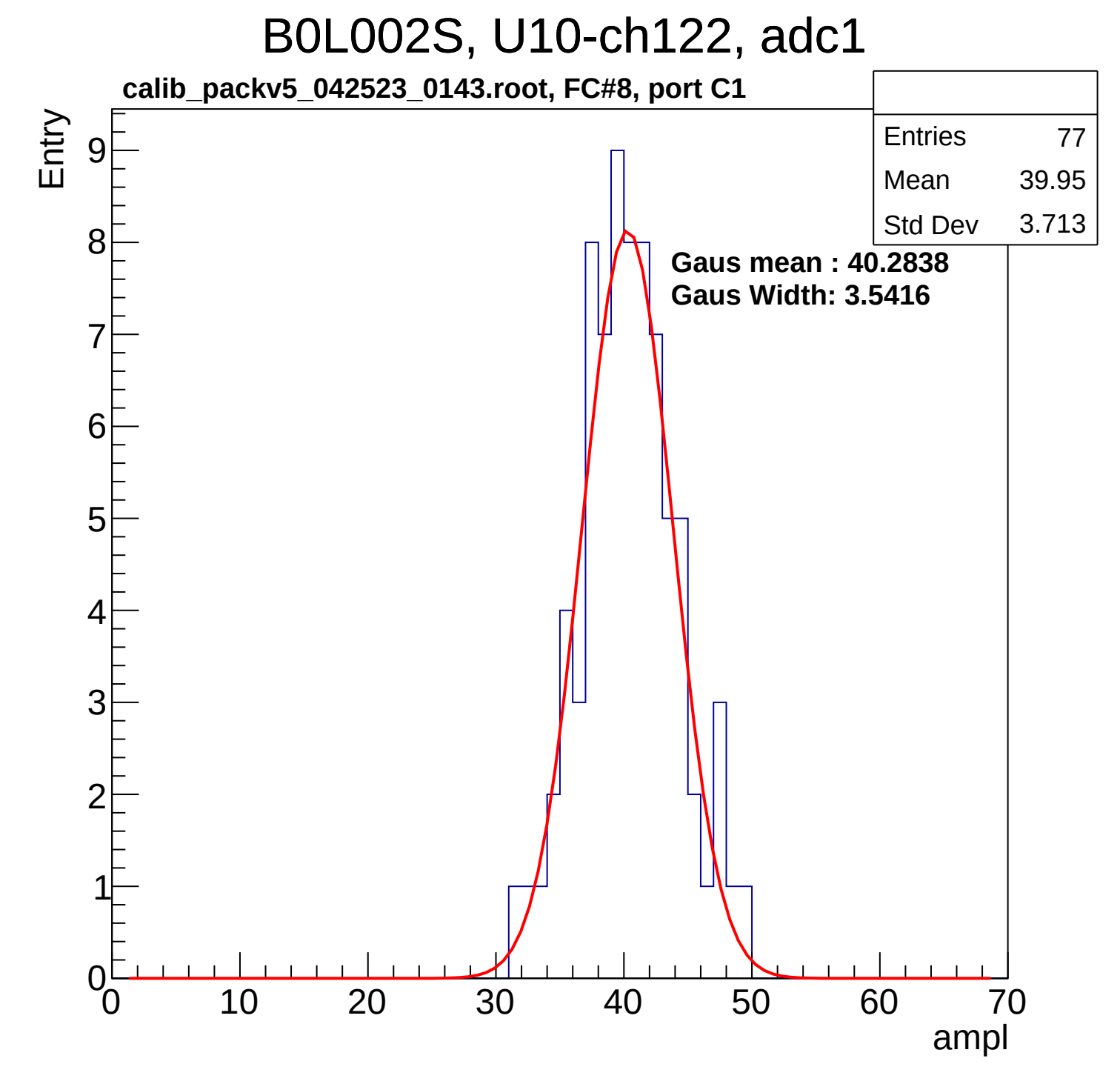

B0L002S, U10-ch121, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 59
Mean 55.93
Std Dev 3.468
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

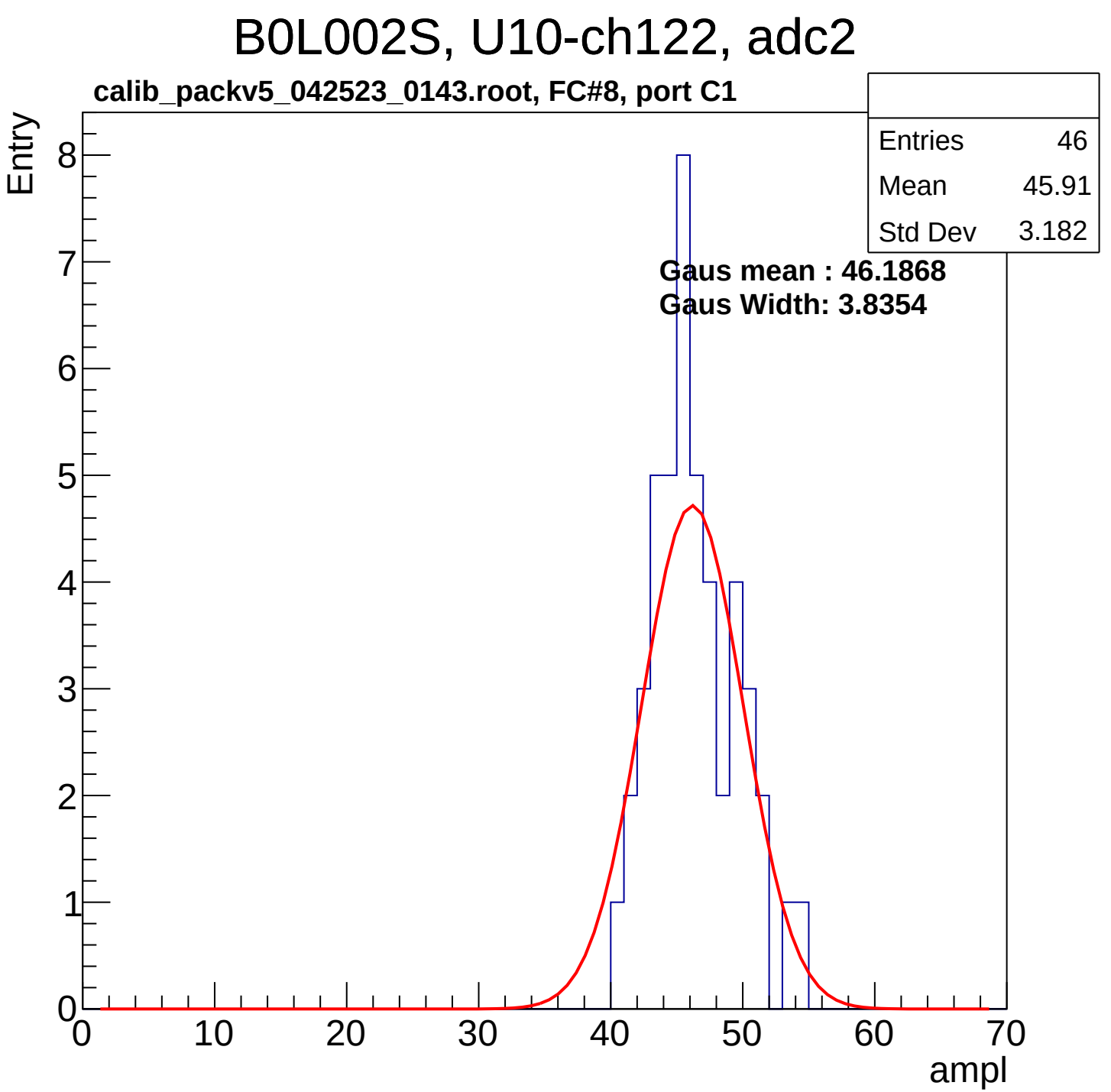

B0L002S, U10-ch121, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 41
Mean 58.76
Std Dev 9.512
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

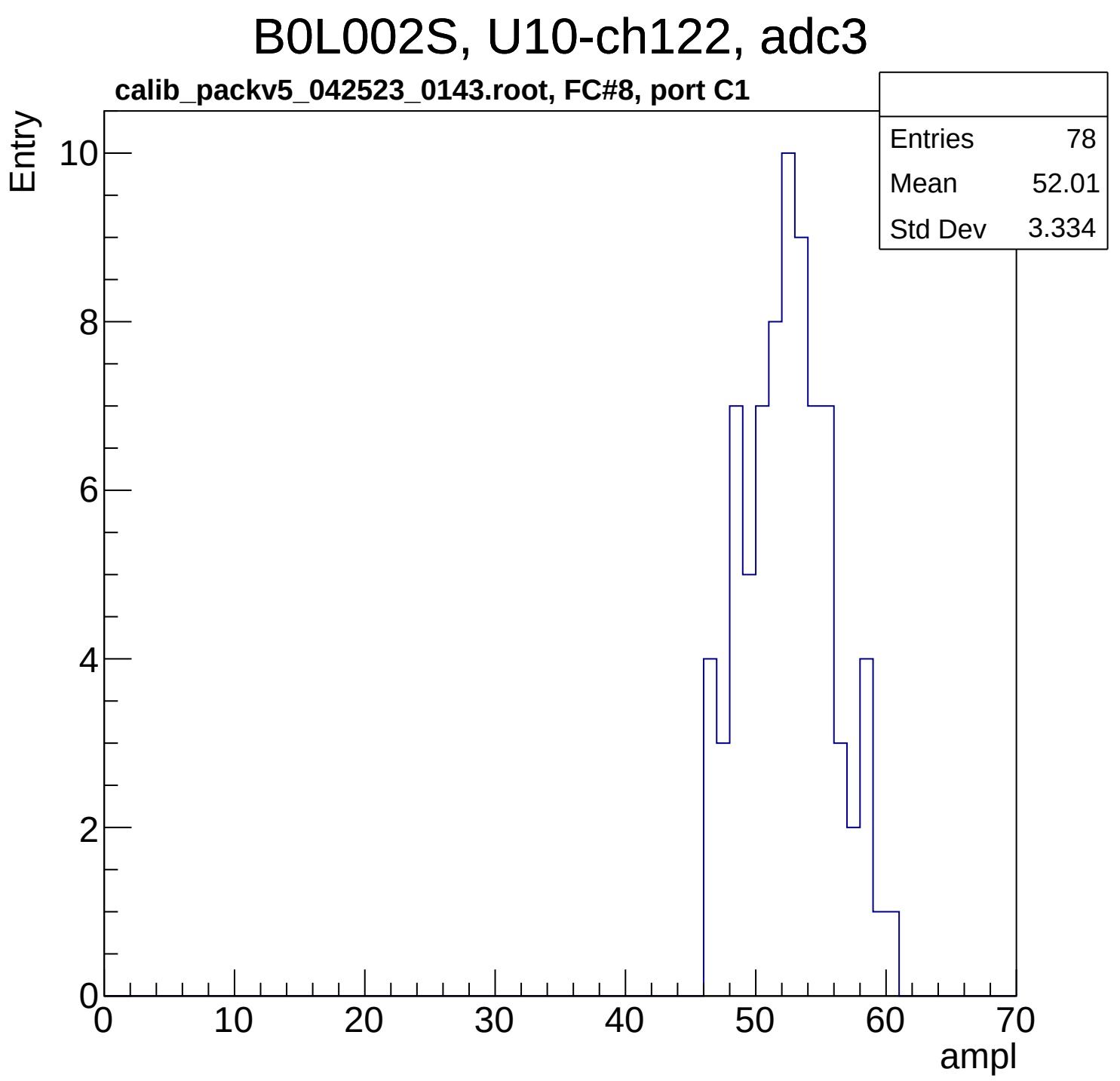

B0L002S, U10-ch121, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

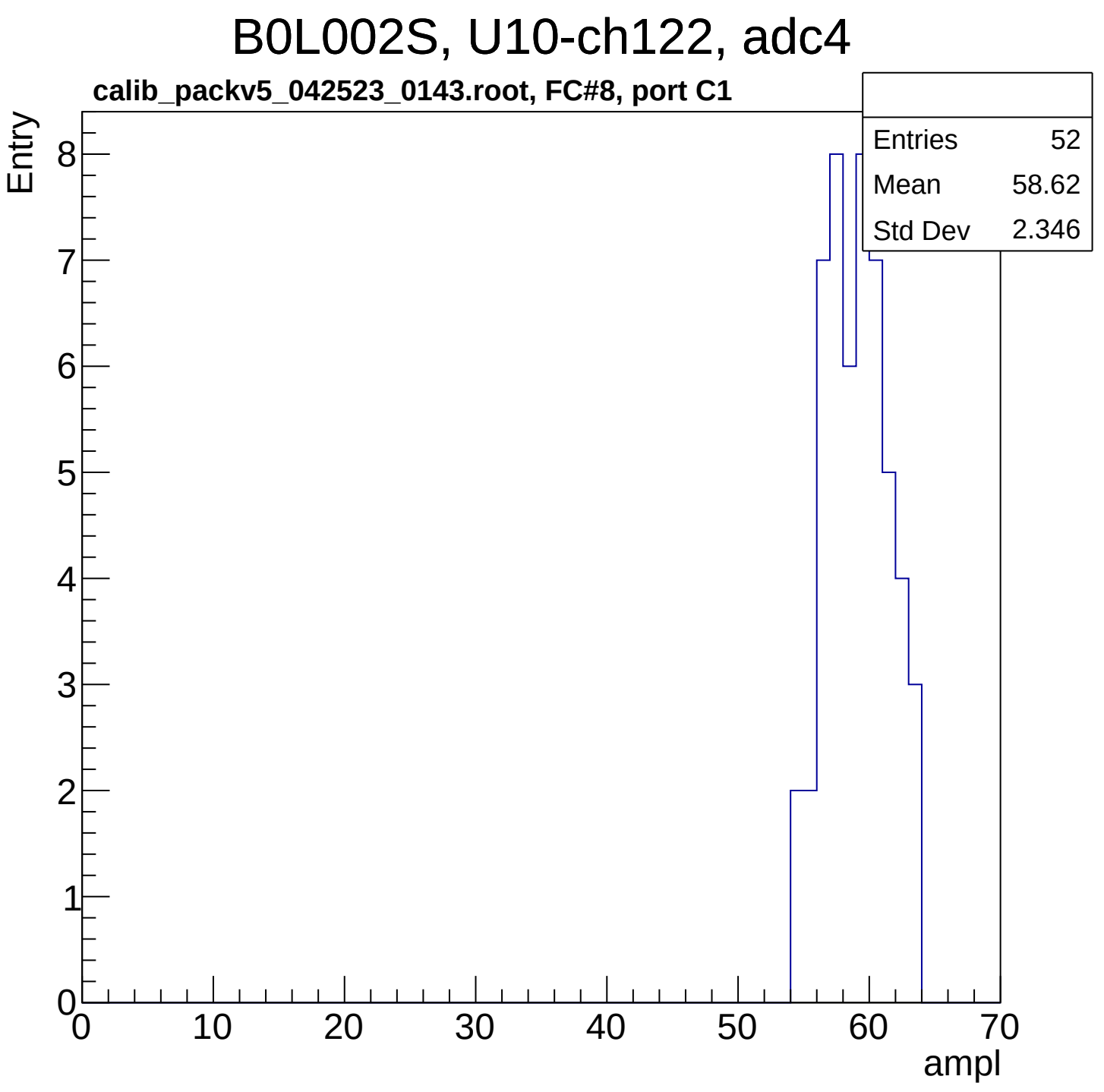

B0L002S, U10-ch121, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

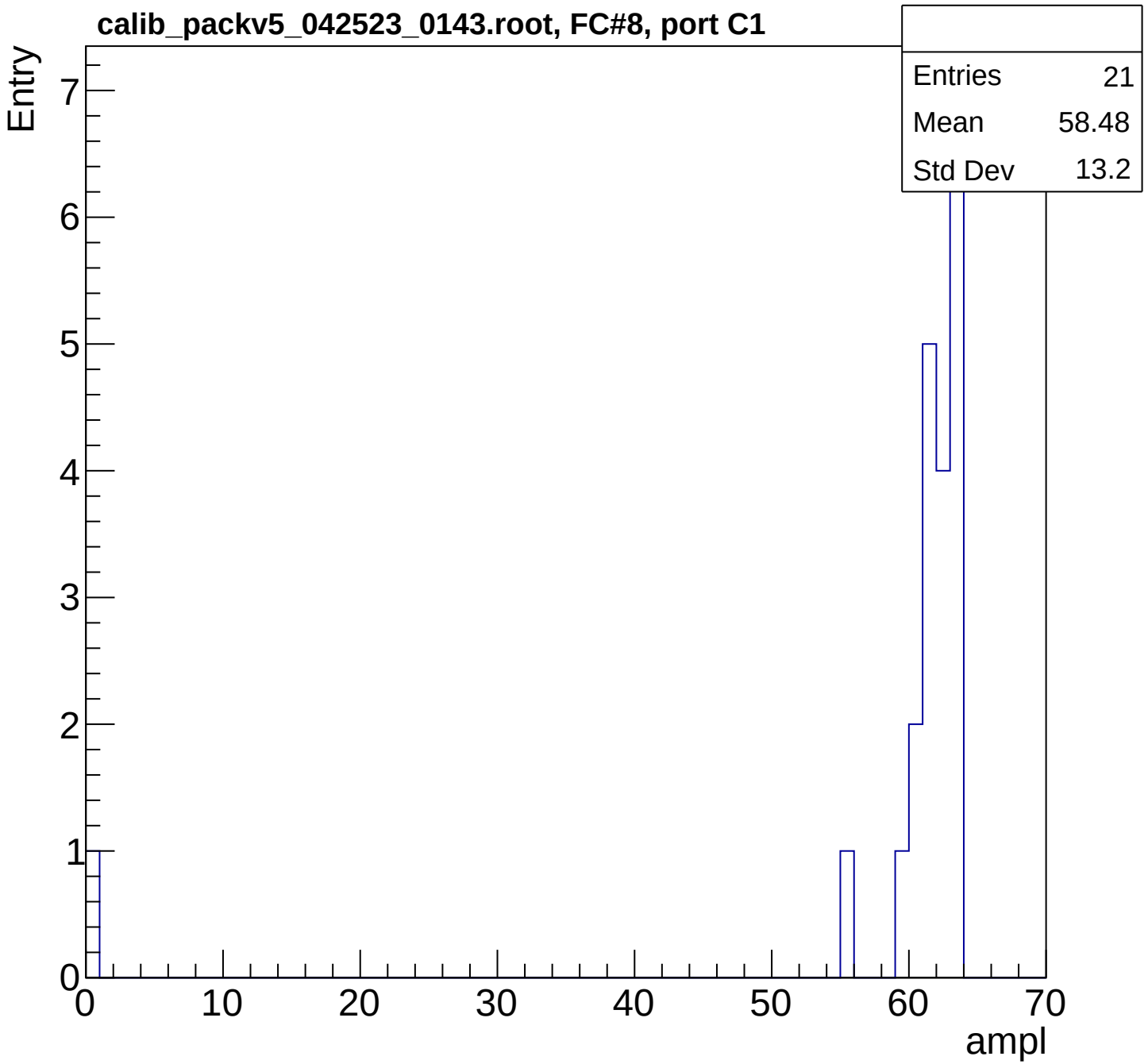

B0L002S, U10-ch122, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 81
Mean 30.81
Std Dev 6.923
Gaus mean : 32.6088
Gaus Width: 3.6491
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

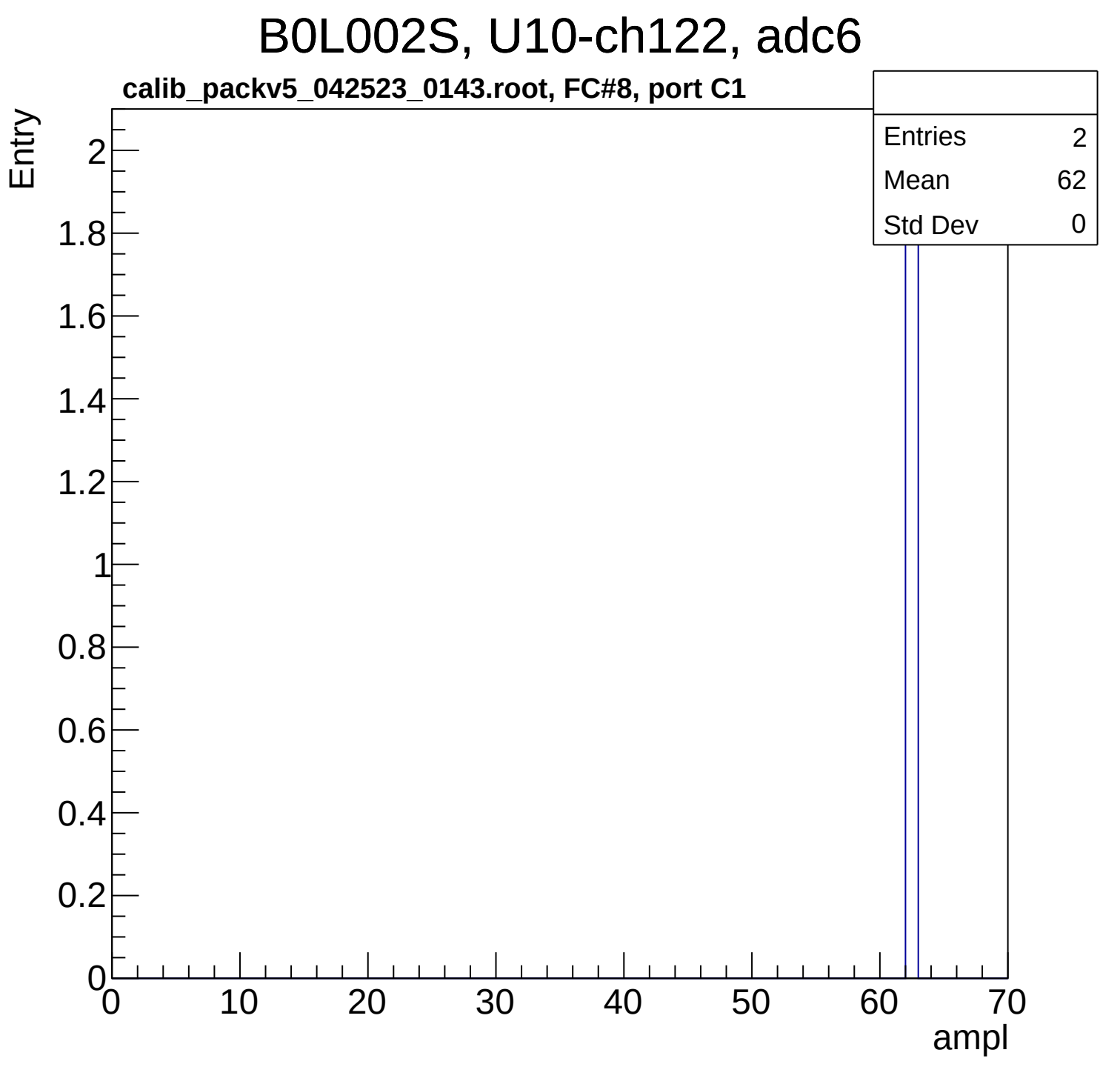

B0L002S, U10-ch122, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 77
Mean 39.95
Std Dev 3.713
Gaus mean : 40.2838
Gaus Width: 3.5416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

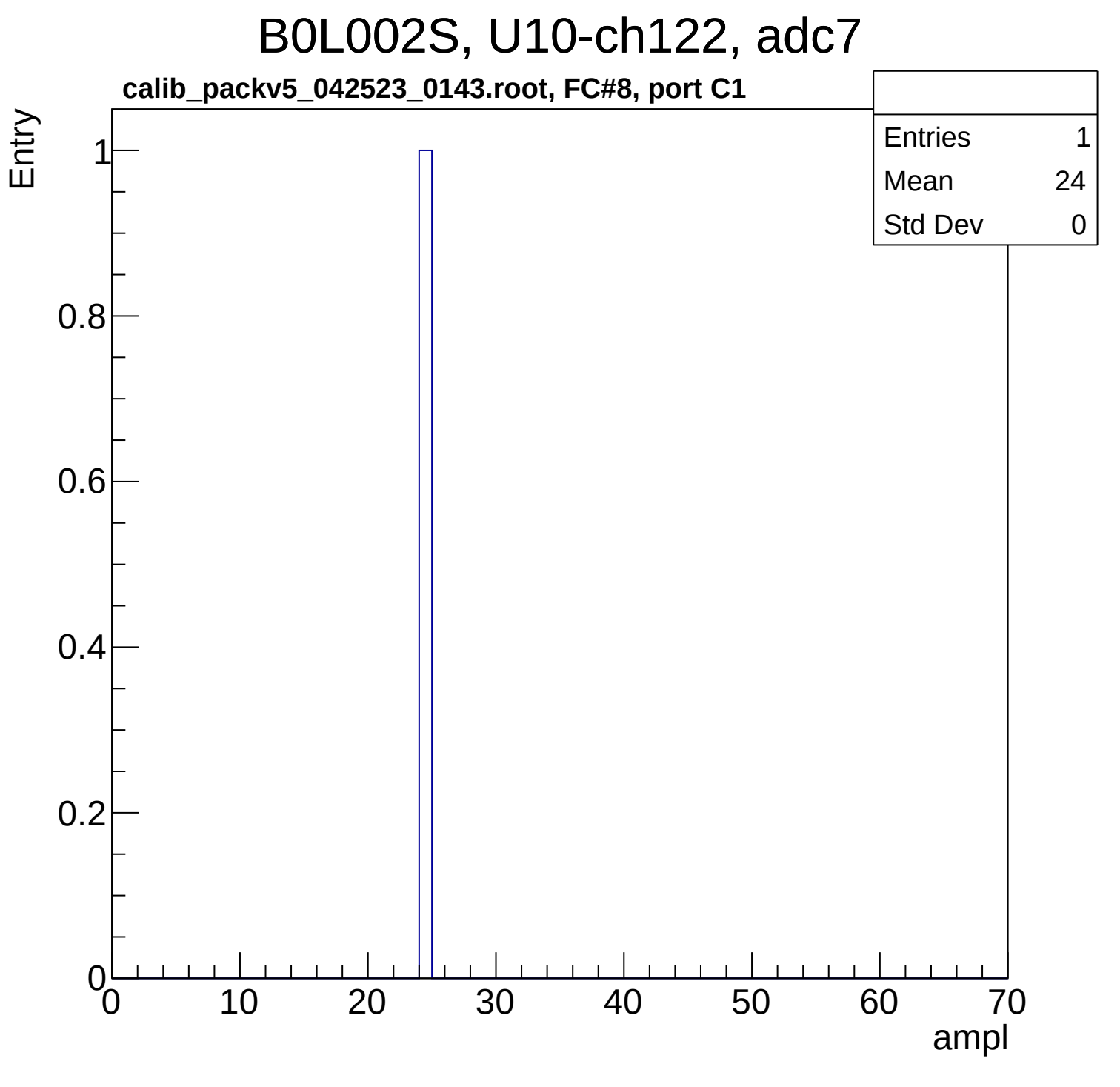

B0L002S, U10-ch122, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 46
Mean 45.91
Std Dev 3.182
Gaus mean : 46.1868
Gaus Width: 3.8354
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

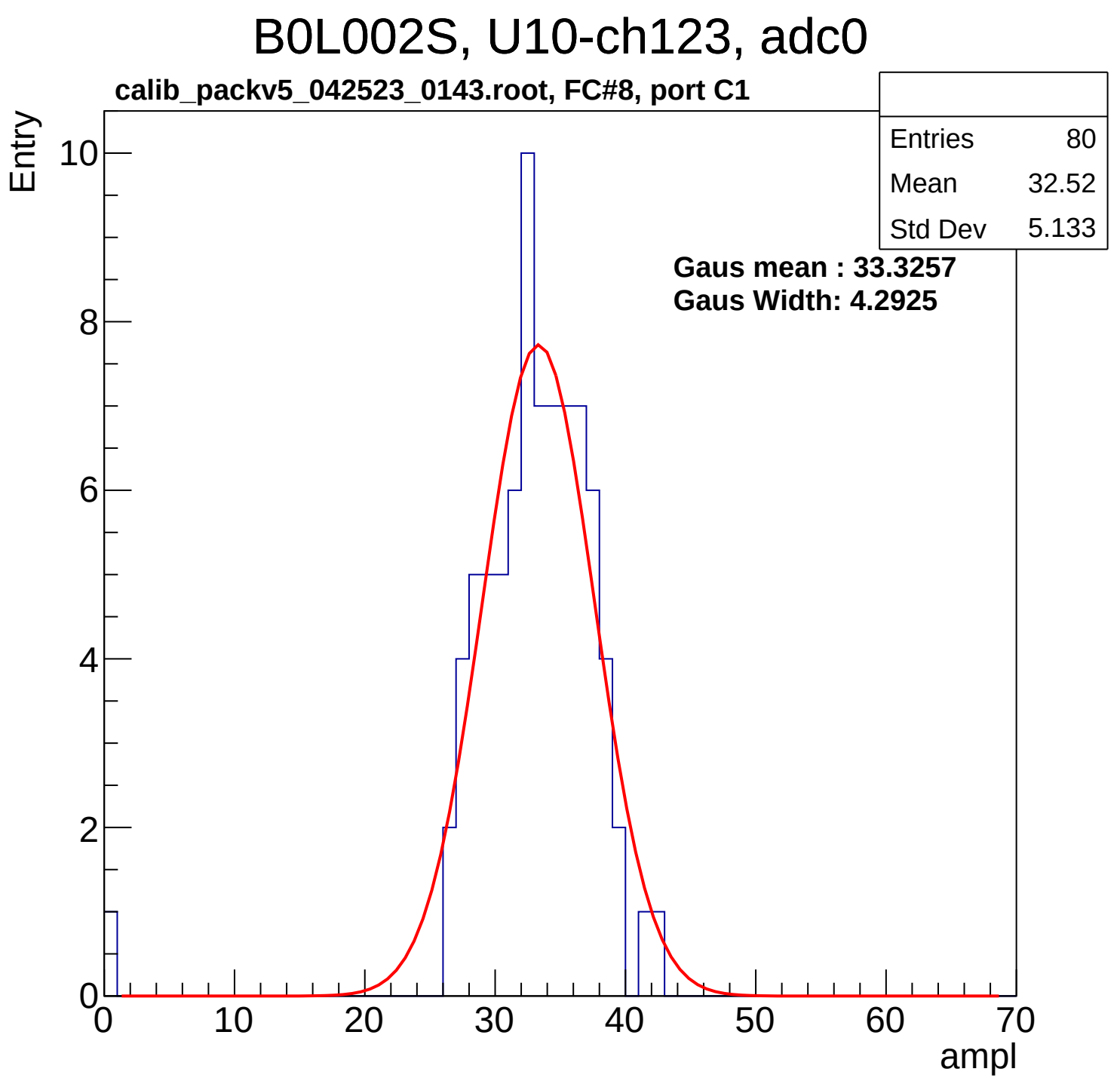

B0L002S, U10-ch122, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 52.01
Std Dev 3.334
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

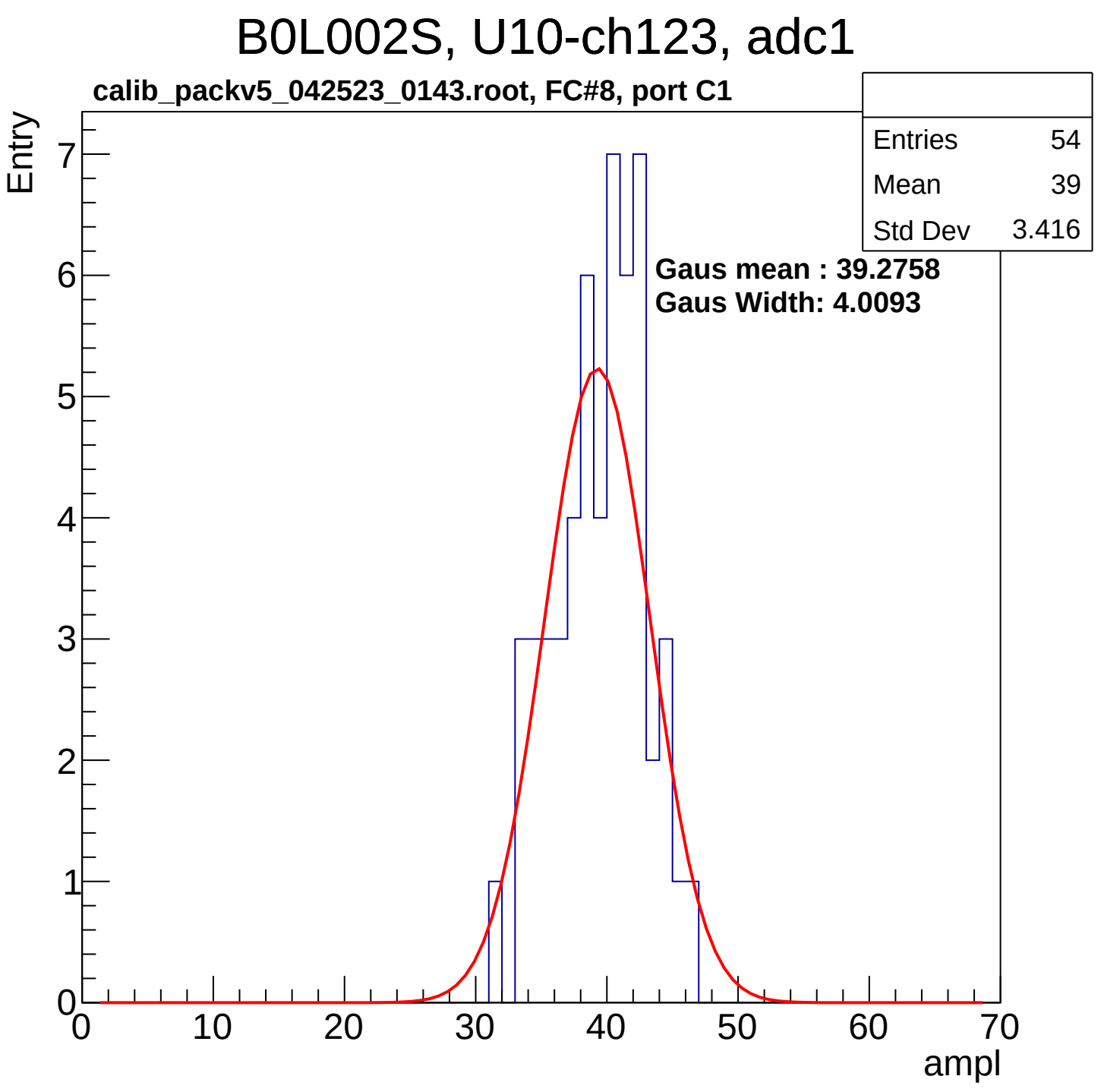

B0L002S, U10-ch122, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 52
Mean 58.62
Std Dev 2.346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

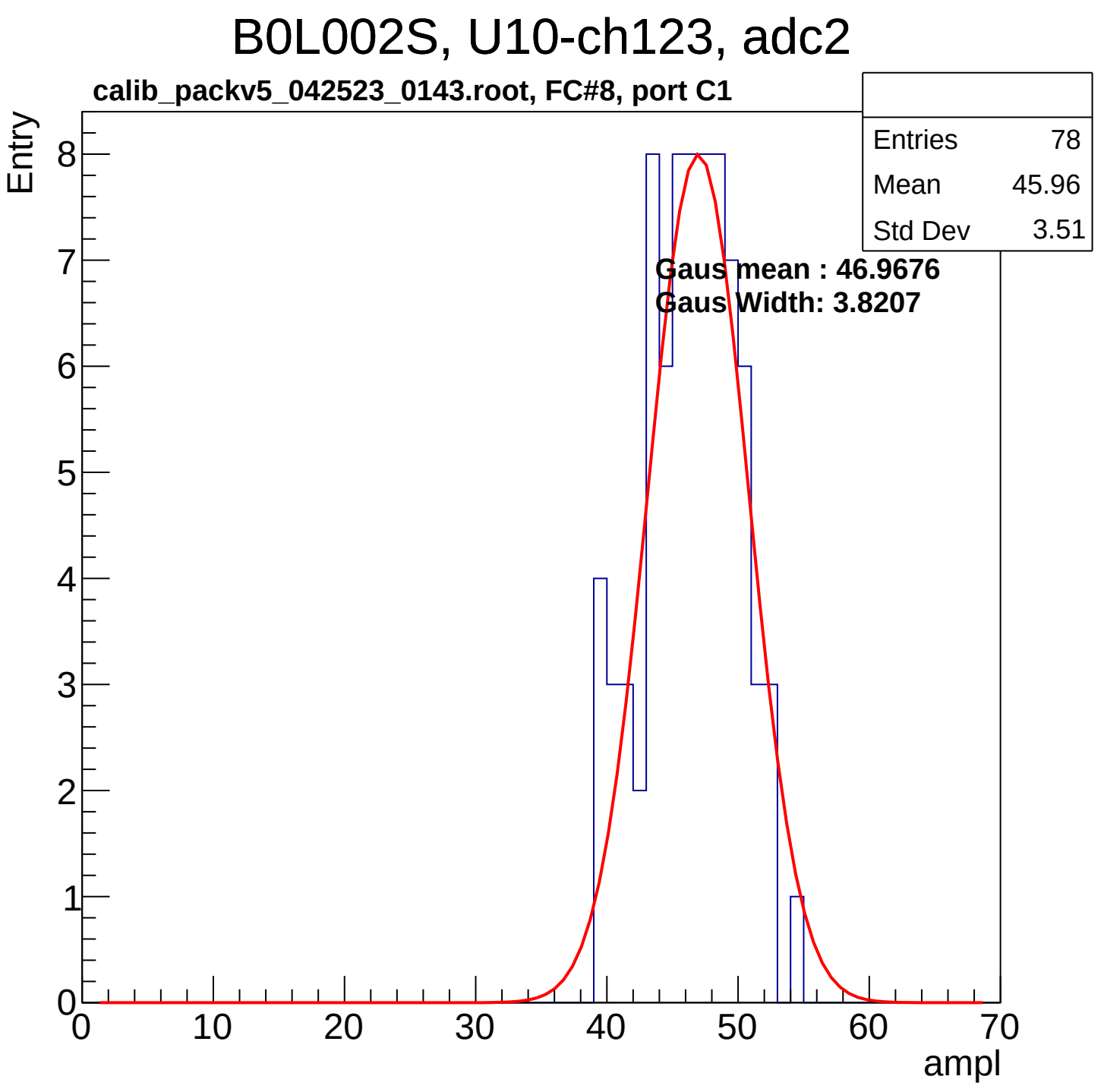

B0L002S, U10-ch122, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 21
Mean 58.48
Std Dev 13.2
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

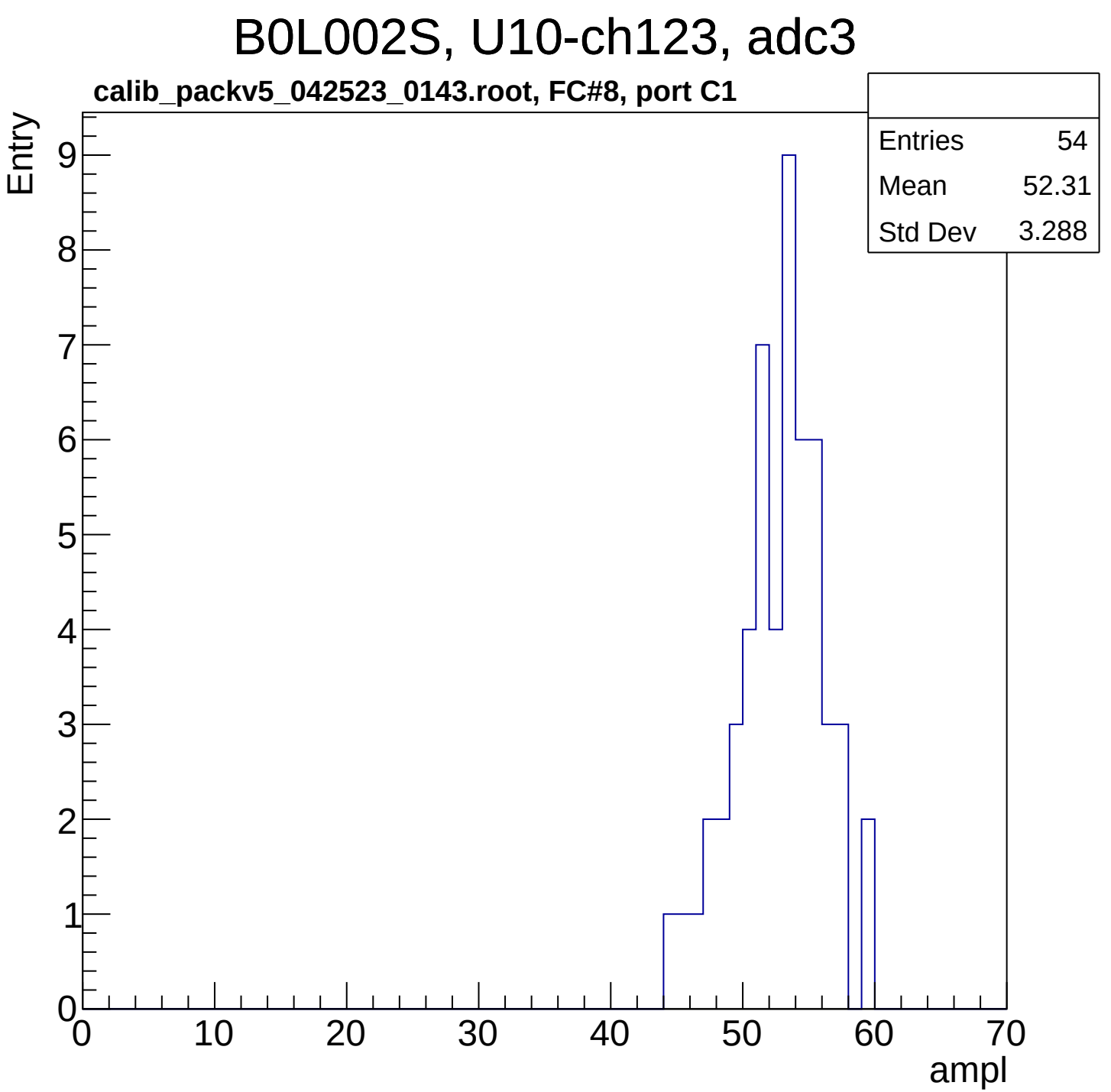

B0L002S, U10-ch122, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 62
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

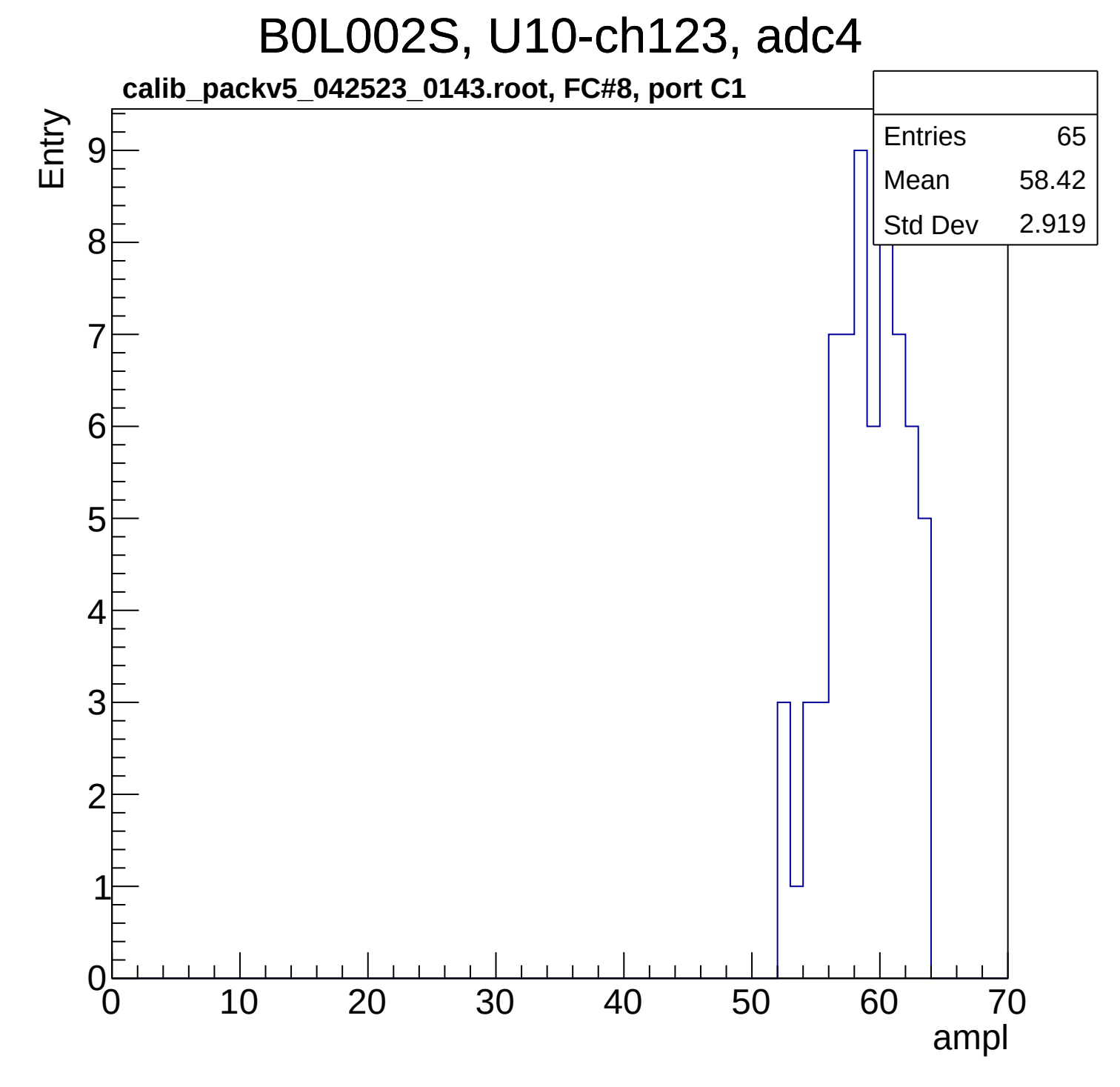

B0L002S, U10-ch122, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

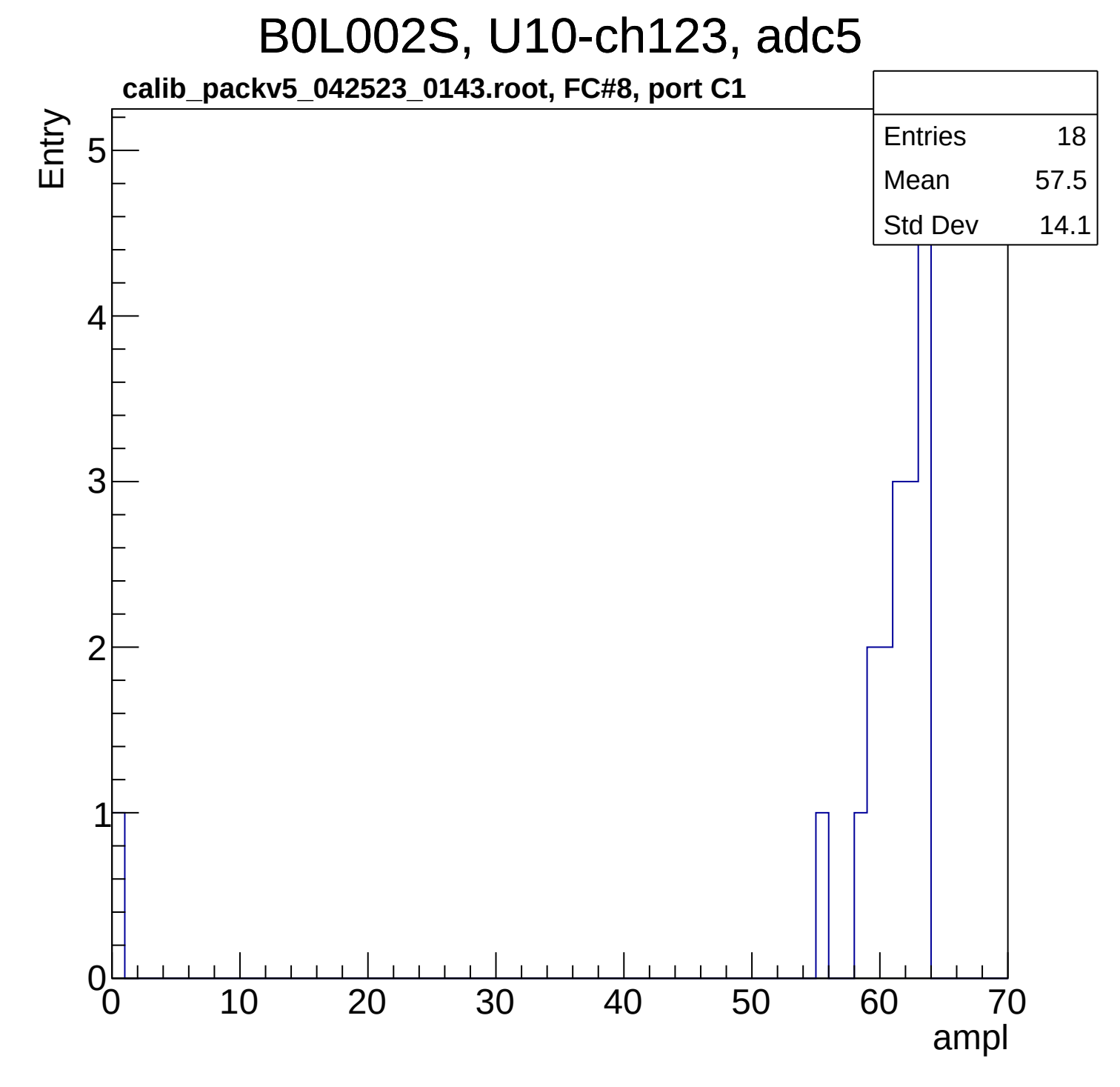

B0L002S, U10-ch123, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 80
Mean 32.52
Std Dev 5.133
Gaus mean : 33.3257
Gaus Width: 4.2925
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

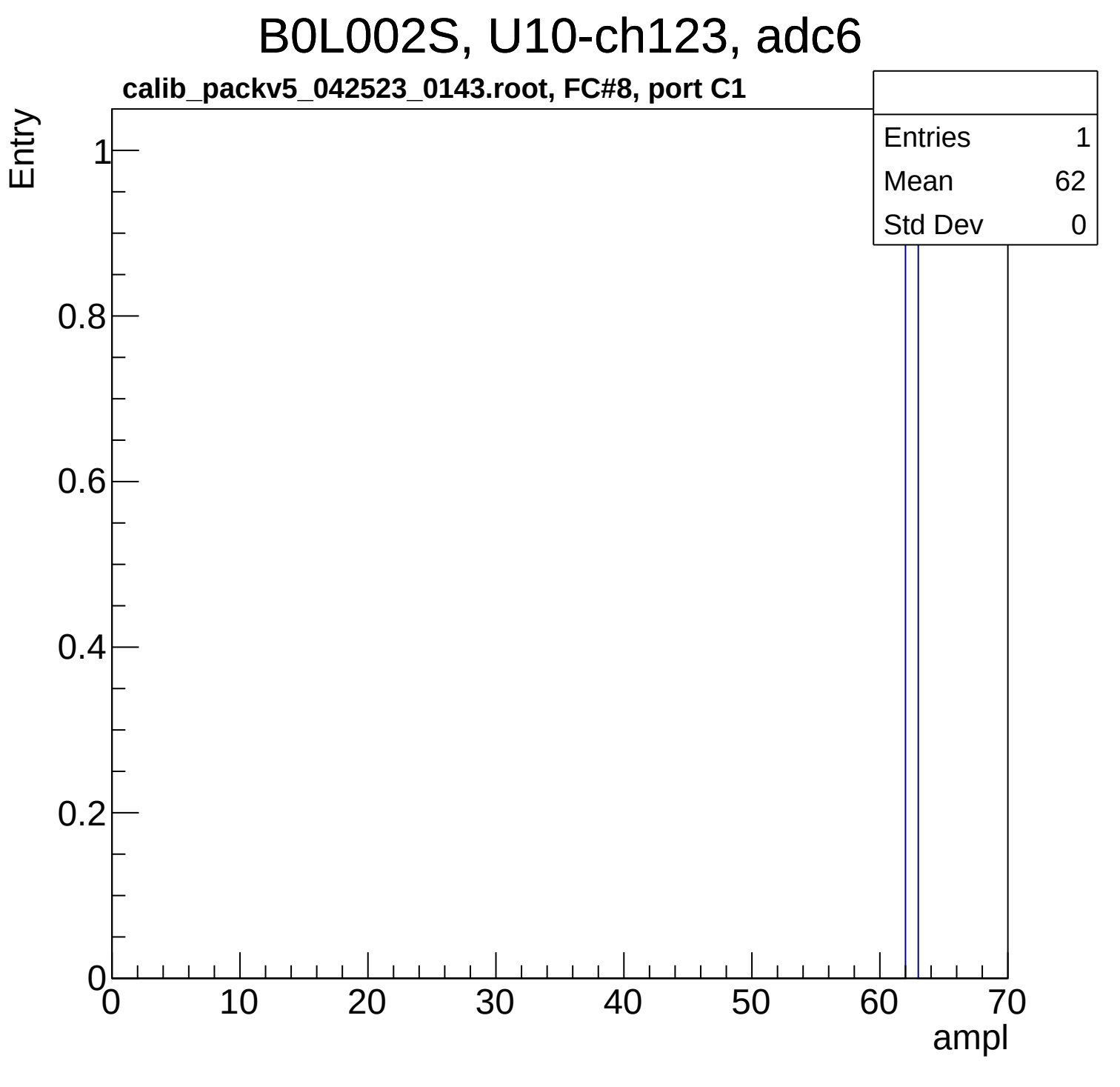

B0L002S, U10-ch123, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 39
Std Dev 3.416
Gaus mean : 39.2758
Gaus Width: 4.0093
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

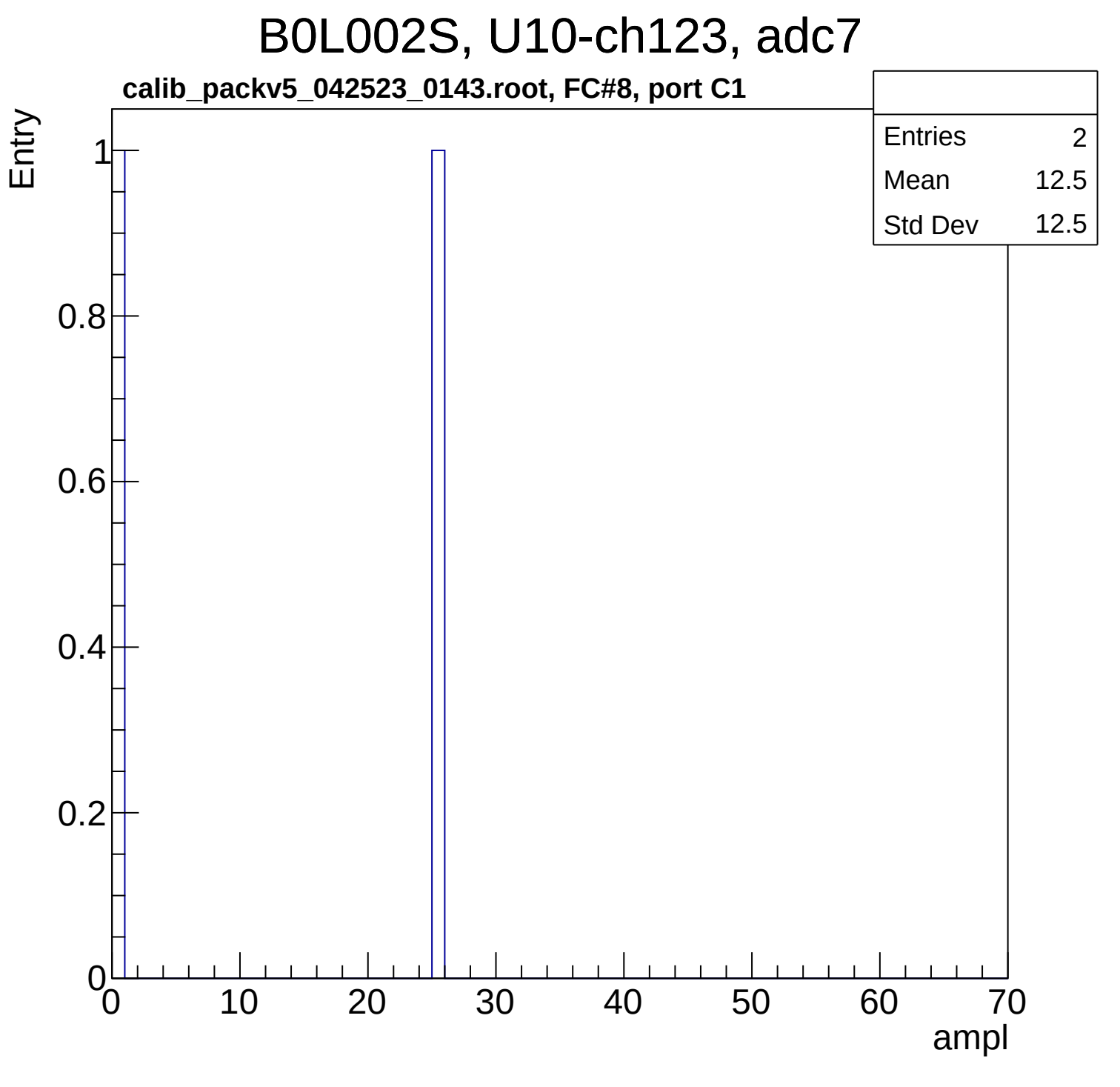

B0L002S, U10-ch123, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 78
Mean 45.96
Std Dev 3.51
Gaus mean : 46.9676
Gaus Width: 3.8207
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

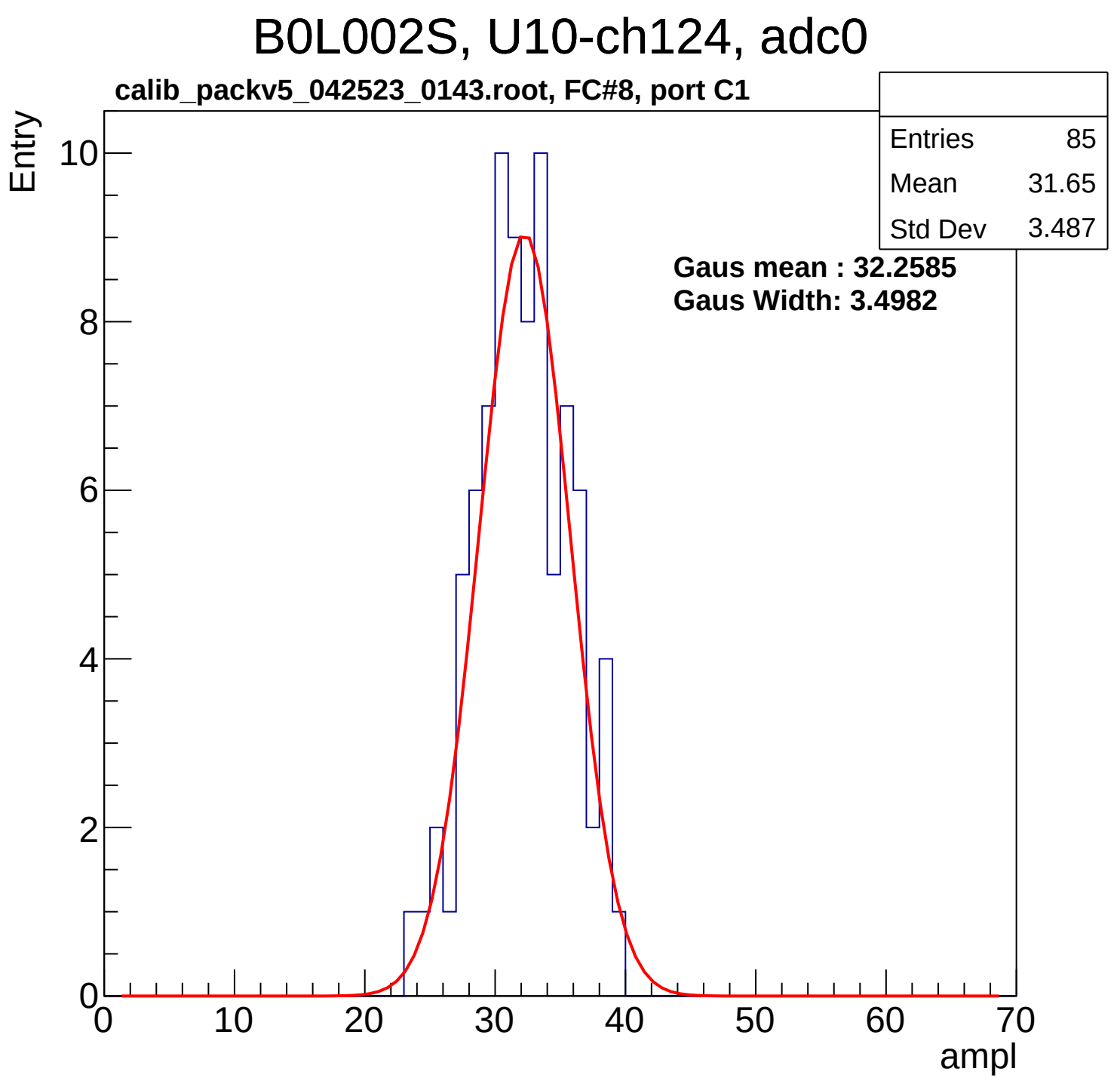

B0L002S, U10-ch123, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 54
Mean 52.31
Std Dev 3.288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

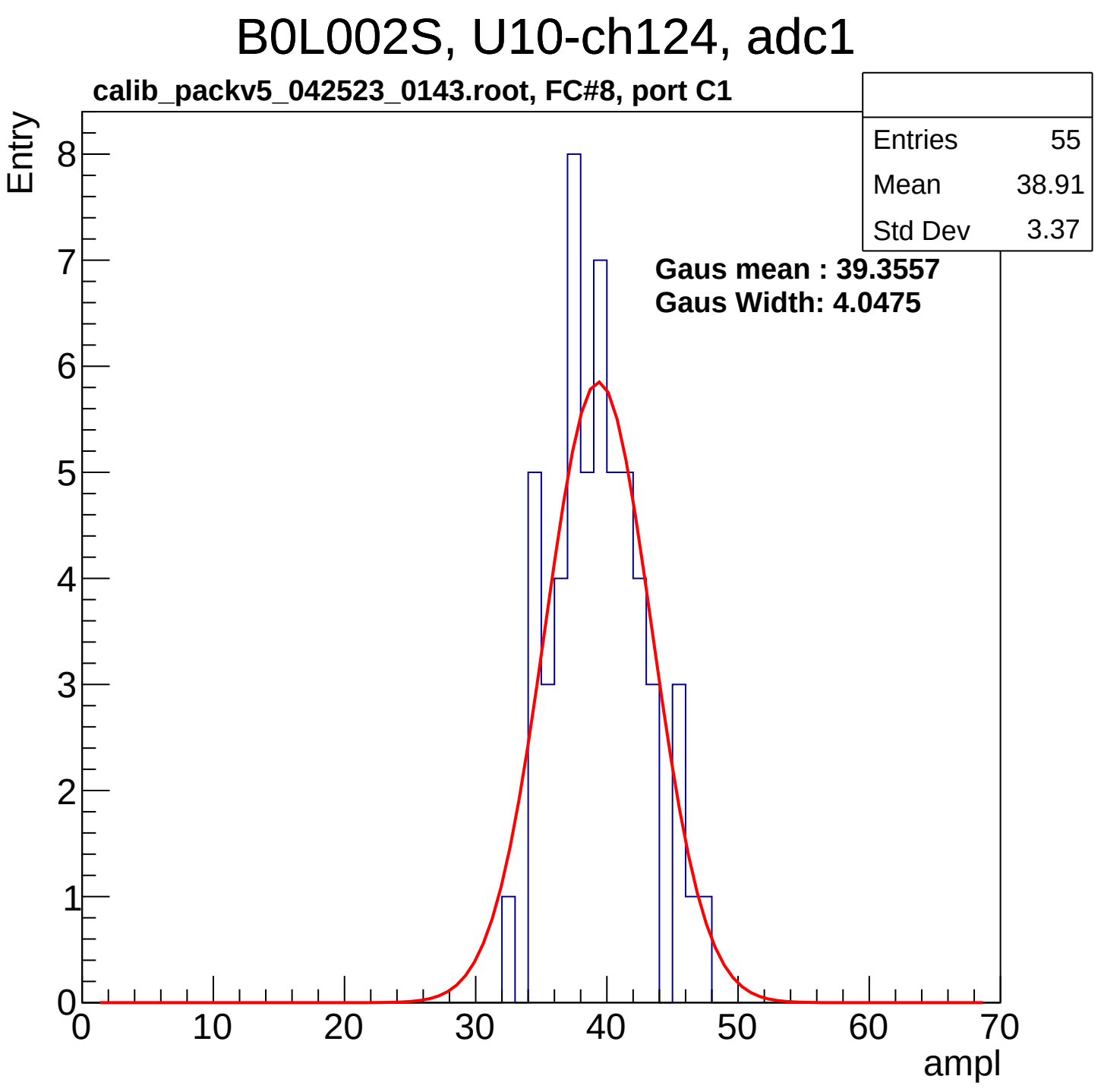

B0L002S, U10-ch123, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 58.42
Std Dev 2.919
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

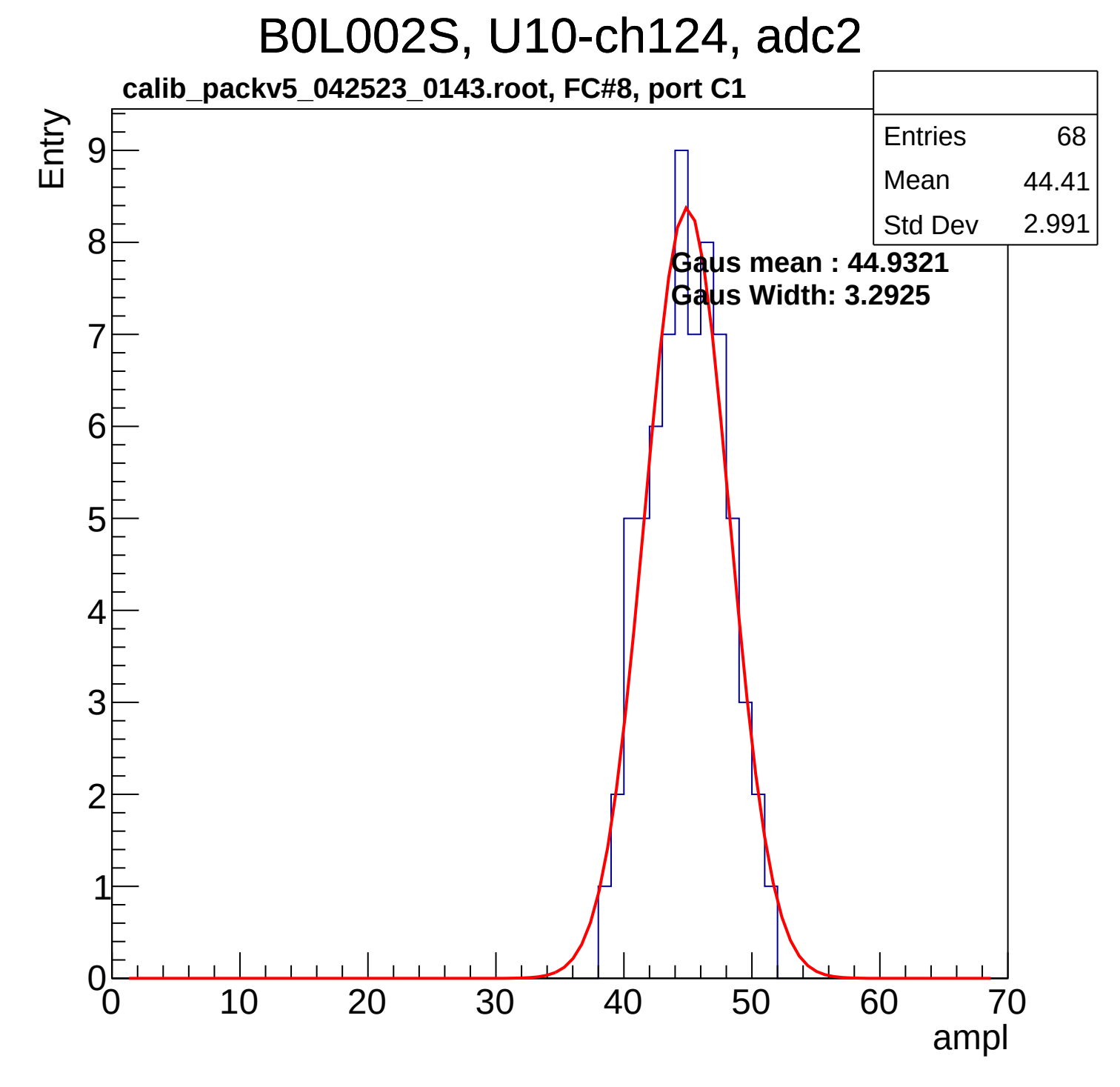

B0L002S, U10-ch123, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 18
Mean 57.5
Std Dev 14.1
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

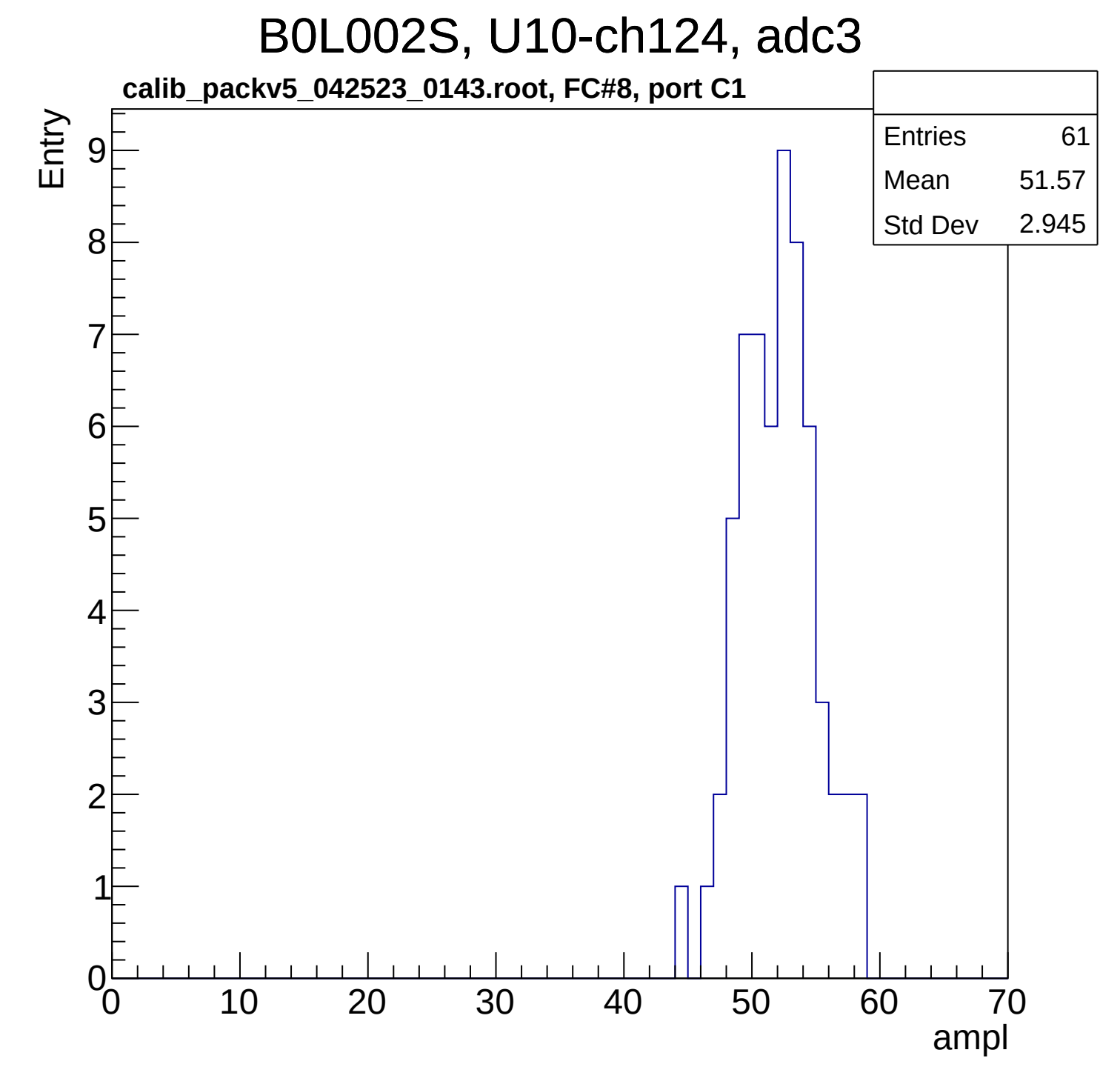

B0L002S, U10-ch123, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

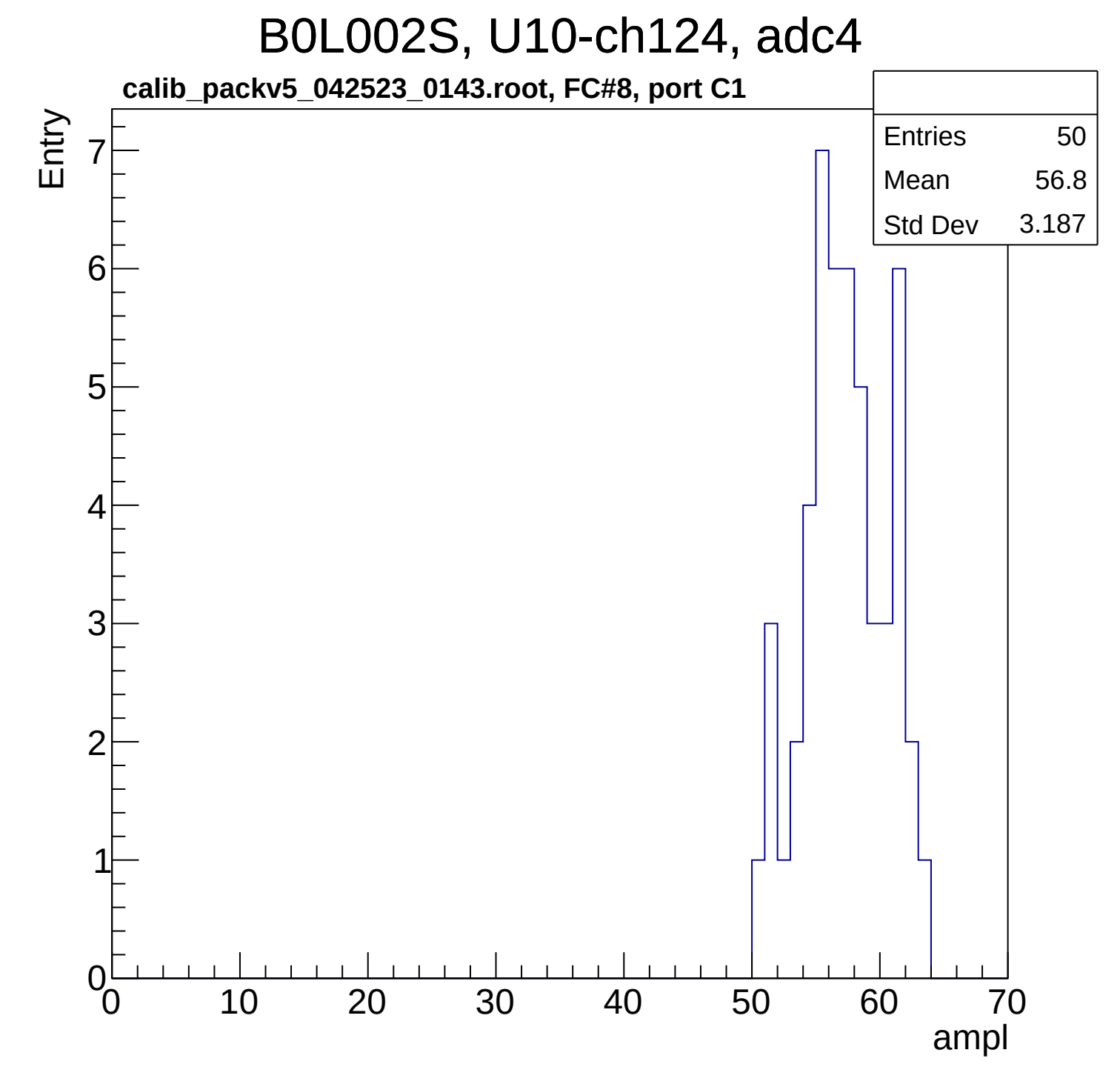

B0L002S, U10-ch123, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

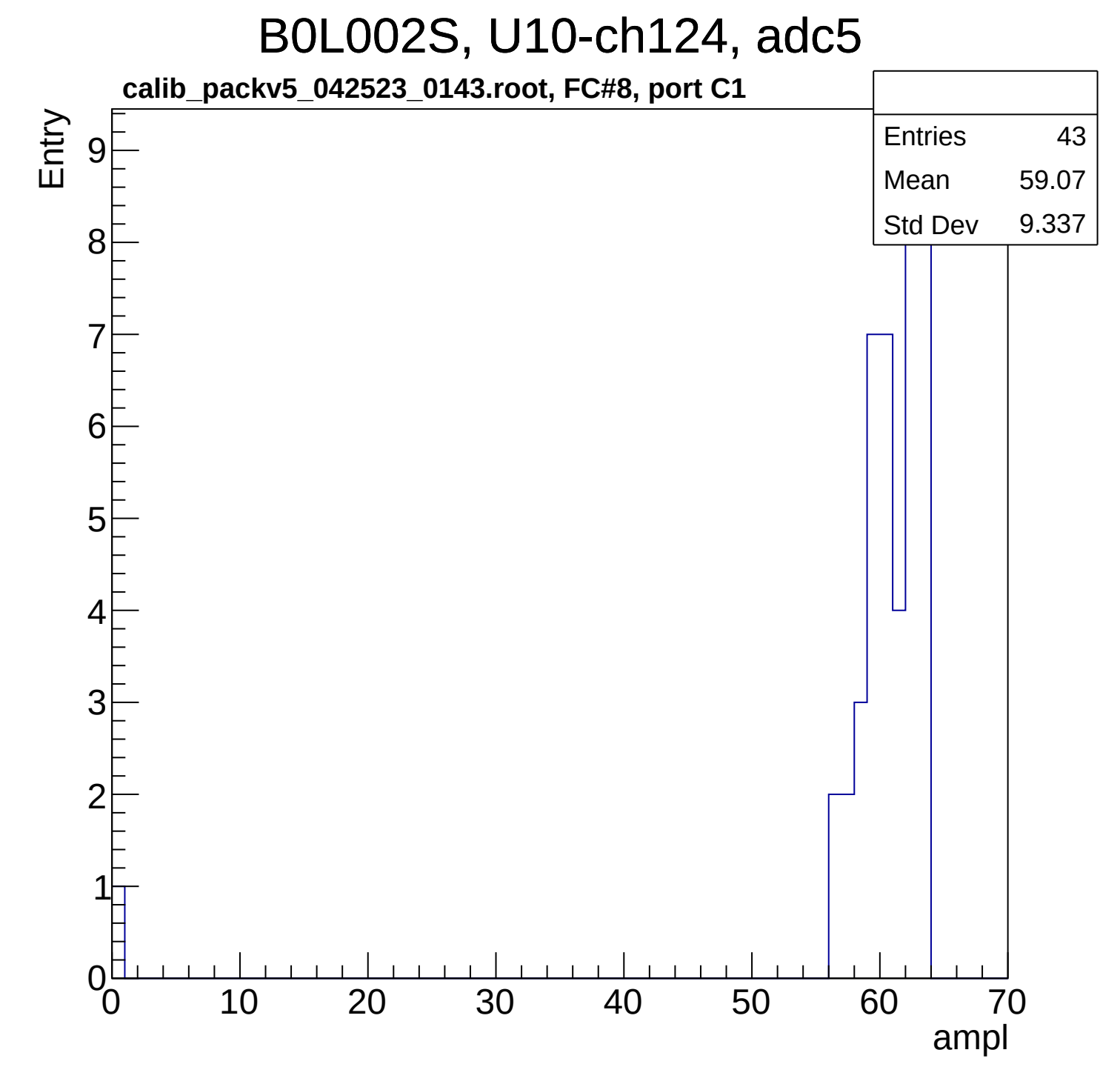

B0L002S, U10-ch124, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 85
Mean 31.65
Std Dev 3.487
Gaus mean : 32.2585
Gaus Width: 3.4982
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

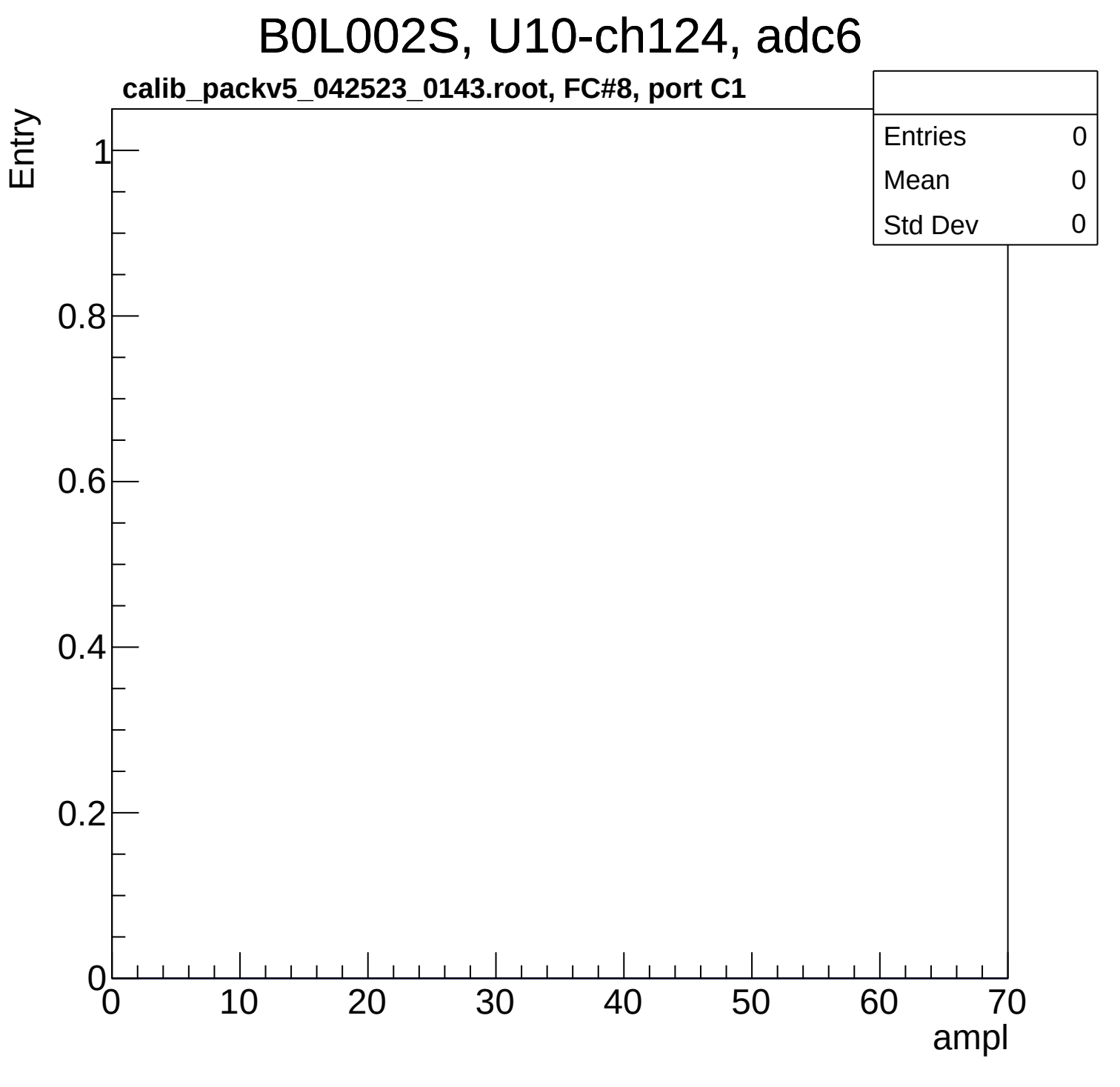

B0L002S, U10-ch124, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 55
Mean 38.91
Std Dev 3.37
Gaus mean : 39.3557
Gaus Width: 4.0475
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

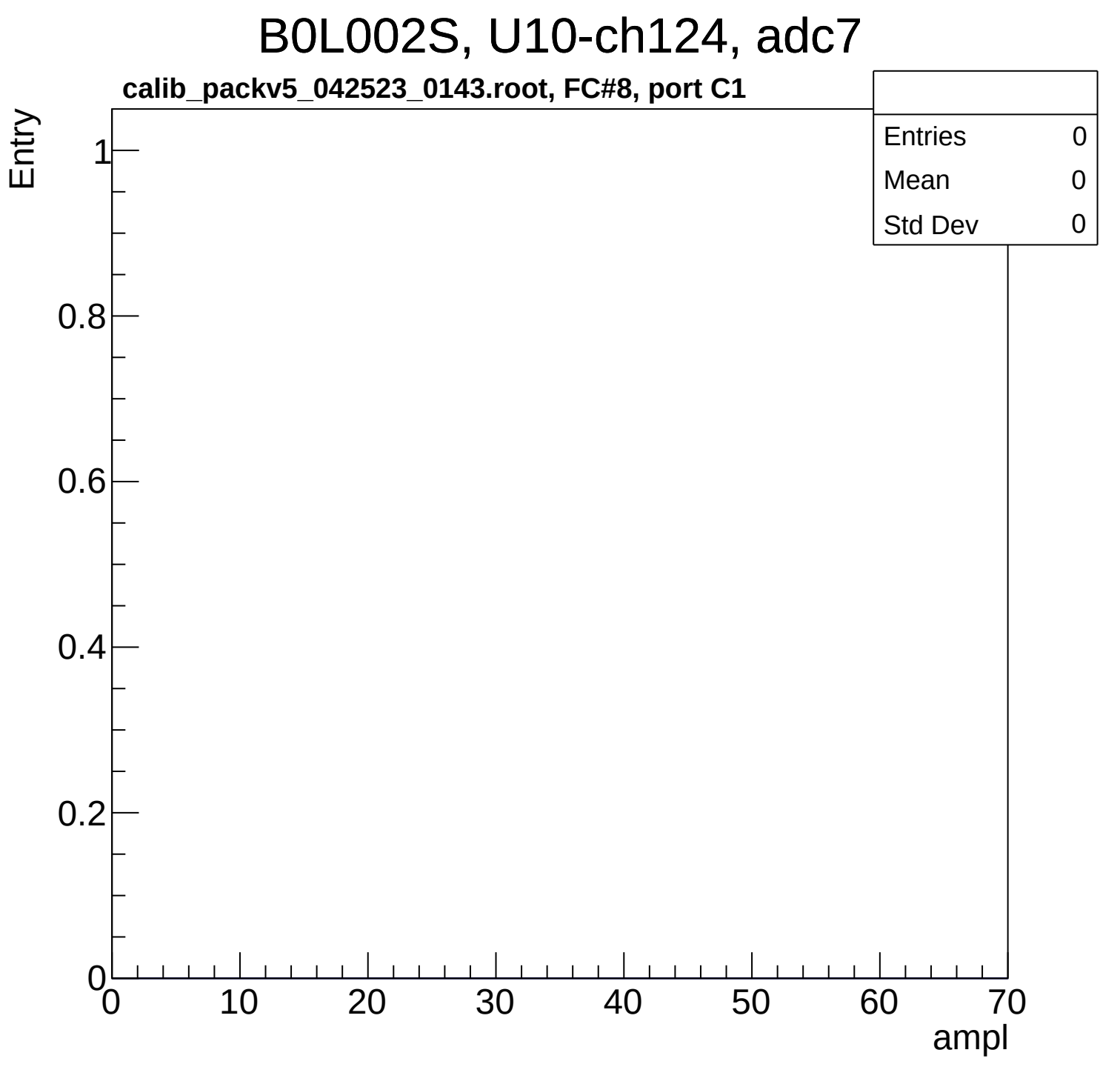

B0L002S, U10-ch124, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 68
Mean 44.41
Std Dev 2.991
Gaus mean : 44.9321
Gaus Width: 3.2925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

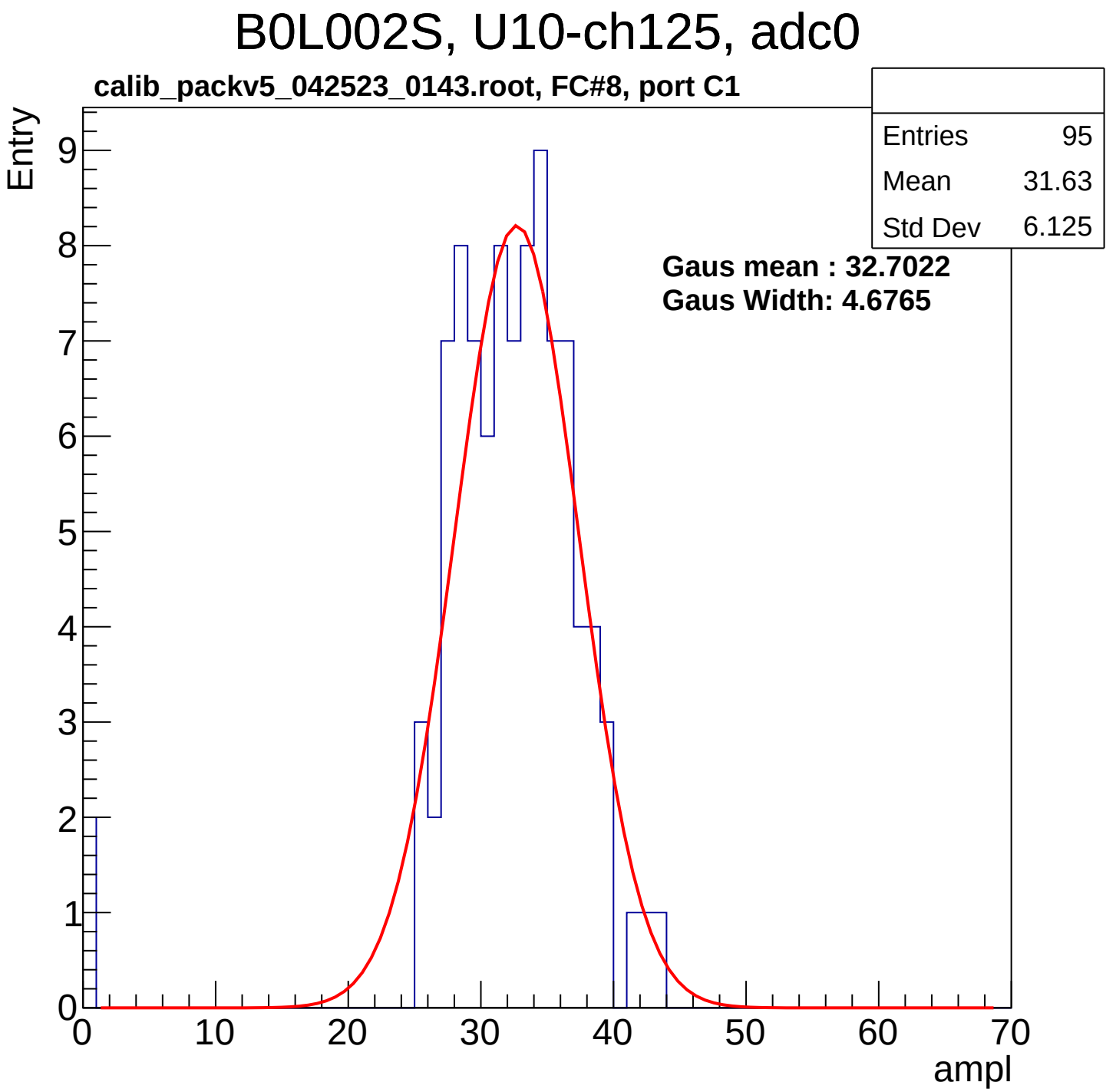

B0L002S, U10-ch124, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 61
Mean 51.57
Std Dev 2.945
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

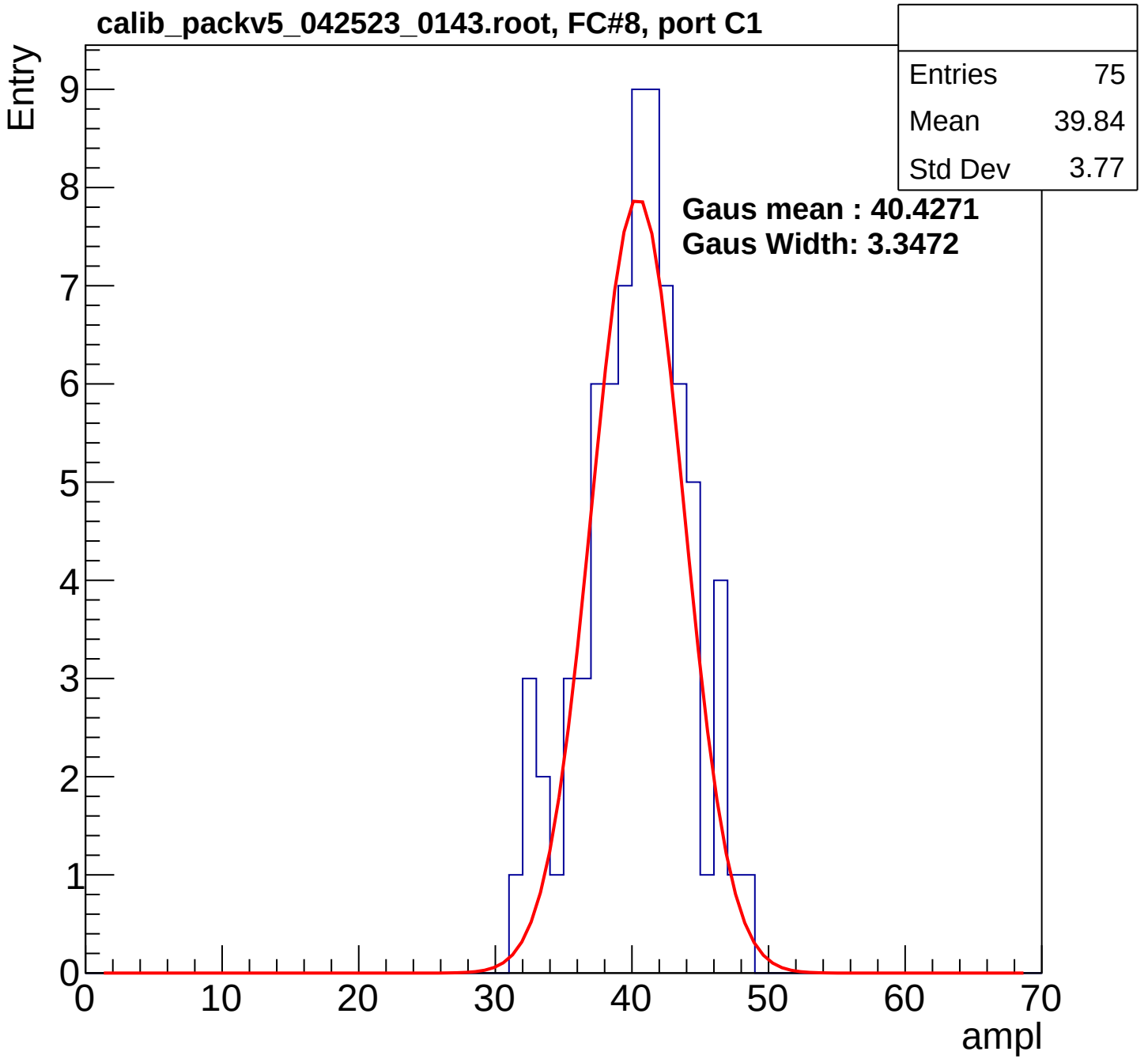

B0L002S, U10-ch124, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 50
Mean 56.8
Std Dev 3.187
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

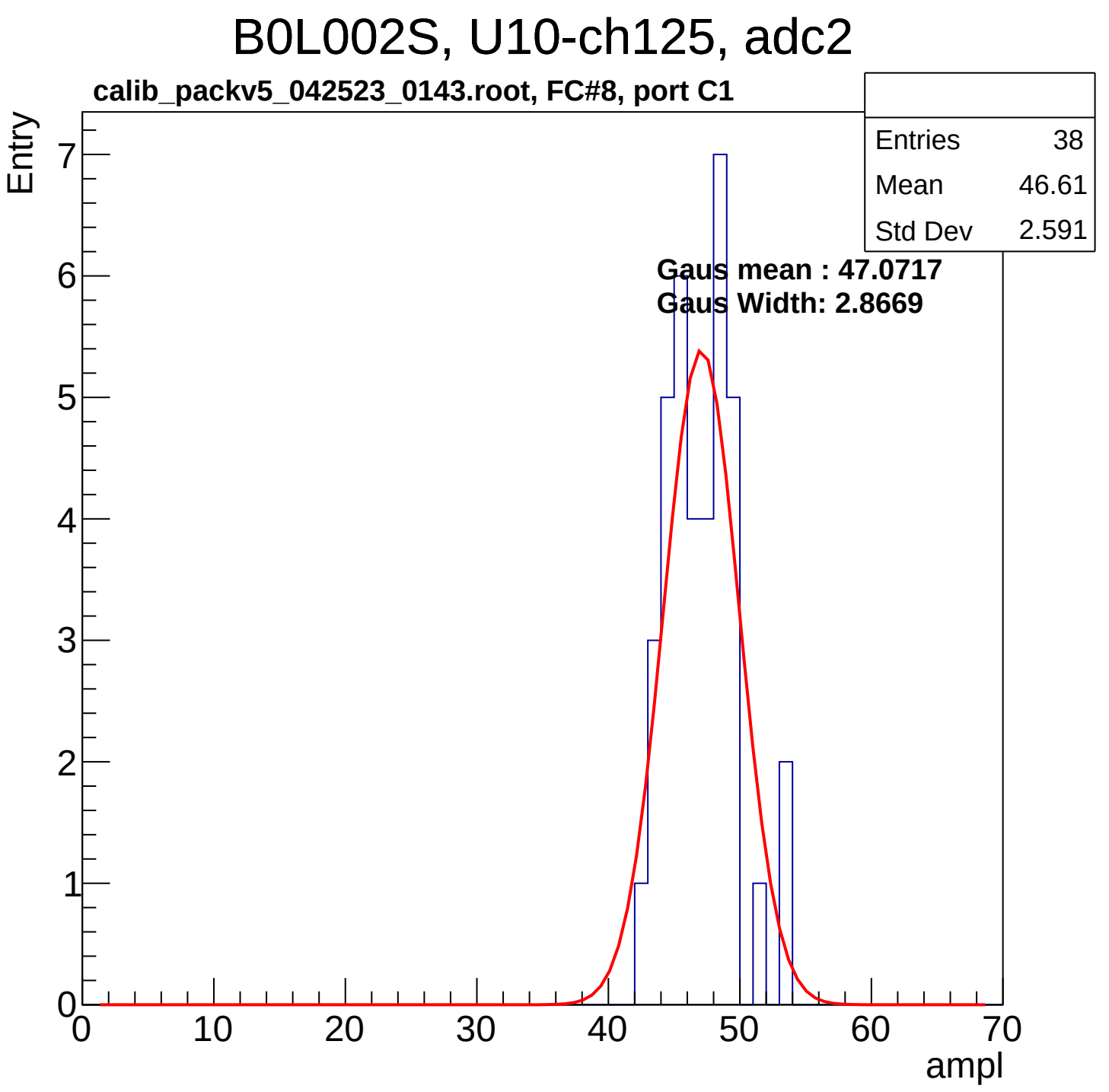

B0L002S, U10-ch124, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 43
Mean 59.07
Std Dev 9.337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

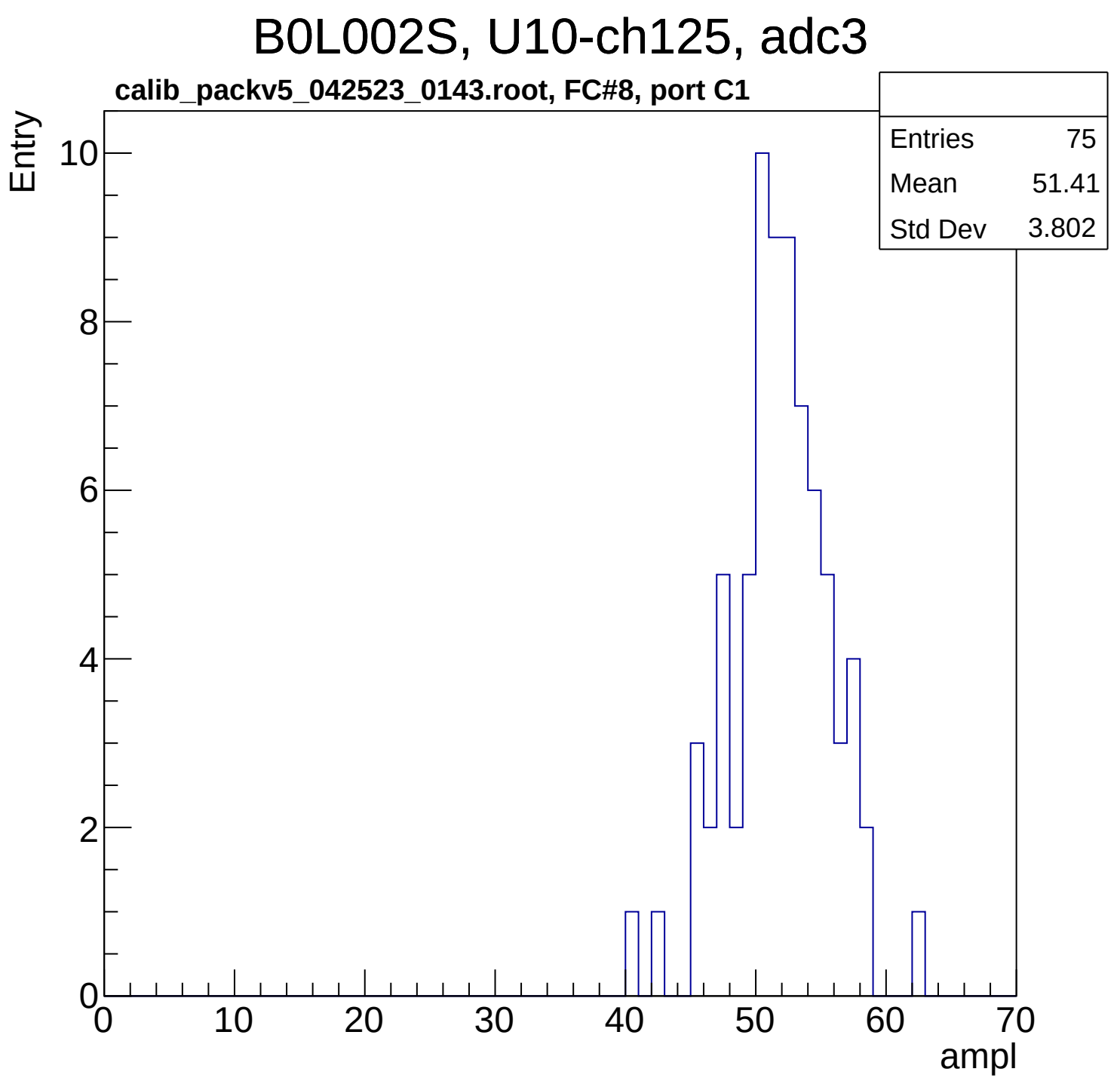

B0L002S, U10-ch124, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

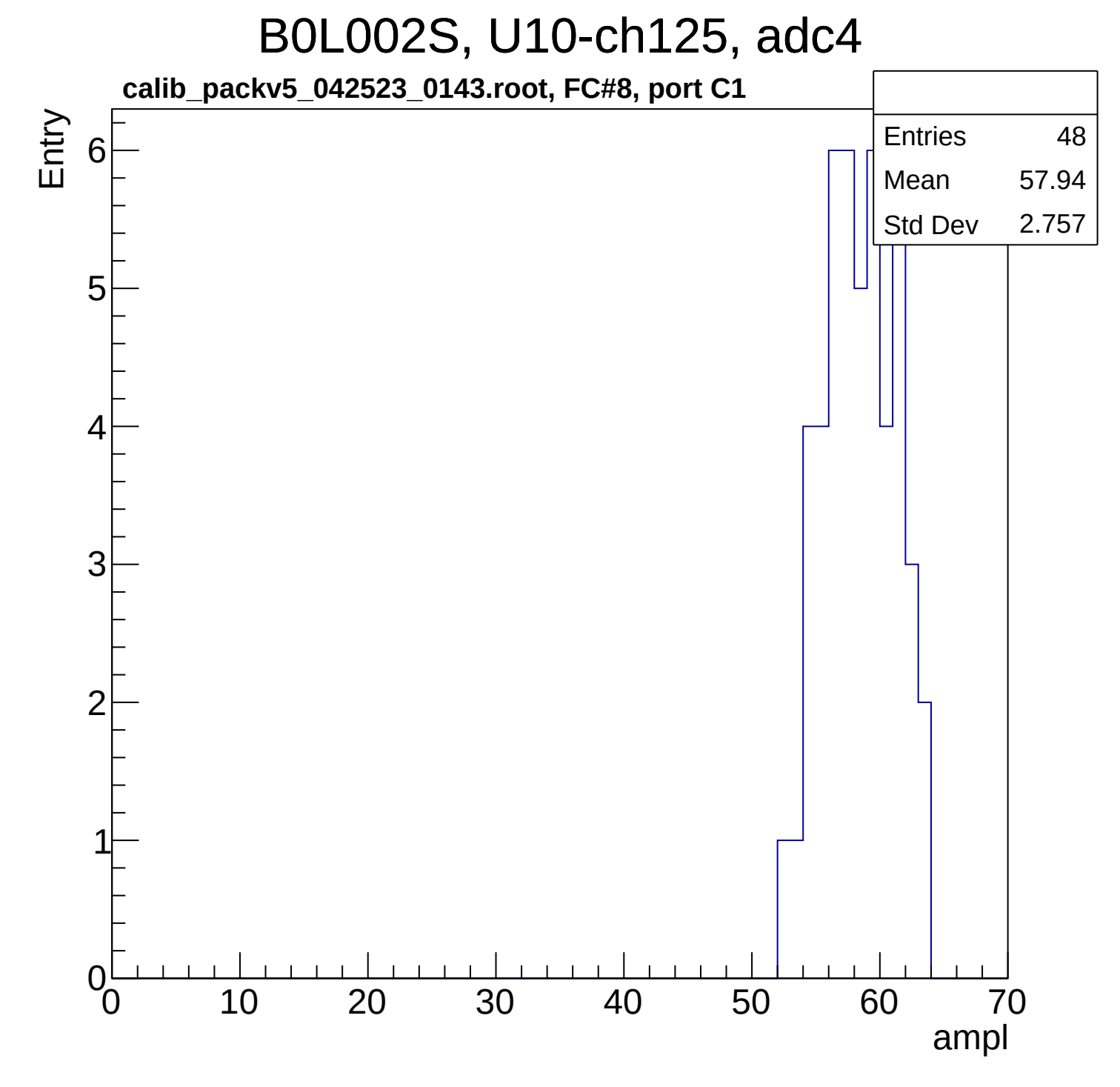

B0L002S, U10-ch124, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

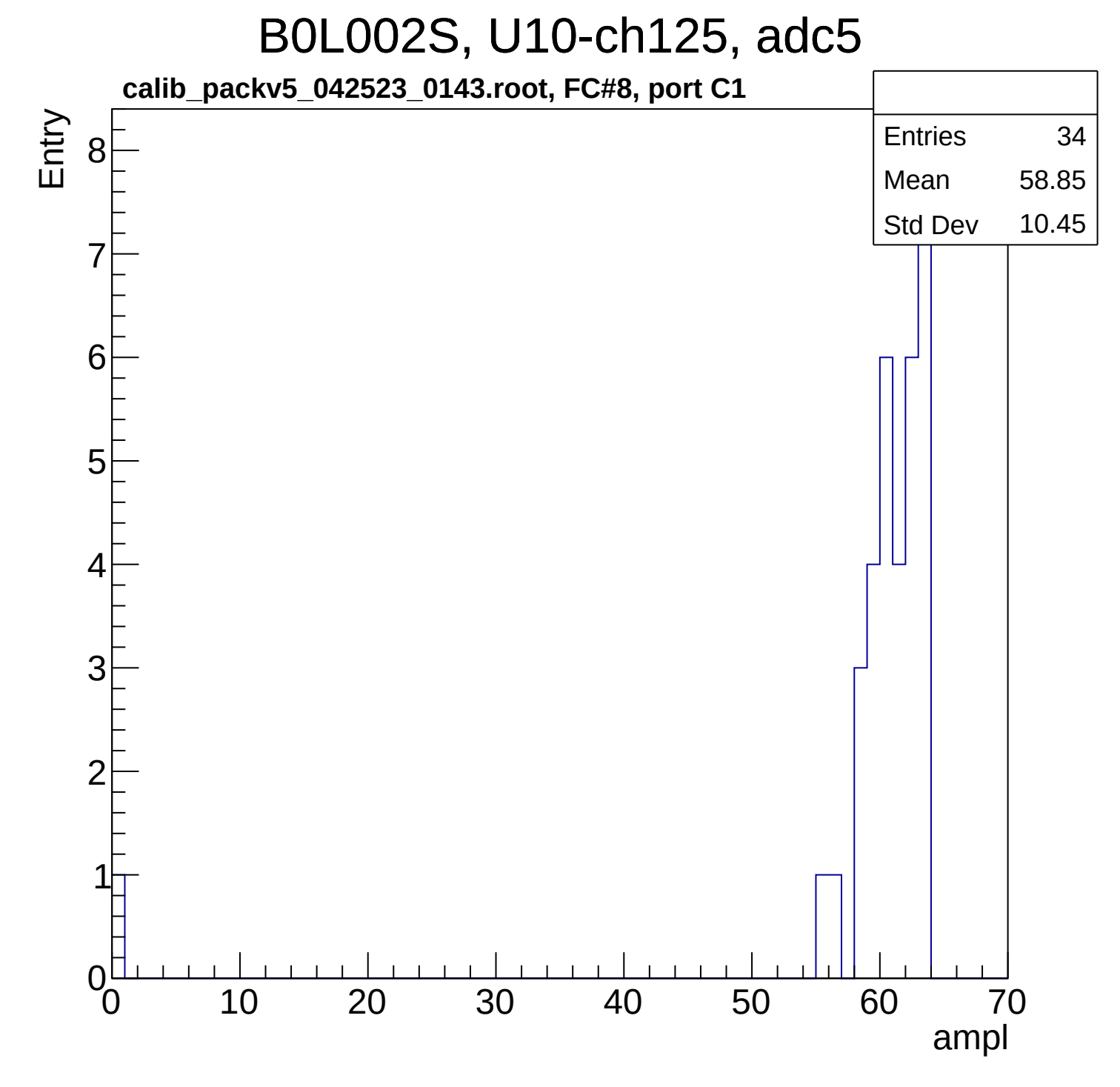

B0L002S, U10-ch125, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 95
Mean 31.63
Std Dev 6.125
Gaus mean : 32.7022
Gaus Width: 4.6765
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

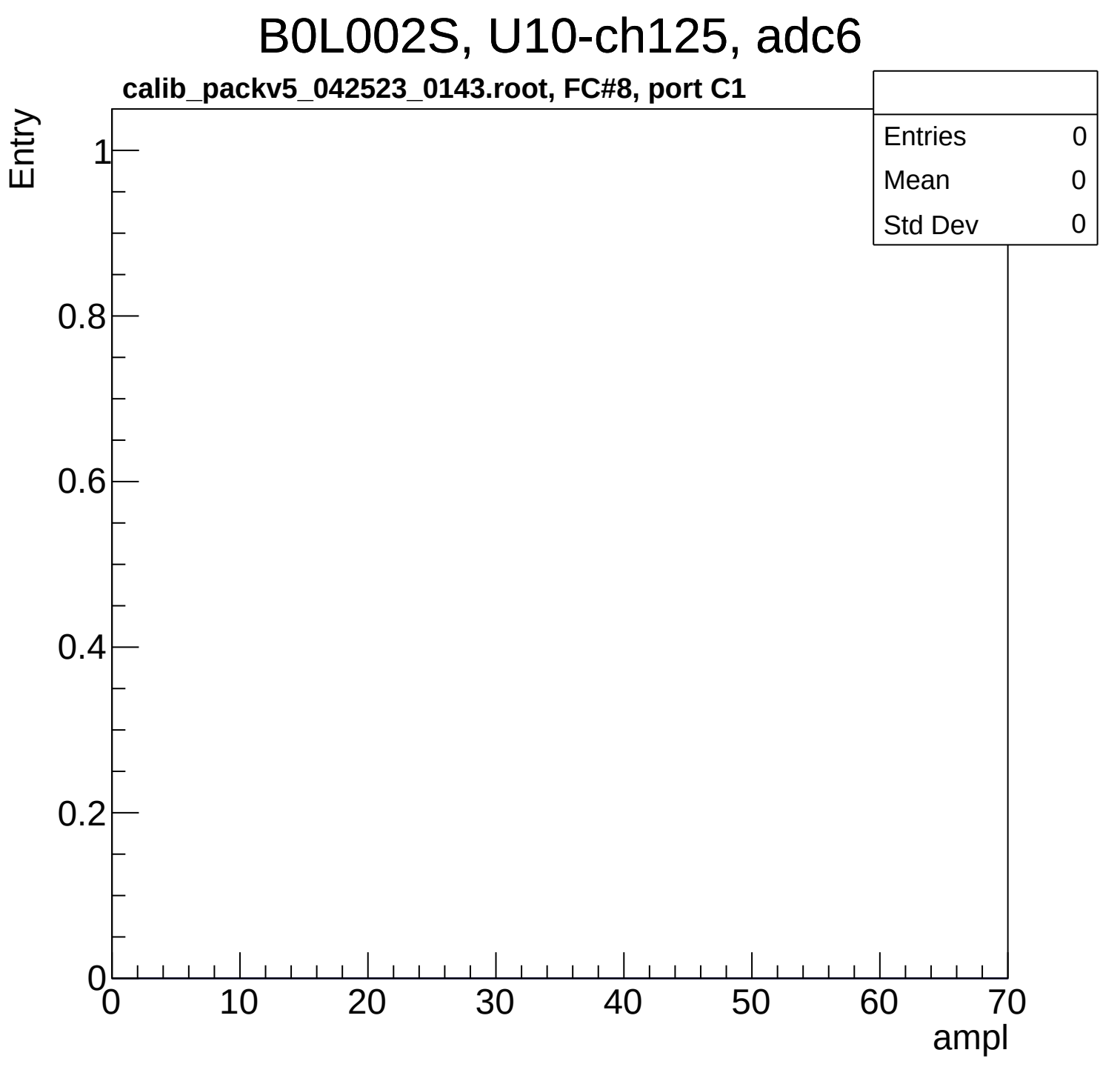

B0L002S, U10-ch125, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 39.84
Std Dev 3.77
Gaus mean : 40.4271
Gaus Width: 3.3472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

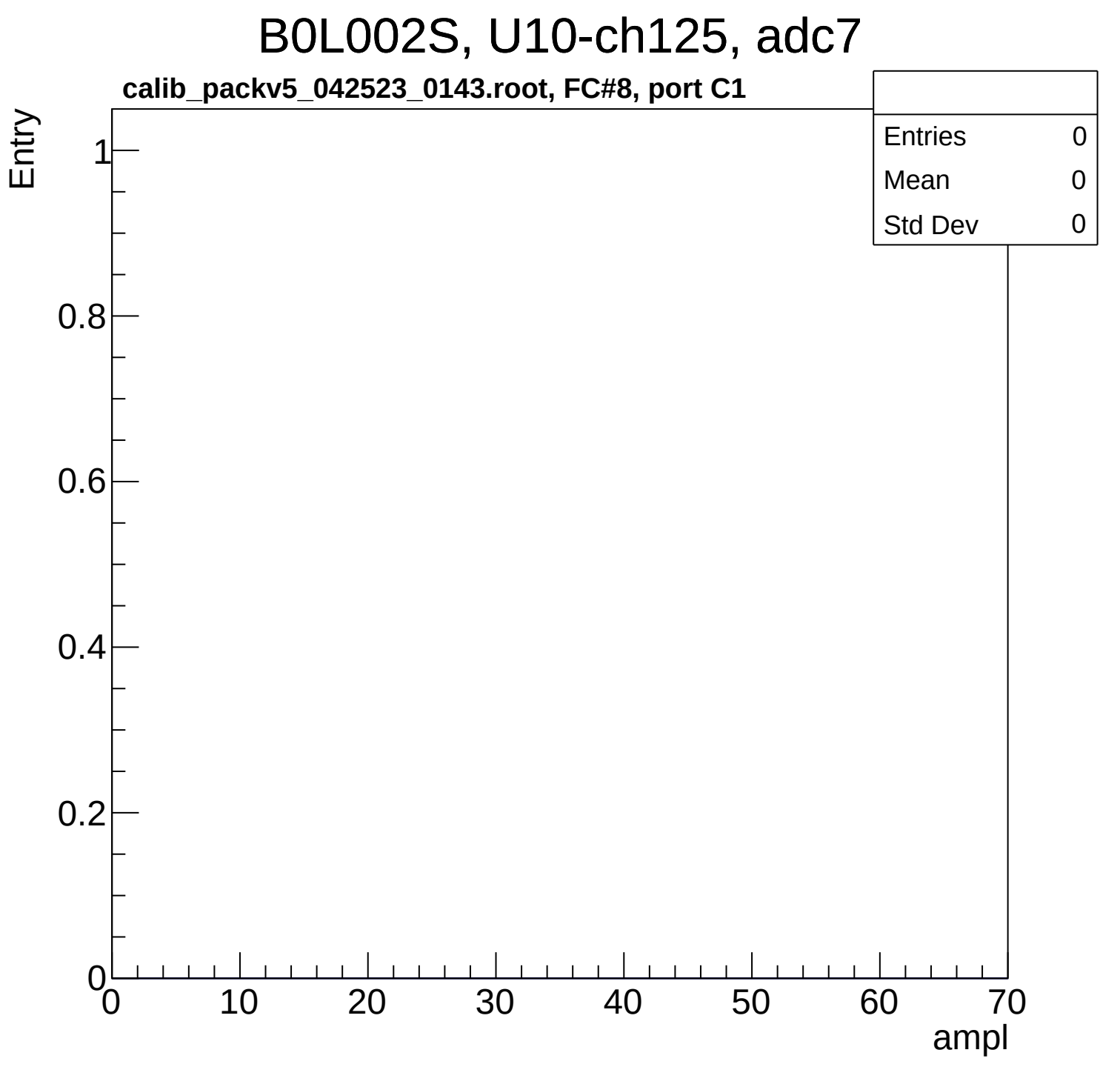

B0L002S, U10-ch125, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 38
Mean 46.61
Std Dev 2.591
Gaus mean : 47.0717
Gaus Width: 2.8669
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

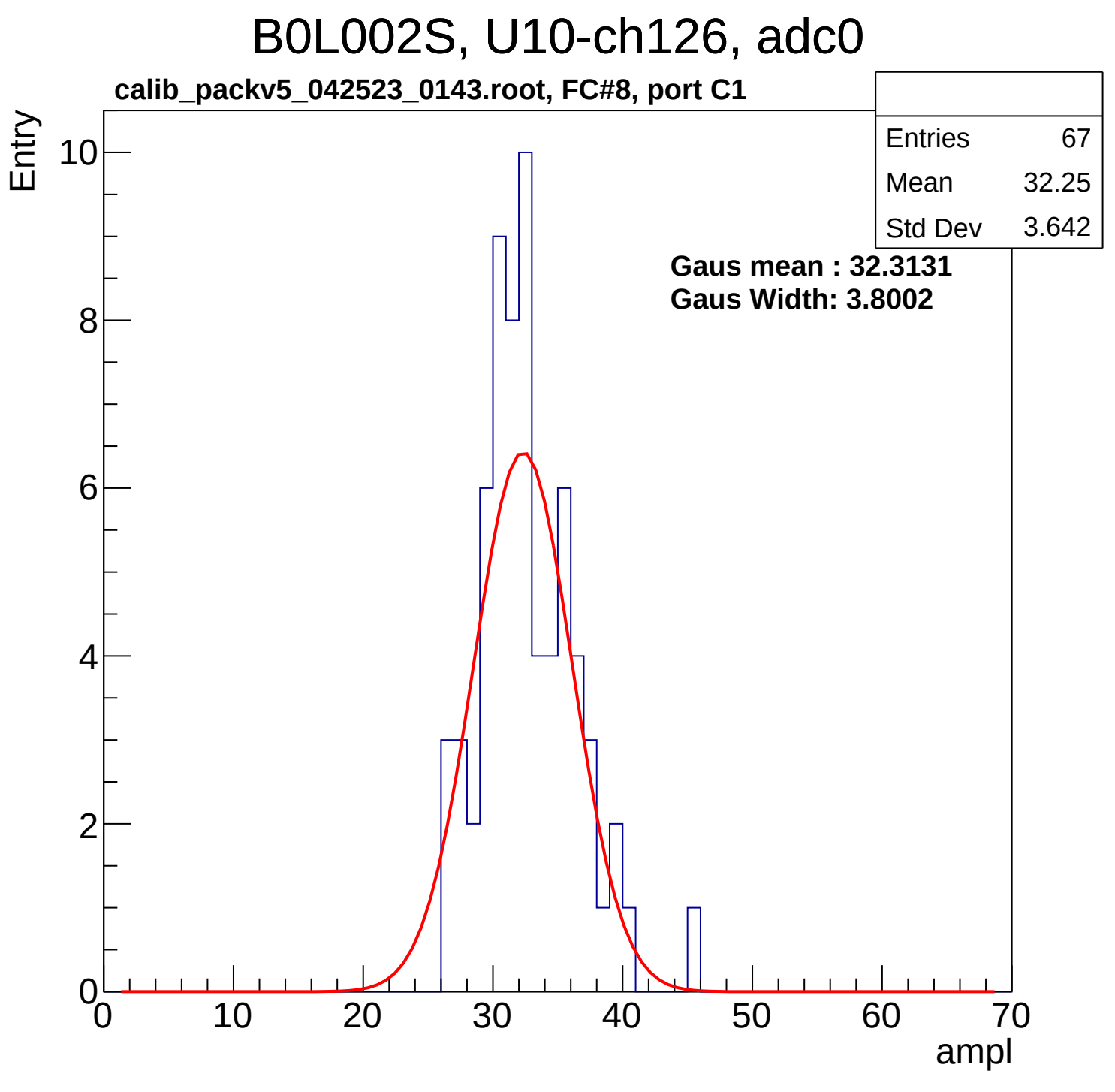

B0L002S, U10-ch125, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 51.41
Std Dev 3.802
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

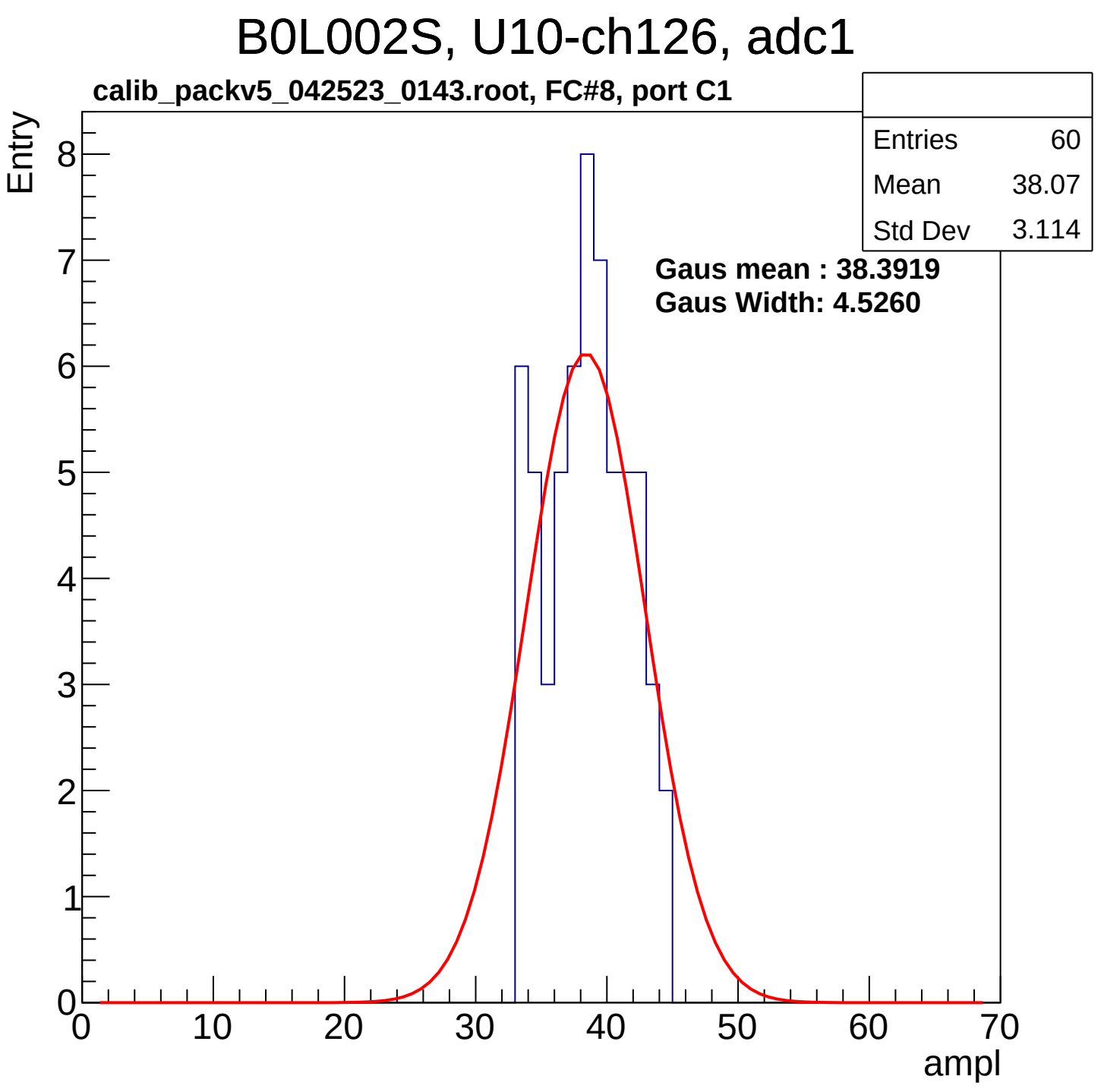

B0L002S, U10-ch125, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 48
Mean 57.94
Std Dev 2.757
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

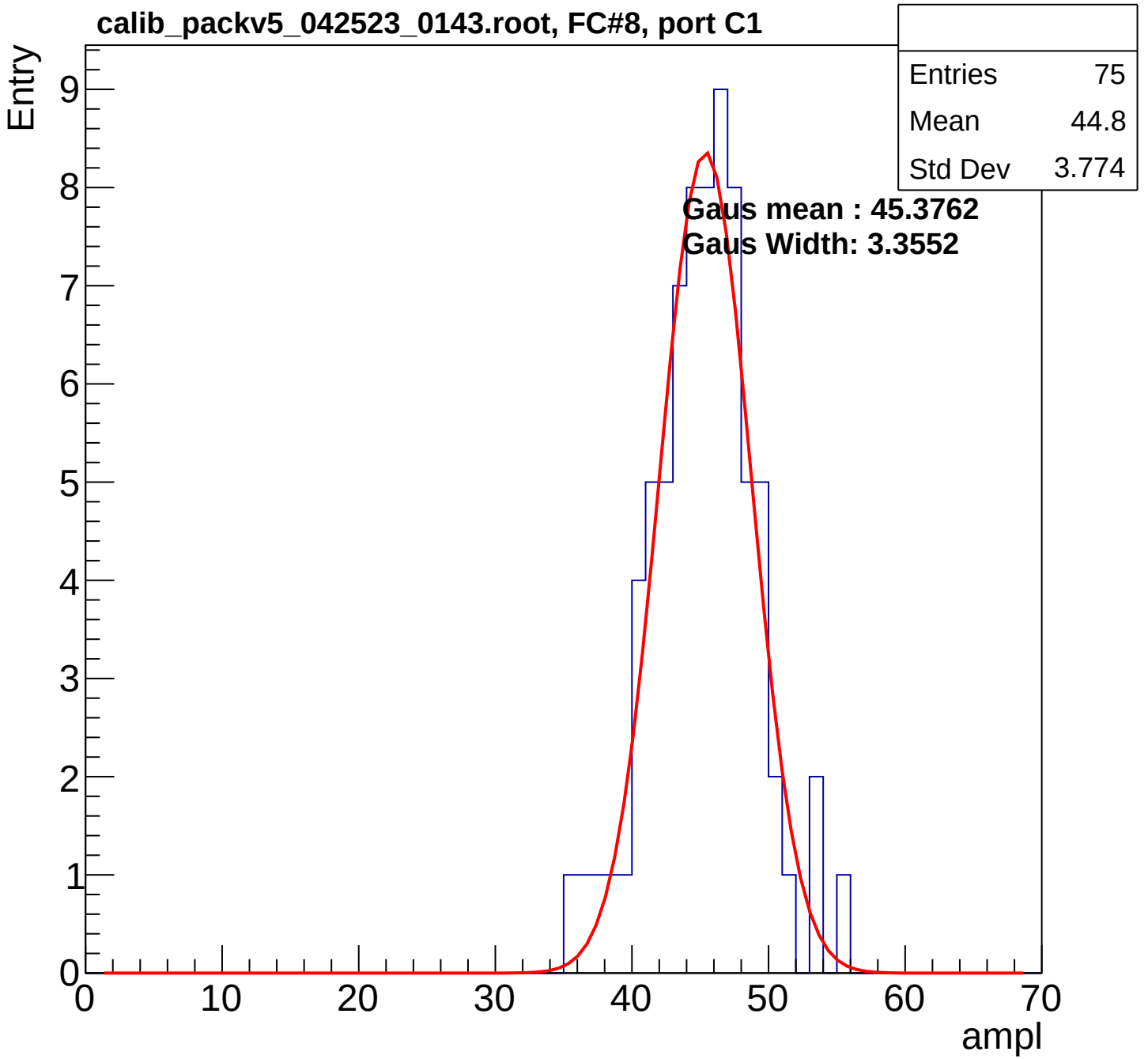

B0L002S, U10-ch125, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 34
Mean 58.85
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

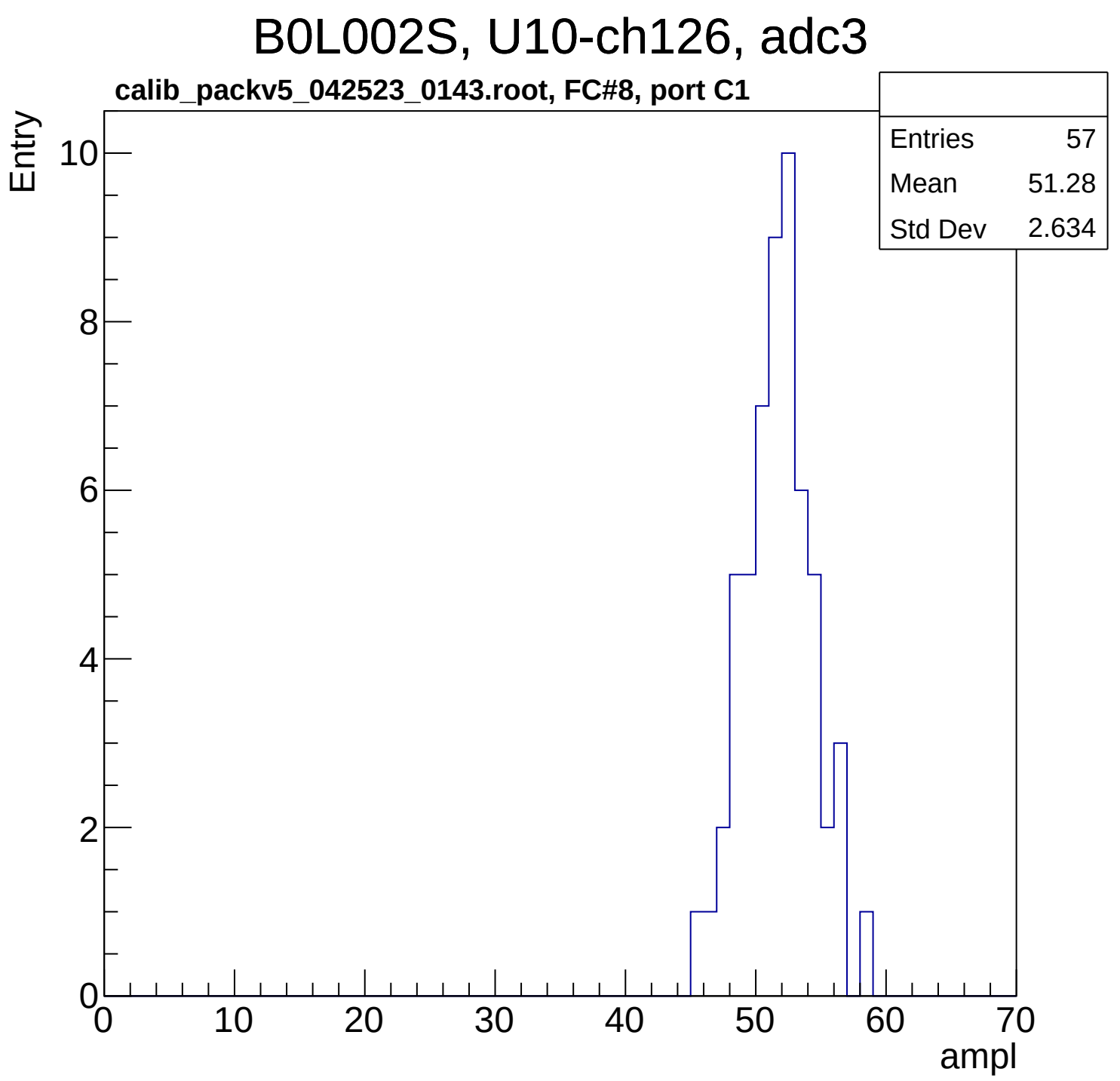

B0L002S, U10-ch125, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

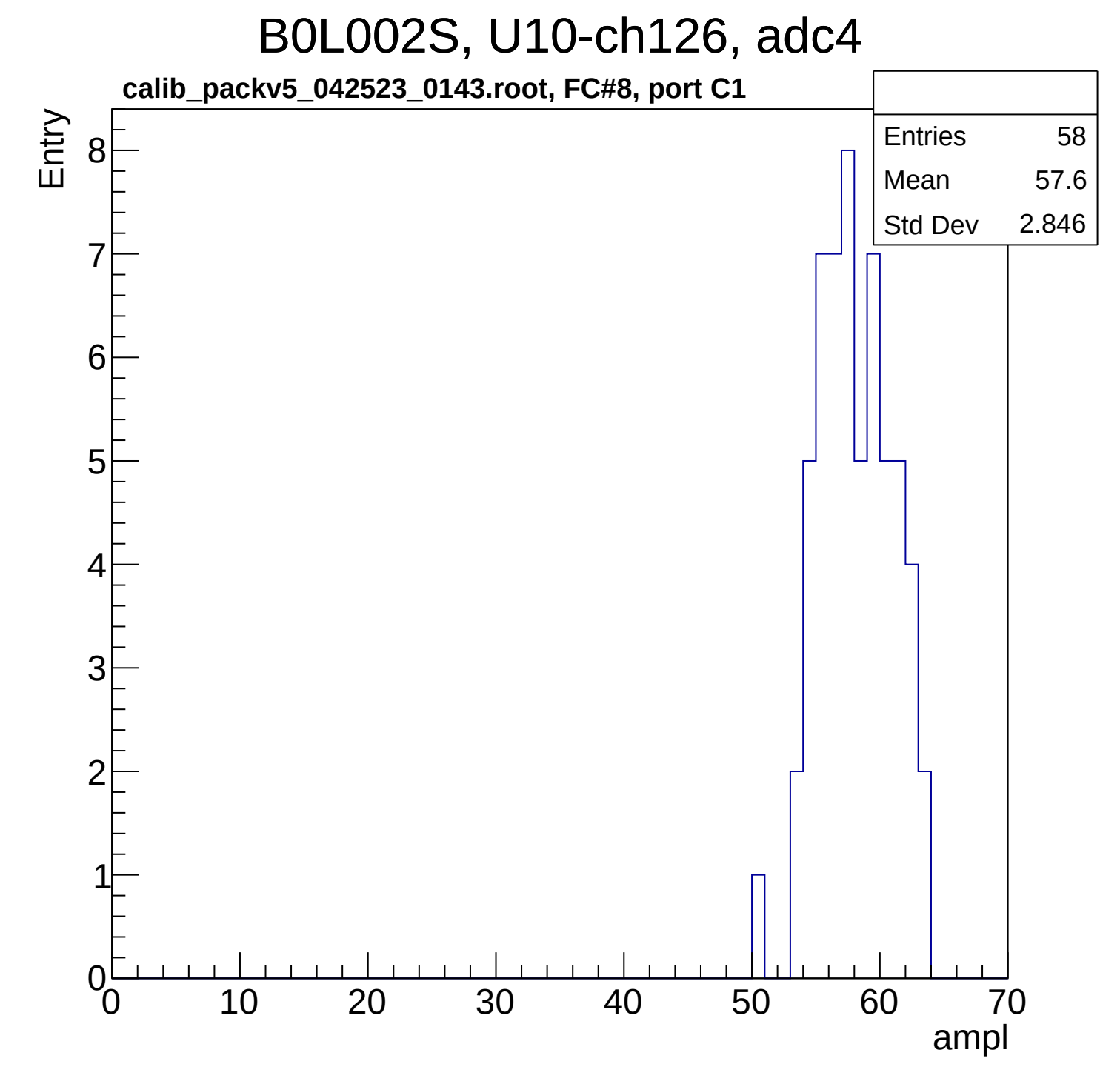

B0L002S, U10-ch125, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

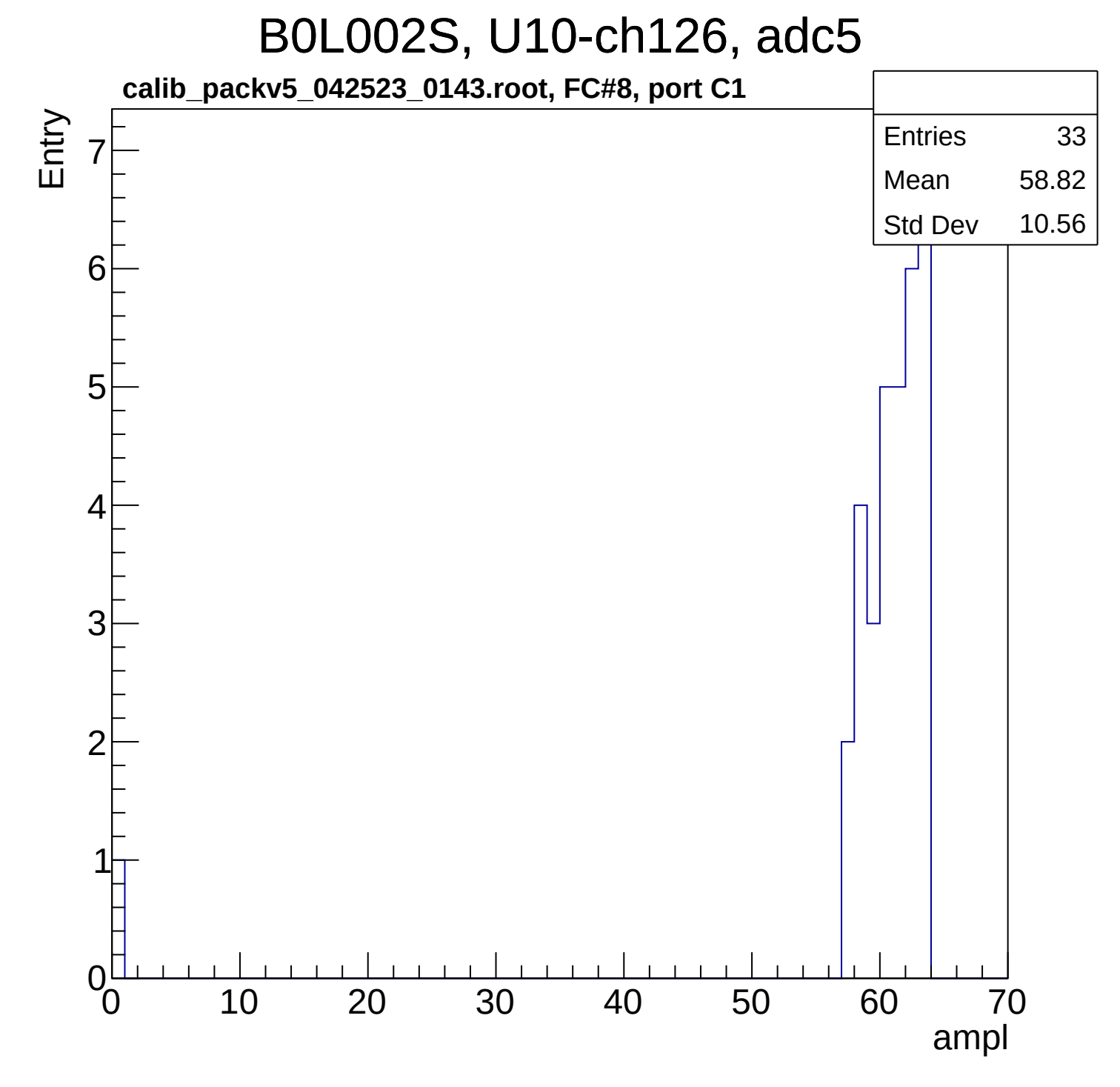

B0L002S, U10-ch126, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 67
Mean 32.25
Std Dev 3.642
Gaus mean : 32.3131
Gaus Width: 3.8002
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

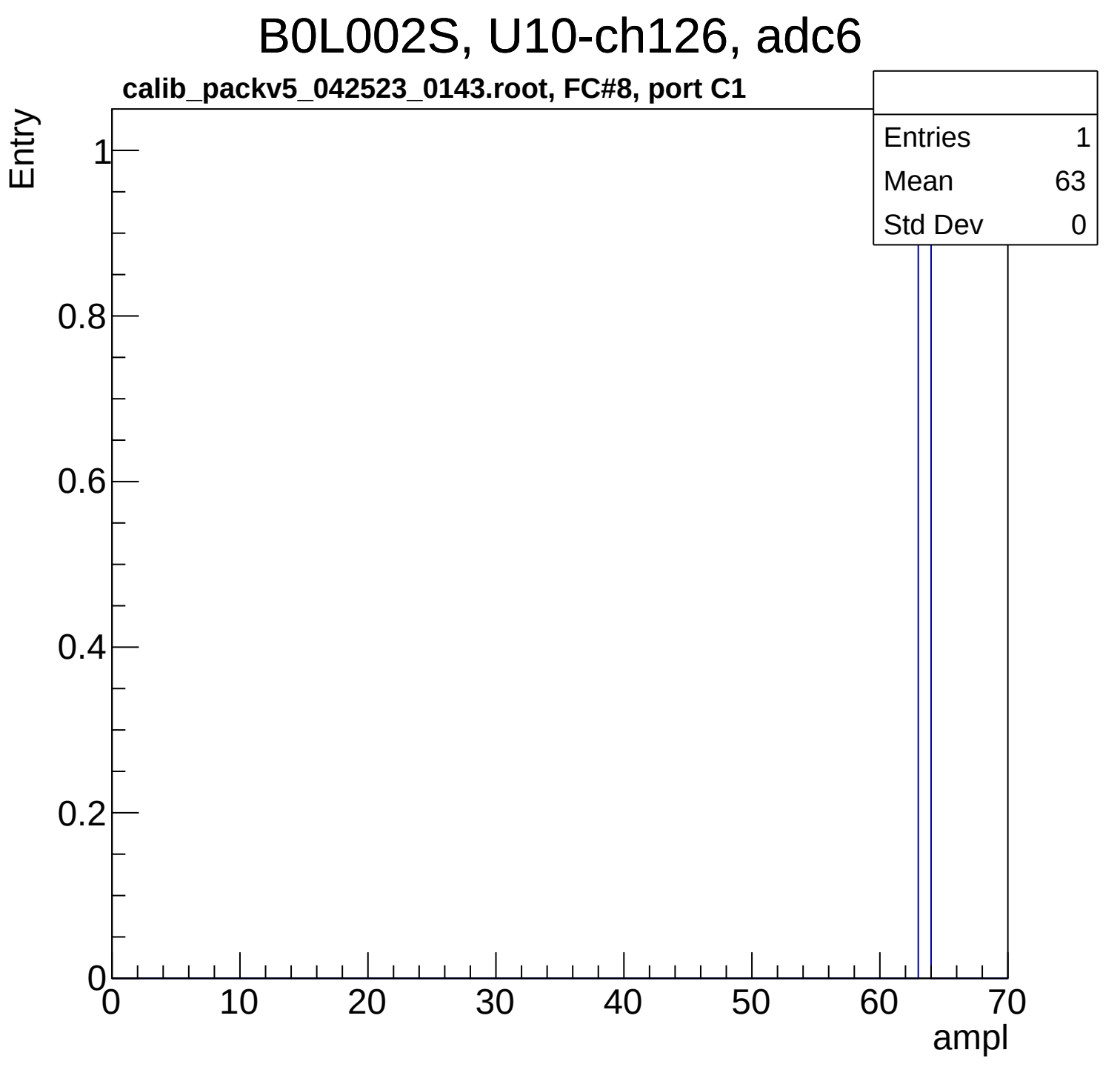

B0L002S, U10-ch126, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 60
Mean 38.07
Std Dev 3.114
Gaus mean : 38.3919
Gaus Width: 4.5260
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

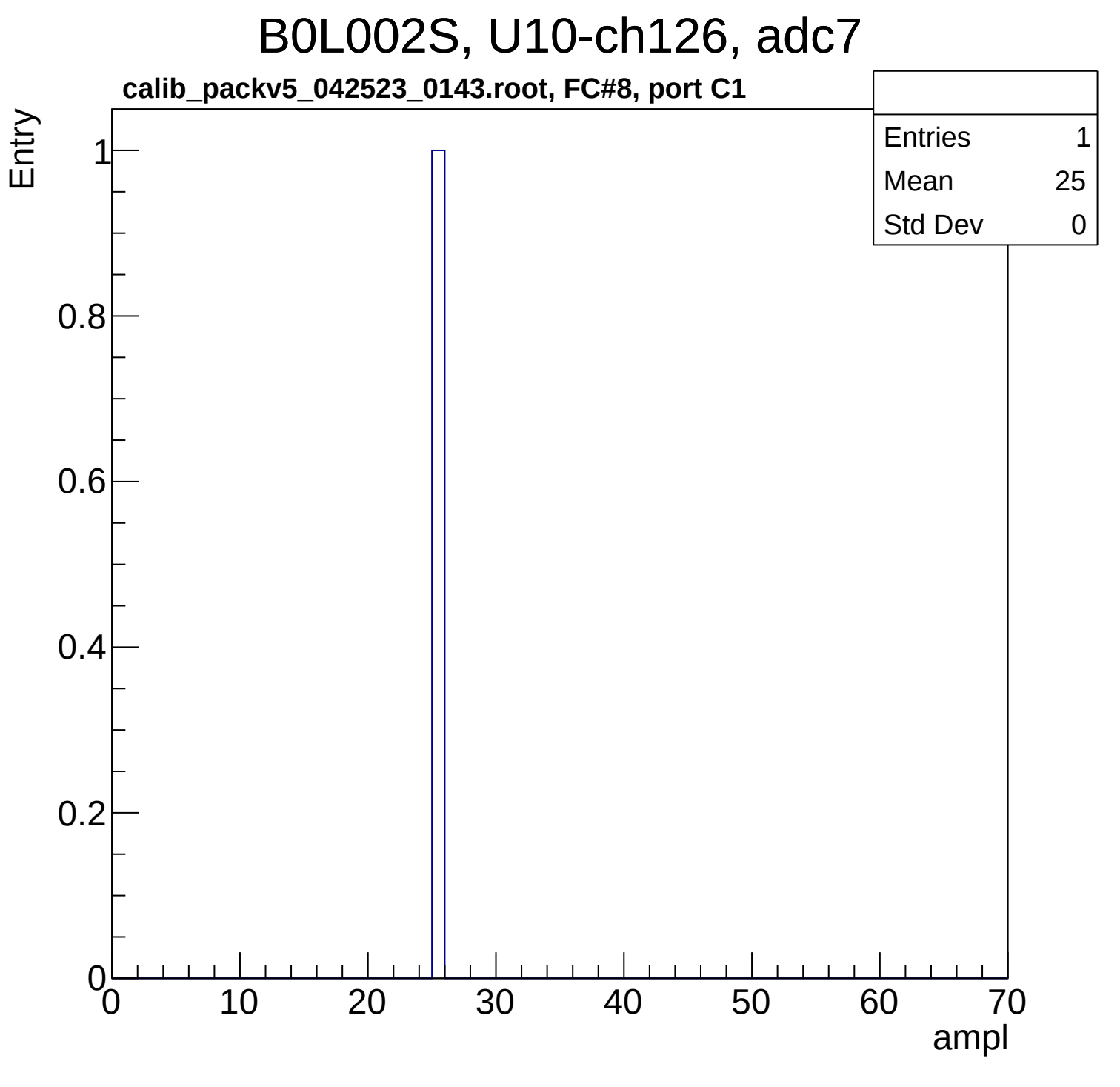

B0L002S, U10-ch126, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 75
Mean 44.8
Std Dev 3.774
Gaus mean : 45.3762
Gaus Width: 3.3552
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

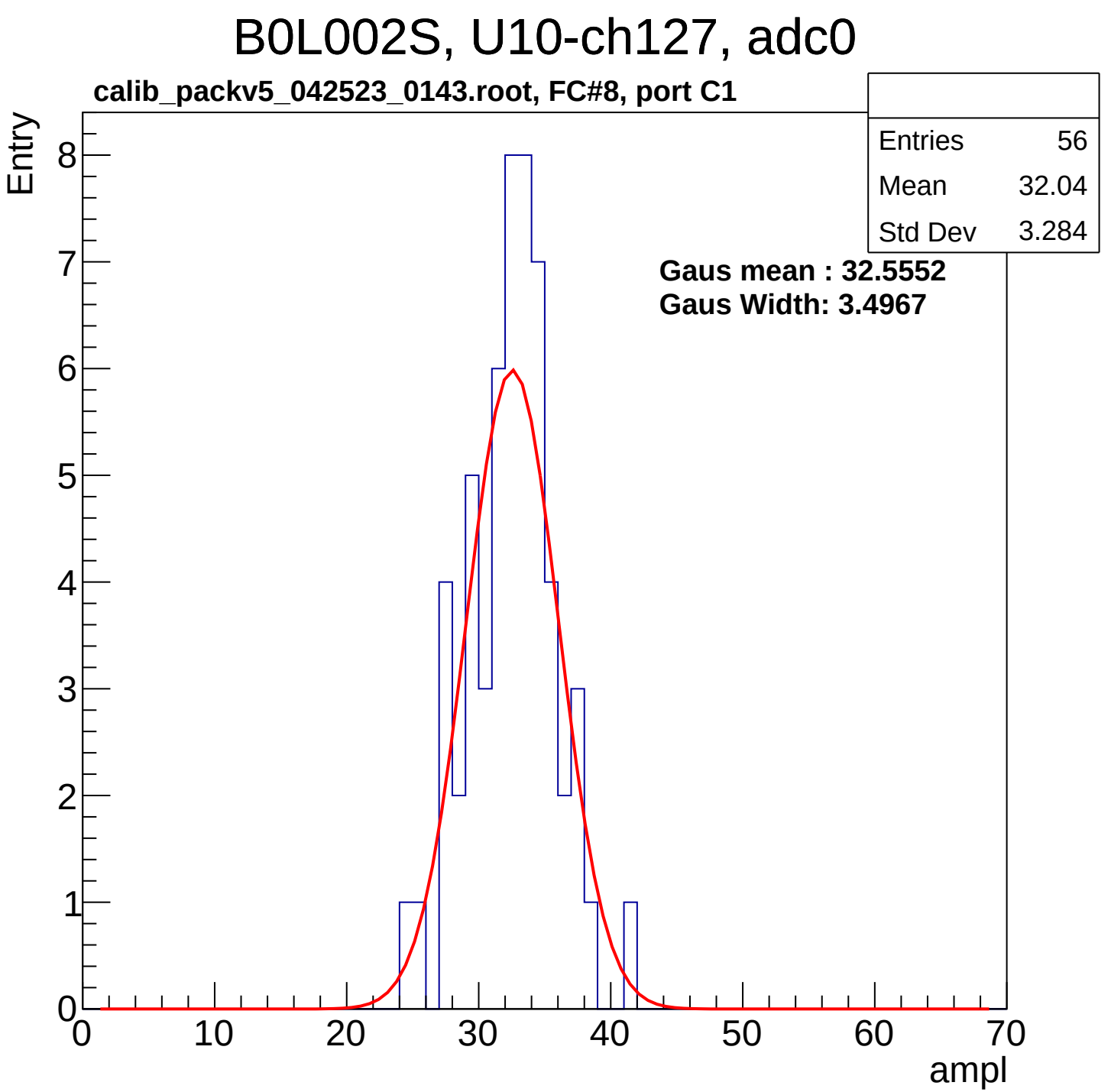

B0L002S, U10-ch126, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 57
Mean 51.28
Std Dev 2.634
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

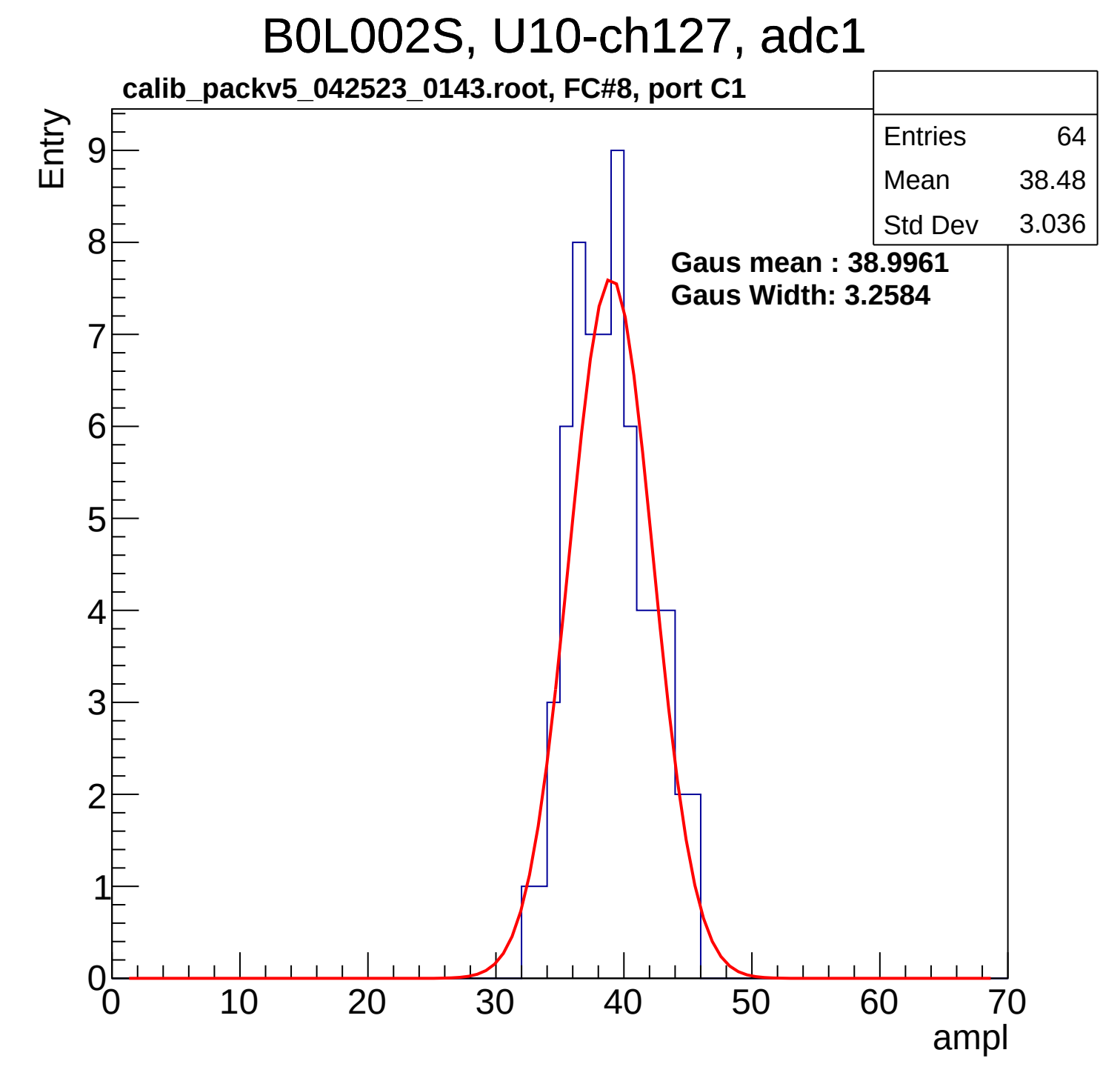

B0L002S, U10-ch126, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 58
Mean 57.6
Std Dev 2.846
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

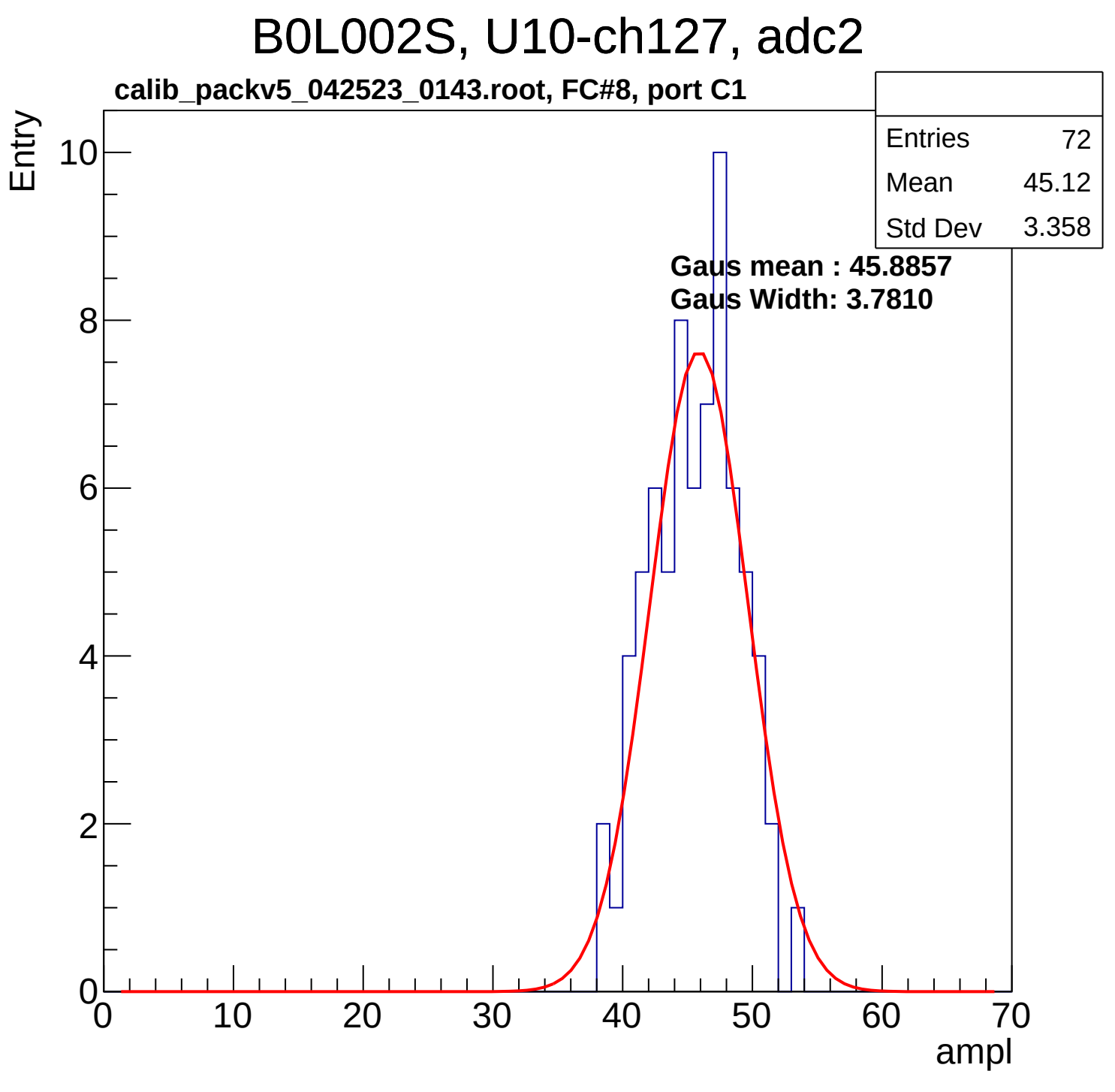

B0L002S, U10-ch126, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 33
Mean 58.82
Std Dev 10.56
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

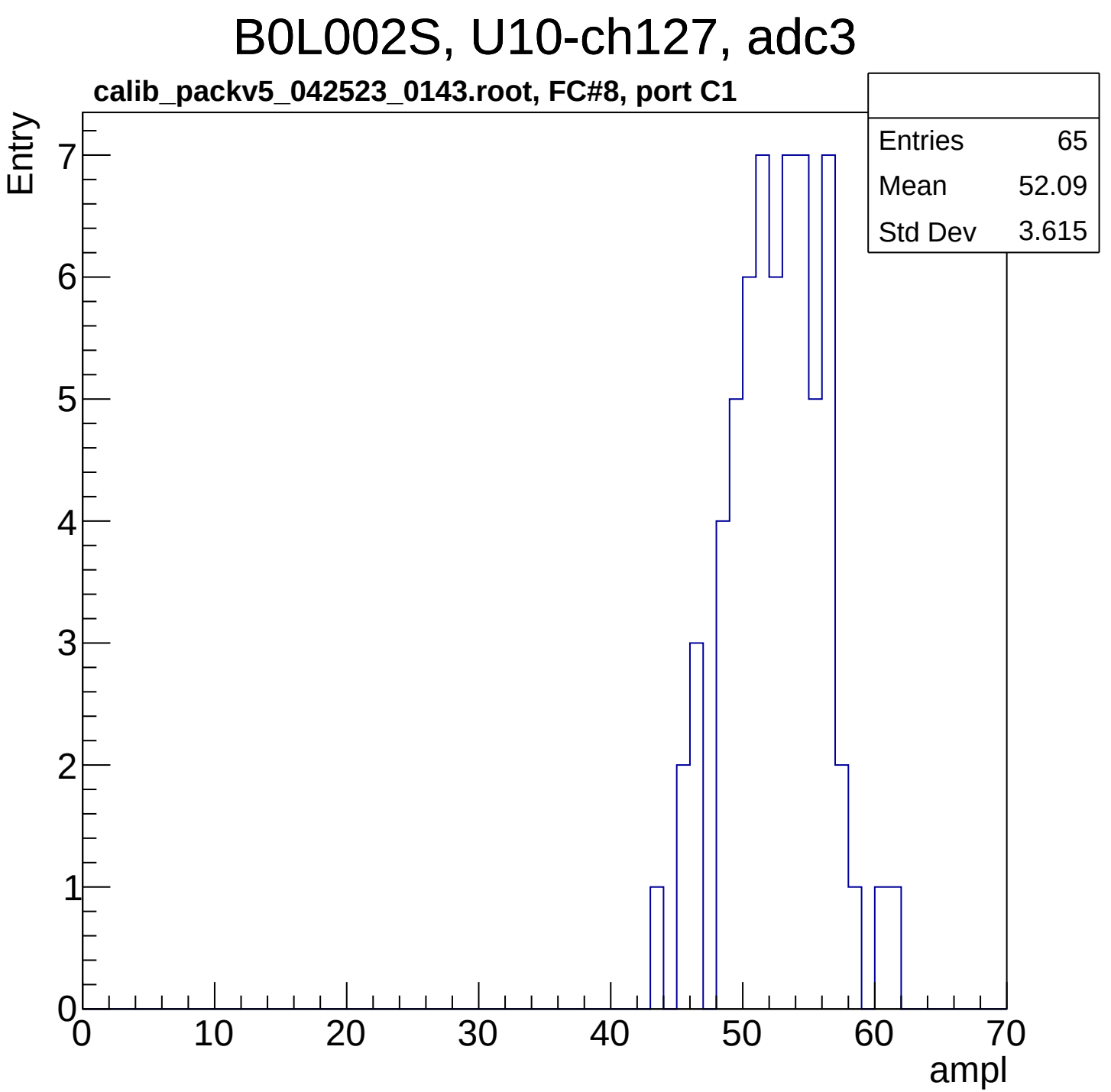

B0L002S, U10-ch126, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

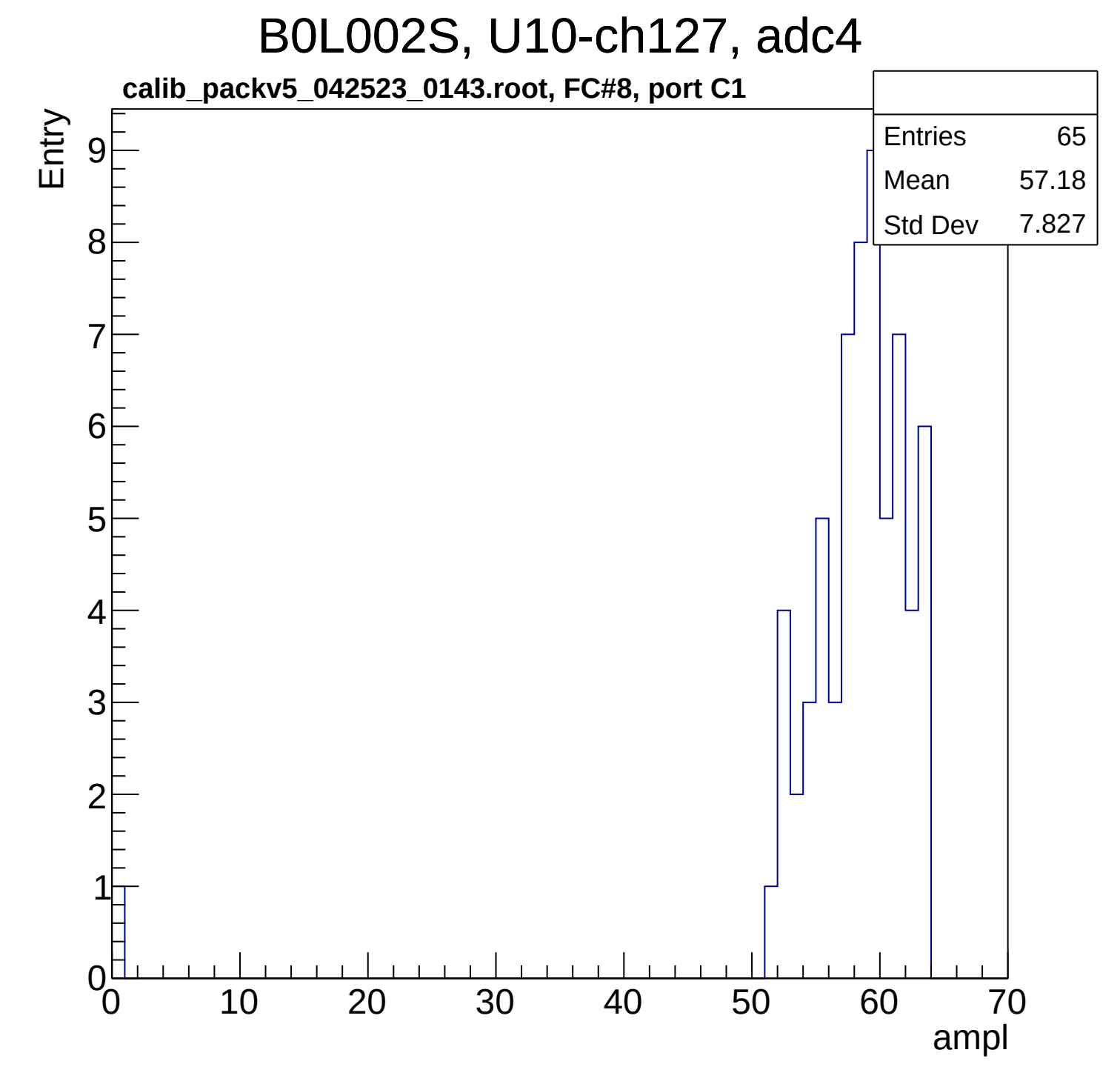

B0L002S, U10-ch126, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 1
Mean 25
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

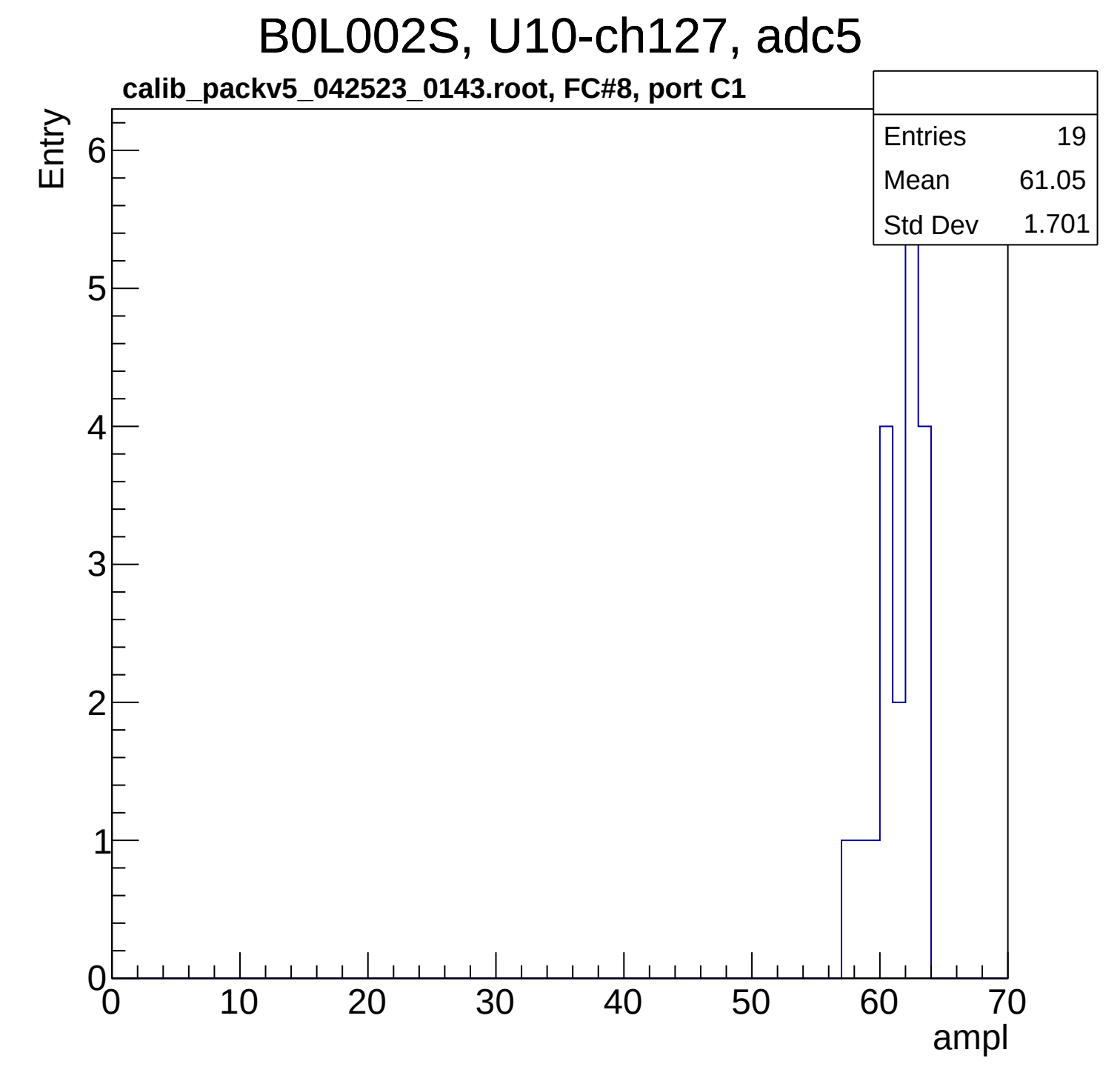

B0L002S, U10-ch127, adc0
calib_packv5_042523_0143.root, FC#8, port C1
Entries 56
Mean 32.04
Std Dev 3.284
Gaus mean : 32.5552
Gaus Width: 3.4967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

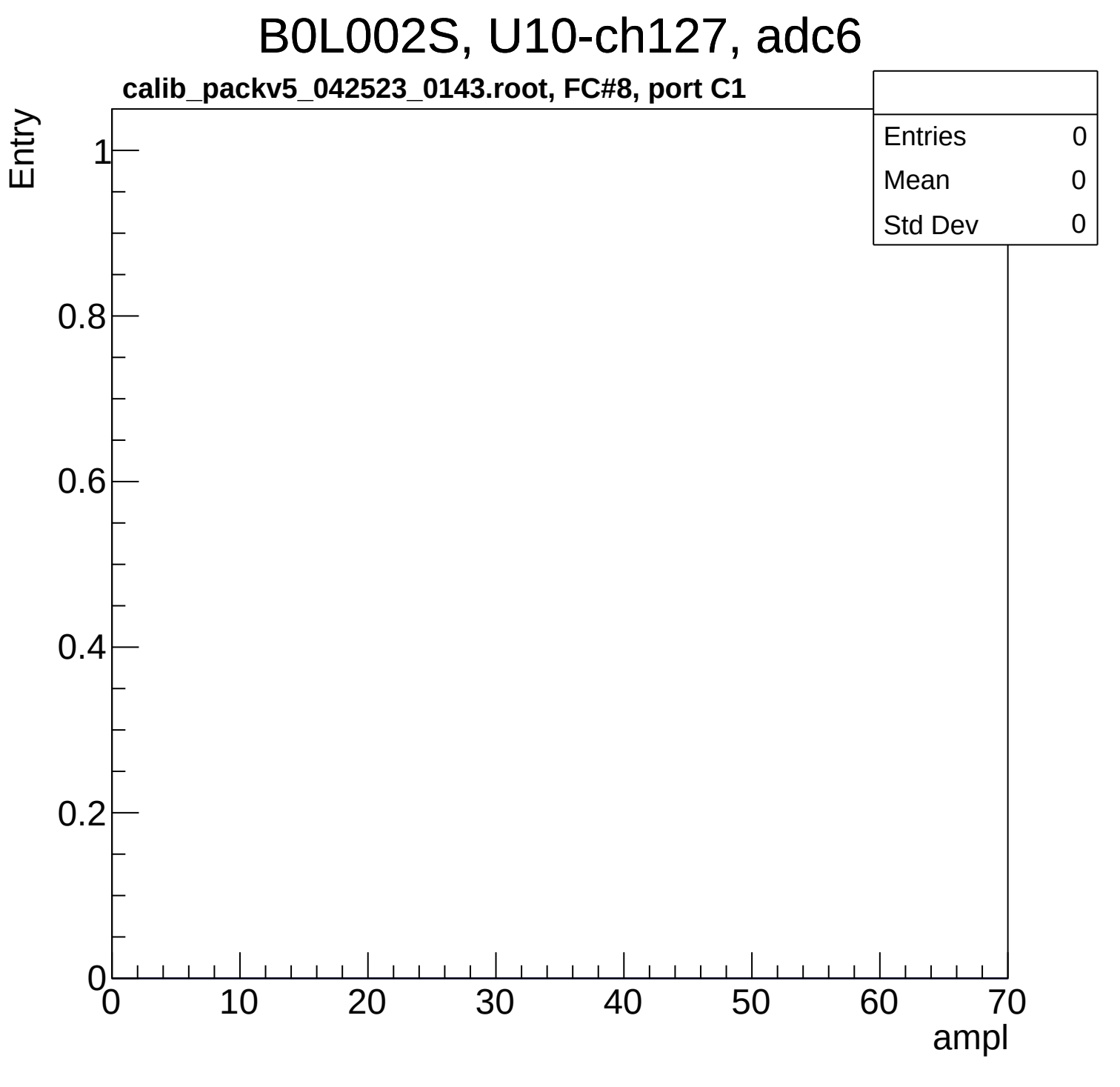

B0L002S, U10-ch127, adc1
calib_packv5_042523_0143.root, FC#8, port C1
Entries 64
Mean 38.48
Std Dev 3.036
Gaus mean : 38.9961
Gaus Width: 3.2584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

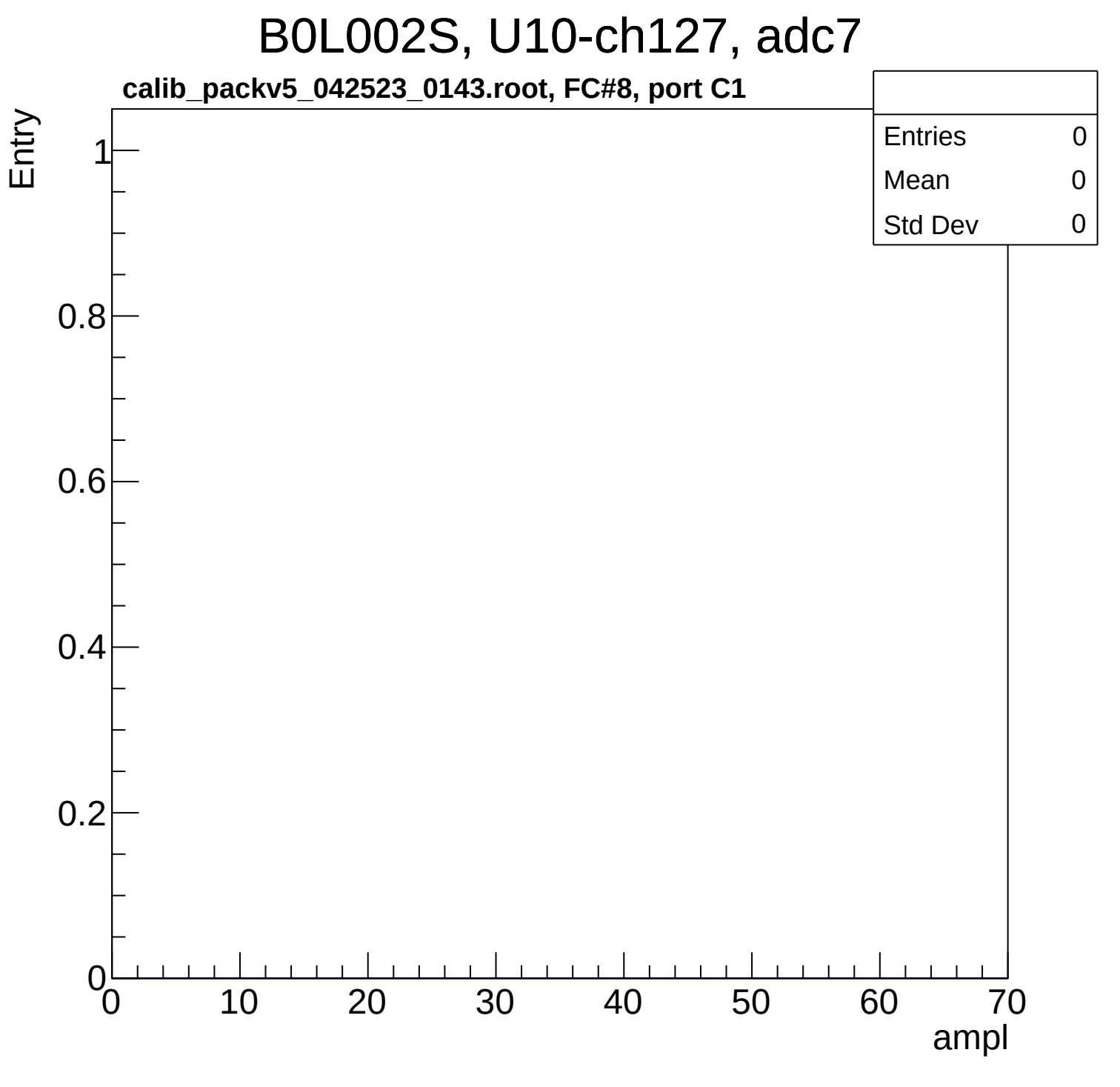

B0L002S, U10-ch127, adc2
calib_packv5_042523_0143.root, FC#8, port C1
Entries 72
Mean 45.12
Std Dev 3.358
Gaus mean : 45.8857
Gaus Width: 3.7810
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

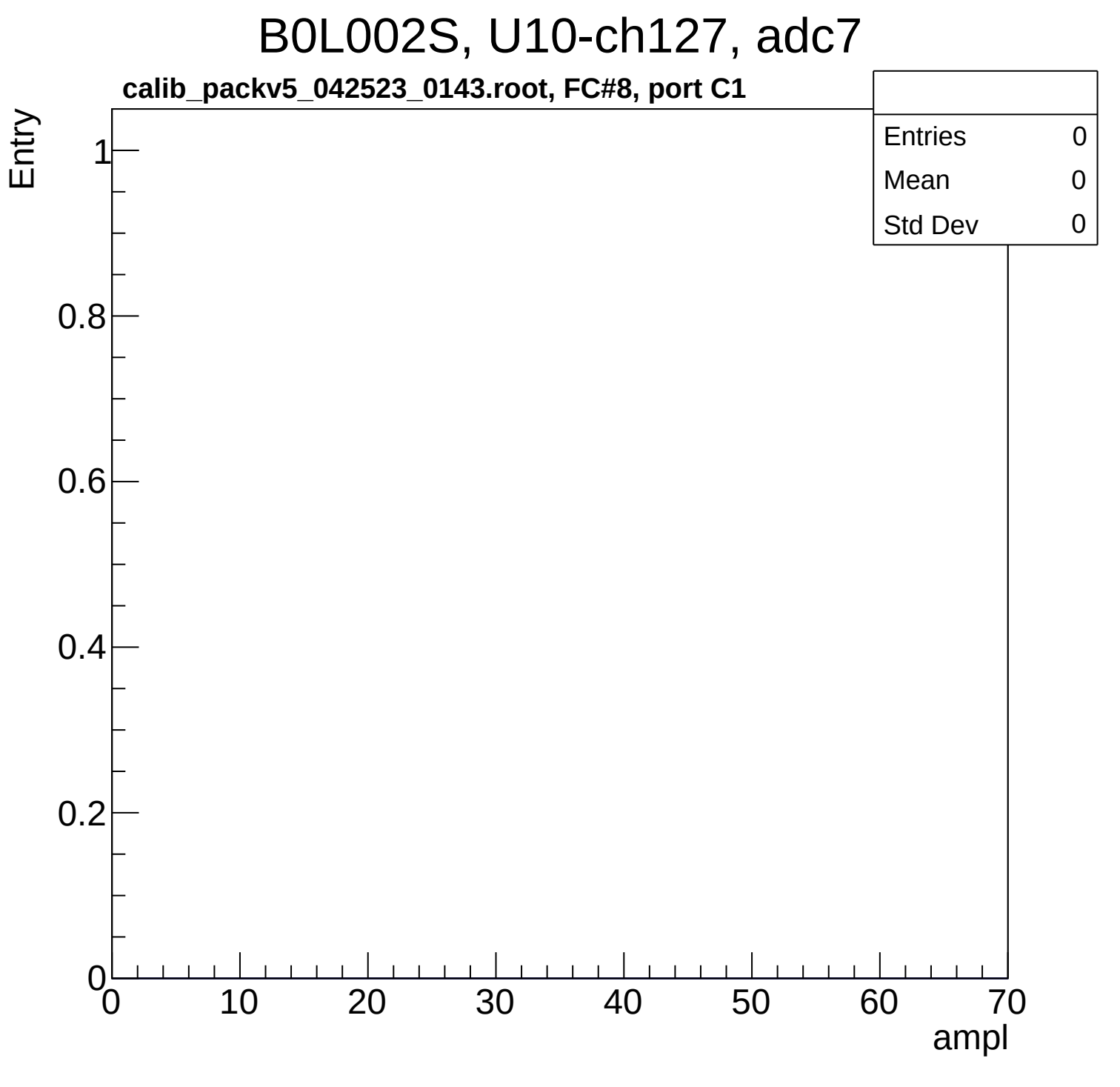

B0L002S, U10-ch127, adc3
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 52.09
Std Dev 3.615
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B0L002S, U10-ch127, adc4
calib_packv5_042523_0143.root, FC#8, port C1
Entries 65
Mean 57.18
Std Dev 7.827
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B0L002S, U10-ch127, adc5
calib_packv5_042523_0143.root, FC#8, port C1
Entries 19
Mean 61.05
Std Dev 1.701
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B0L002S, U10-ch127, adc6
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B0L002S, U10-ch127, adc7
calib_packv5_042523_0143.root, FC#8, port C1
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl