

B1L000S, U4-ch0

calib_packv5_042523_0143.root, FC#3, port B3

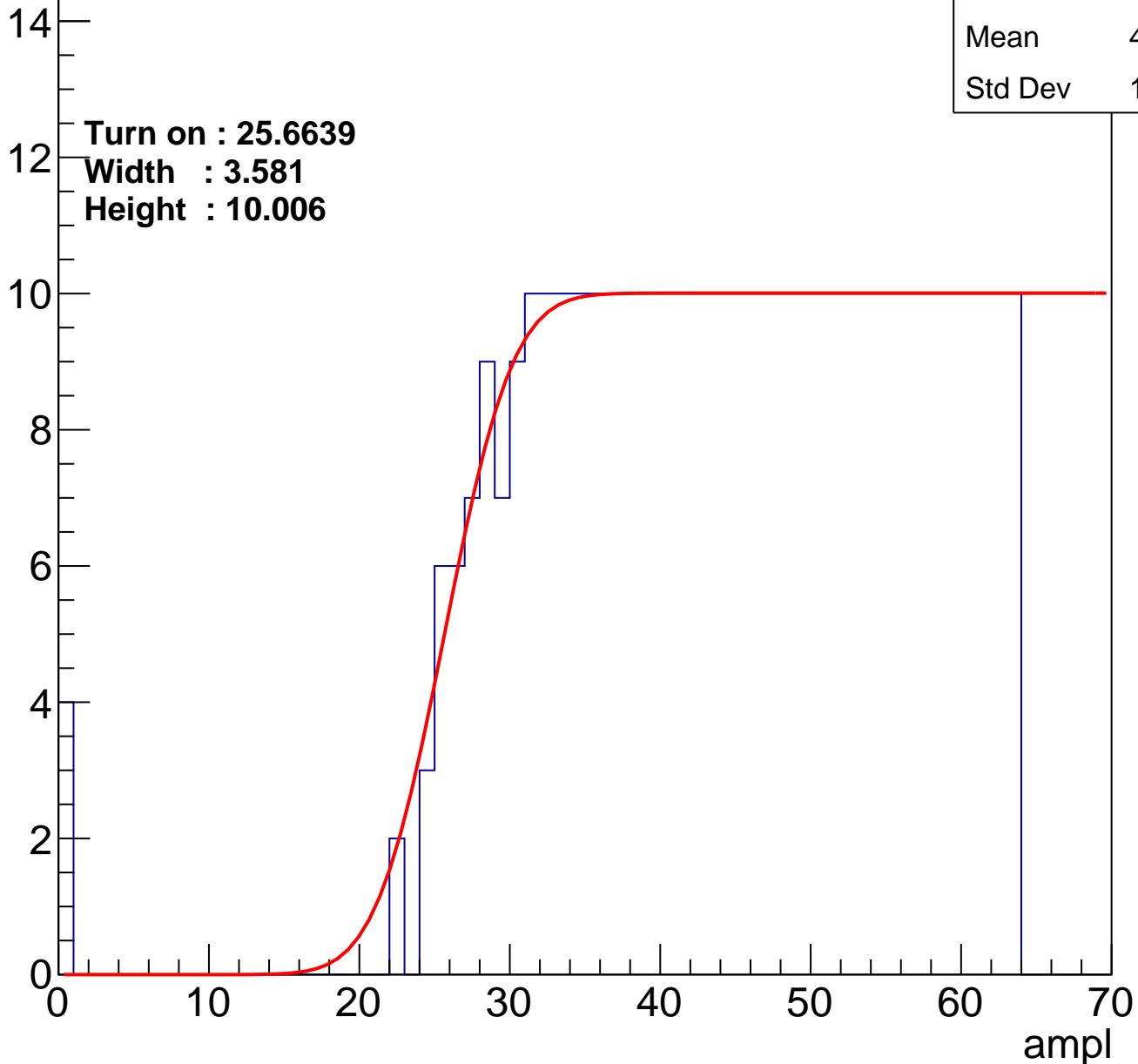
Entries	383
Mean	43.98
Std Dev	11.94

Turn on : 25.6639

Width : 3.581

Height : 10.006

Entry



B1L000S, U4-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.52
Std Dev	11.9

Turn on : 24.4370

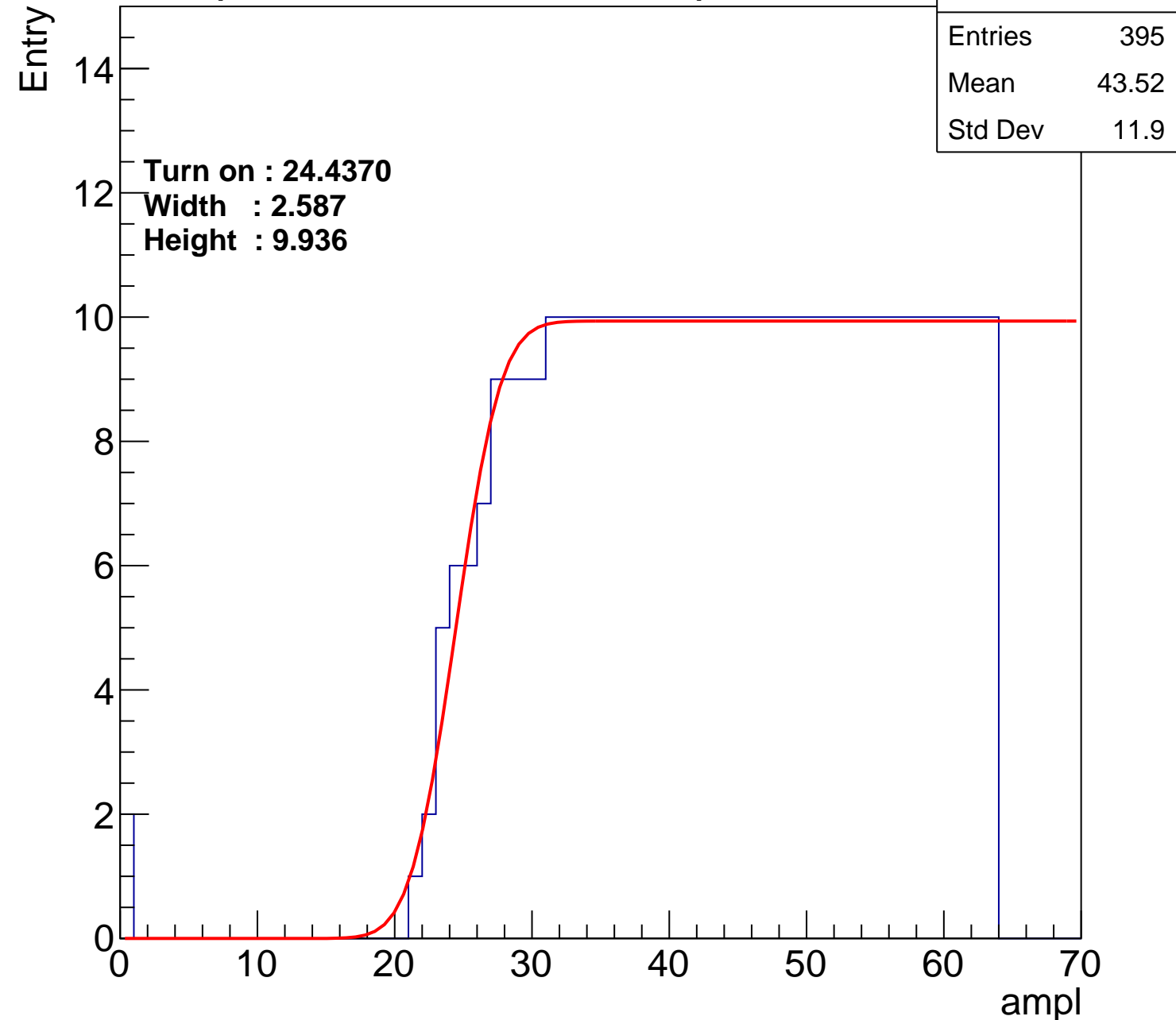
Width : 2.587

Height : 9.936

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.46
Std Dev	11.79

Turn on : 24.7557

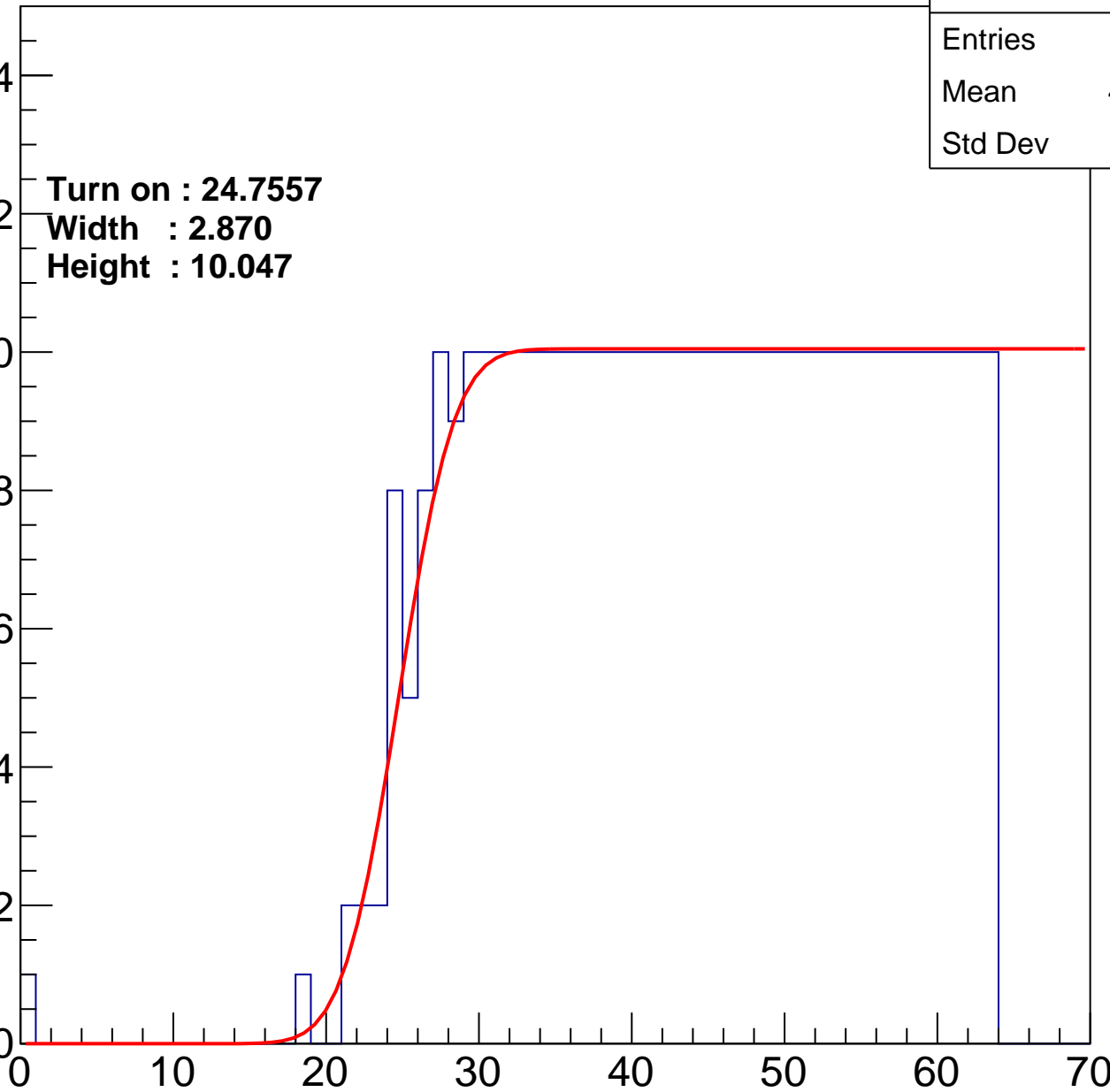
Width : 2.870

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch3

calib_packv5_042523_0143.root, FC#3, port B3

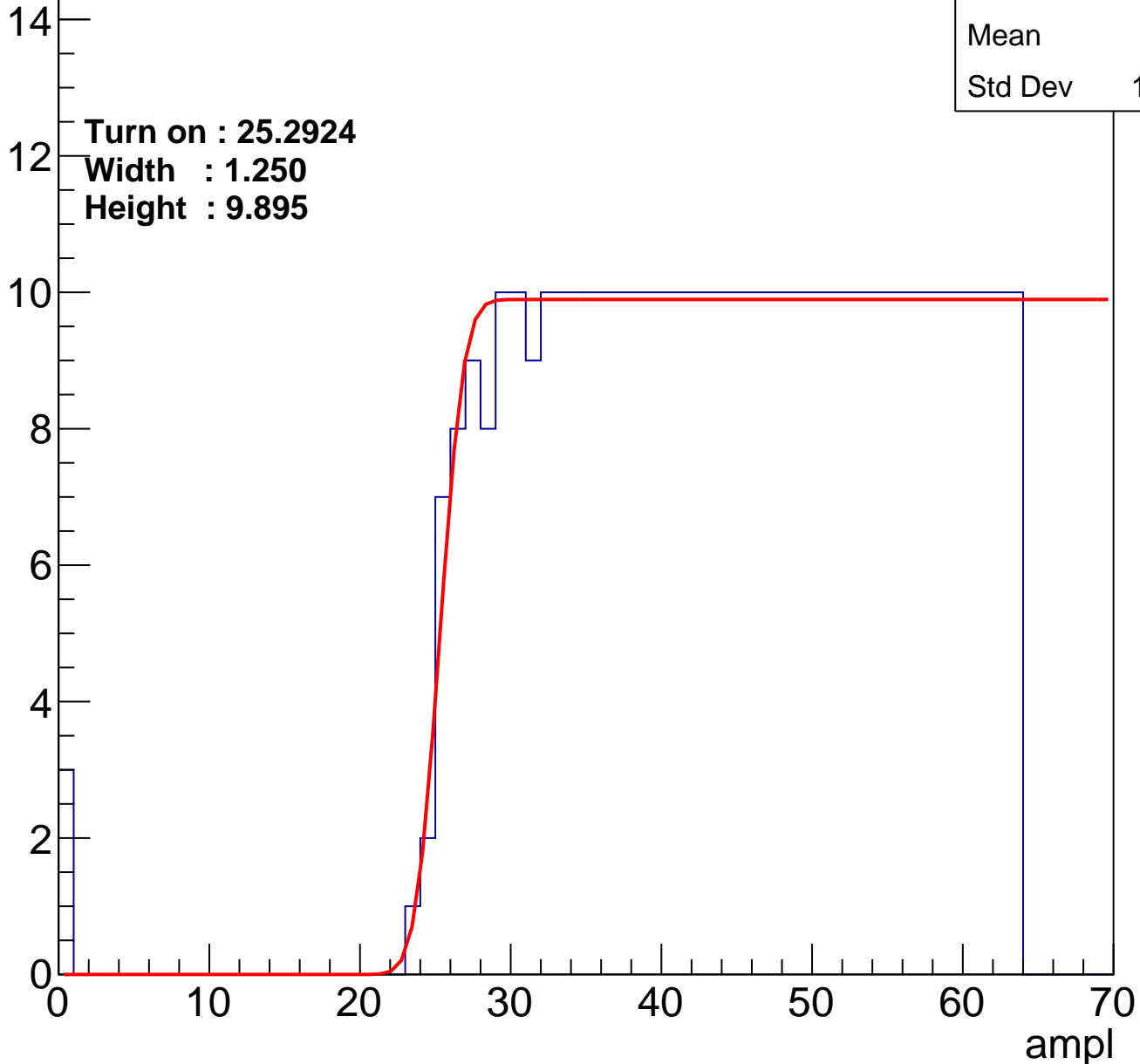
Entries	387
Mean	43.9
Std Dev	11.78

Turn on : 25.2924

Width : 1.250

Height : 9.895

Entry



B1L000S, U4-ch4

calib_packv5_042523_0143.root, FC#3, port B3

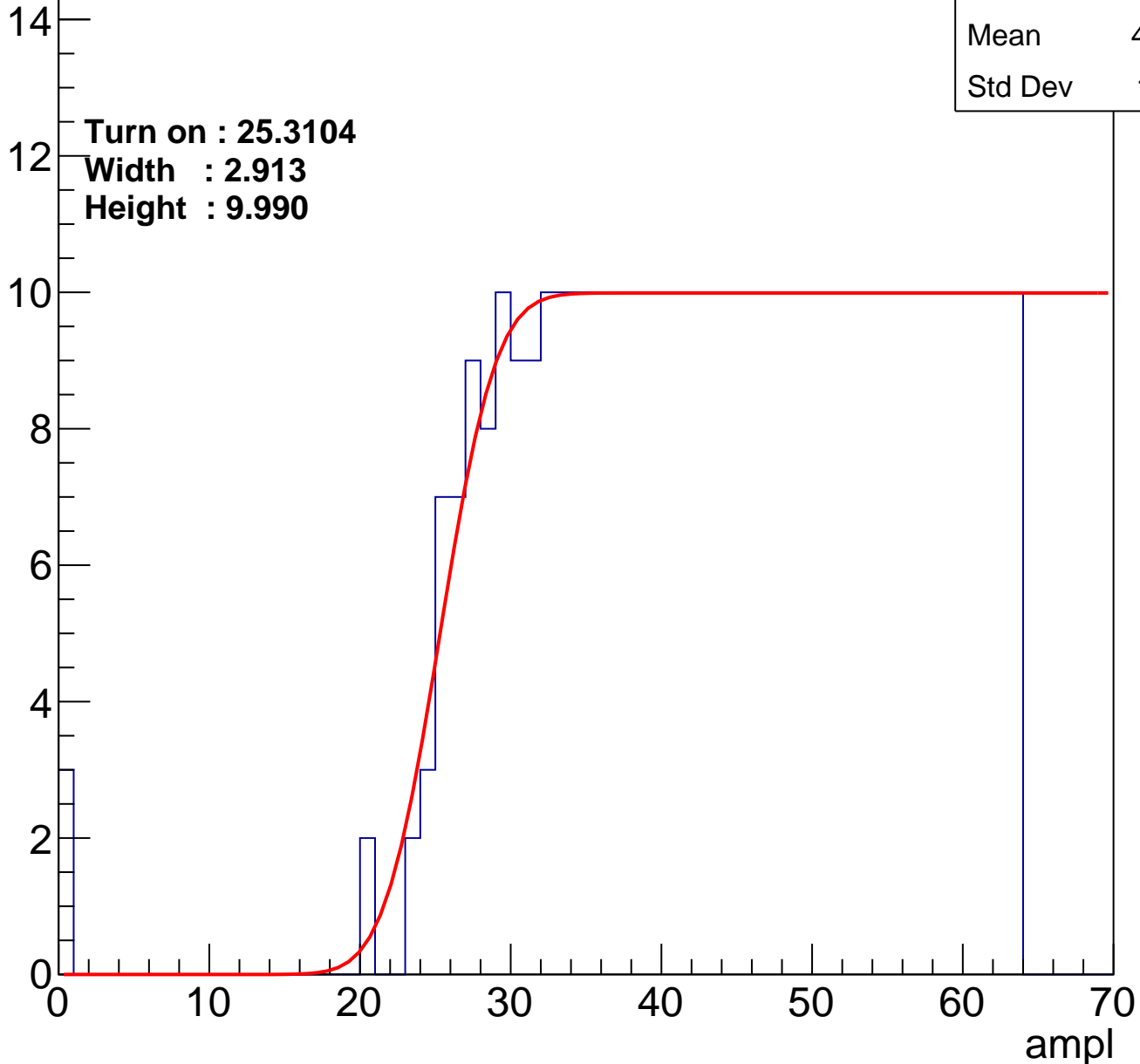
Entries	389
Mean	43.76
Std Dev	11.91

Turn on : 25.3104

Width : 2.913

Height : 9.990

Entry



B1L000S, U4-ch5

calib_packv5_042523_0143.root, FC#3, port B3

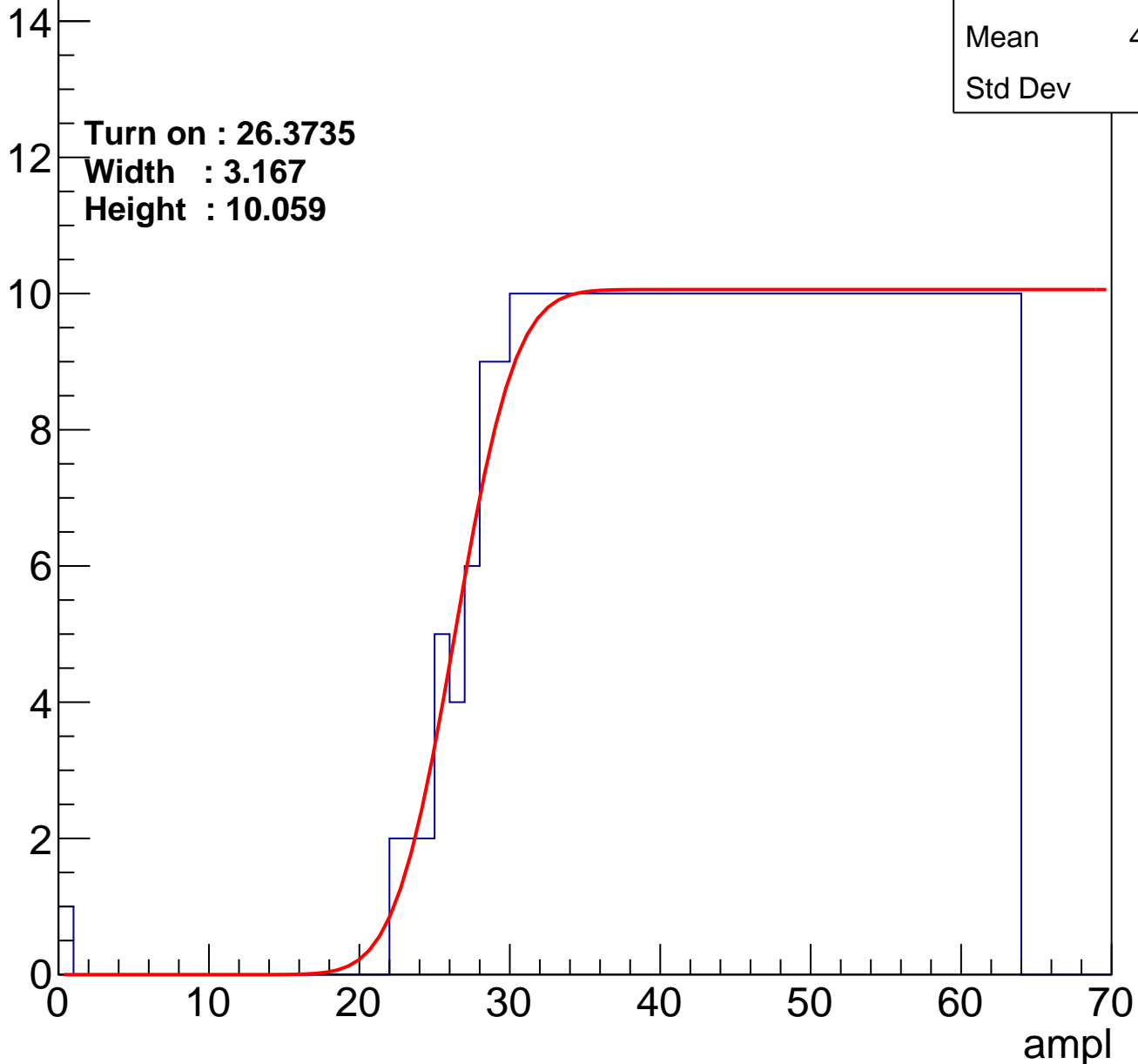
Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.3735

Width : 3.167

Height : 10.059

Entry



B1L000S, U4-ch6

calib_packv5_042523_0143.root, FC#3, port B3

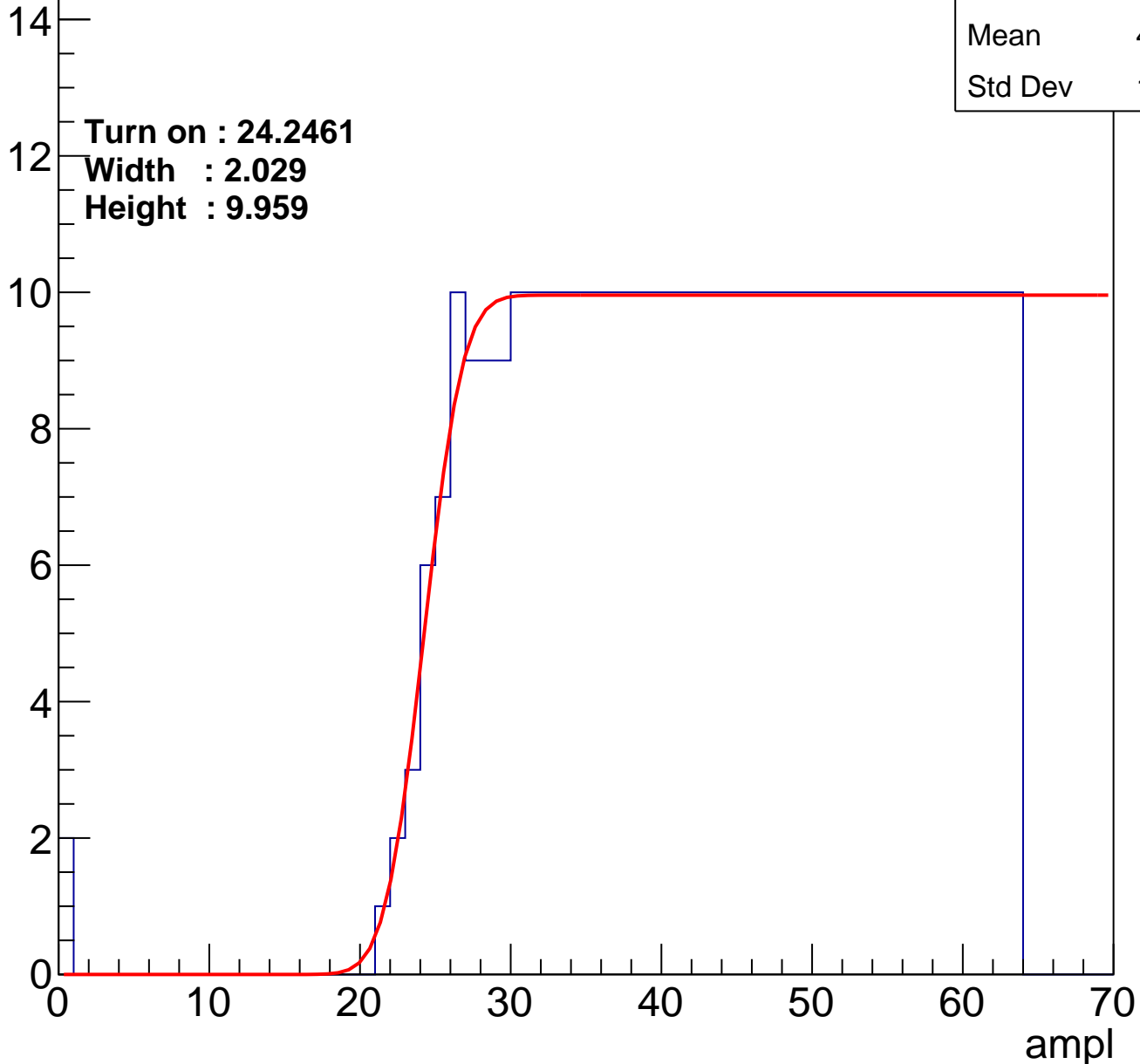
Entries	398
Mean	43.41
Std Dev	11.91

Turn on : 24.2461

Width : 2.029

Height : 9.959

Entry



B1L000S, U4-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.88
Std Dev	12

Turn on : 26.4589

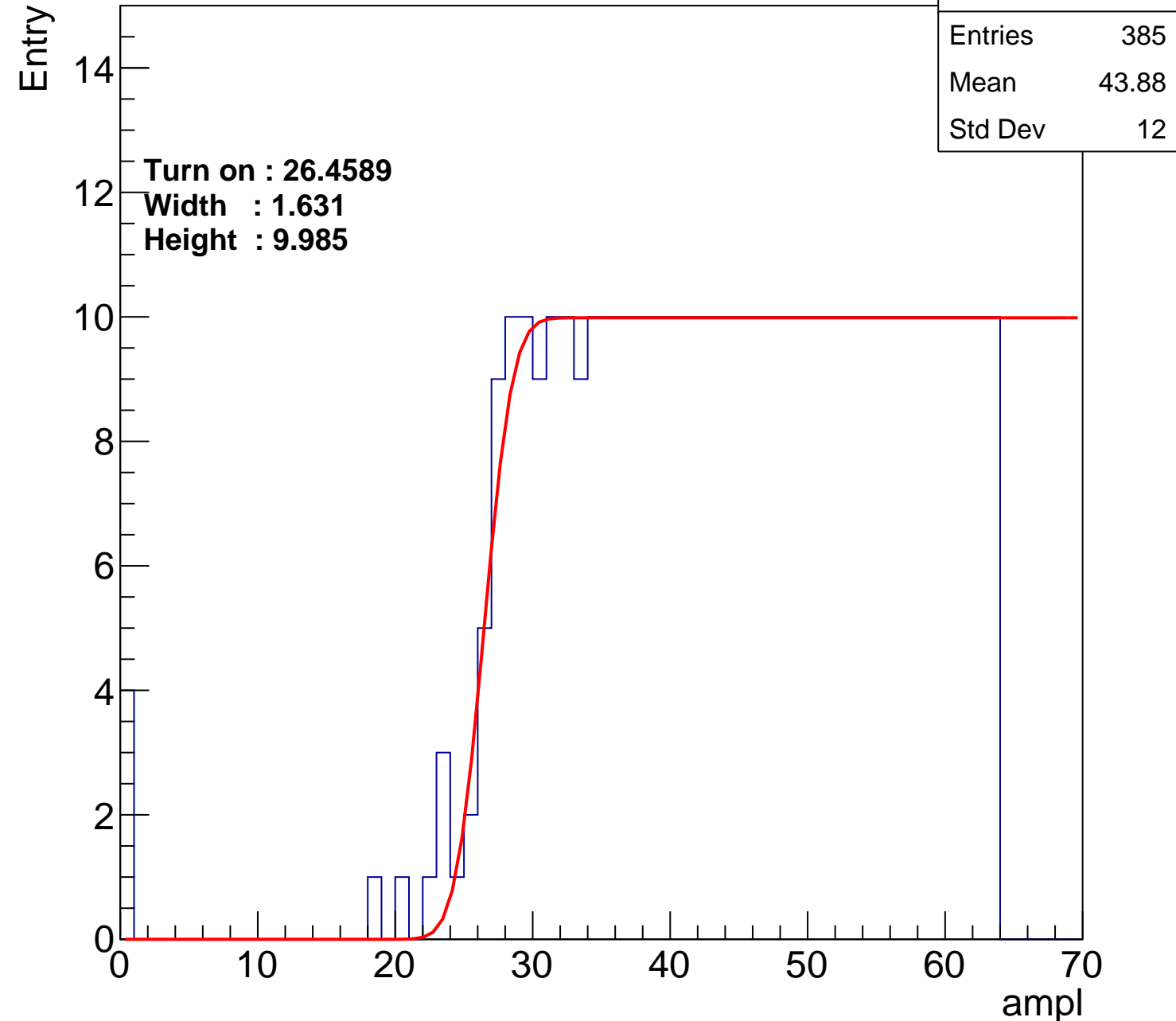
Width : 1.631

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch8

calib_packv5_042523_0143.root, FC#3, port B3

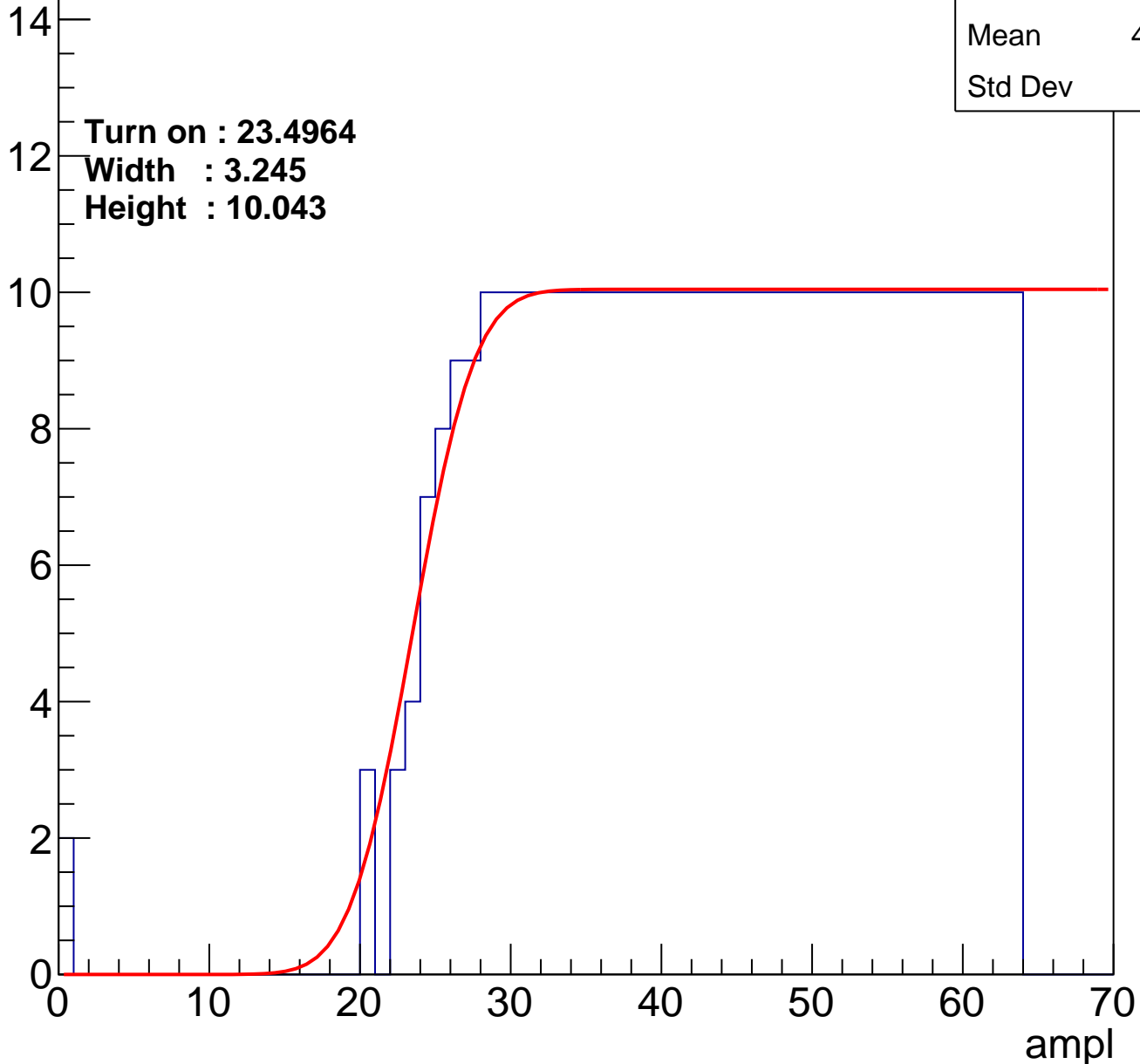
Entries	405
Mean	43.07
Std Dev	12.1

Turn on : 23.4964

Width : 3.245

Height : 10.043

Entry



B1L000S, U4-ch9

calib_packv5_042523_0143.root, FC#3, port B3

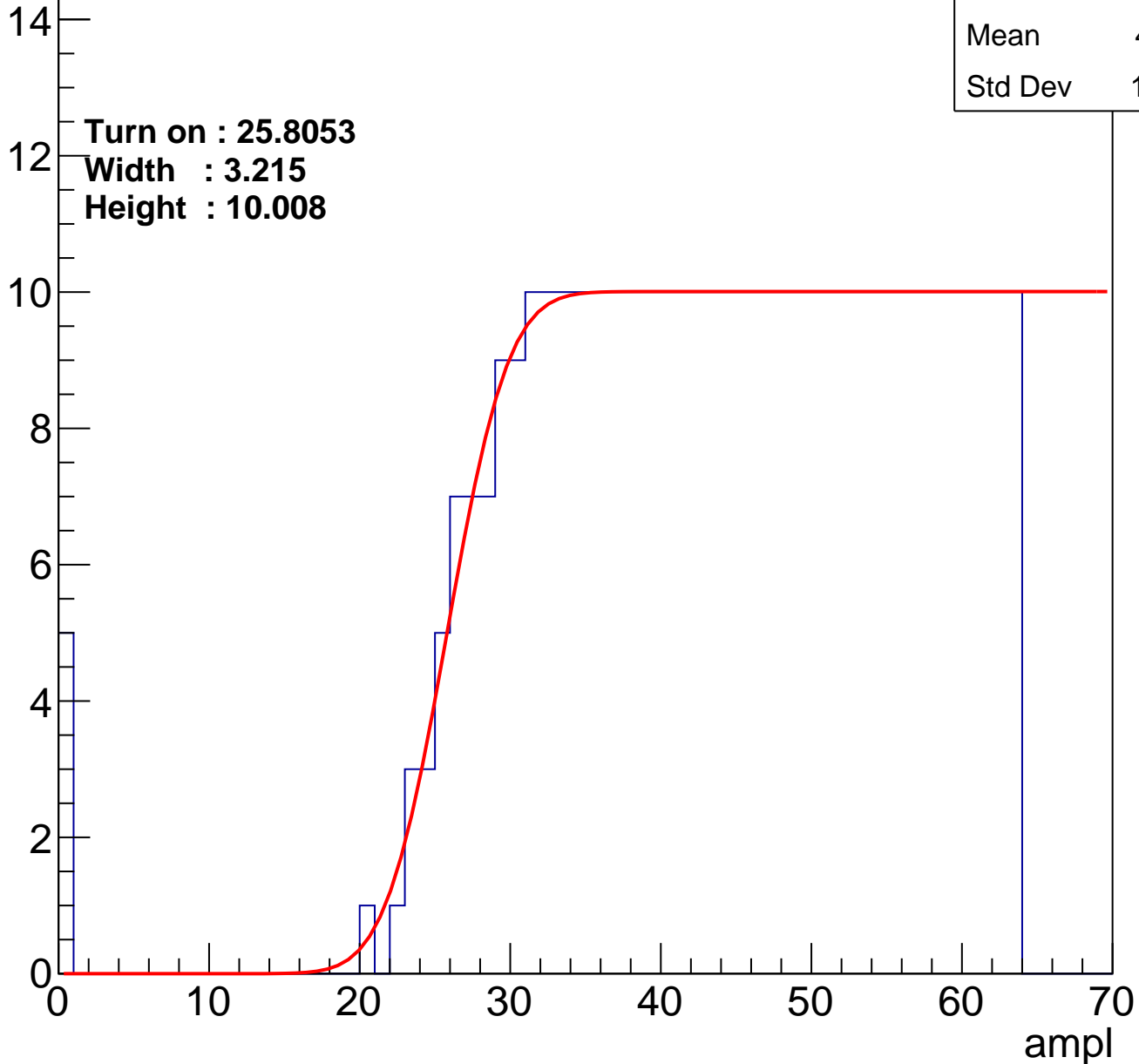
Entries	387
Mean	43.71
Std Dev	12.22

Turn on : 25.8053

Width : 3.215

Height : 10.008

Entry



B1L000S, U4-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.59
Std Dev	11.68

Turn on : 24.9603

Width : 1.679

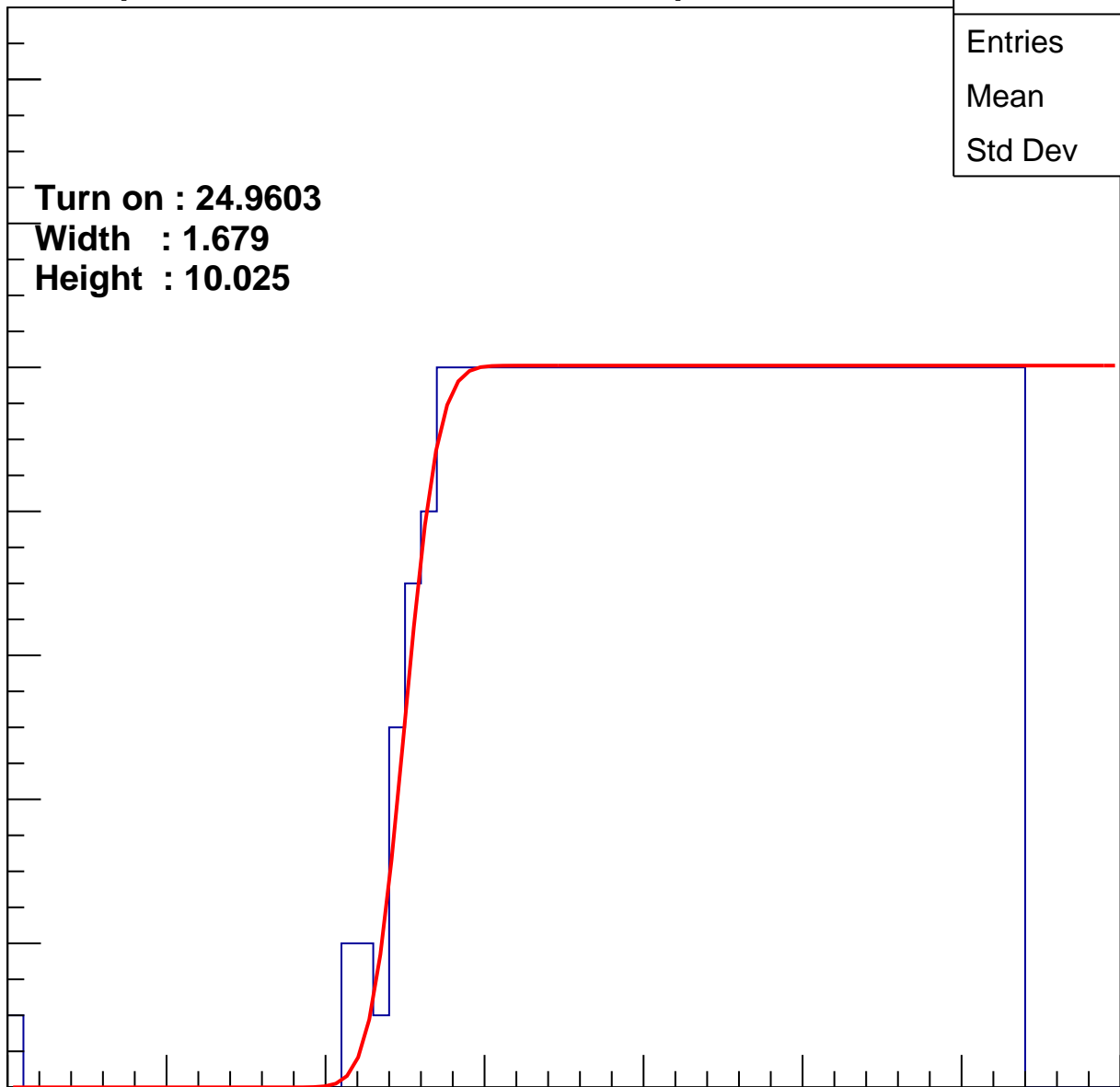
Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U4-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.64
Std Dev	11.53

Turn on : 27.4879

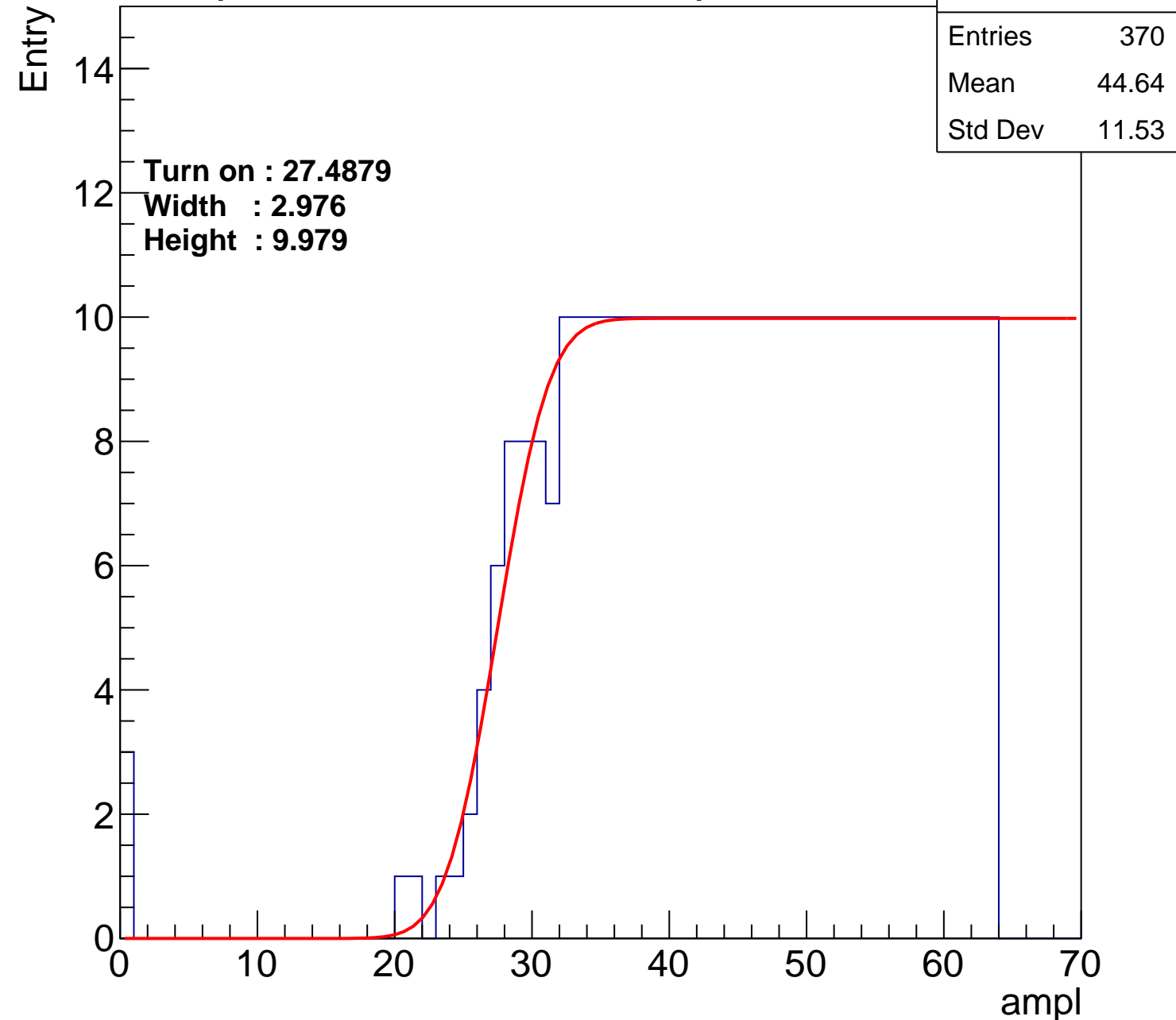
Width : 2.976

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.04
Std Dev	11.9

Turn on : 26.2498

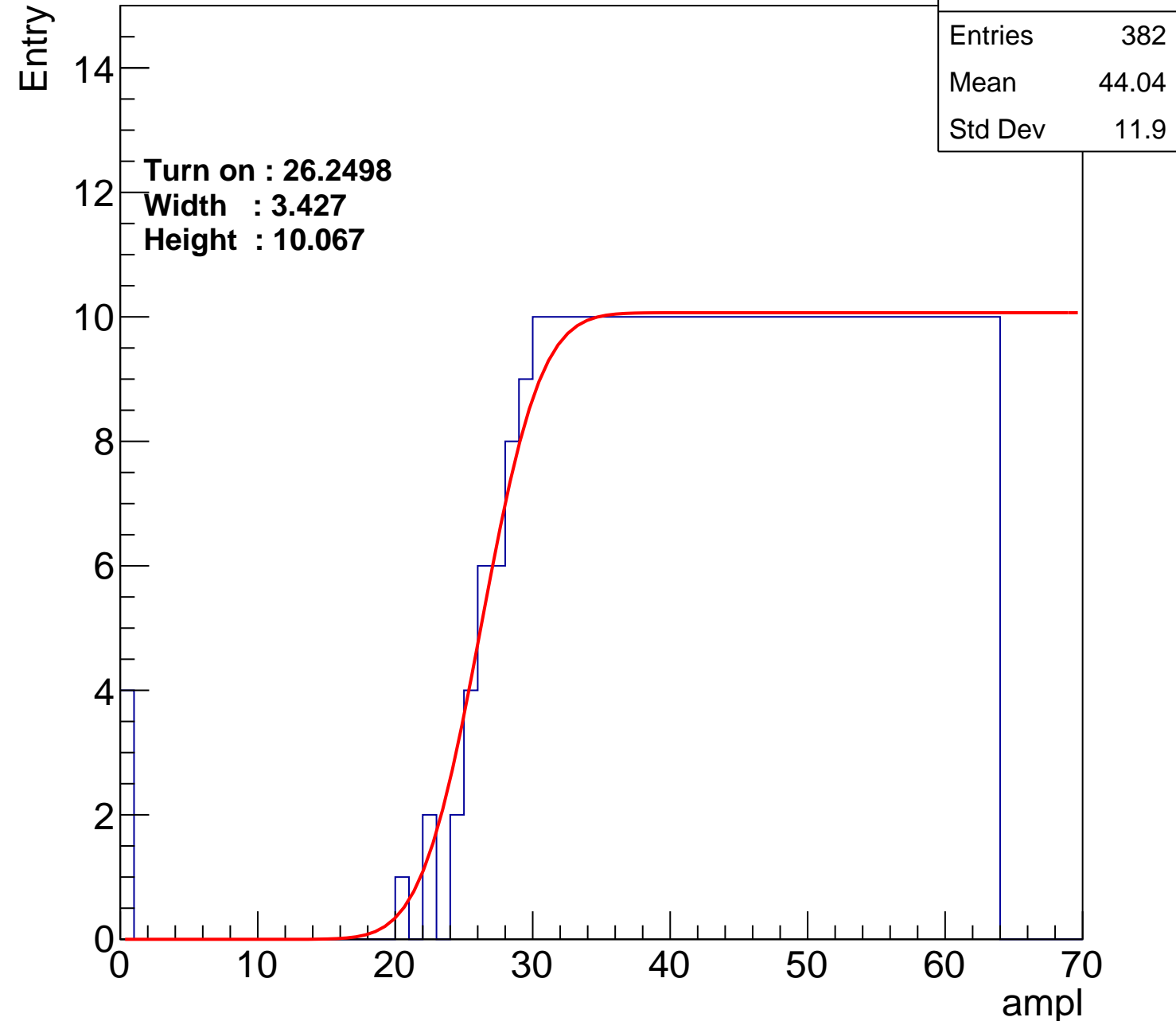
Width : 3.427

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.3
Std Dev	12.17

Turn on : 24.6309

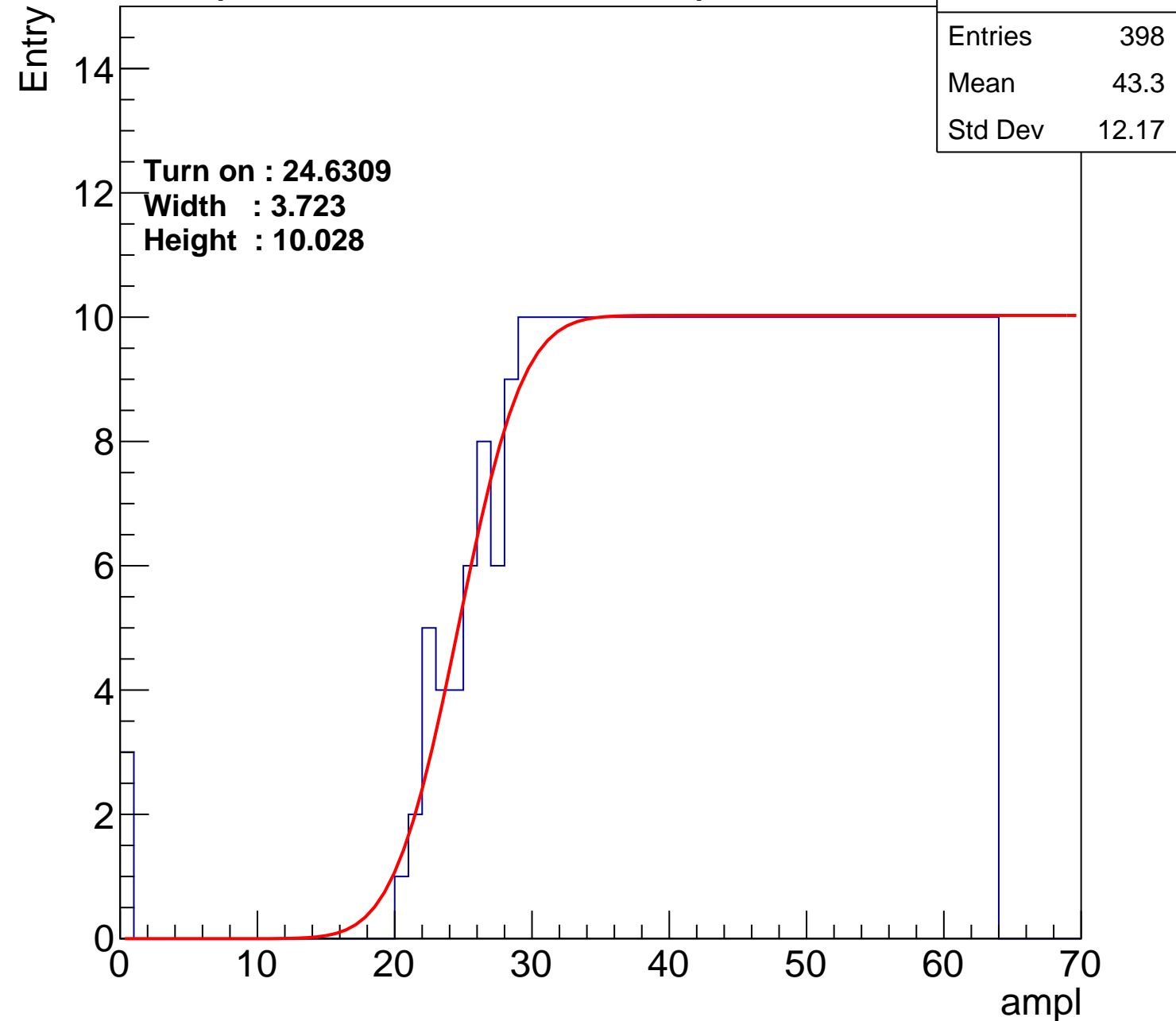
Width : 3.723

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	43
Std Dev	12.21

Turn on : 23.7242

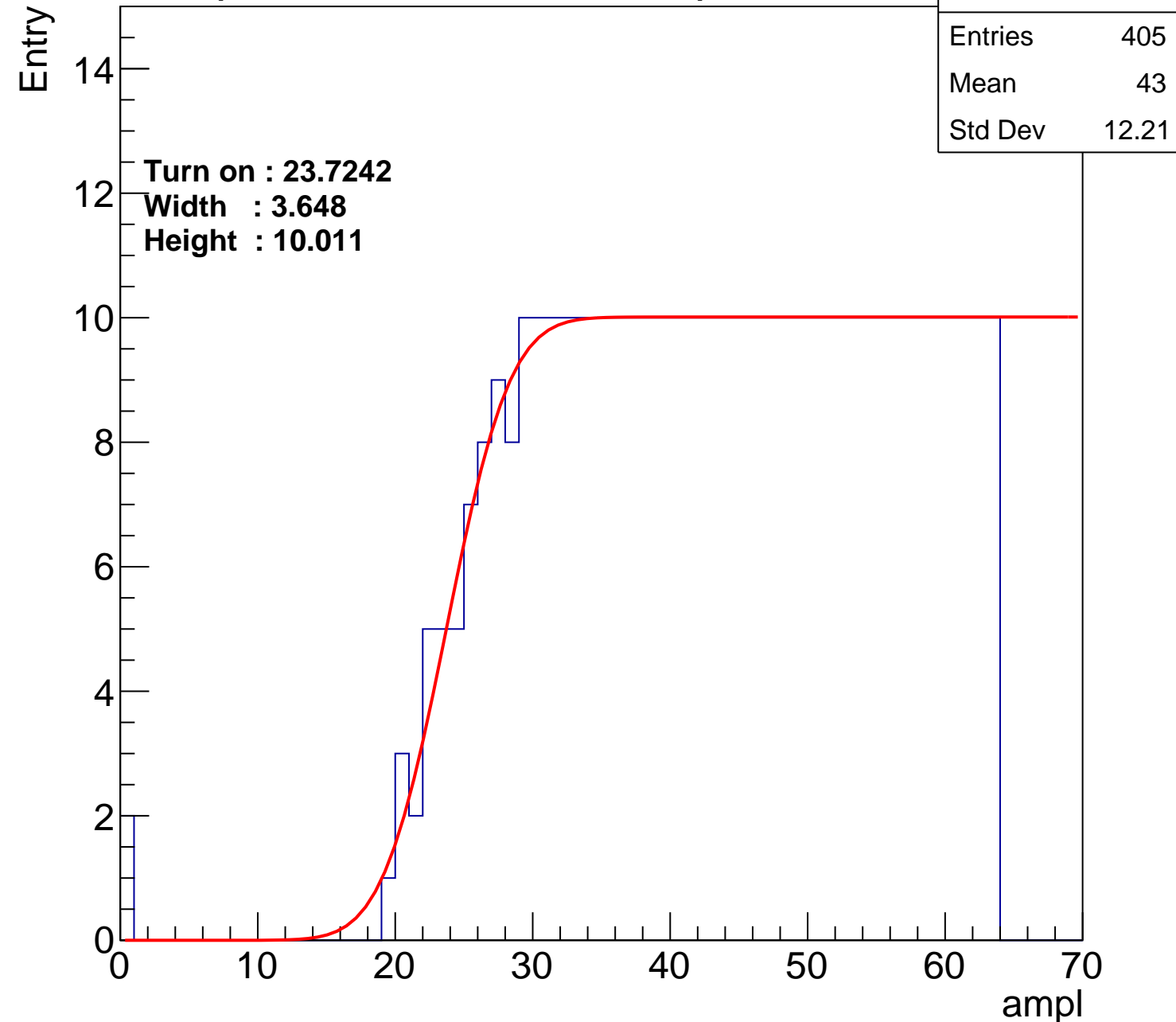
Width : 3.648

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.87
Std Dev	11.72

Turn on : 25.5225

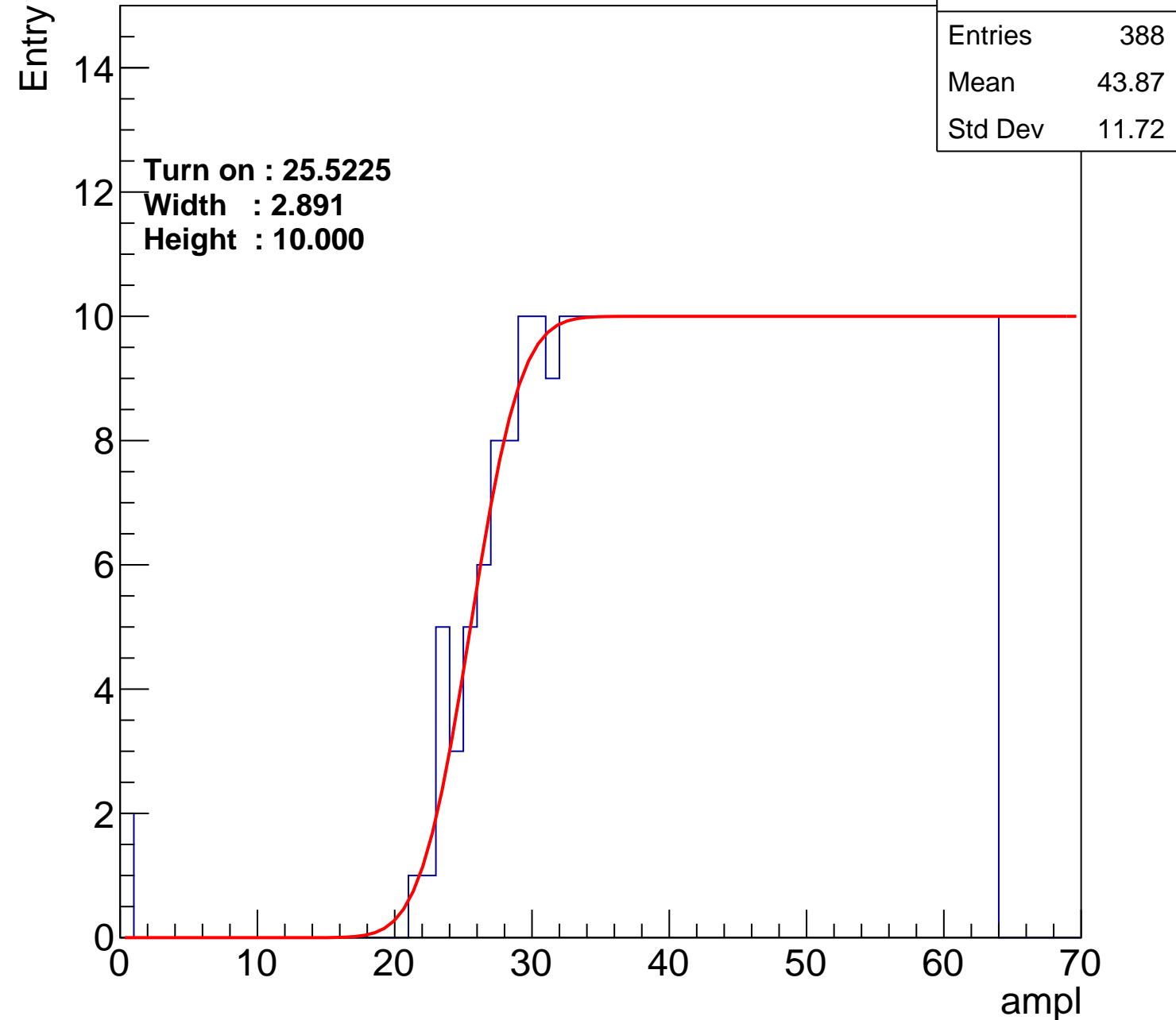
Width : 2.891

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.92
Std Dev	11.94

Turn on : 25.7139

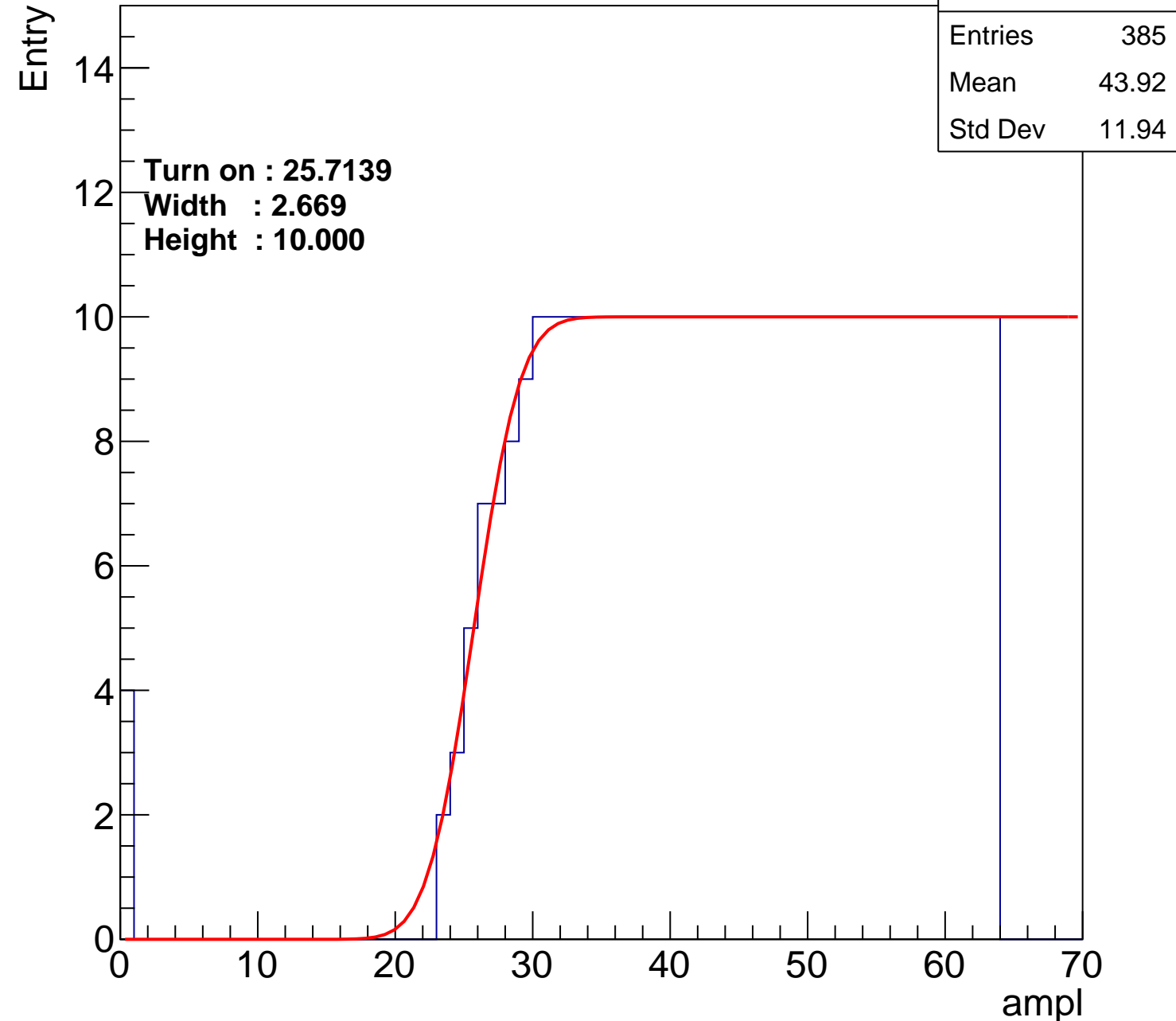
Width : 2.669

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch17

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.65
Std Dev	11.65

Turn on : 28.2466

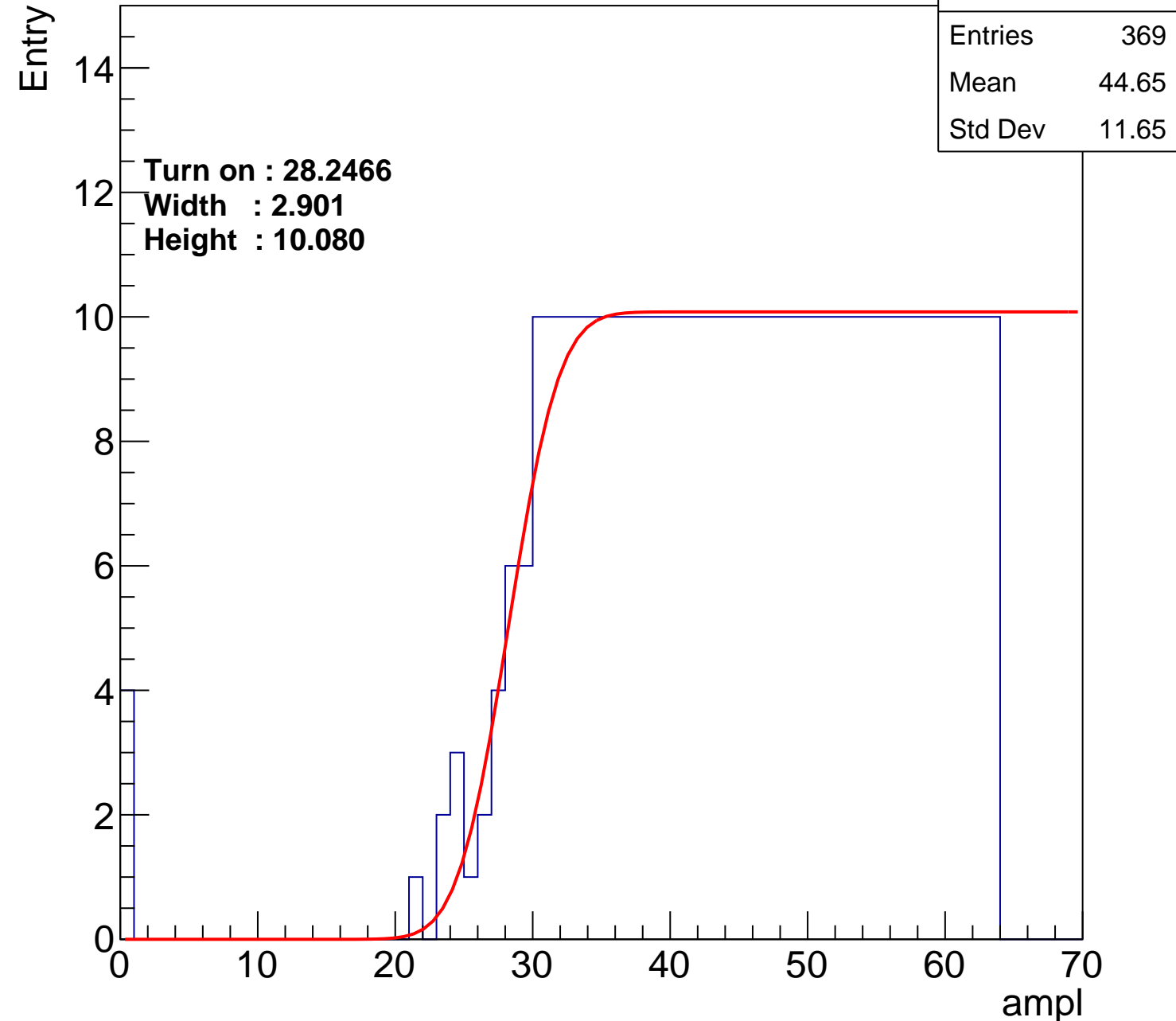
Width : 2.901

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch18

calib_packv5_042523_0143.root, FC#3, port B3

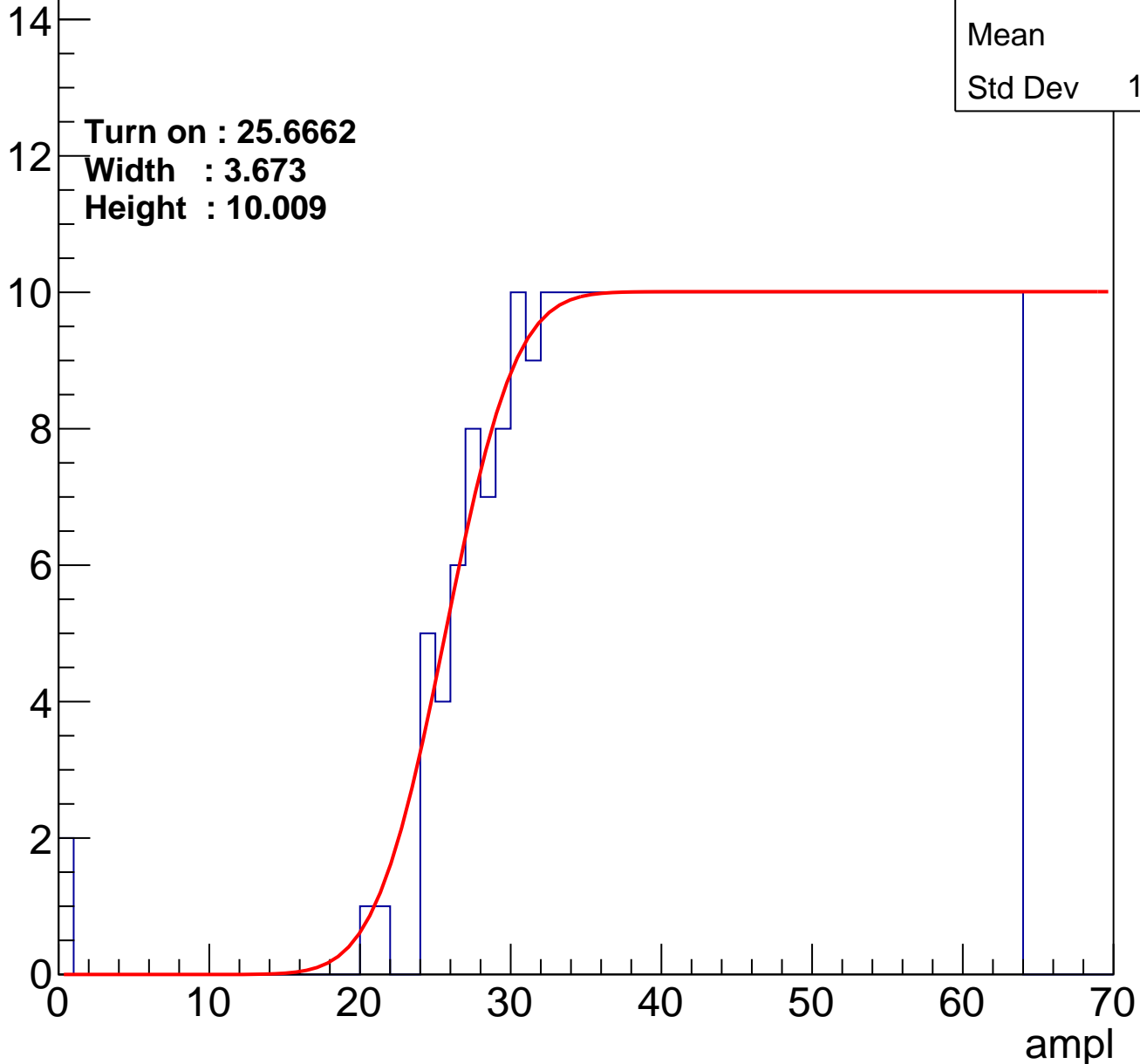
Entries	381
Mean	44.2
Std Dev	11.56

Turn on : 25.6662

Width : 3.673

Height : 10.009

Entry



B1L000S, U4-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.16
Std Dev	11.82

Turn on : 26.7579

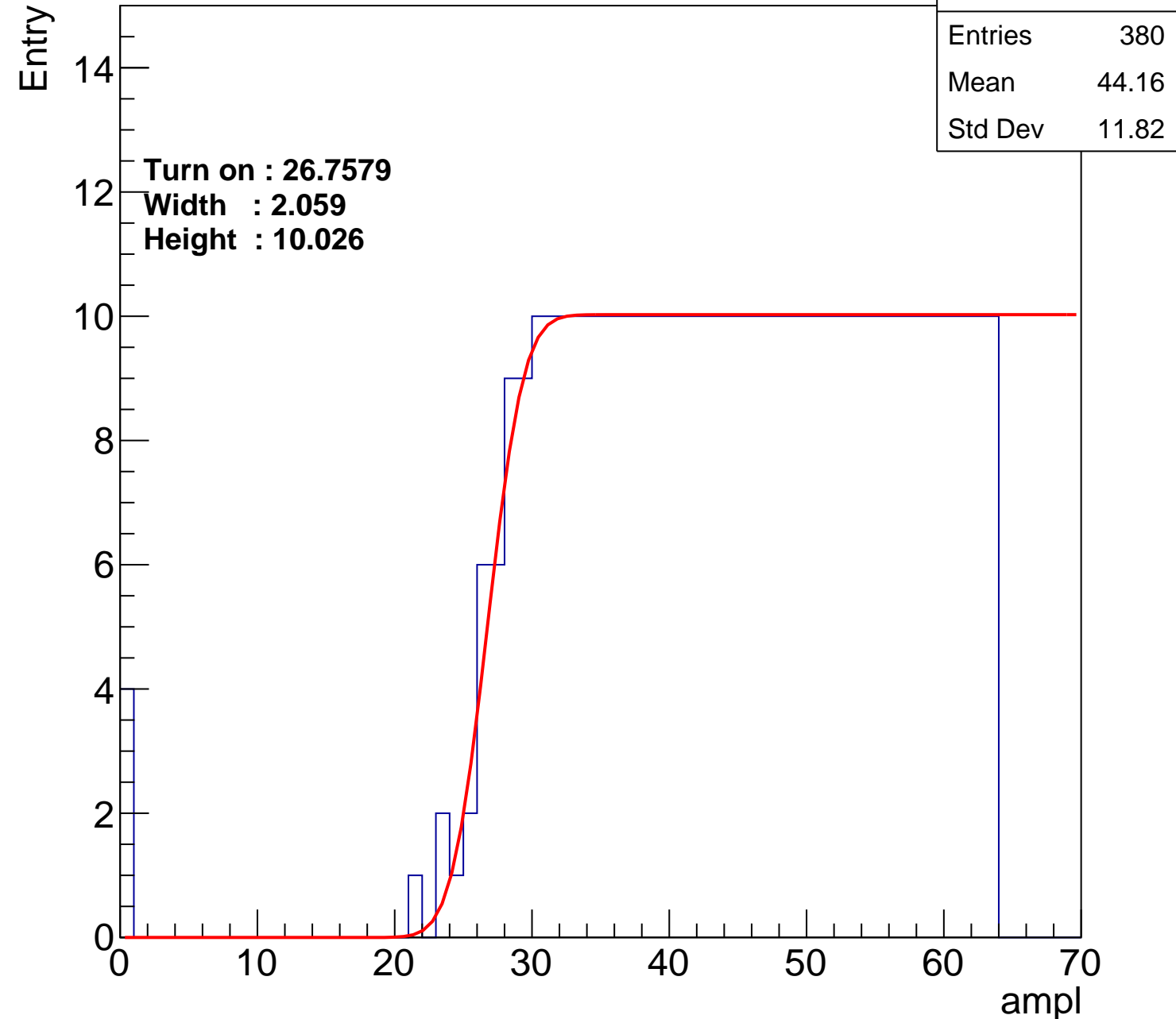
Width : 2.059

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch20

calib_packv5_042523_0143.root, FC#3, port B3

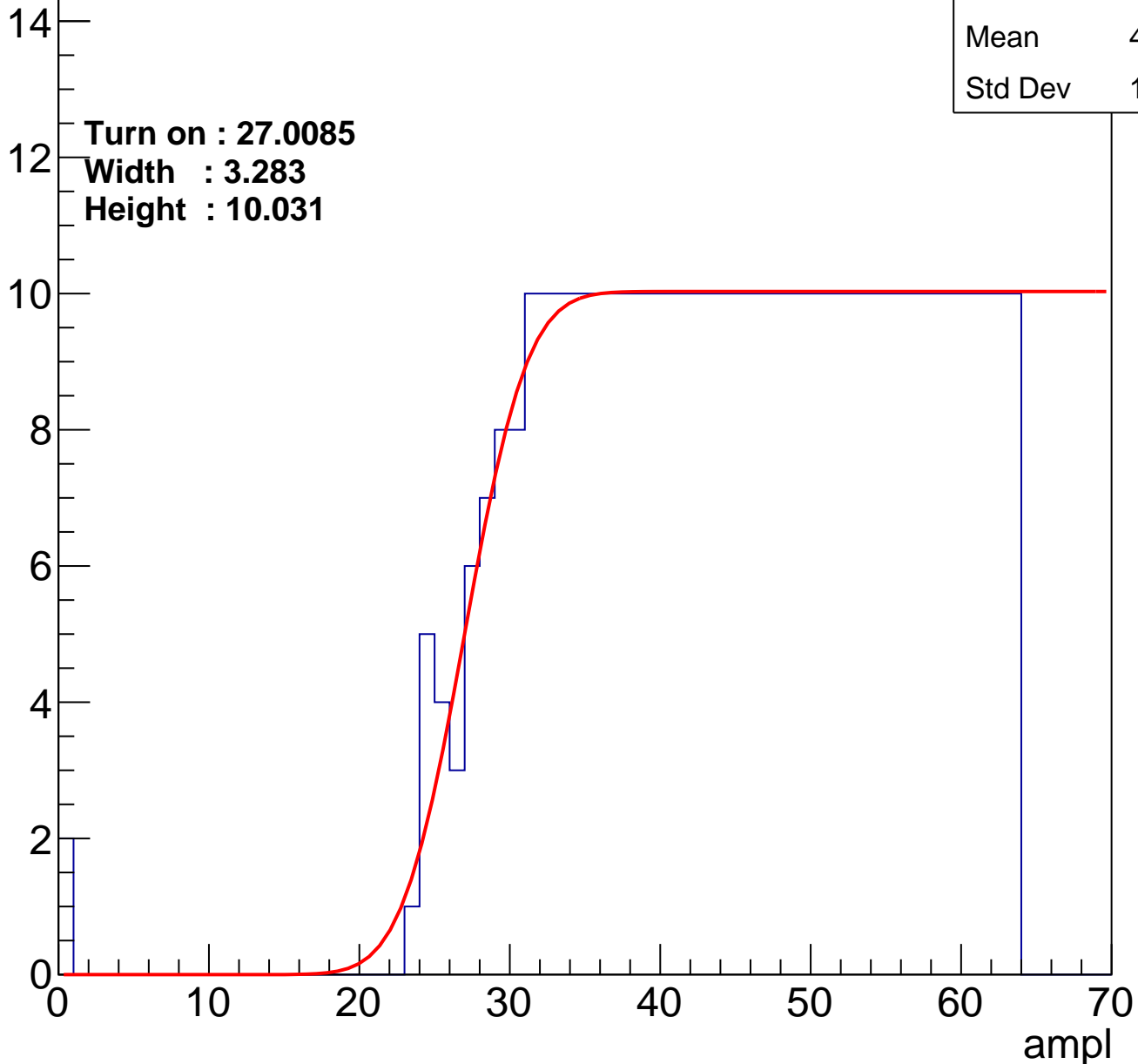
Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 27.0085

Width : 3.283

Height : 10.031

Entry



B1L000S, U4-ch21

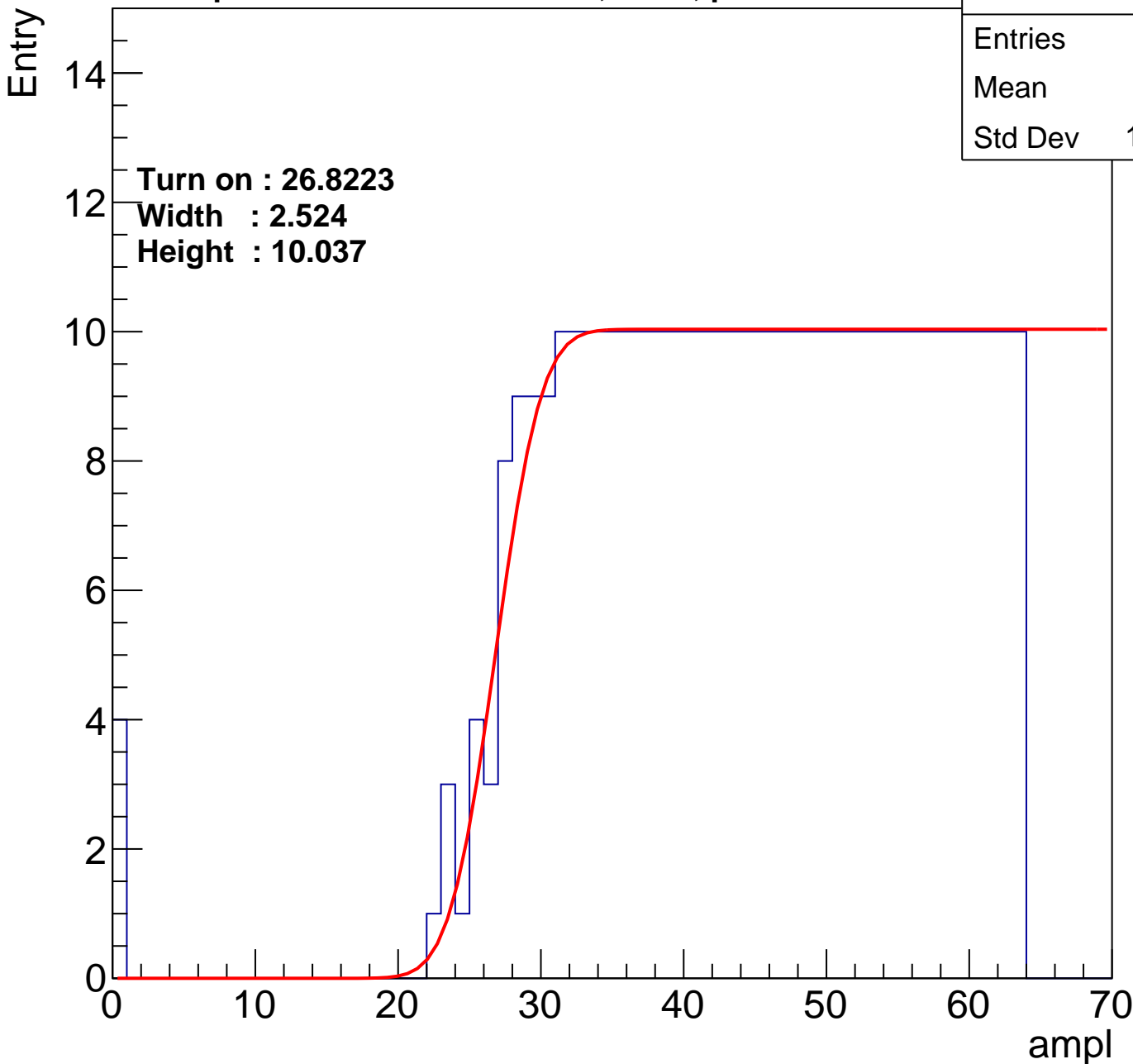
calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.1
Std Dev	11.87

Turn on : 26.8223

Width : 2.524

Height : 10.037



B1L000S, U4-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.22
Std Dev	11.52

Turn on : 26.4278

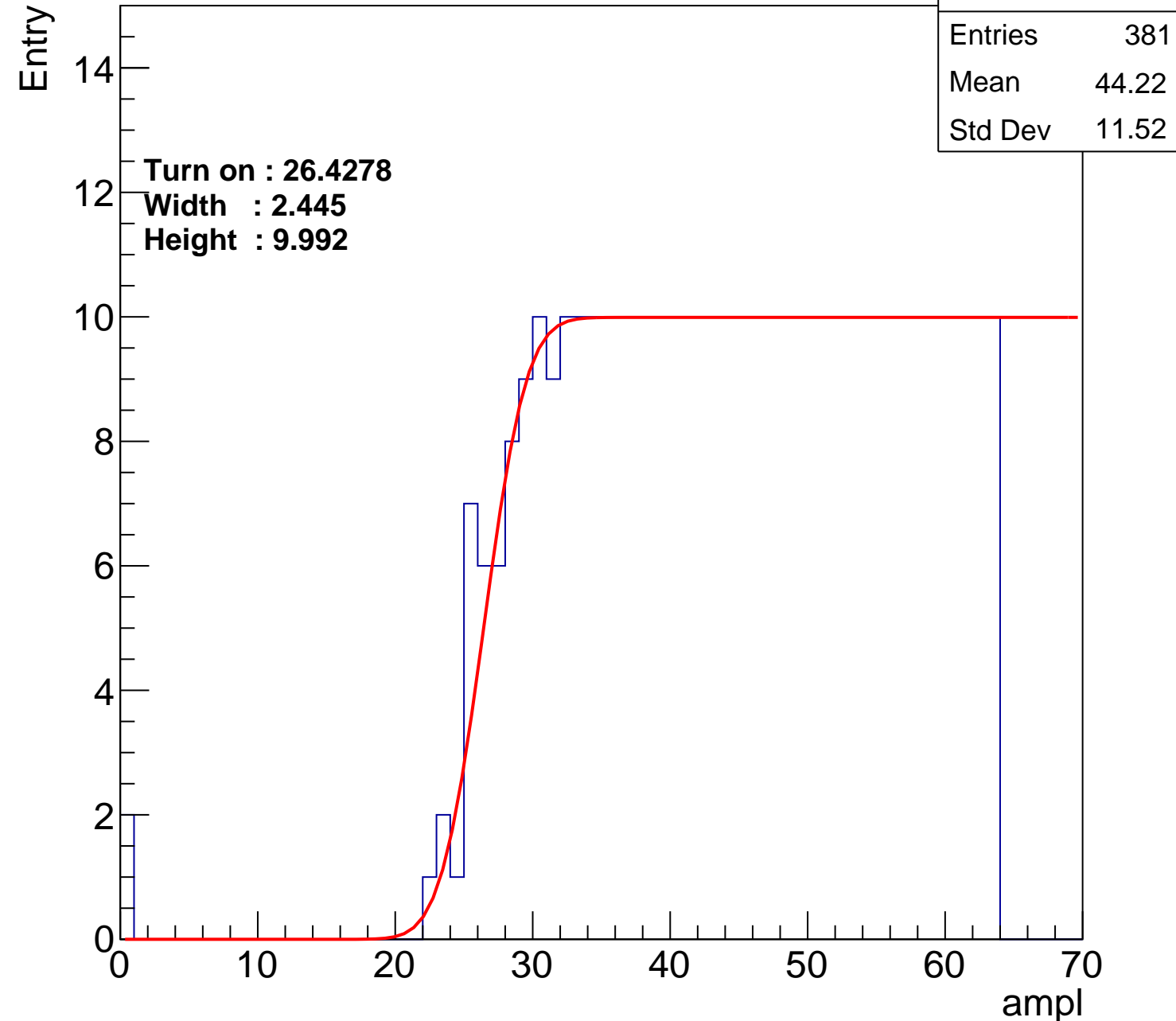
Width : 2.445

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	363
Mean	45.01
Std Dev	11.31

Turn on : 27.9752

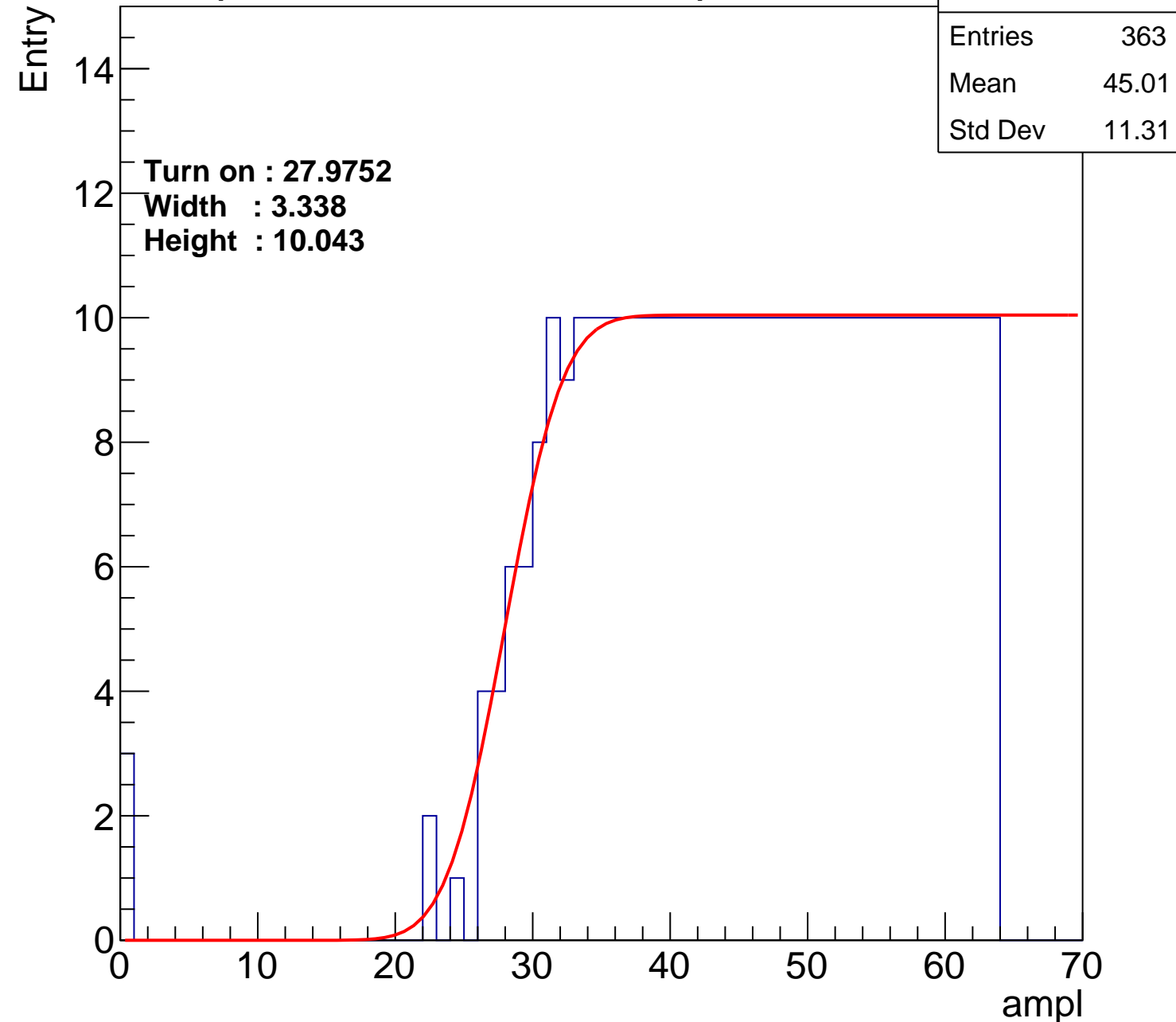
Width : 3.338

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.5028

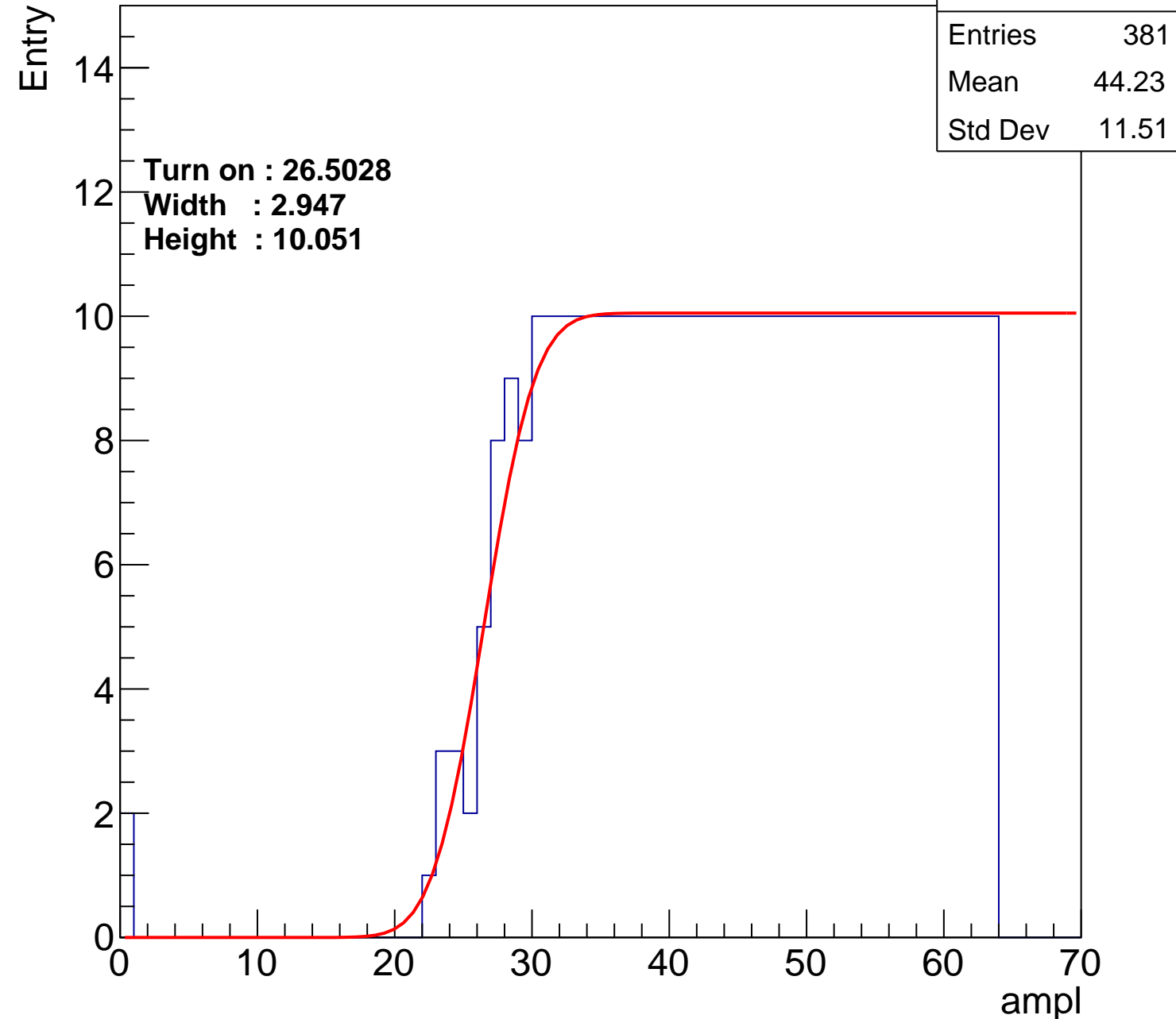
Width : 2.947

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.12
Std Dev	11.84

Turn on : 26.2076

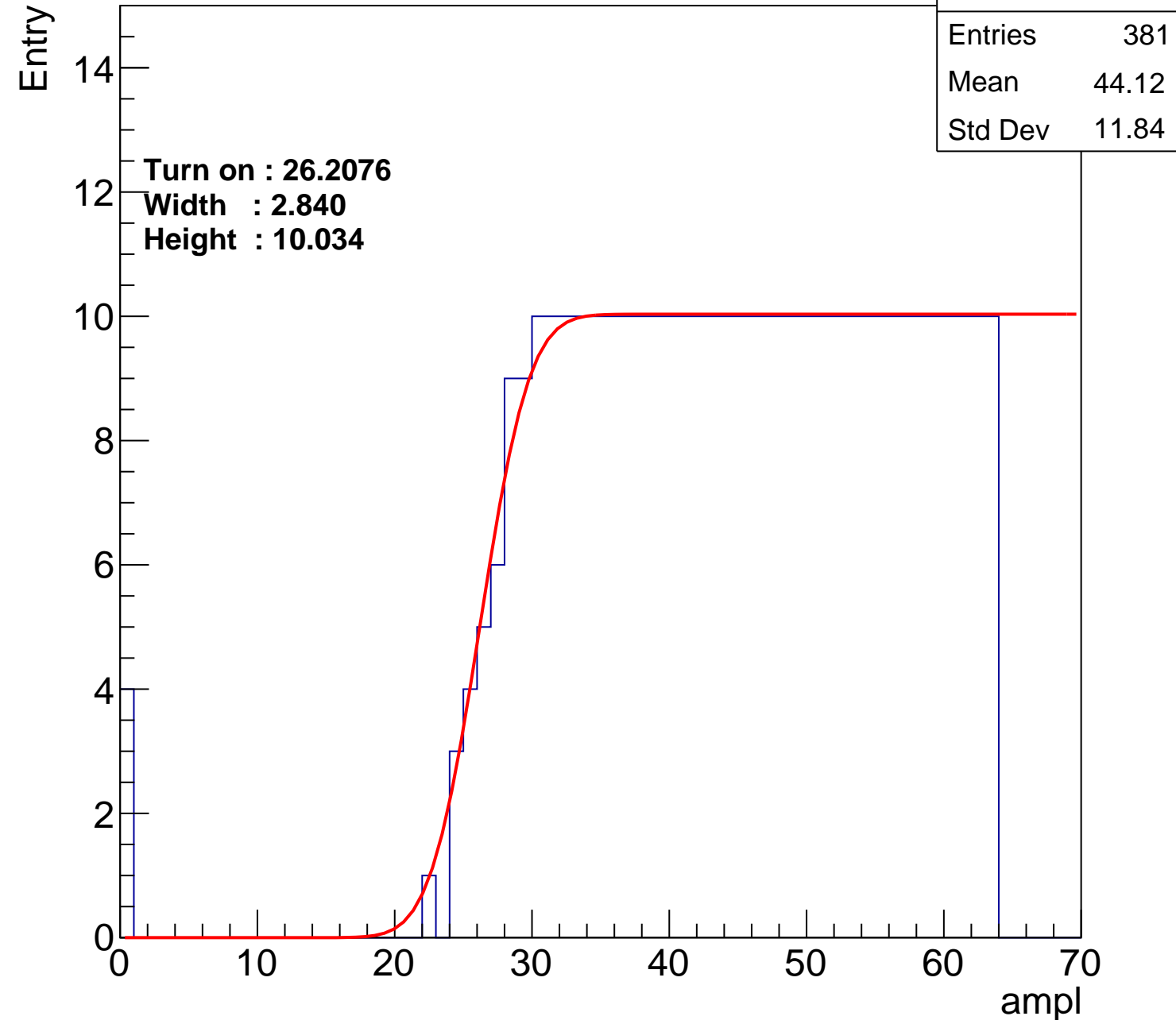
Width : 2.840

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch26

calib_packv5_042523_0143.root, FC#3, port B3

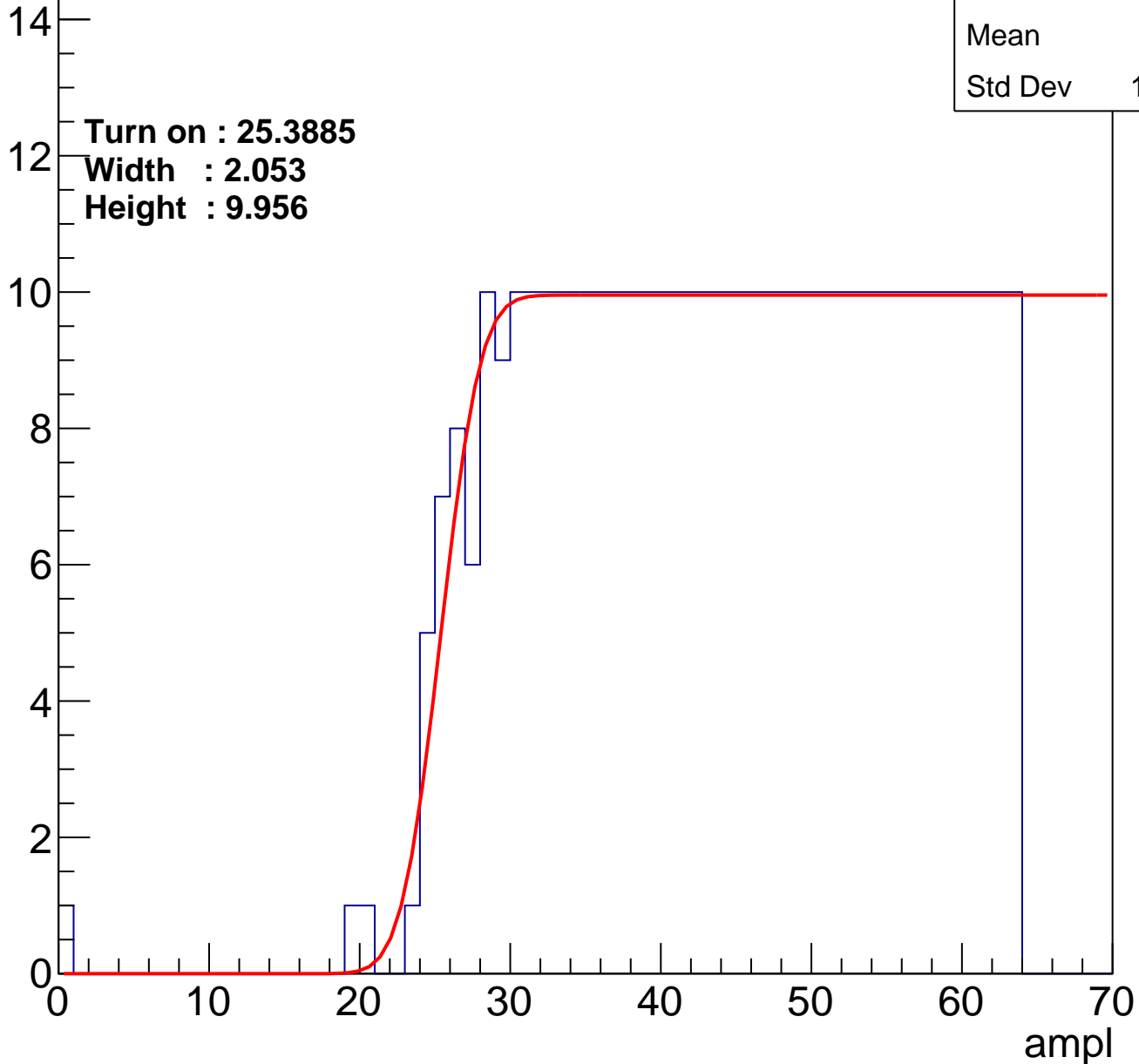
Entries	389
Mean	43.9
Std Dev	11.55

Turn on : 25.3885

Width : 2.053

Height : 9.956

Entry



B1L000S, U4-ch27

calib_packv5_042523_0143.root, FC#3, port B3

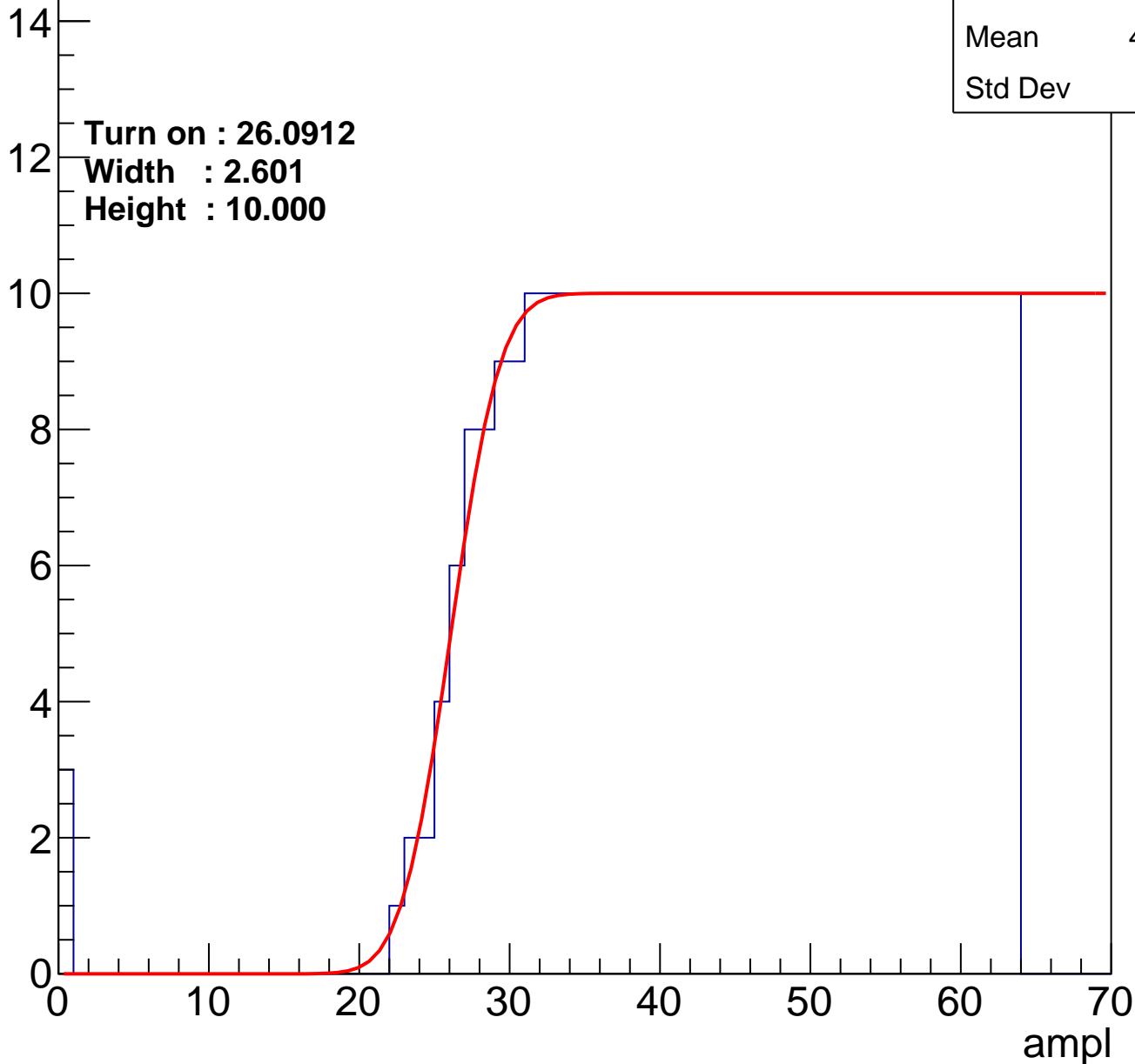
Entry

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.0912

Width : 2.601

Height : 10.000



B1L000S, U4-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.88
Std Dev	11.87

Turn on : 26.0068

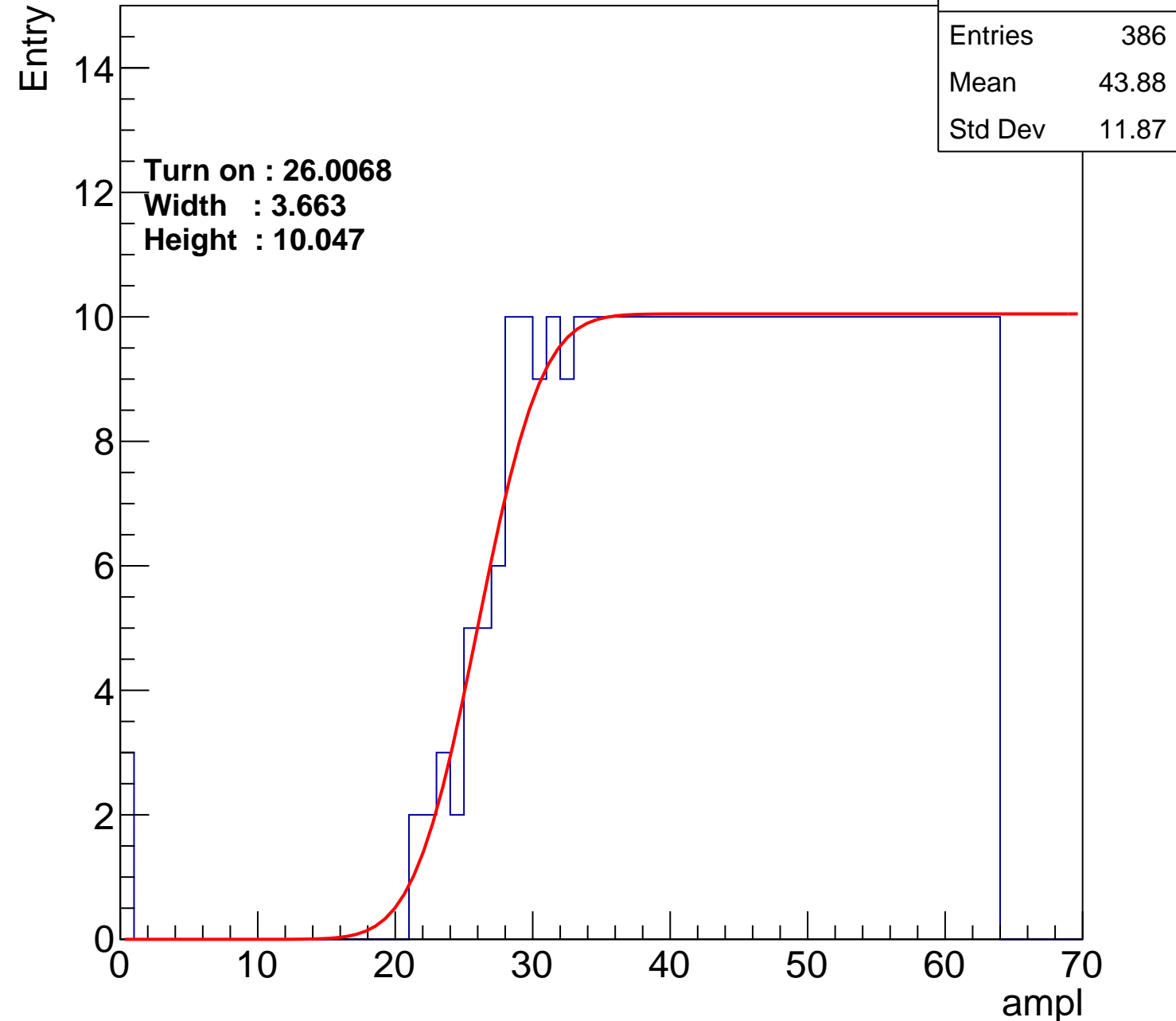
Width : 3.663

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch29

calib_packv5_042523_0143.root, FC#3, port B3

Entries	360
Mean	45.29
Std Dev	10.94

Turn on : 28.5923

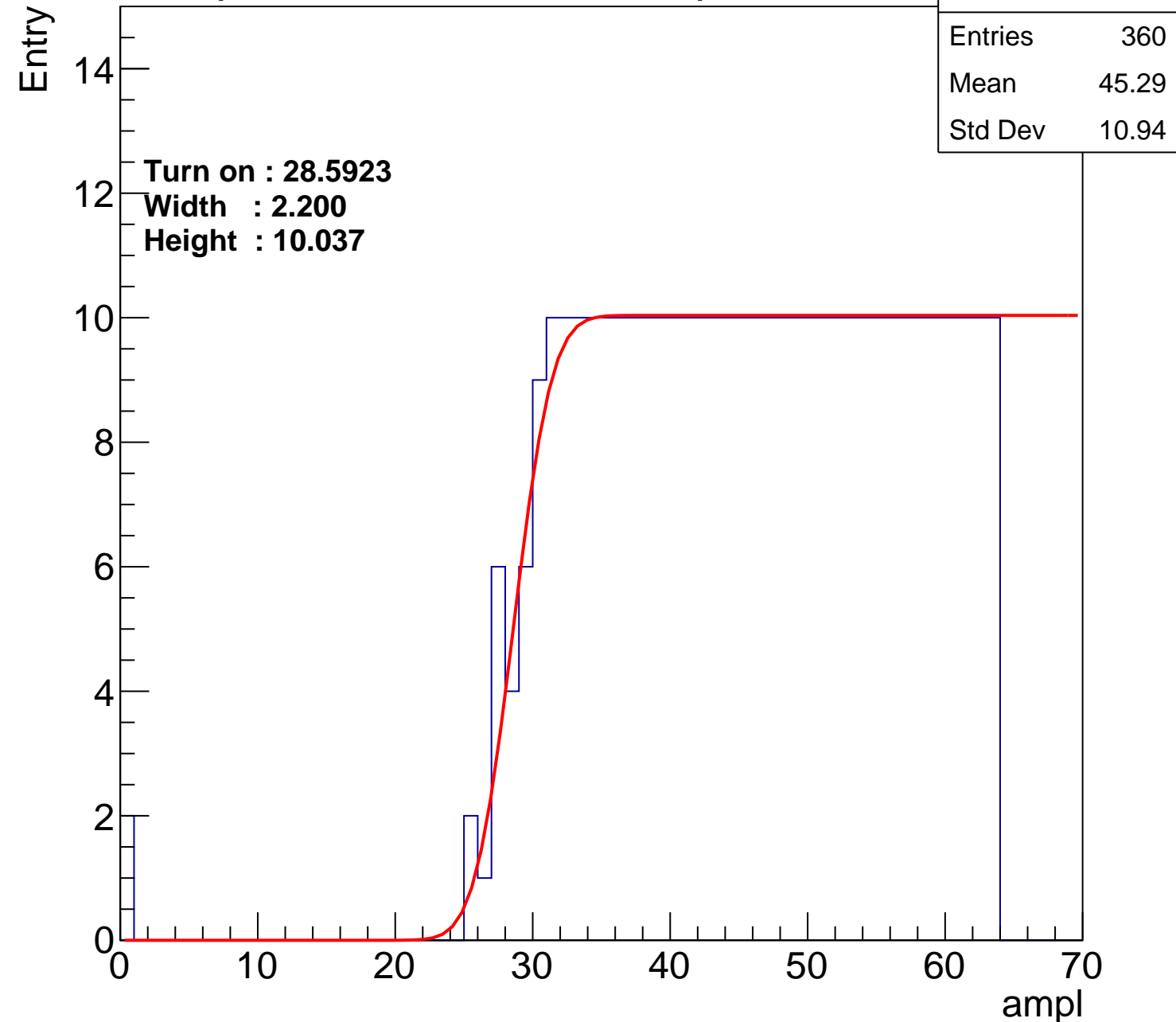
Width : 2.200

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.2593

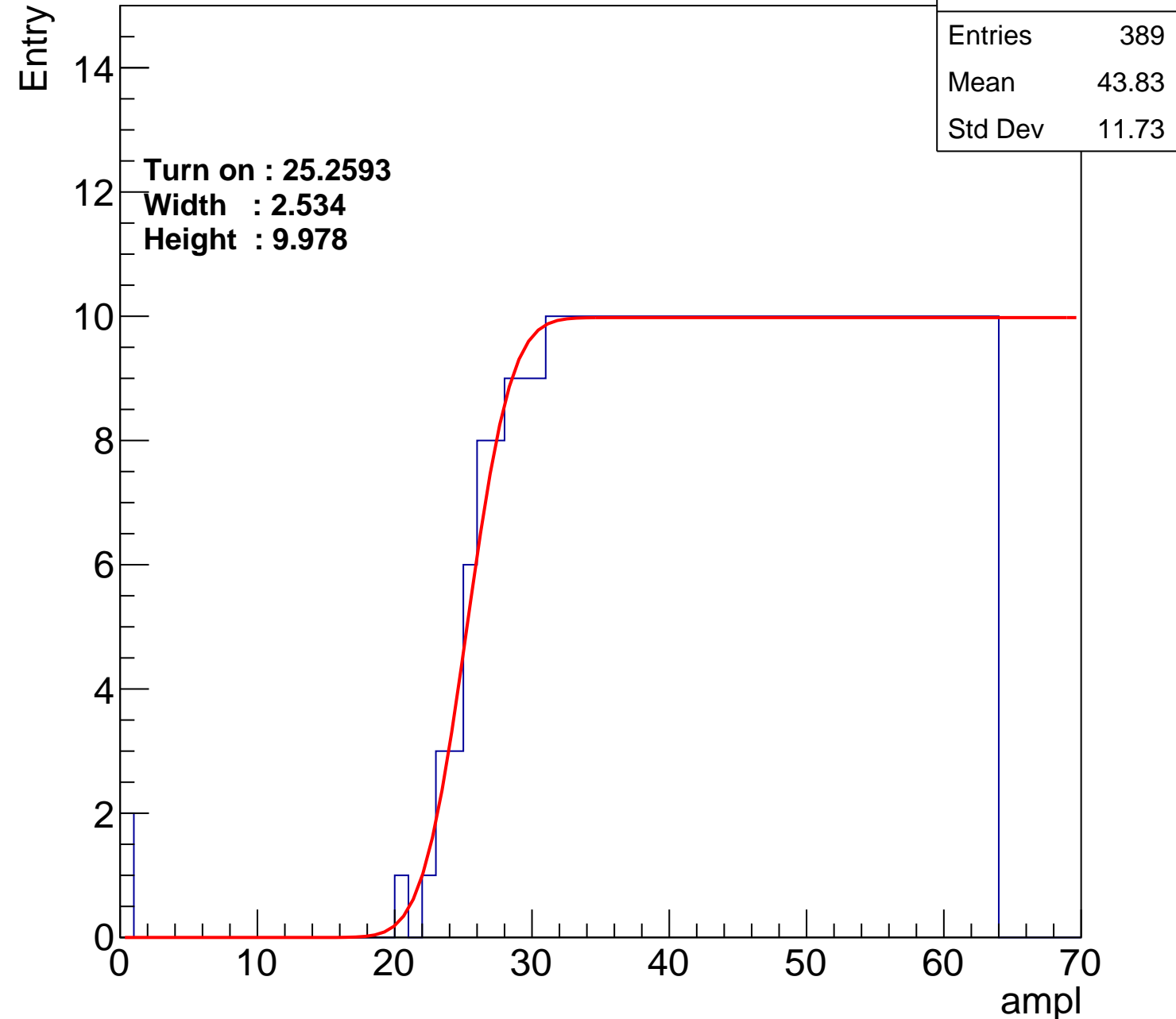
Width : 2.534

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch31

calib_packv5_042523_0143.root, FC#3, port B3

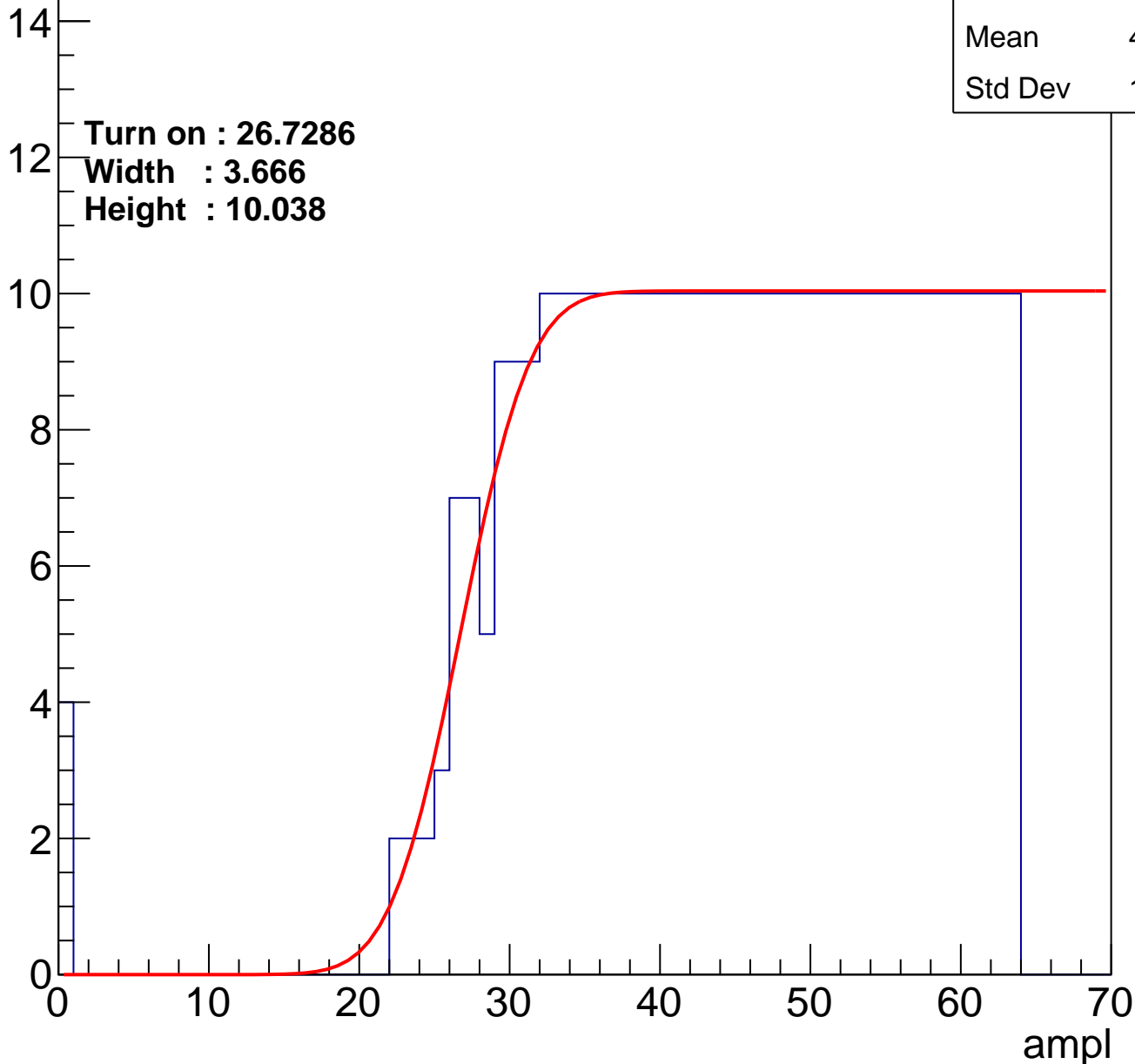
Entries	379
Mean	44.15
Std Dev	11.89

Turn on : 26.7286

Width : 3.666

Height : 10.038

Entry



B1L000S, U4-ch32

calib_packv5_042523_0143.root, FC#3, port B3

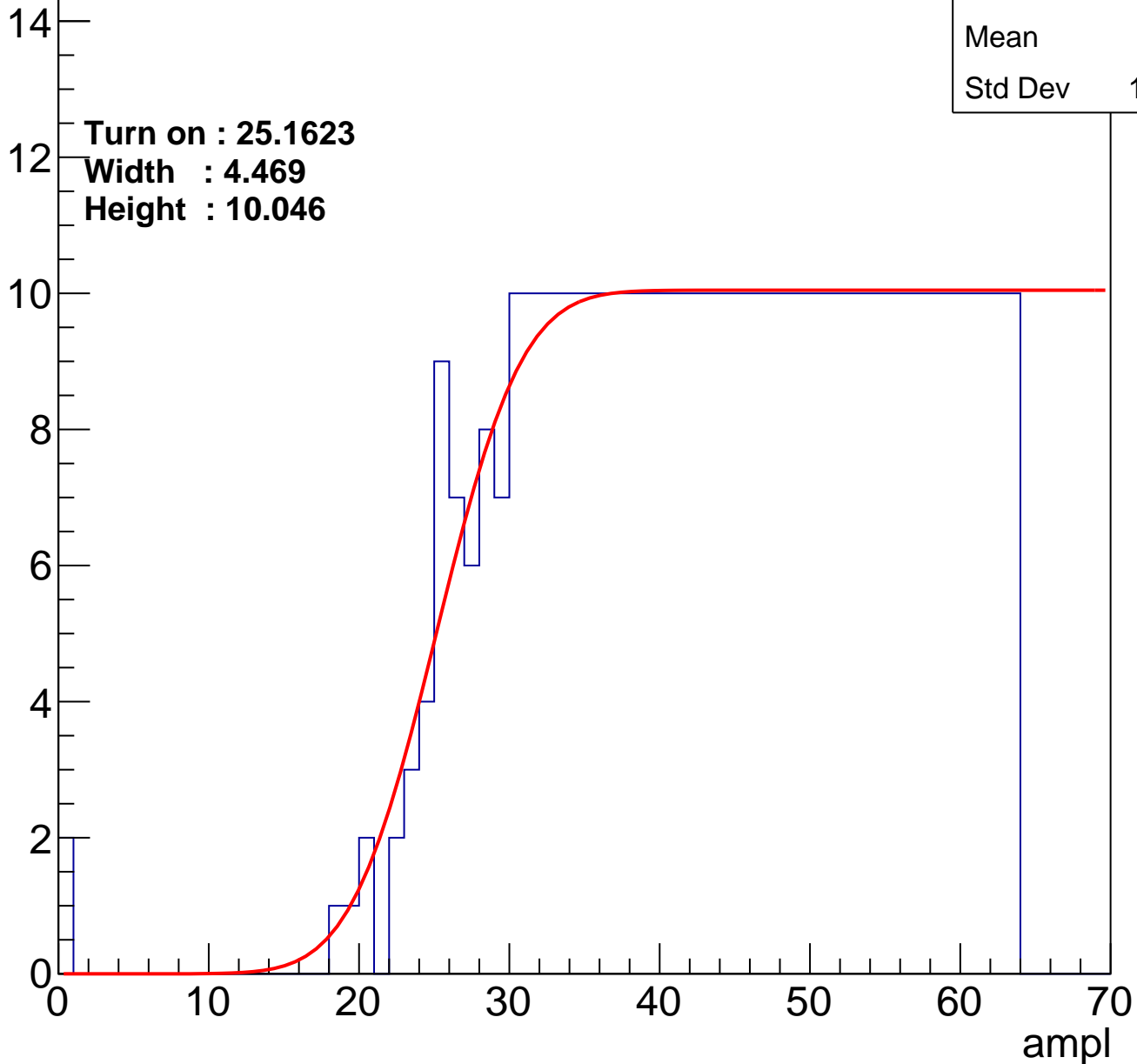
Entries	392
Mean	43.6
Std Dev	11.94

Turn on : 25.1623

Width : 4.469

Height : 10.046

Entry



B1L000S, U4-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.4139

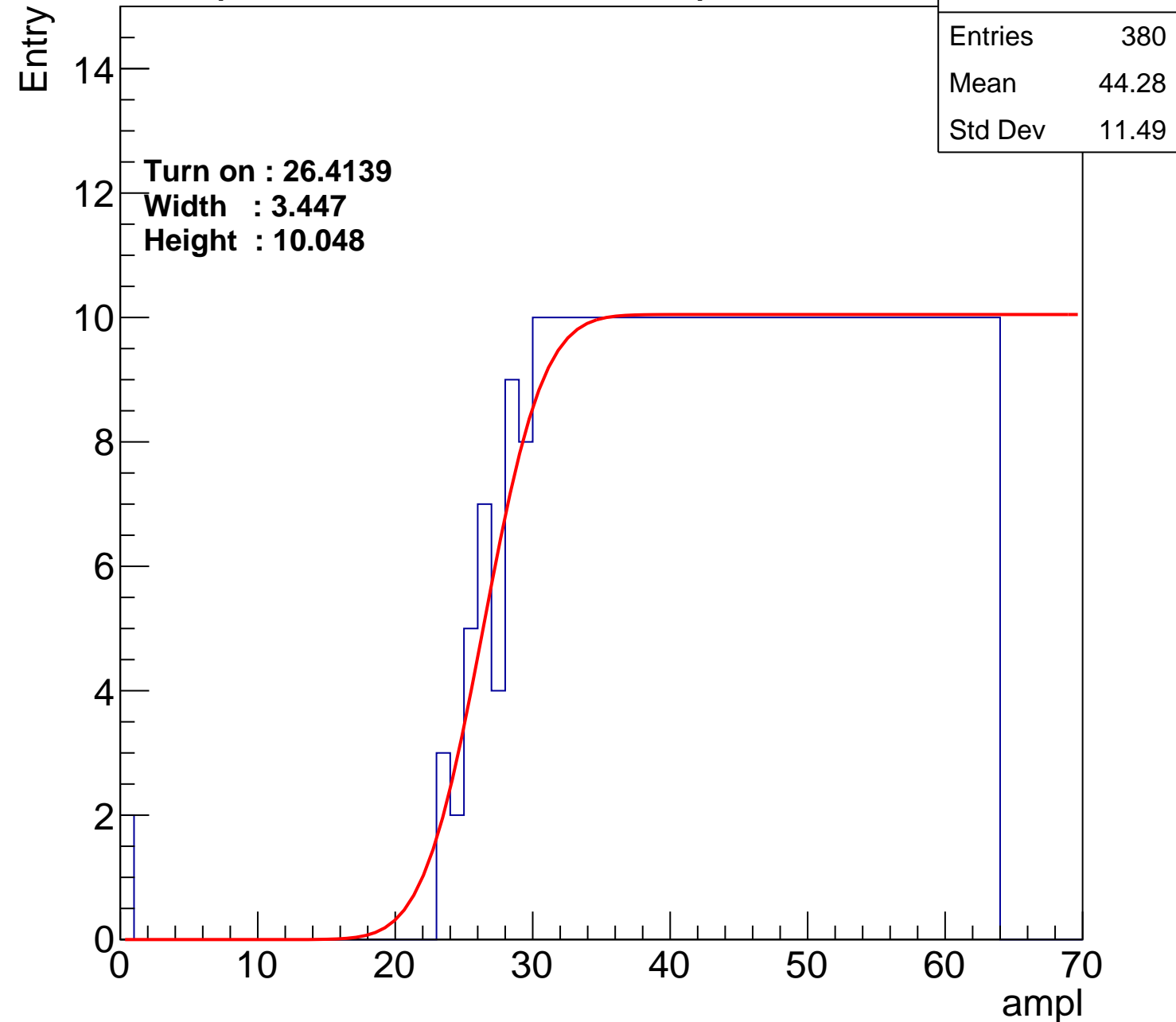
Width : 3.447

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.14
Std Dev	12.2

Turn on : 23.8109

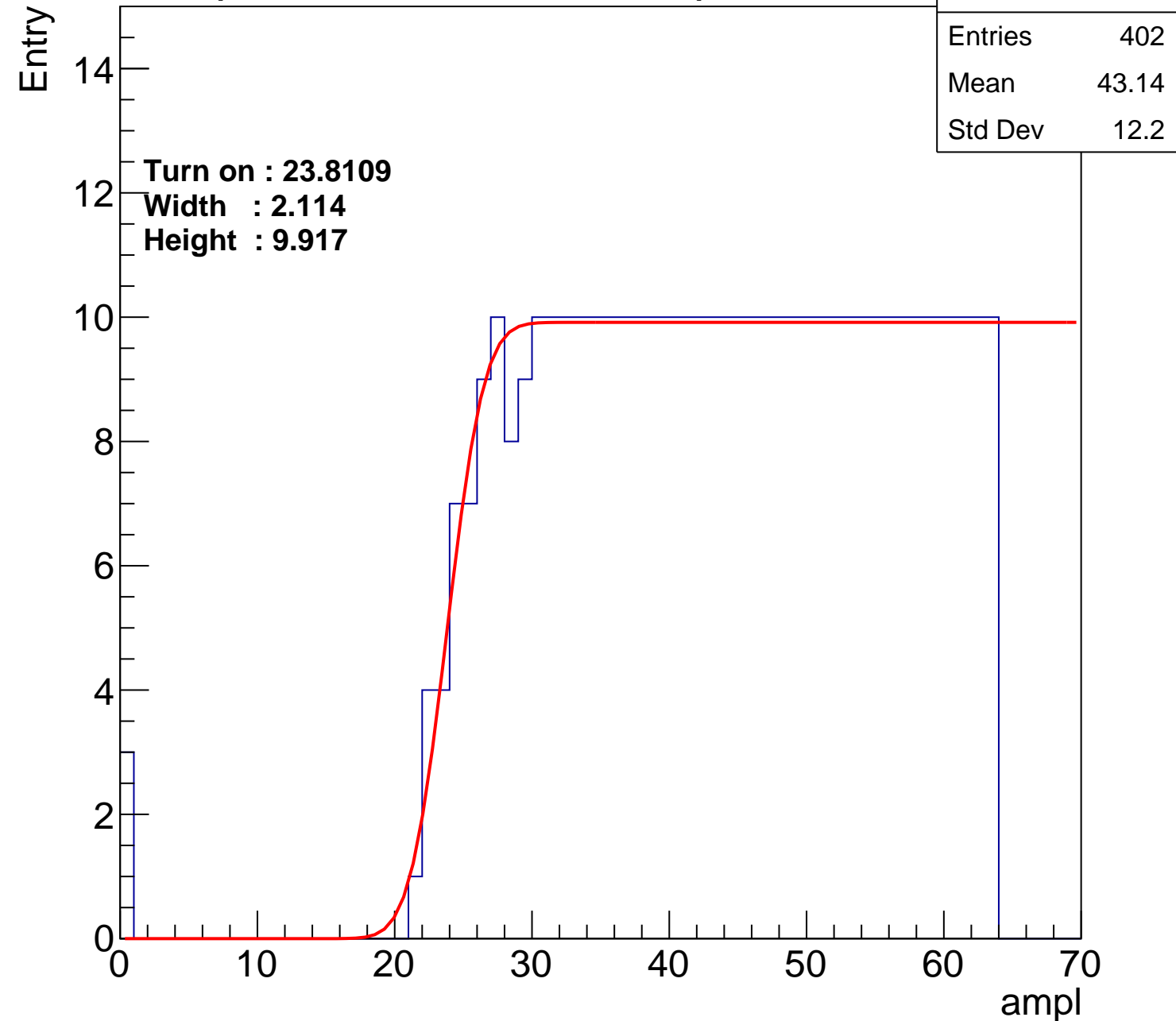
Width : 2.114

Height : 9.917

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.13
Std Dev	11.44

Turn on : 26.0367

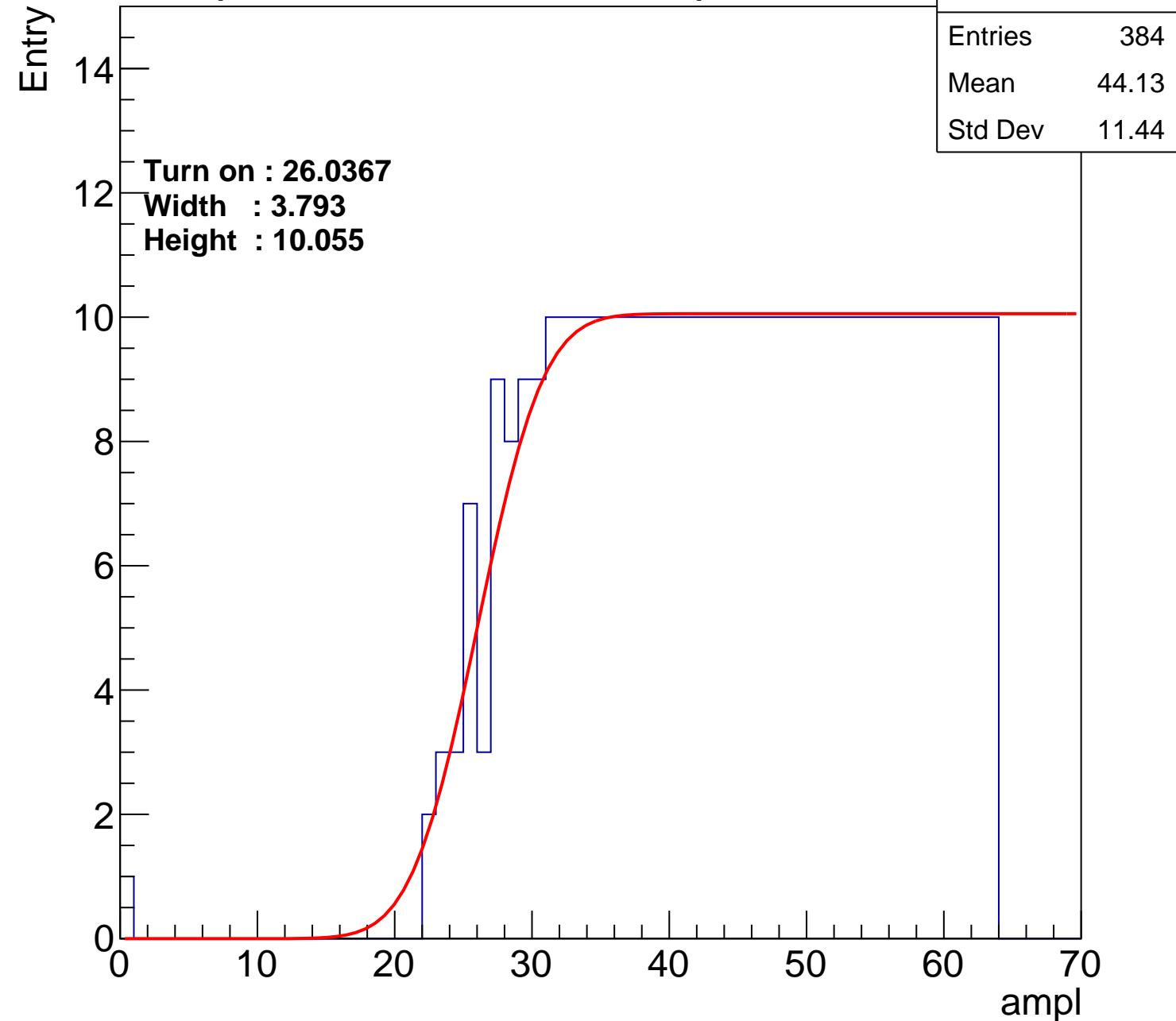
Width : 3.793

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch36

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.52
Std Dev	11.18

Turn on : 26.2614

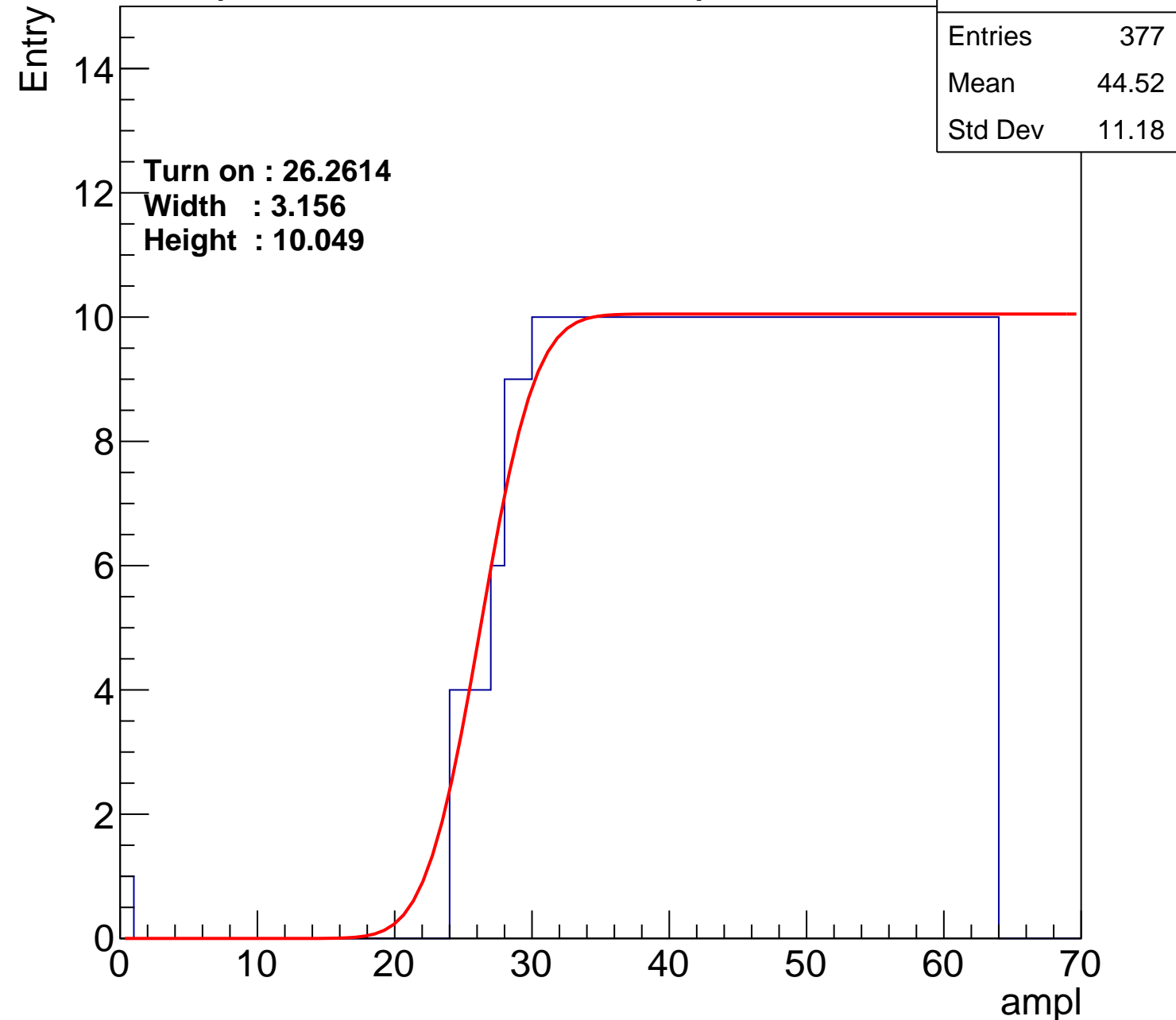
Width : 3.156

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch37

calib_packv5_042523_0143.root, FC#3, port B3

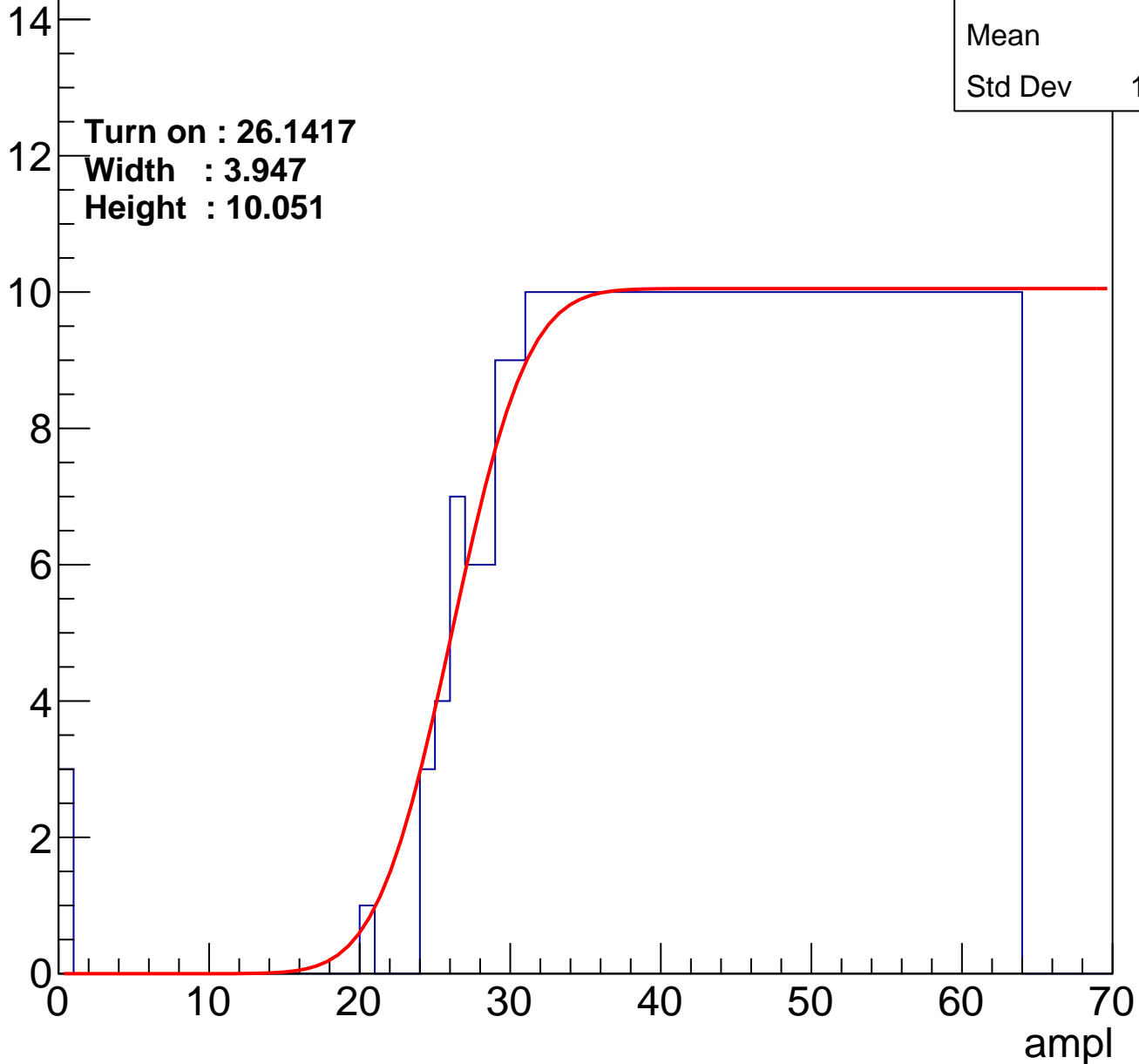
Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.1417

Width : 3.947

Height : 10.051

Entry



B1L000S, U4-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	44.05
Std Dev	11.46

Turn on : 25.5869

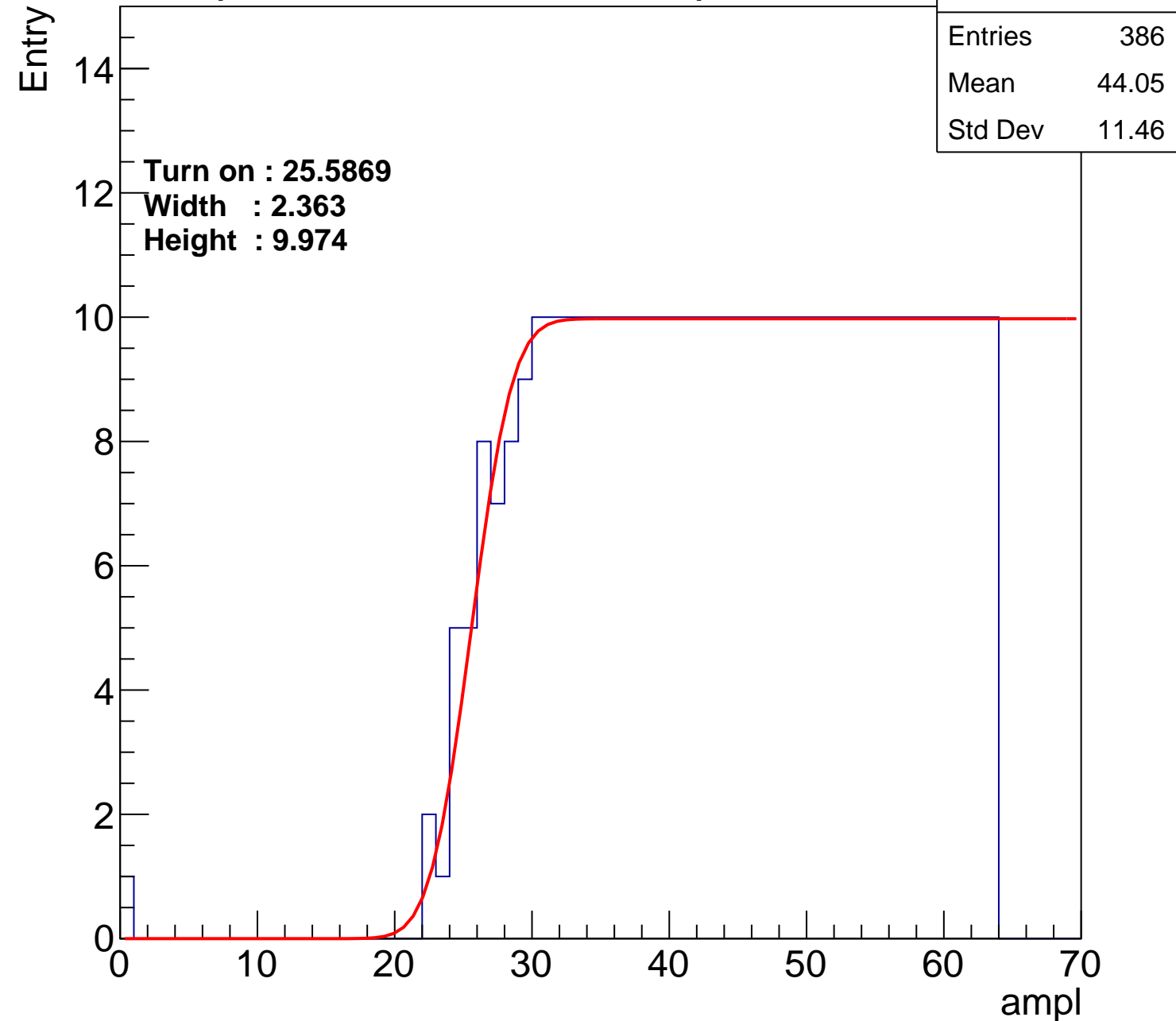
Width : 2.363

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.52
Std Dev	11.69

Turn on : 27.1916

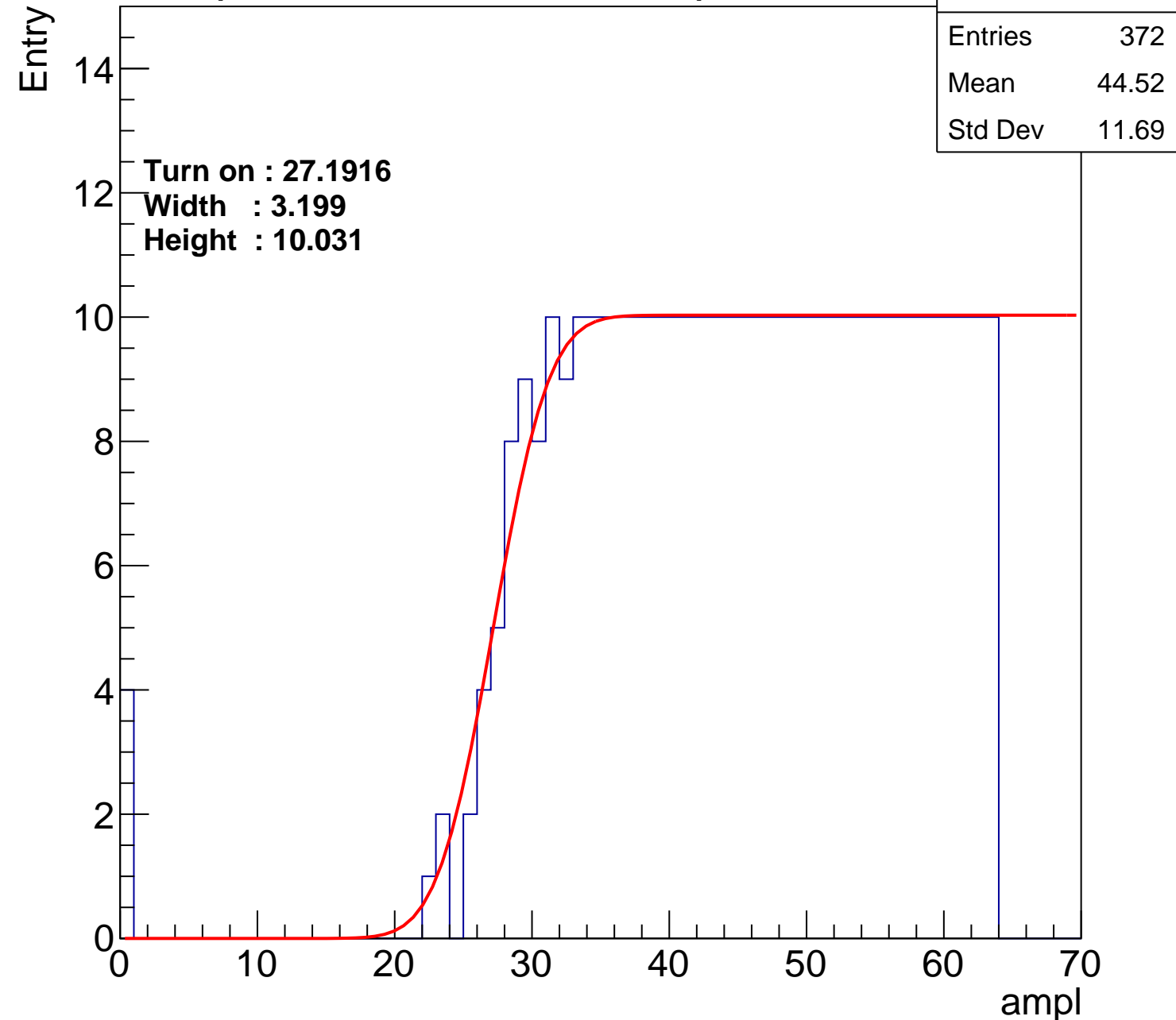
Width : 3.199

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.1521

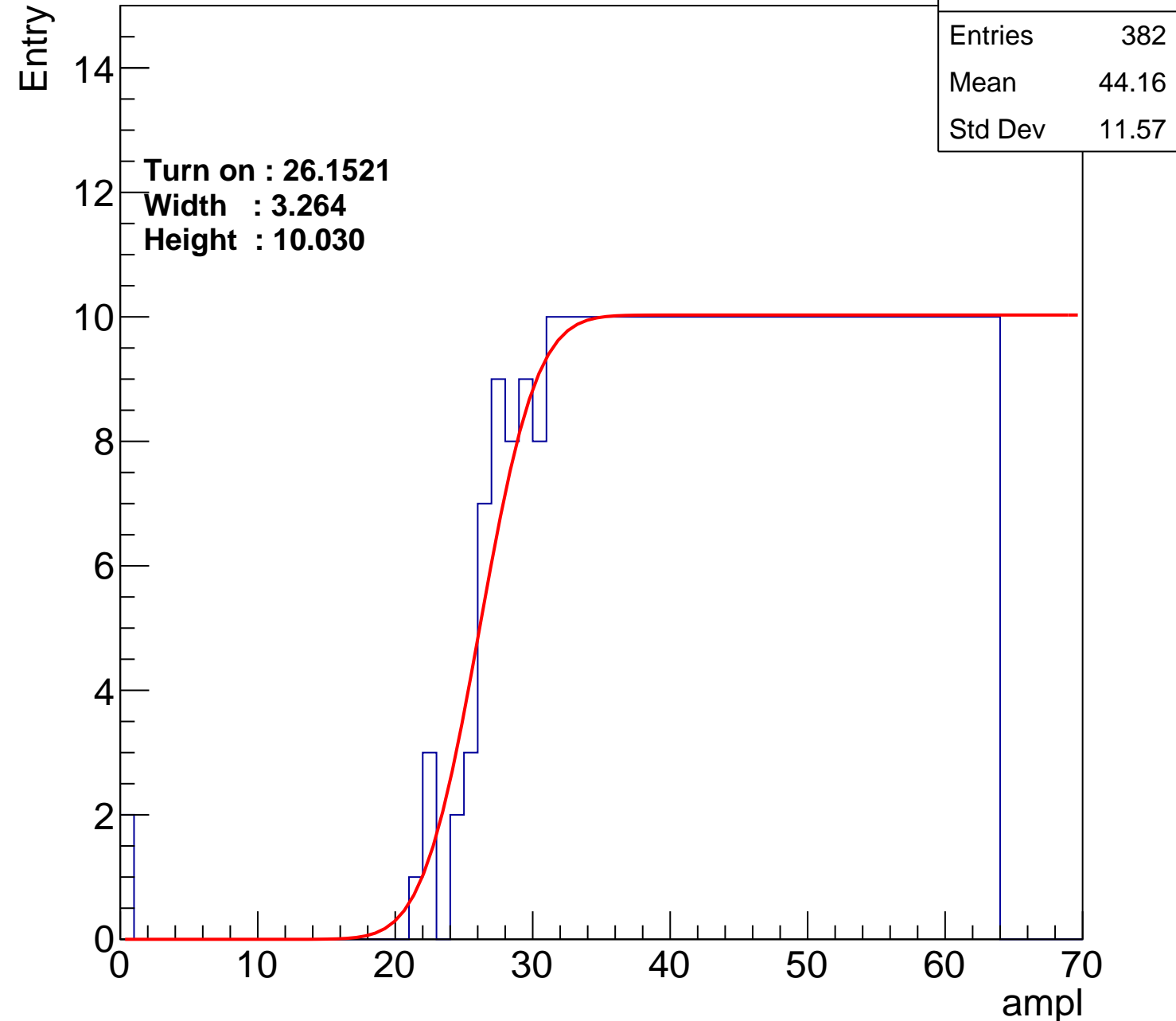
Width : 3.264

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.63
Std Dev	11.78

Turn on : 24.7307

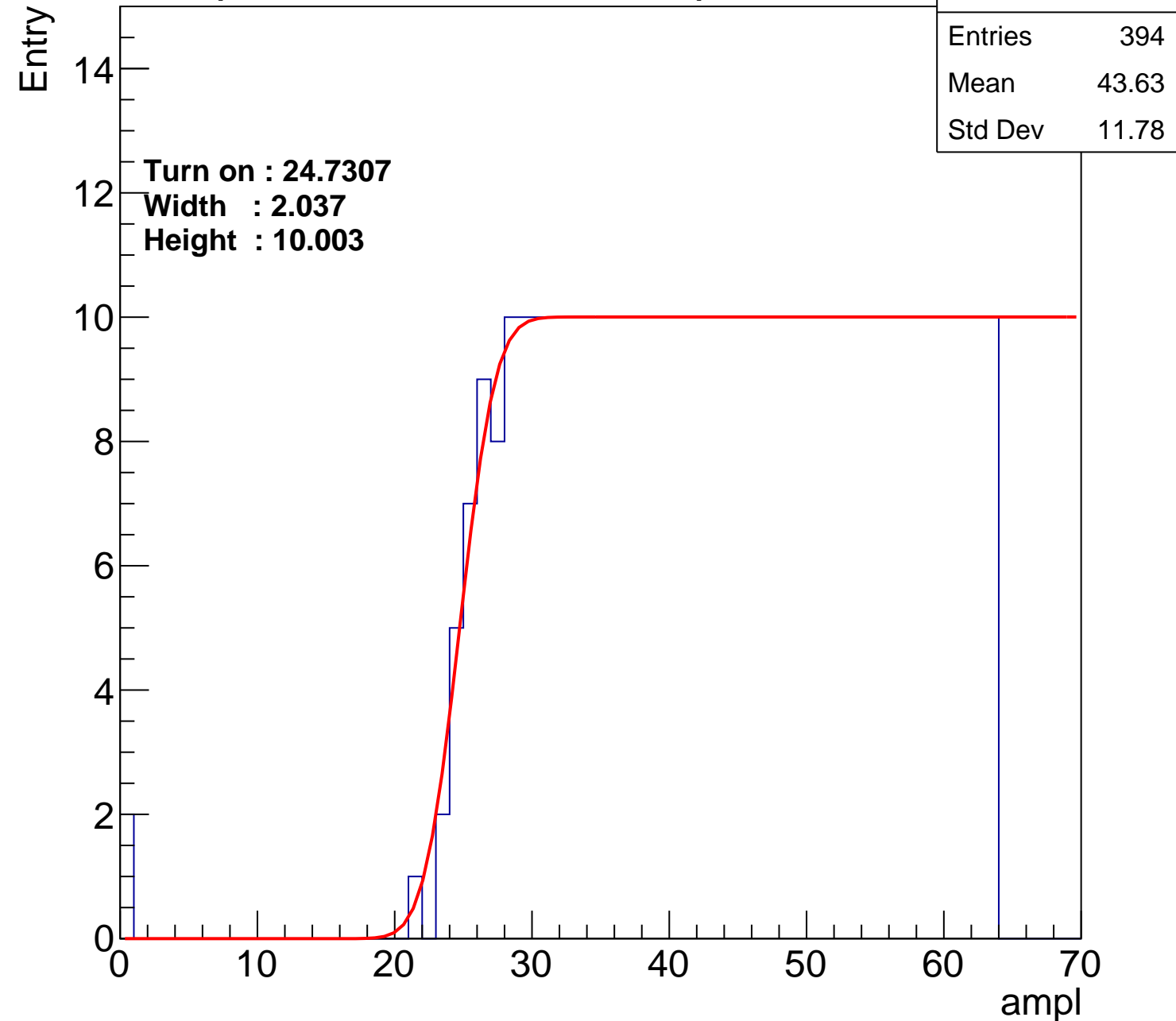
Width : 2.037

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.44
Std Dev	11.93

Turn on : 24.3827

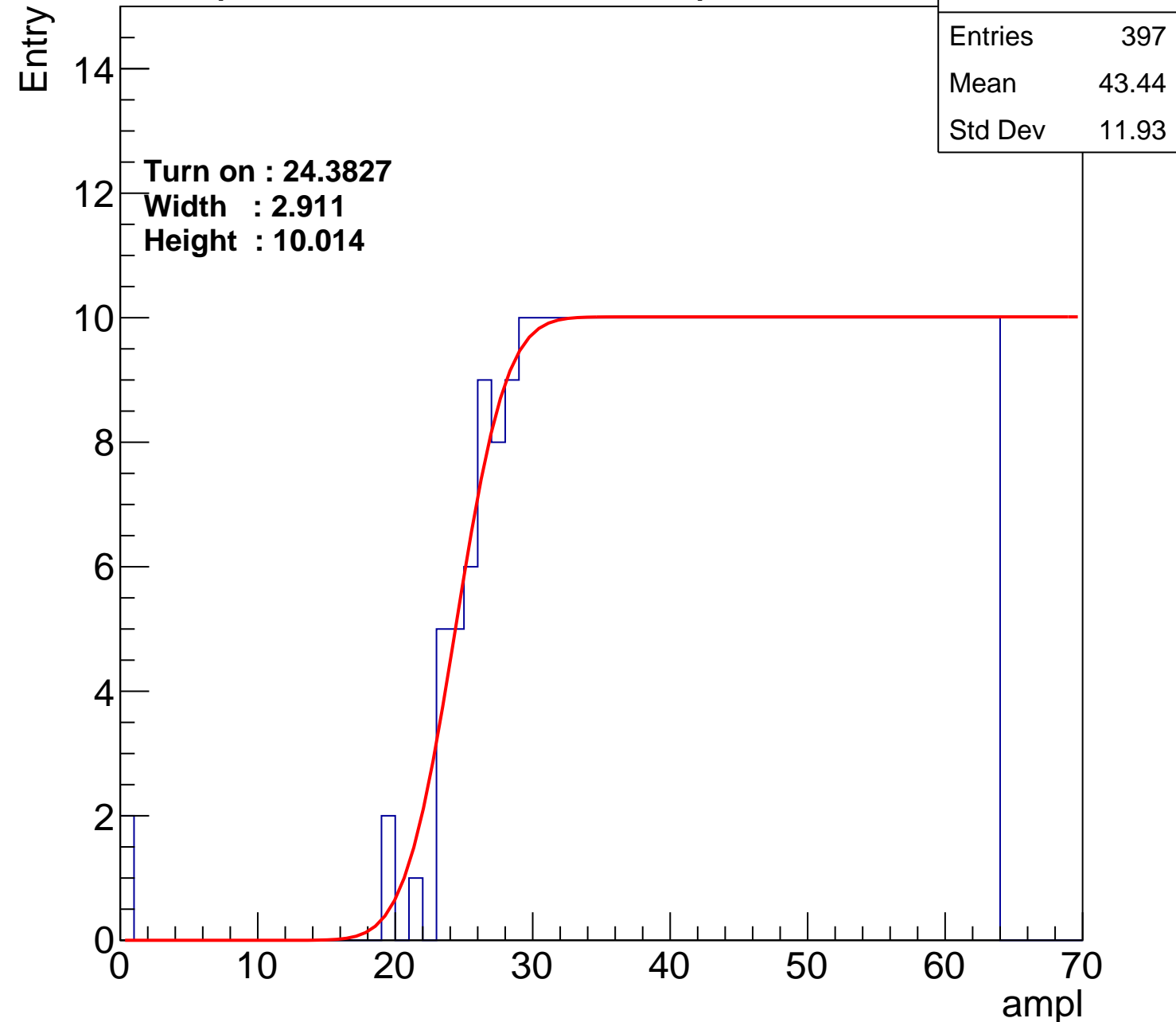
Width : 2.911

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.72
Std Dev	11.8

Turn on : 25.1677

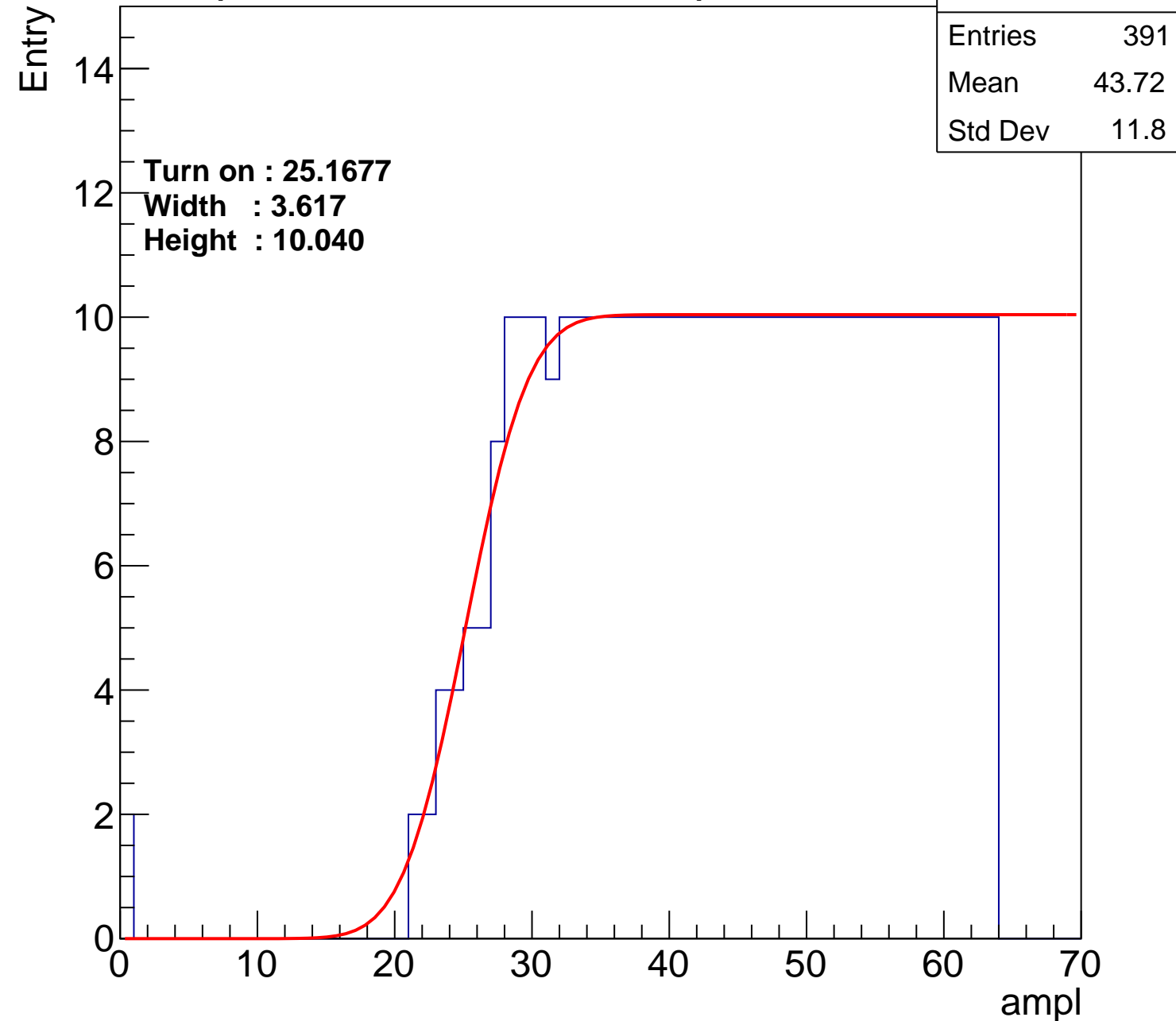
Width : 3.617

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch44

calib_packv5_042523_0143.root, FC#3, port B3

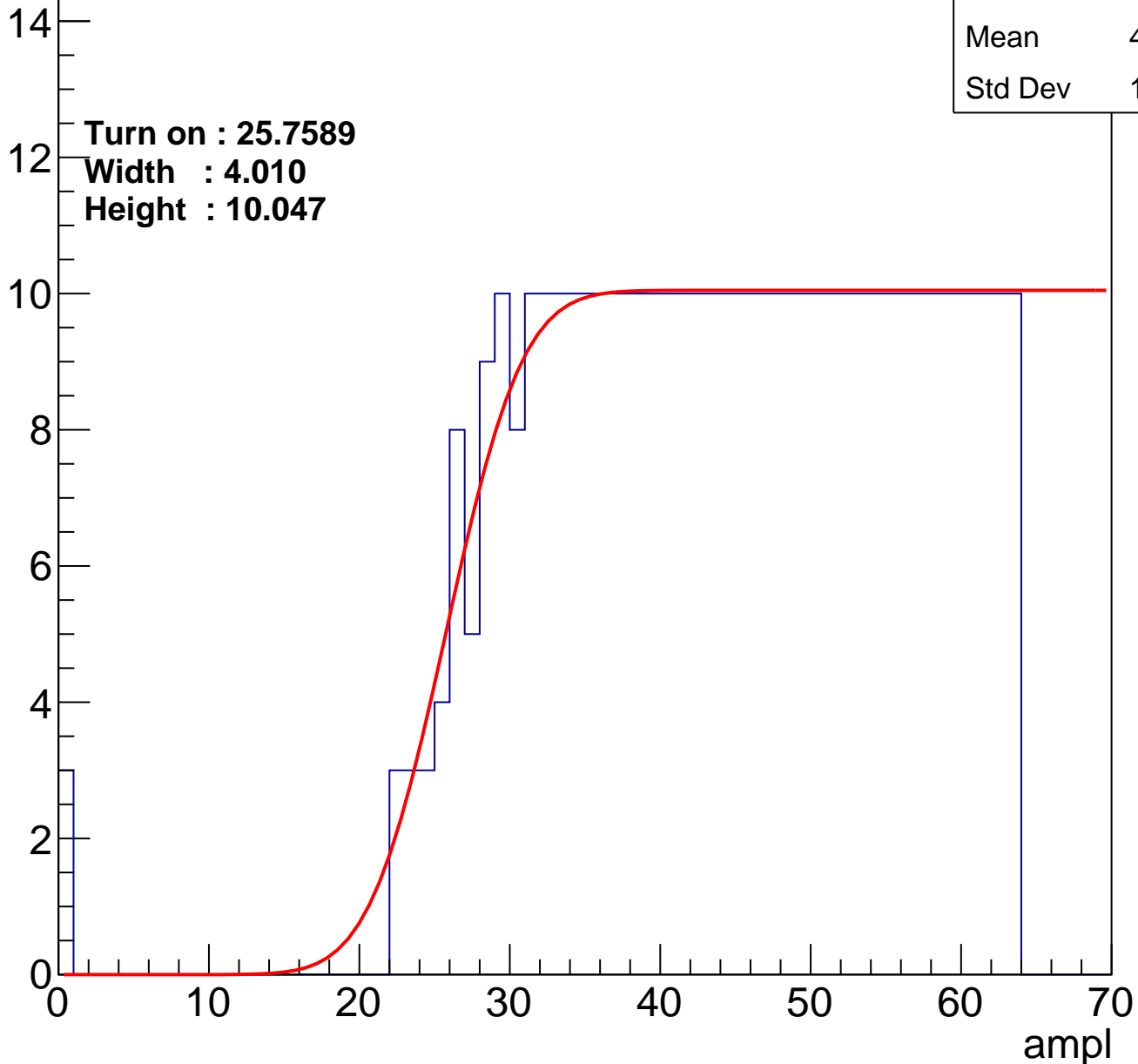
Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.7589

Width : 4.010

Height : 10.047

Entry



B1L000S, U4-ch45

calib_packv5_042523_0143.root, FC#3, port B3

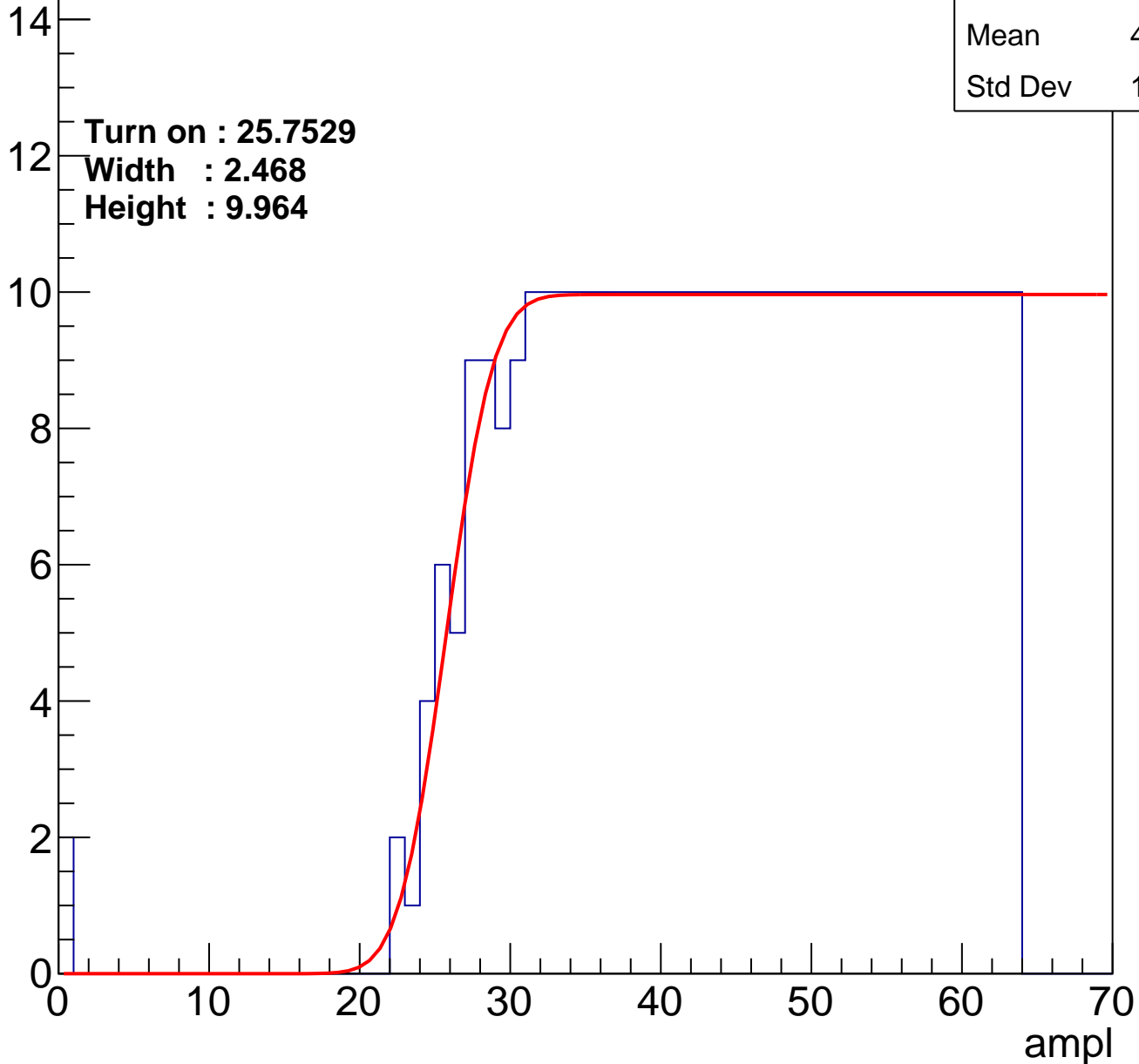
Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.7529

Width : 2.468

Height : 9.964

Entry



B1L000S, U4-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.34
Std Dev	11.75

Turn on : 26.9182

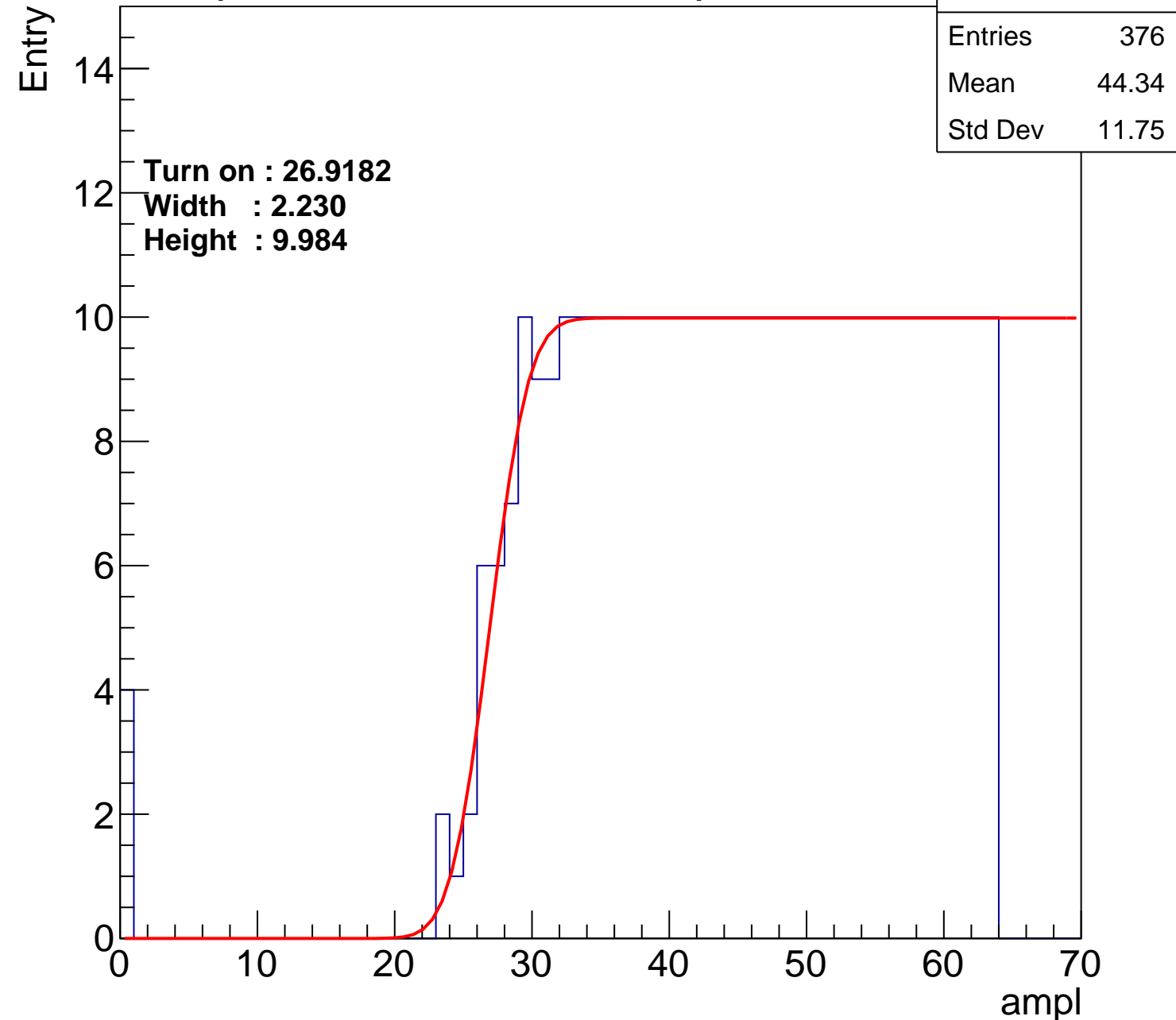
Width : 2.230

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch47

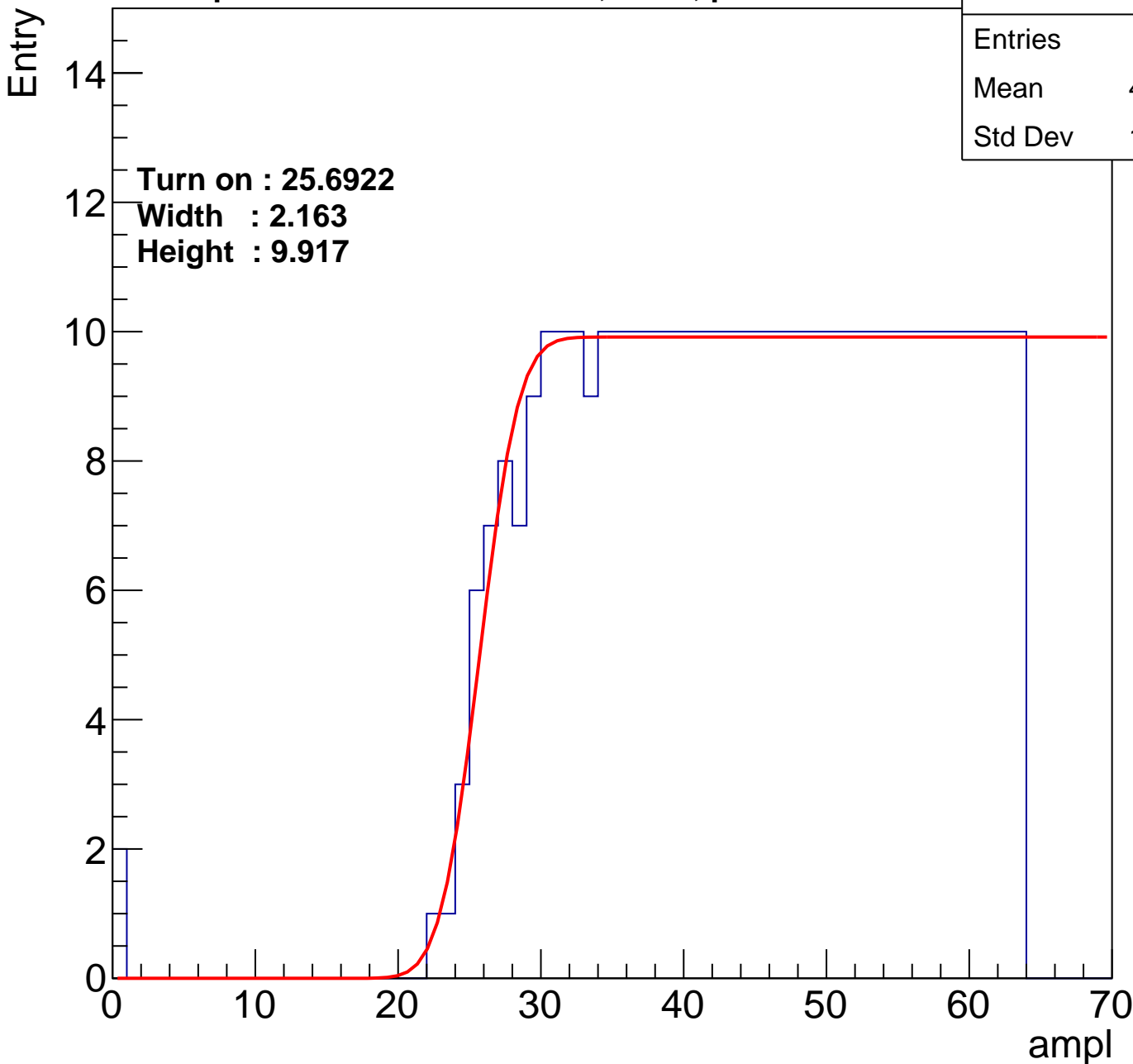
calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.12
Std Dev	11.57

Turn on : 25.6922

Width : 2.163

Height : 9.917



B1L000S, U4-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.91
Std Dev	11.72

Turn on : 25.6390

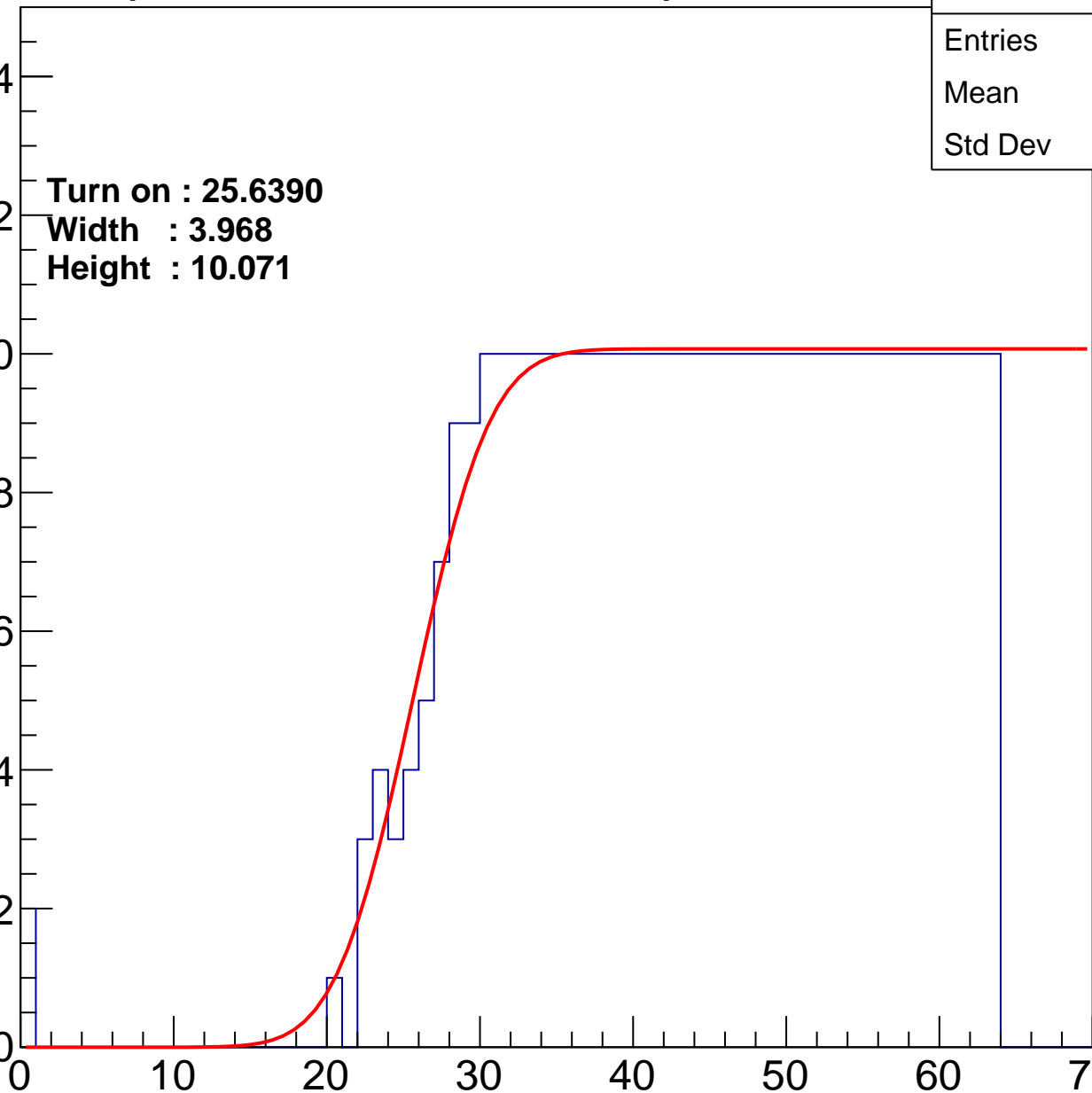
Width : 3.968

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.37
Std Dev	12.21

Turn on : 24.5768

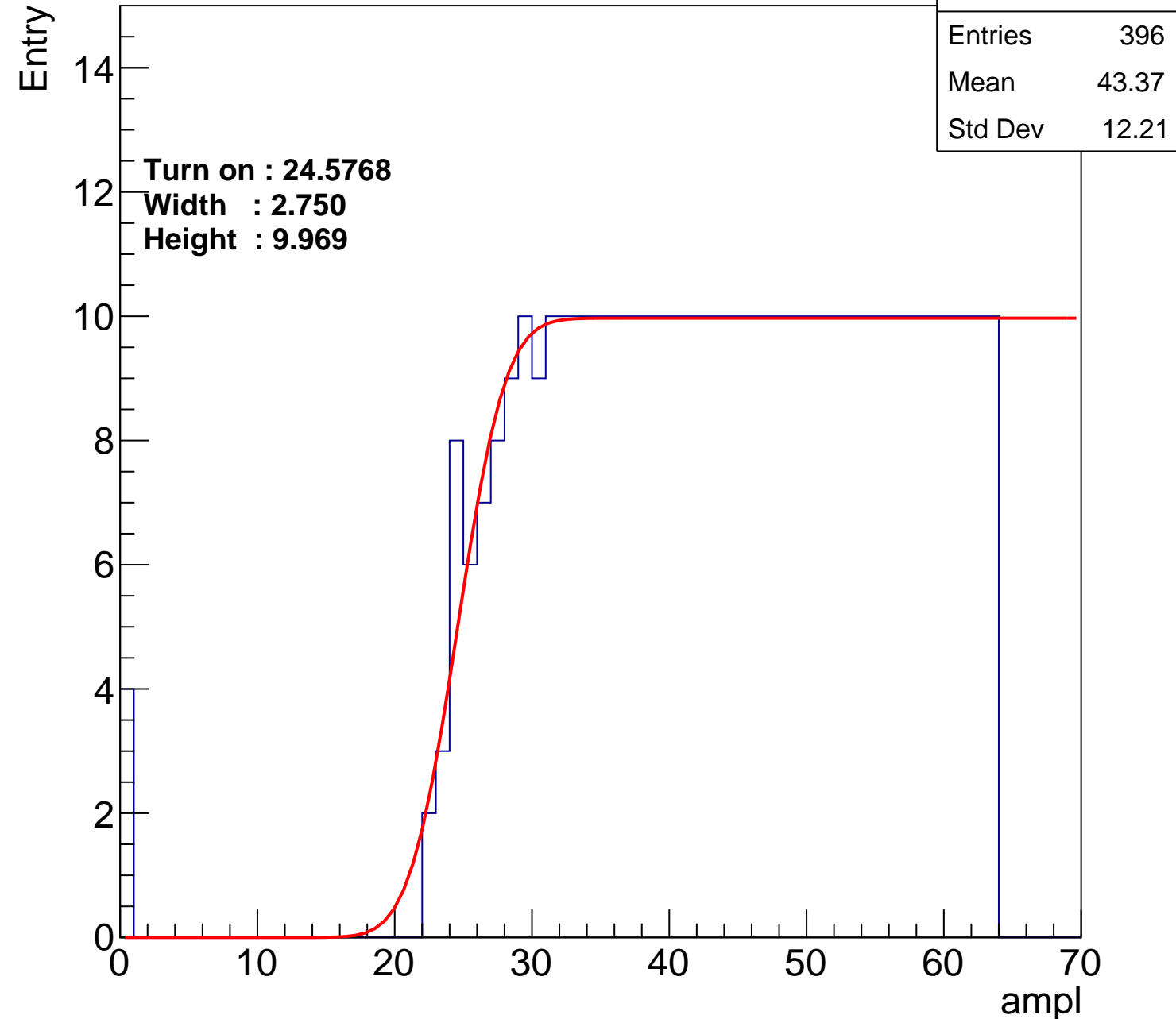
Width : 2.750

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch50

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.04
Std Dev	12.25

Turn on : 24.0414

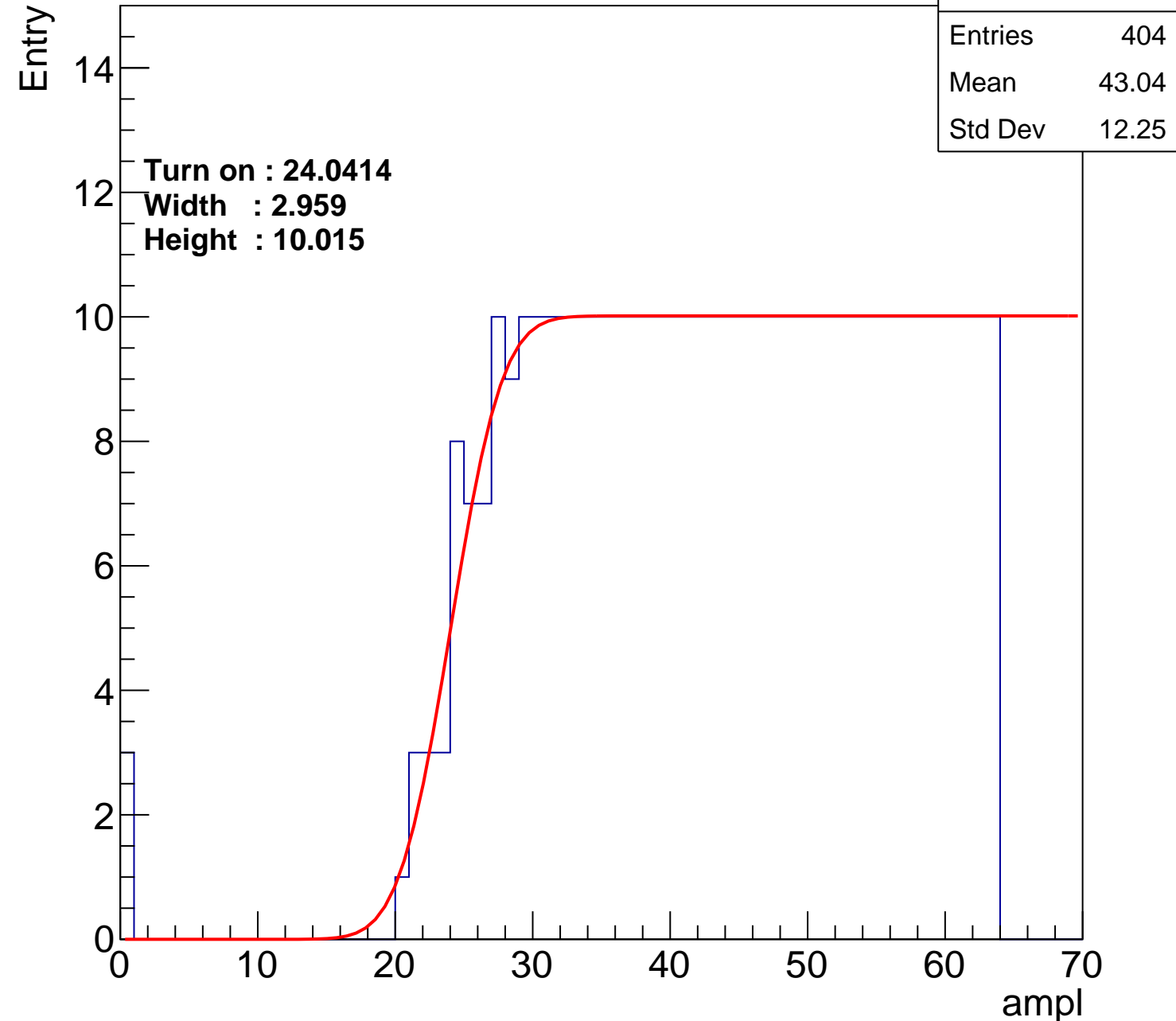
Width : 2.959

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.08
Std Dev	11.47

Turn on : 25.3986

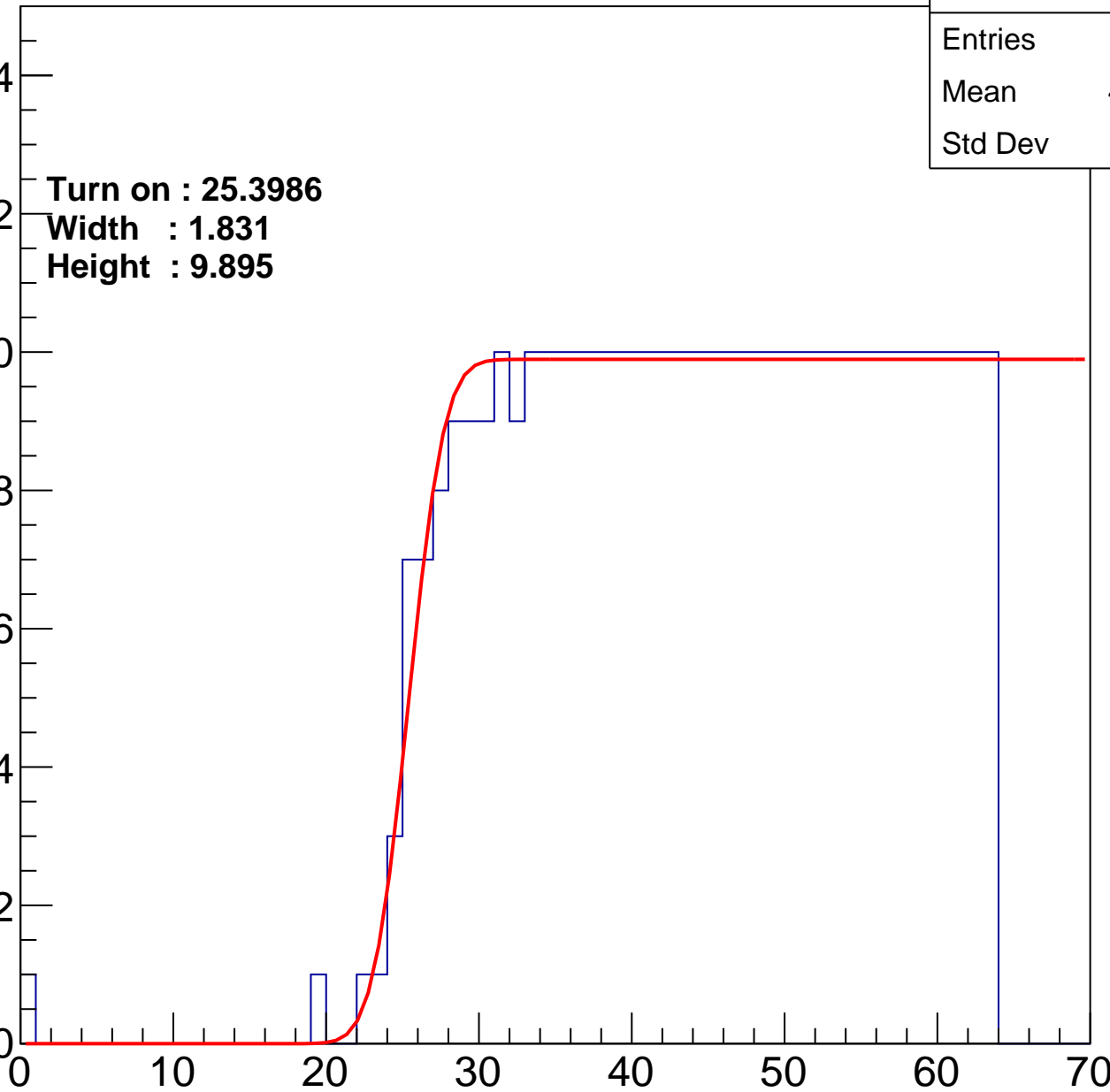
Width : 1.831

Height : 9.895

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.92
Std Dev	11.81

Turn on : 25.4913

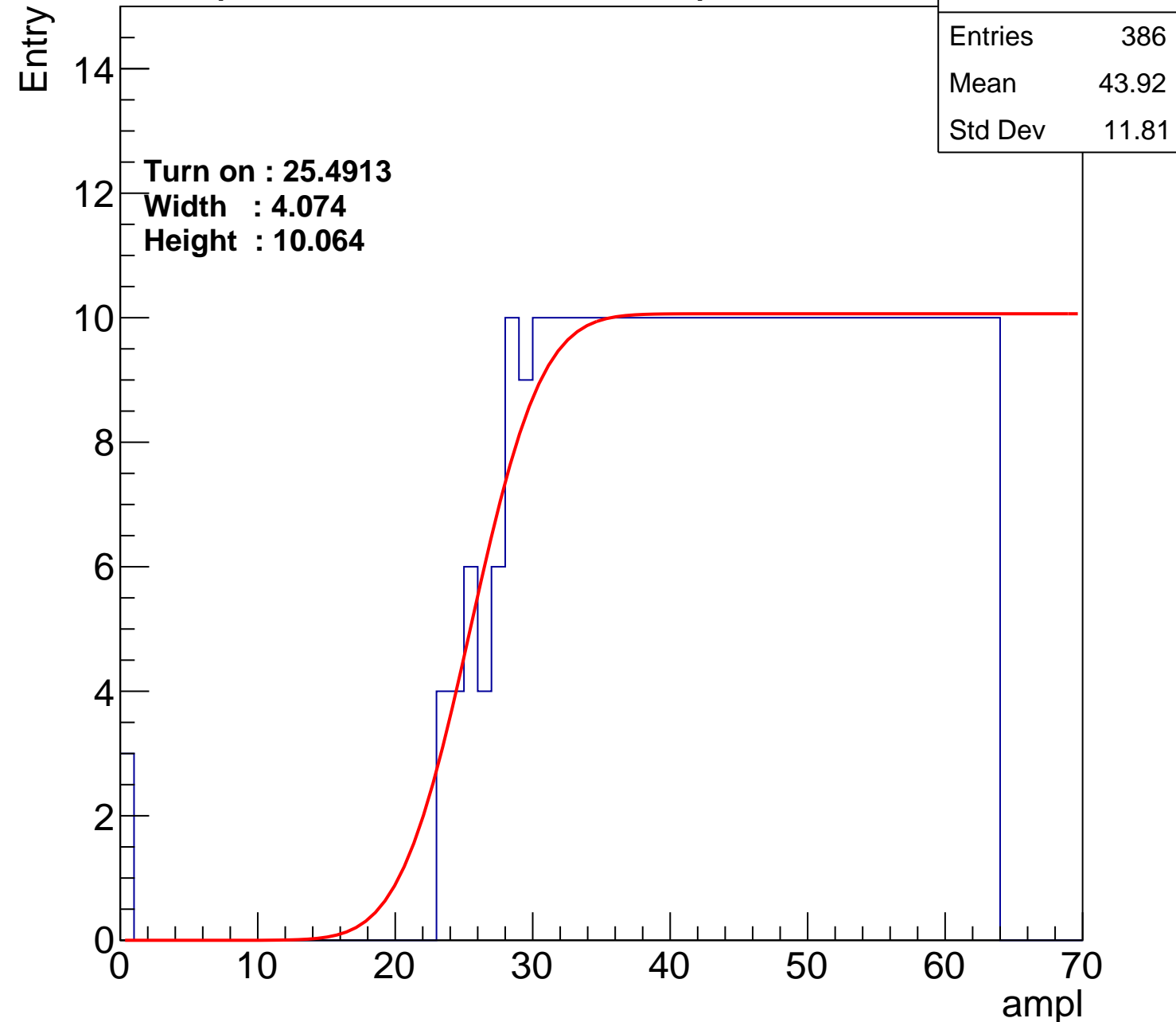
Width : 4.074

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.17
Std Dev	11.68

Turn on : 26.2622

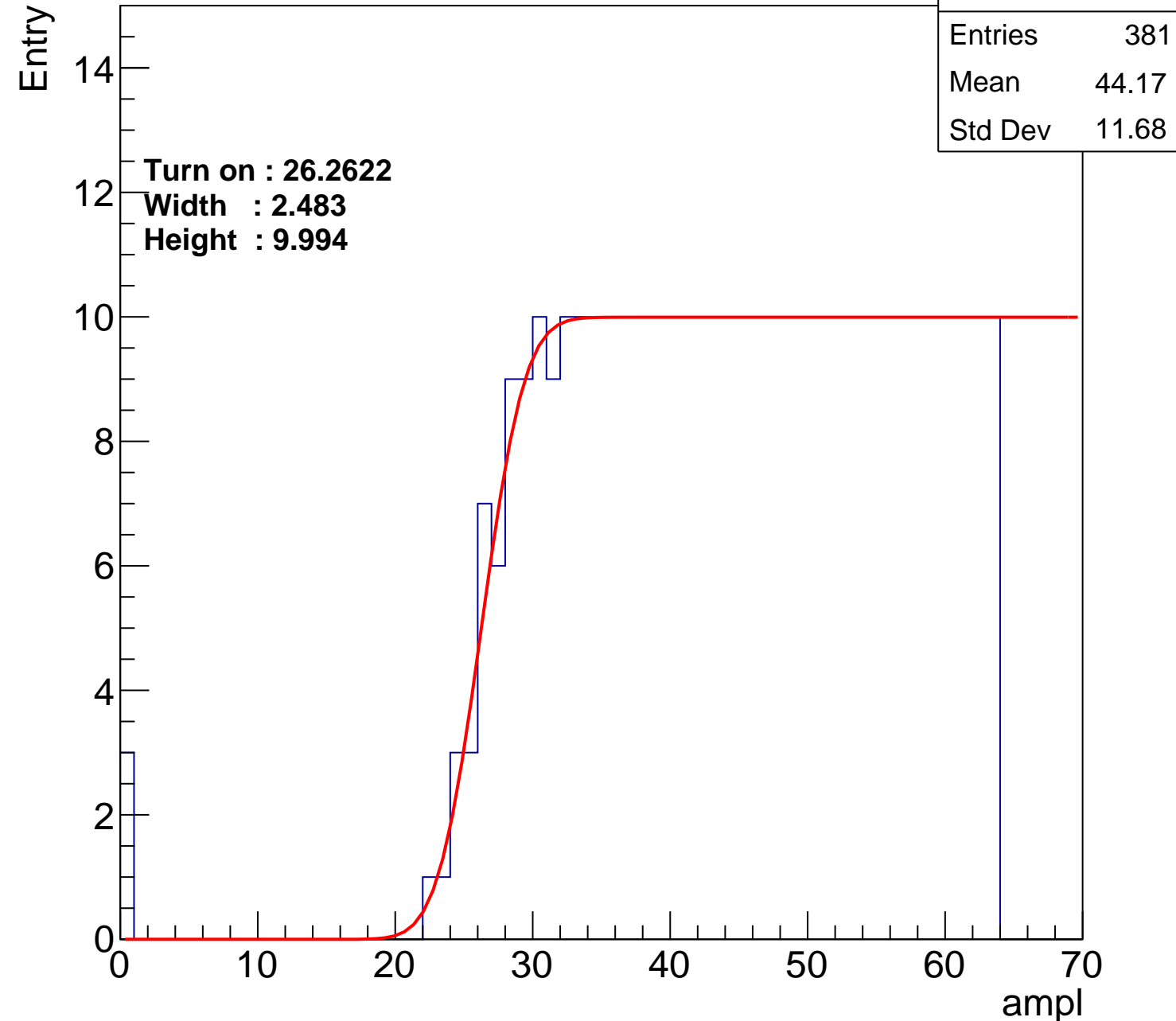
Width : 2.483

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch54

calib_packv5_042523_0143.root, FC#3, port B3

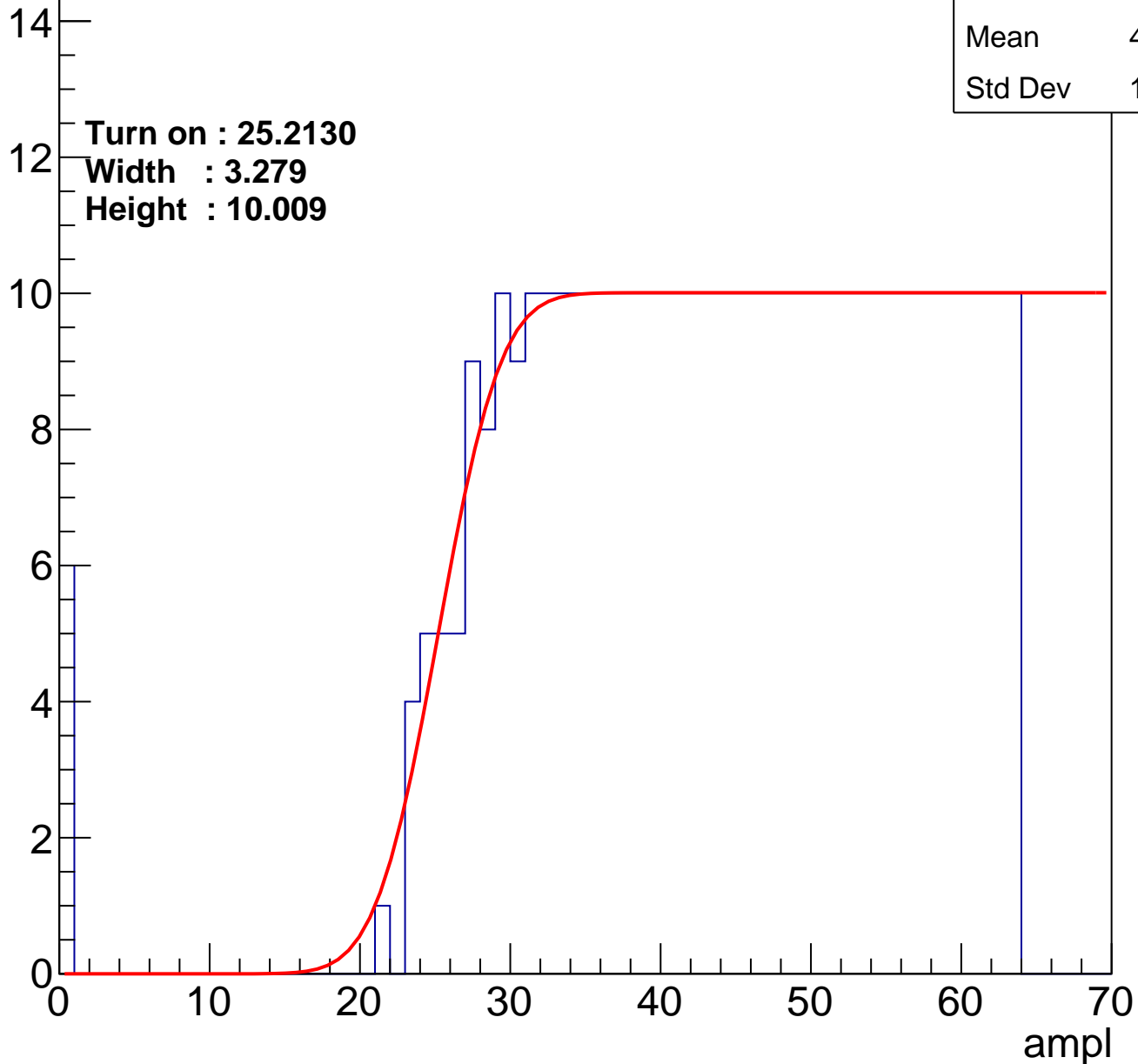
Entries	392
Mean	43.43
Std Dev	12.45

Turn on : 25.2130

Width : 3.279

Height : 10.009

Entry



B1L000S, U4-ch55

calib_packv5_042523_0143.root, FC#3, port B3

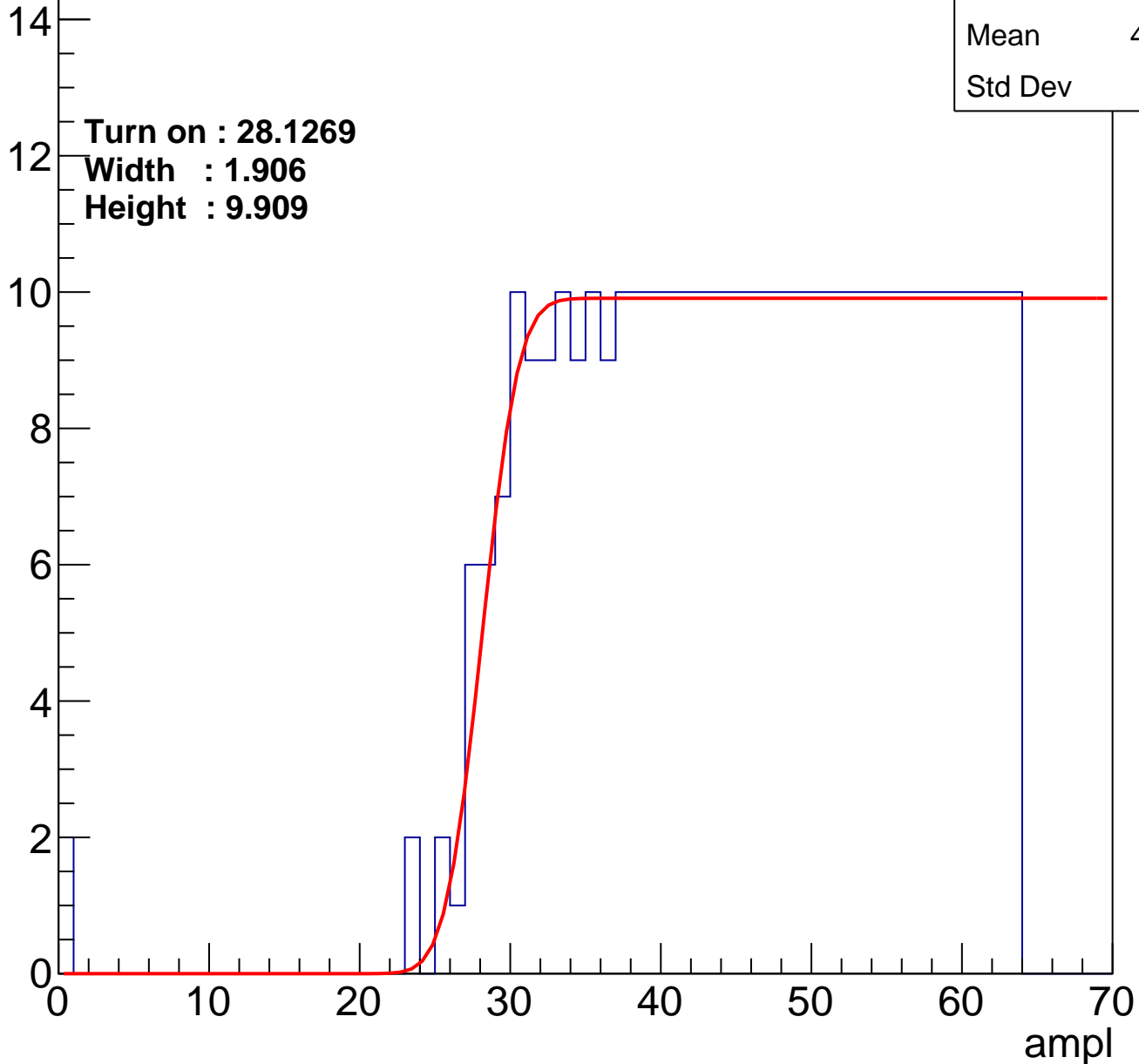
Entries	362
Mean	45.12
Std Dev	11.1

Turn on : 28.1269

Width : 1.906

Height : 9.909

Entry



B1L000S, U4-ch56

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.16
Std Dev	12.06

Turn on : 24.2088

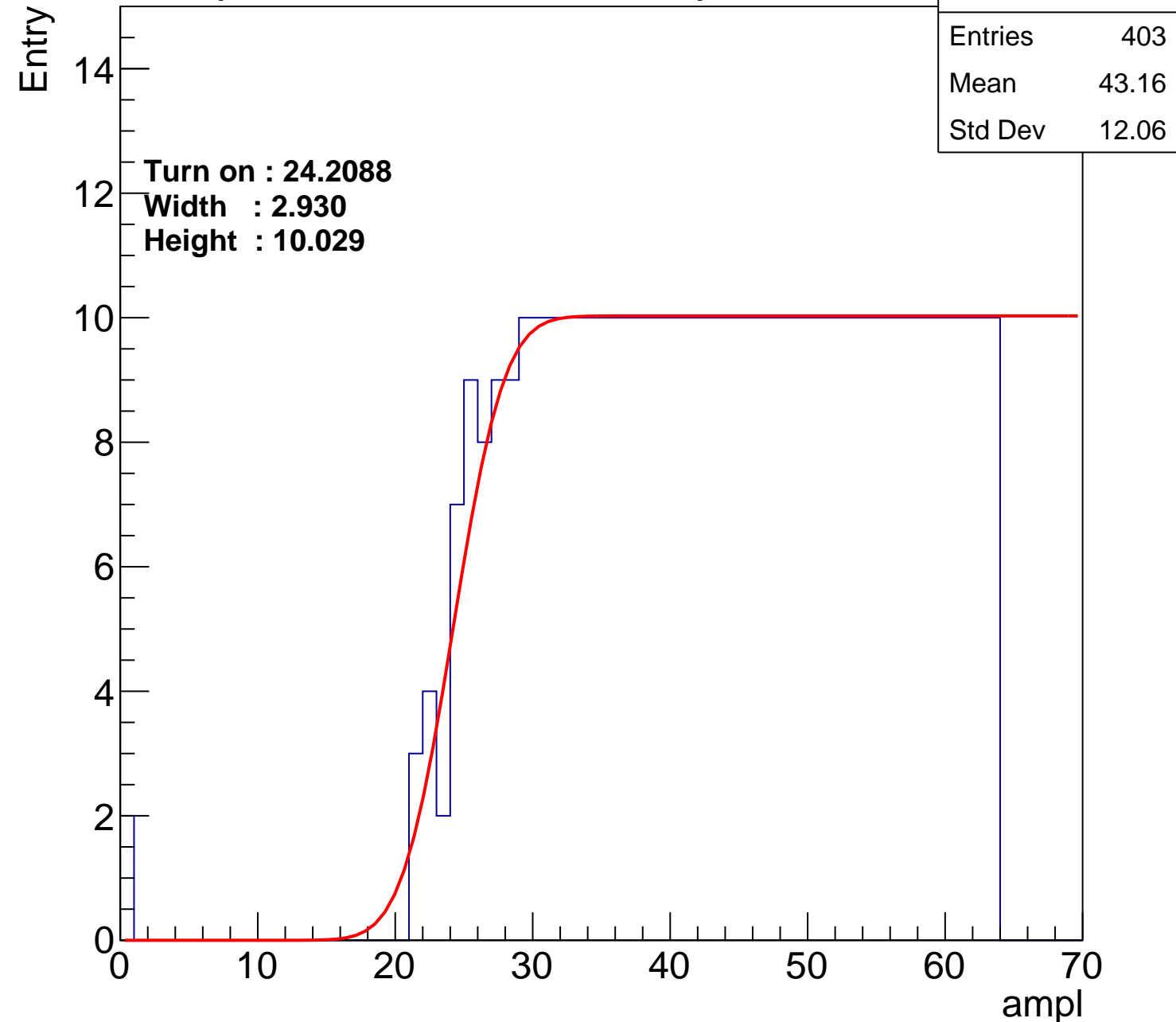
Width : 2.930

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.93
Std Dev	11.14

Turn on : 27.4615

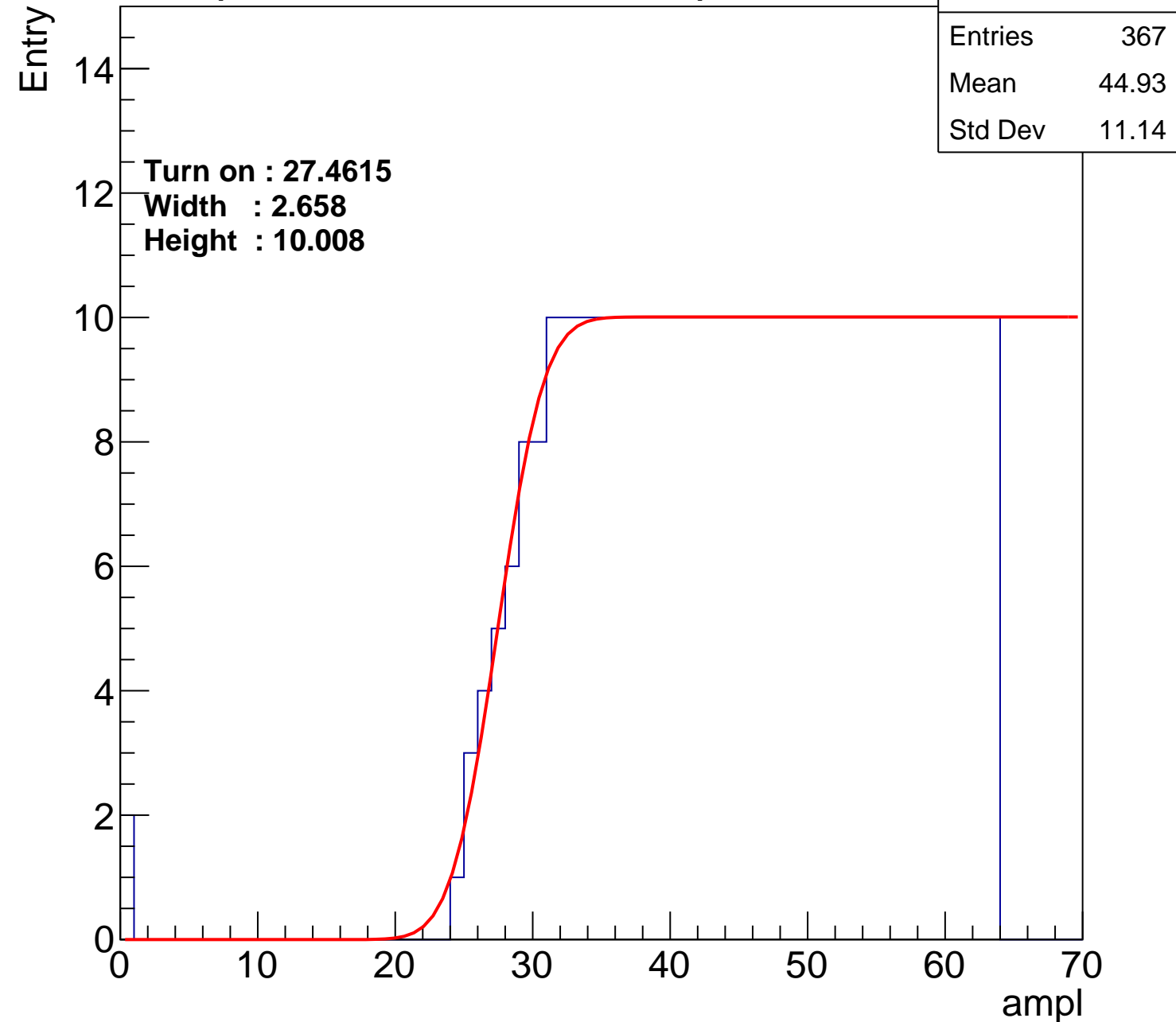
Width : 2.658

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch58

calib_packv5_042523_0143.root, FC#3, port B3

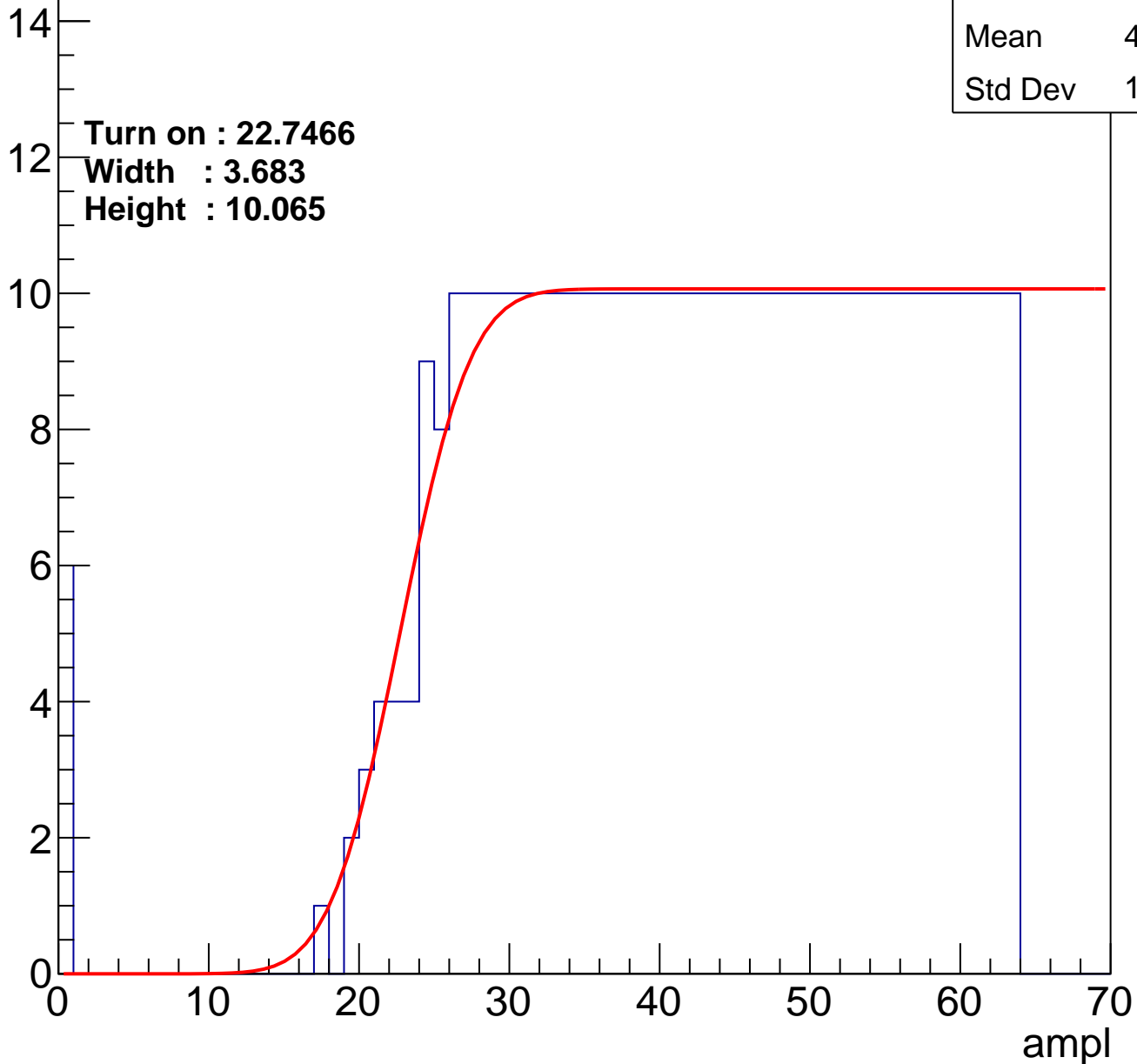
Entries	421
Mean	42.05
Std Dev	13.06

Turn on : 22.7466

Width : 3.683

Height : 10.065

Entry



B1L000S, U4-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.69
Std Dev	11.96

Turn on : 25.3696

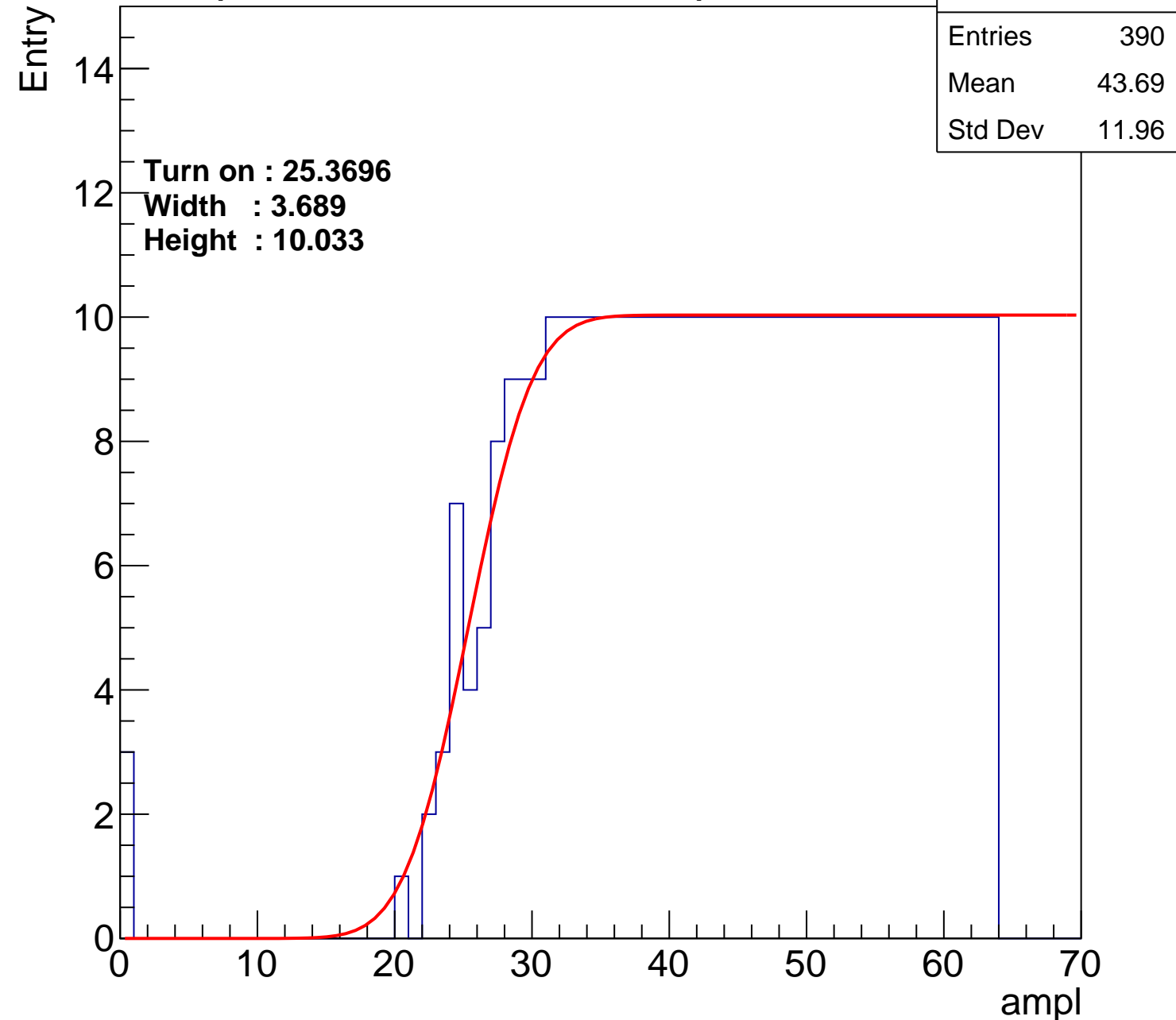
Width : 3.689

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch60

calib_packv5_042523_0143.root, FC#3, port B3

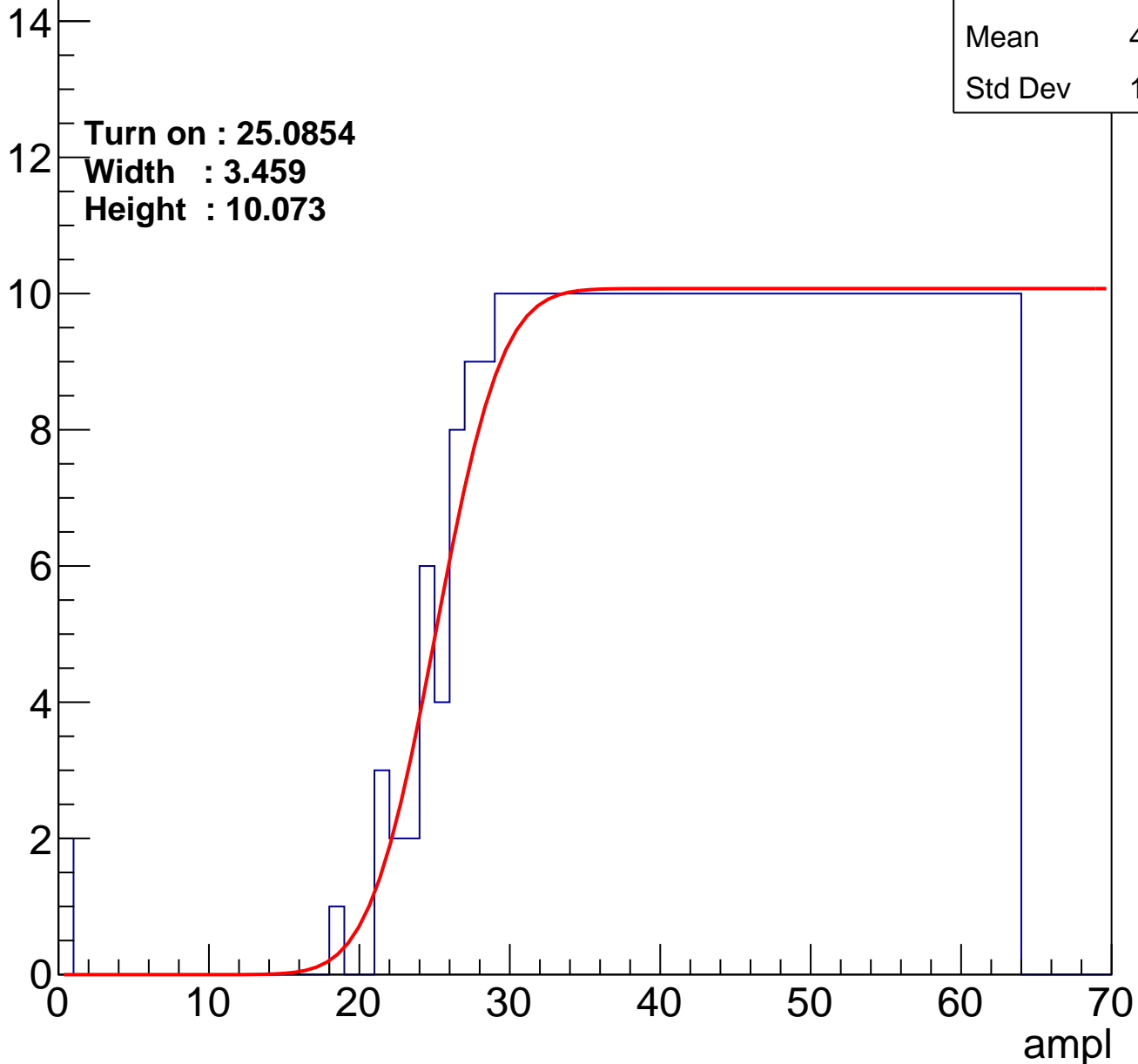
Entries	396
Mean	43.48
Std Dev	11.93

Turn on : 25.0854

Width : 3.459

Height : 10.073

Entry



B1L000S, U4-ch61

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.22
Std Dev	12.19

Turn on : 23.9034

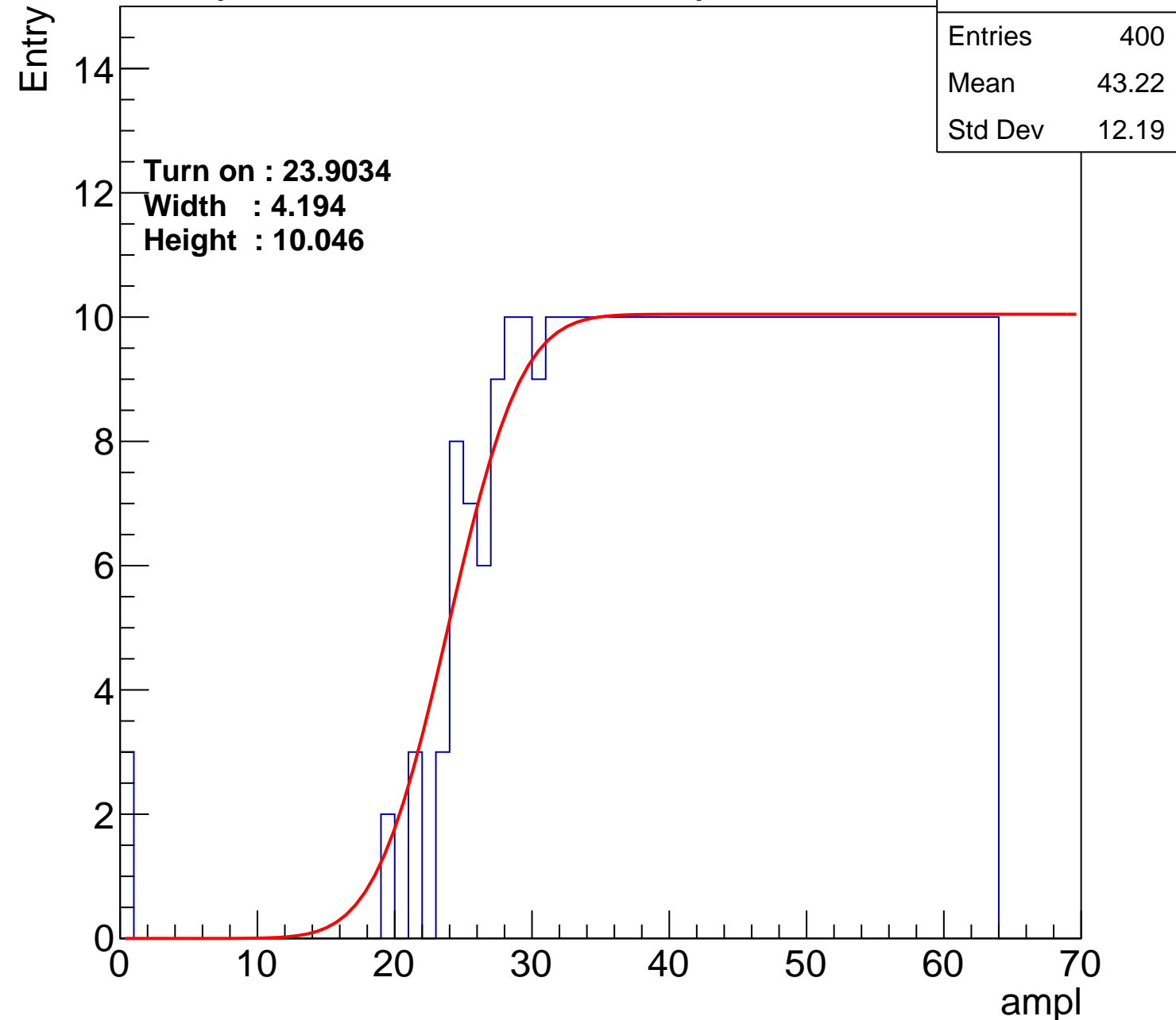
Width : 4.194

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch62

calib_packv5_042523_0143.root, FC#3, port B3

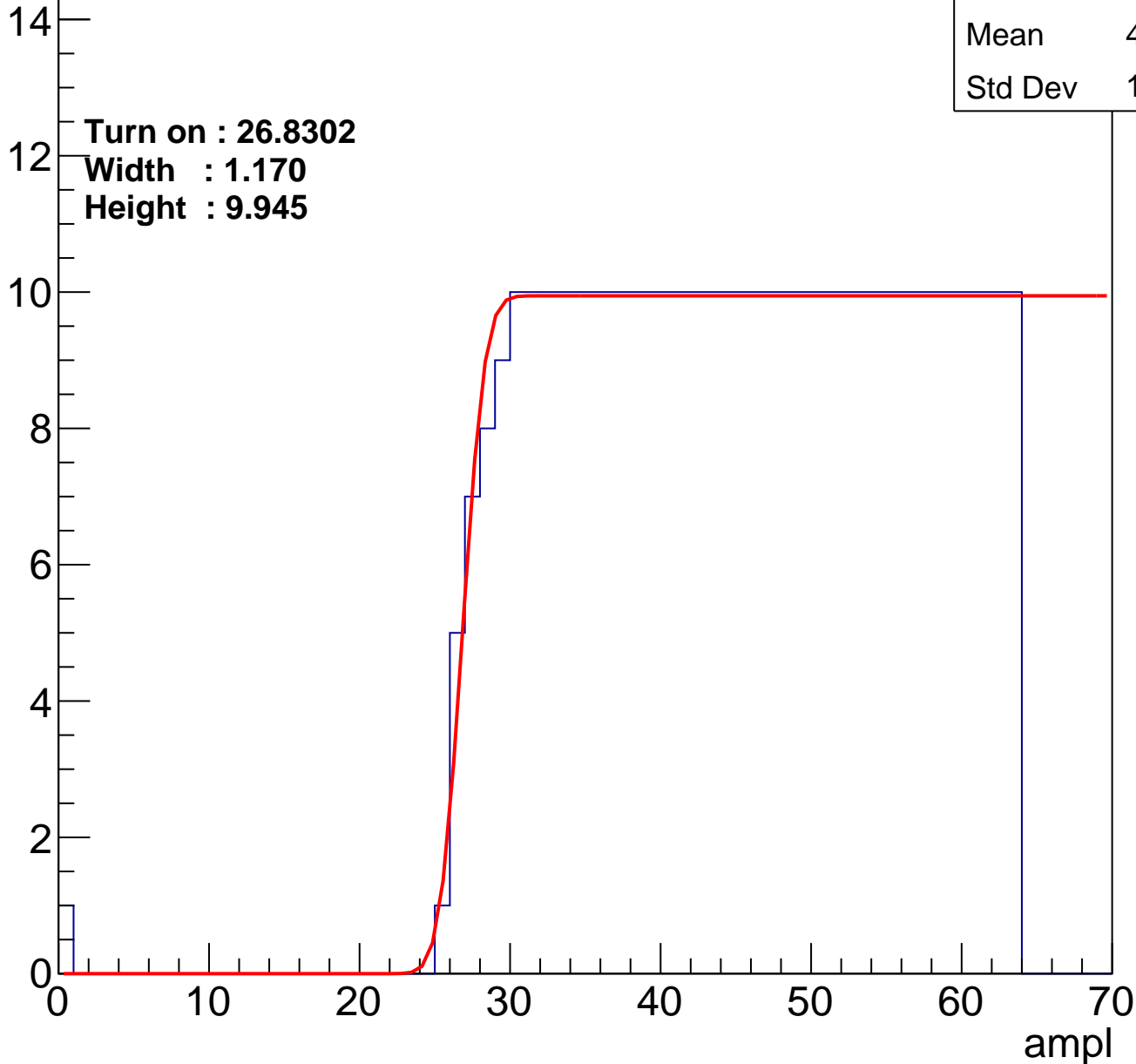
Entry

Entries	371
Mean	44.85
Std Dev	10.96

Turn on : 26.8302

Width : 1.170

Height : 9.945



B1L000S, U4-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.68
Std Dev	11.82

Turn on : 24.8972

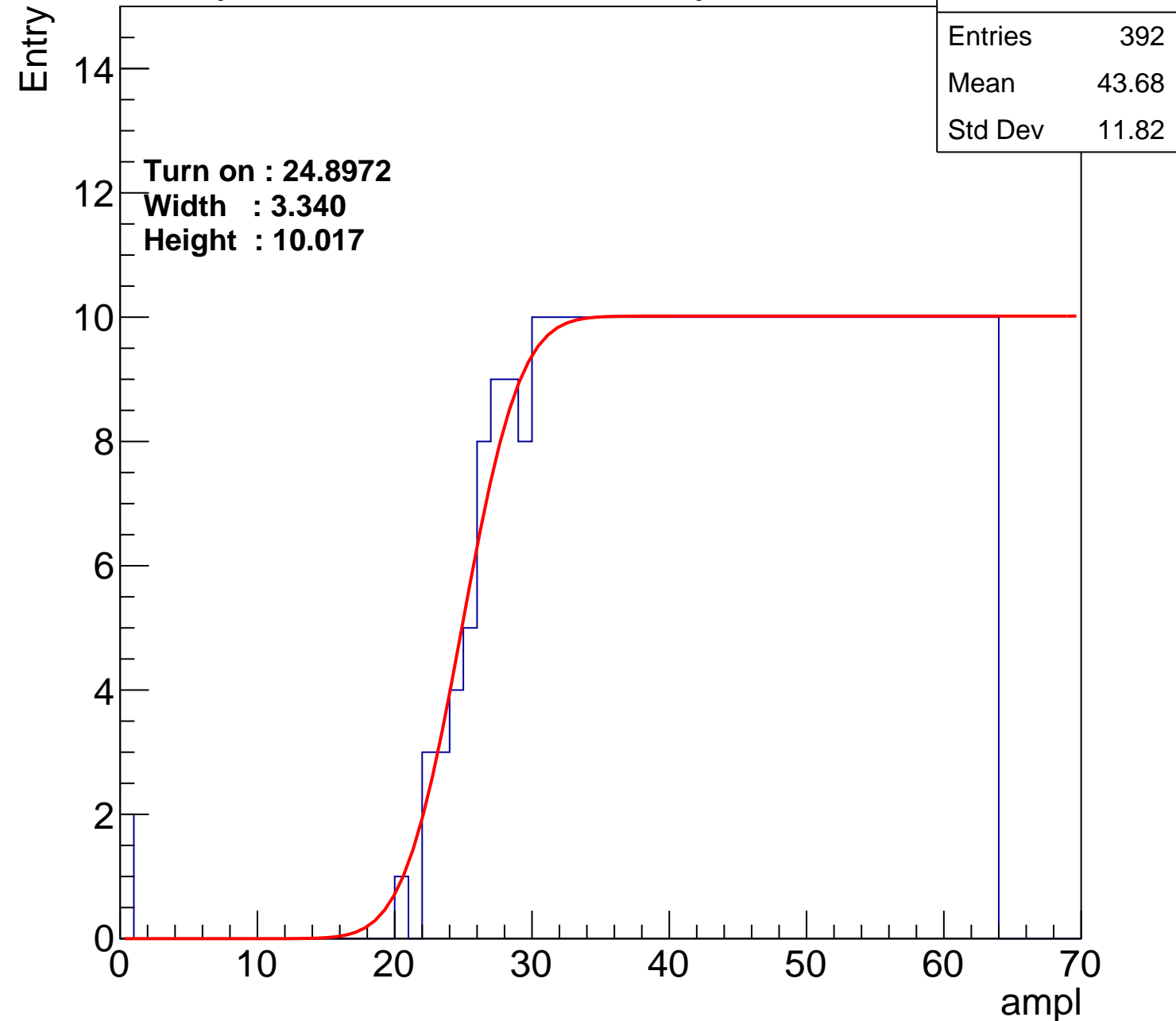
Width : 3.340

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch64

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.1
Std Dev	12.71

Turn on : 25.1895

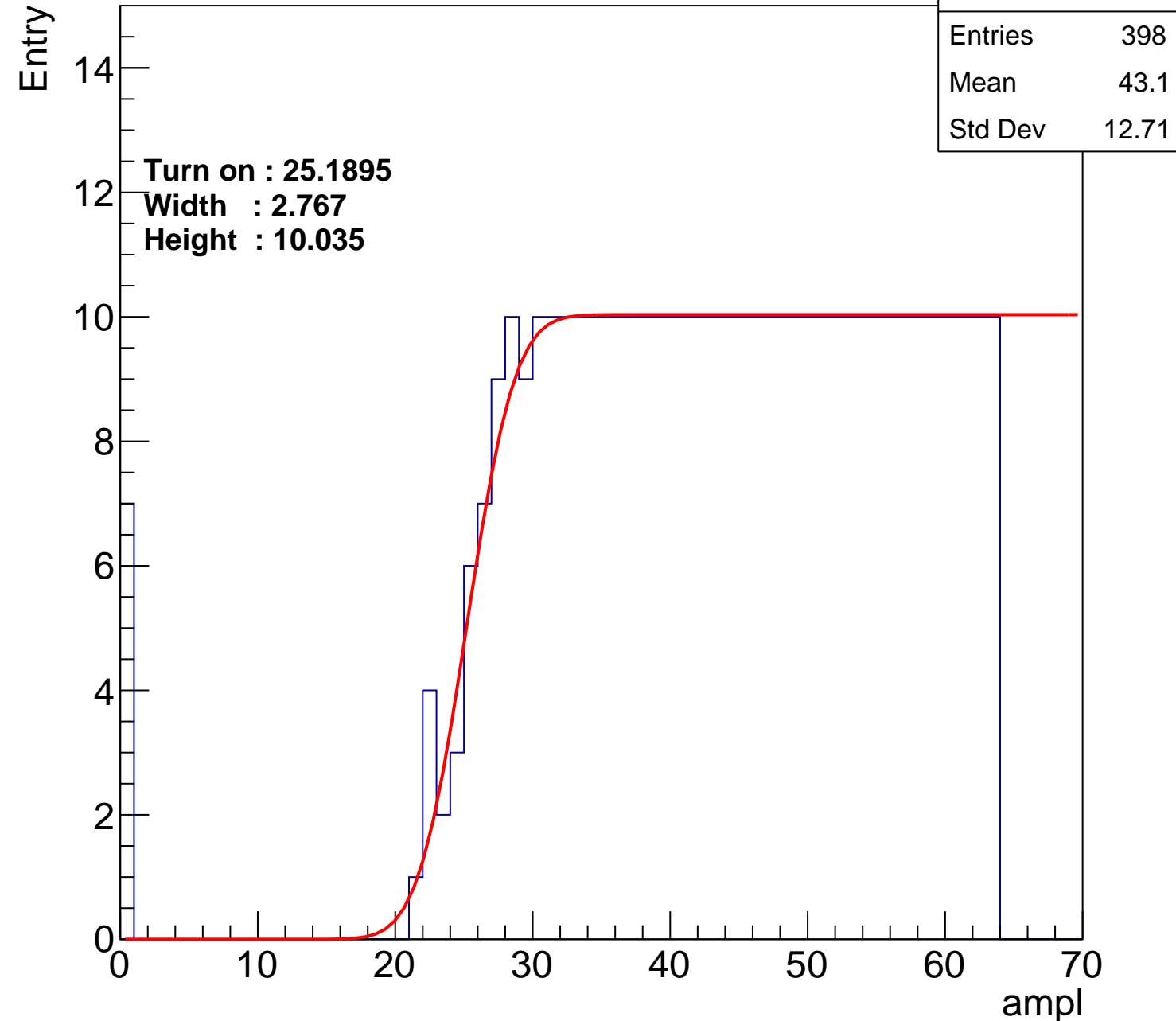
Width : 2.767

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.67
Std Dev	12.47

Turn on : 26.4444

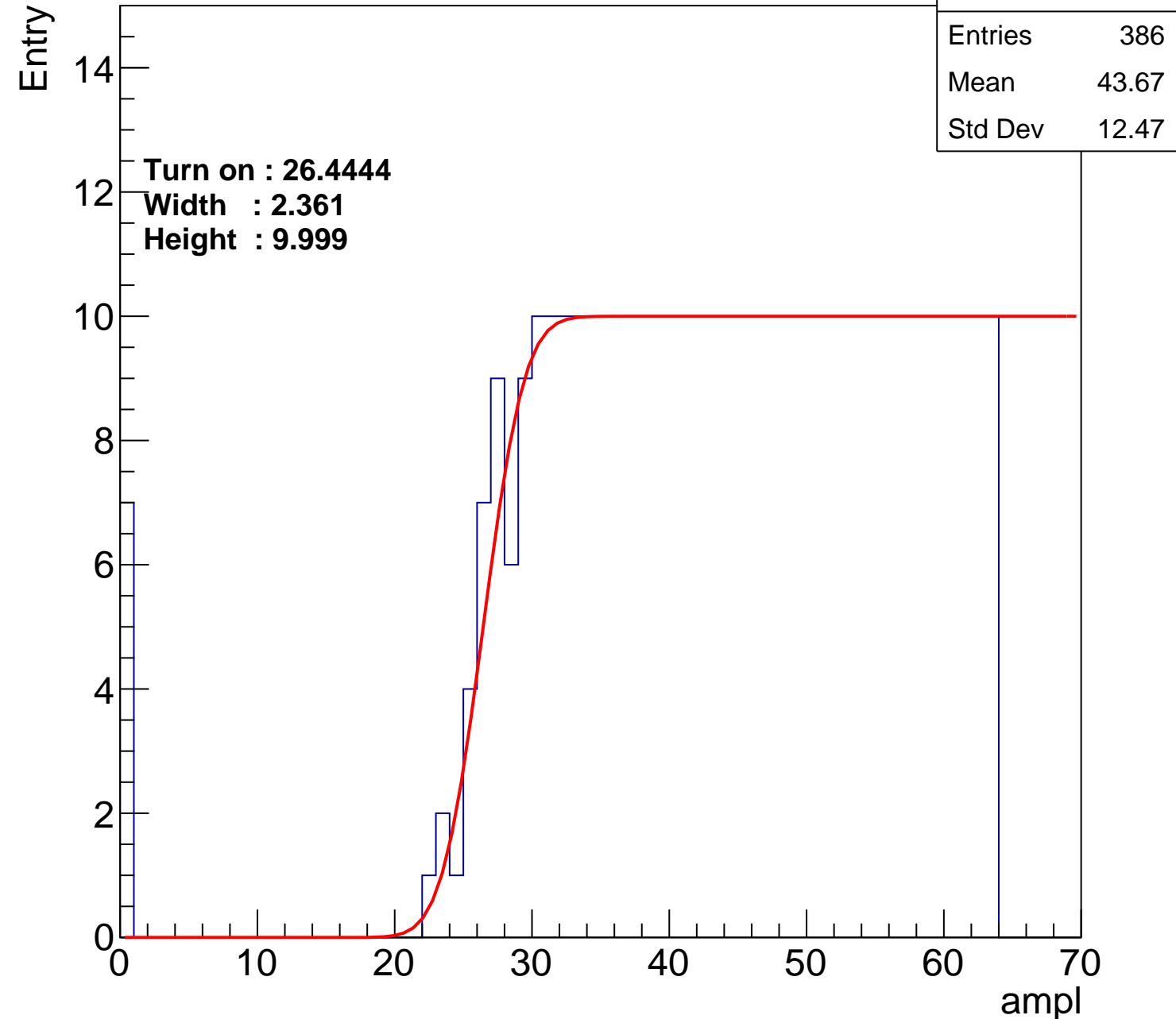
Width : 2.361

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	404
Mean	43.03
Std Dev	12.28

Turn on : 24.0031

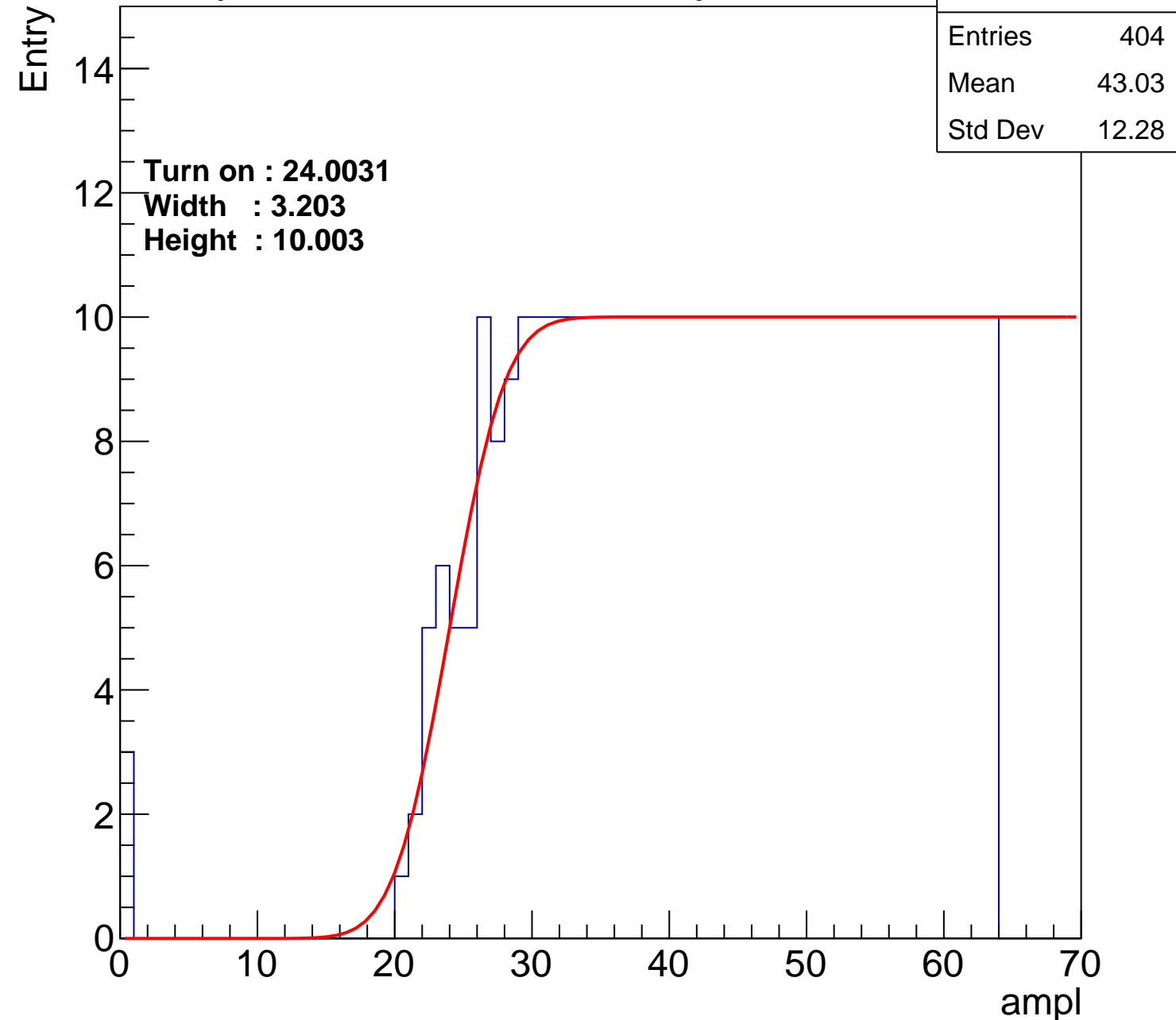
Width : 3.203

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.52
Std Dev	12.09

Turn on : 25.0520

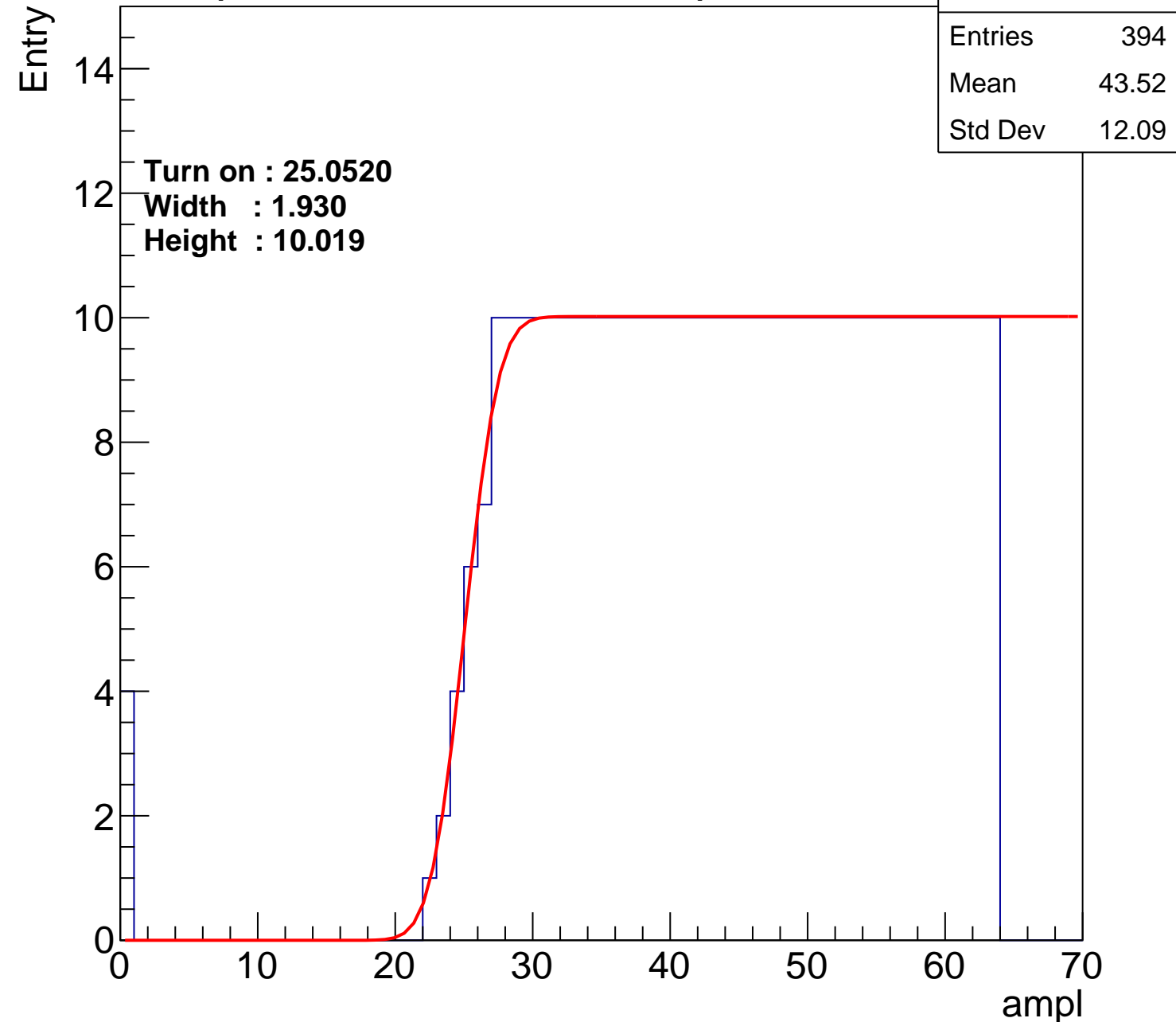
Width : 1.930

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.36
Std Dev	12.07

Turn on : 24.7696

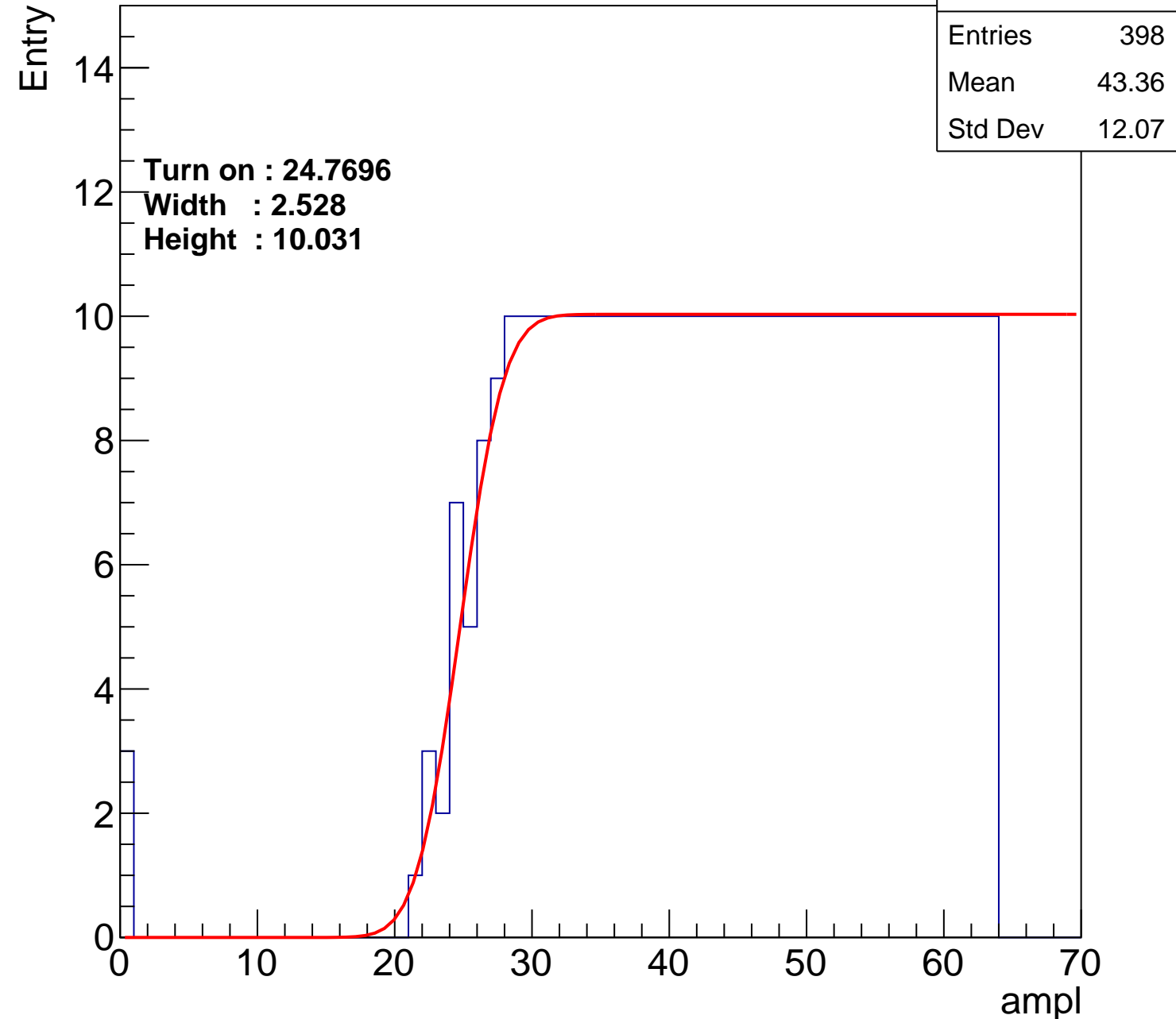
Width : 2.528

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch69

calib_packv5_042523_0143.root, FC#3, port B3

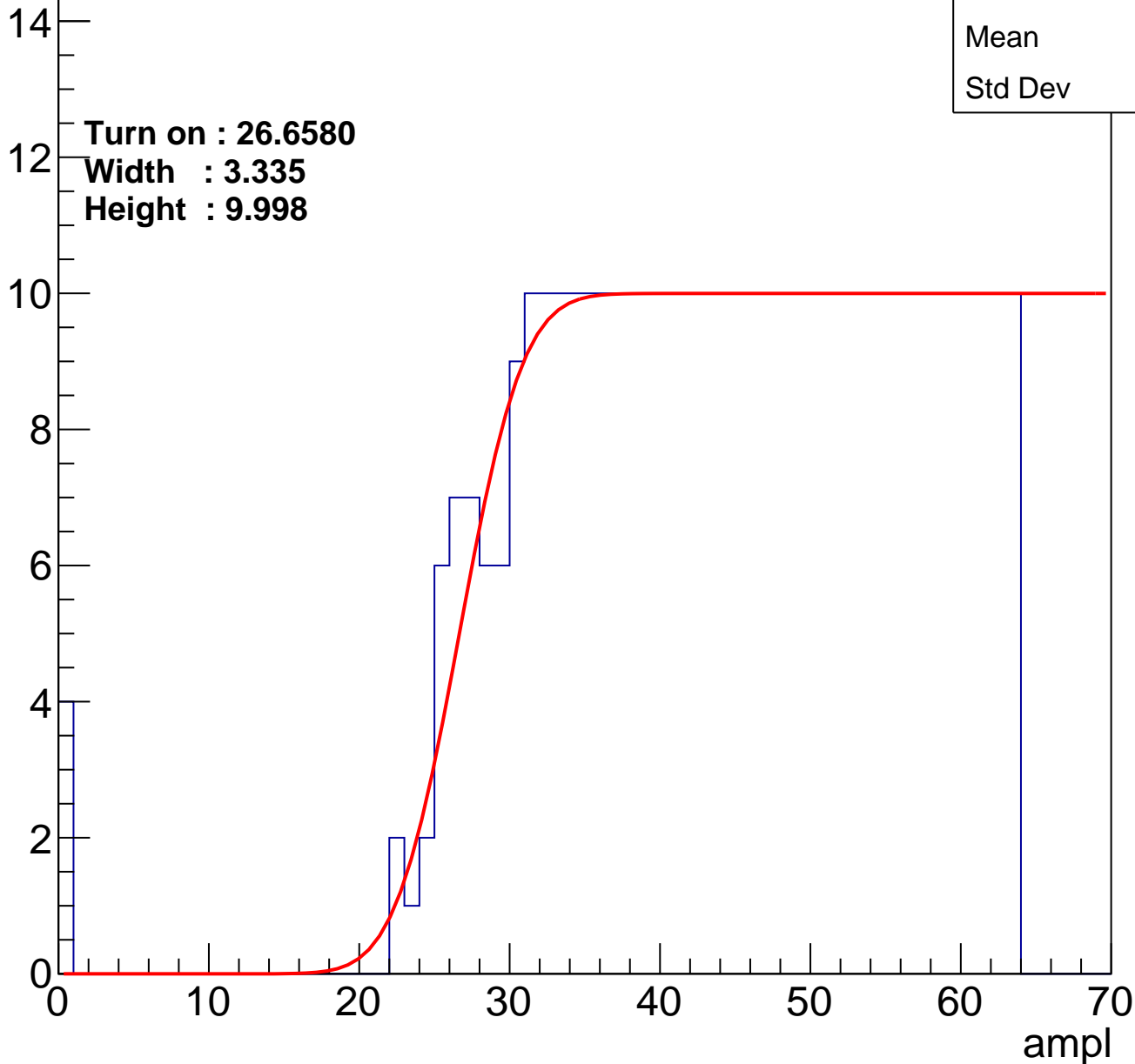
Entry

Entries	380
Mean	44.1
Std Dev	11.91

Turn on : 26.6580

Width : 3.335

Height : 9.998



B1L000S, U4-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.81
Std Dev	11.77

Turn on : 25.7033

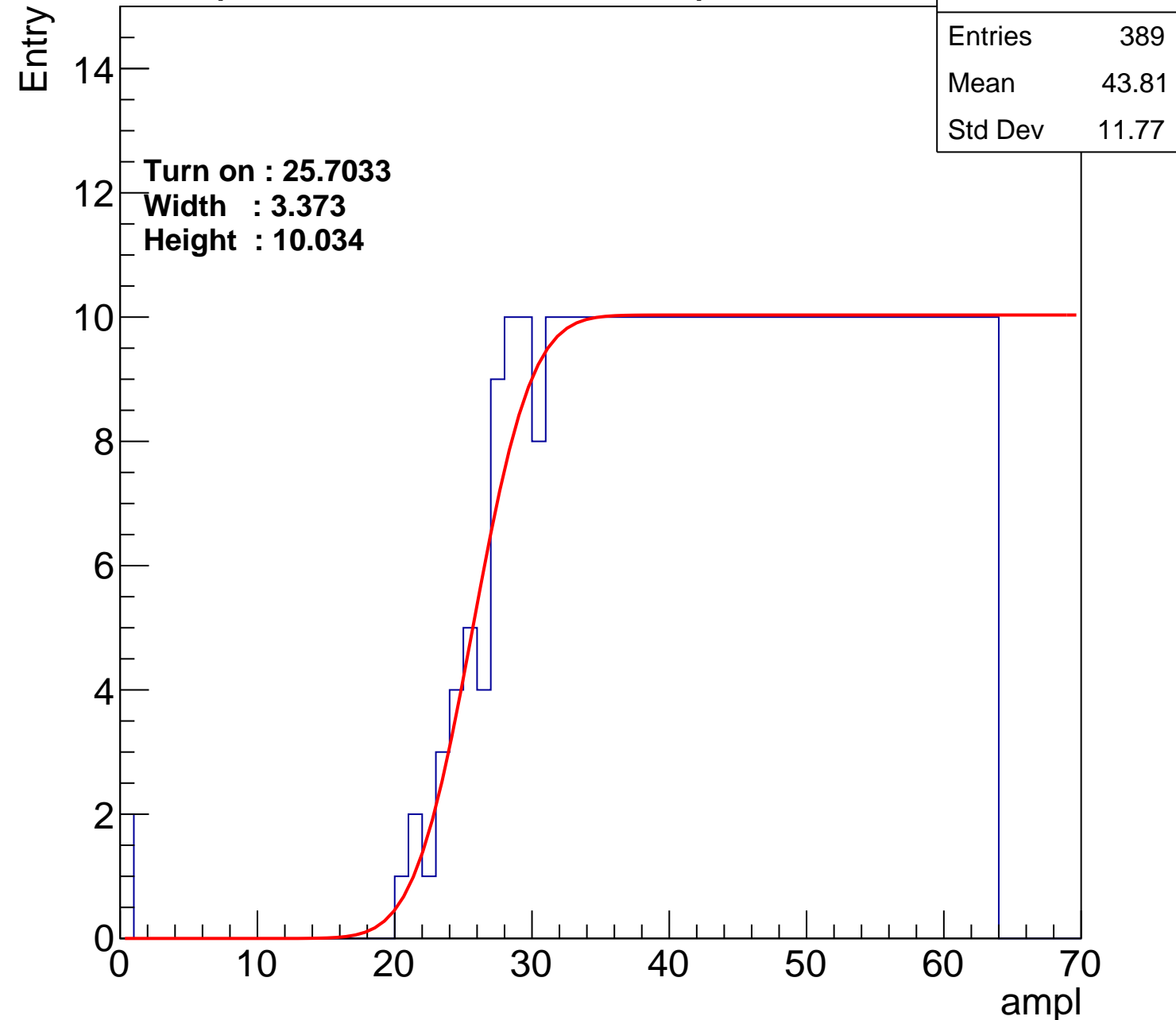
Width : 3.373

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	43.05
Std Dev	12.03

Turn on : 23.1728

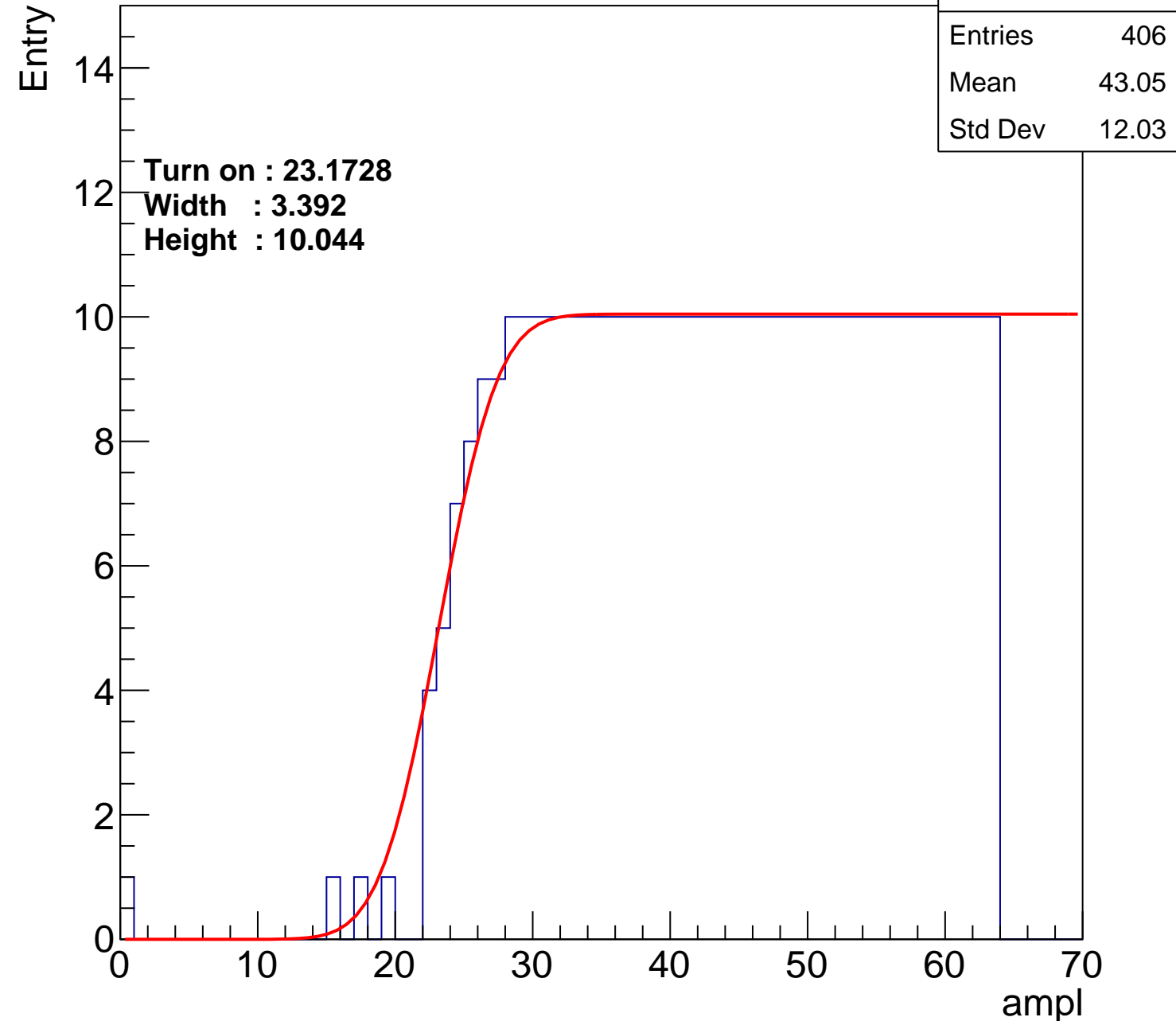
Width : 3.392

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.2779

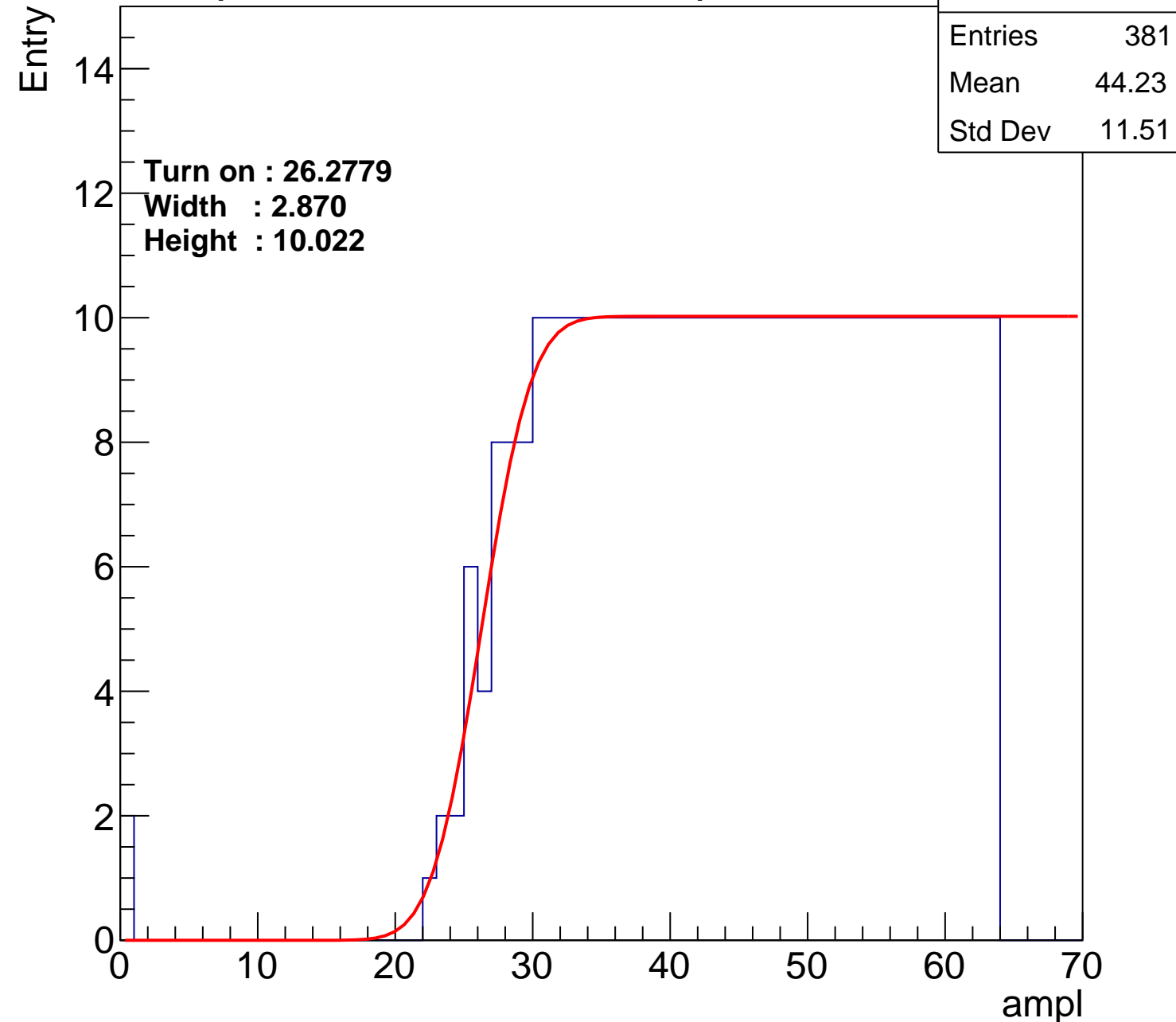
Width : 2.870

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.73
Std Dev	11.84

Turn on : 24.9434

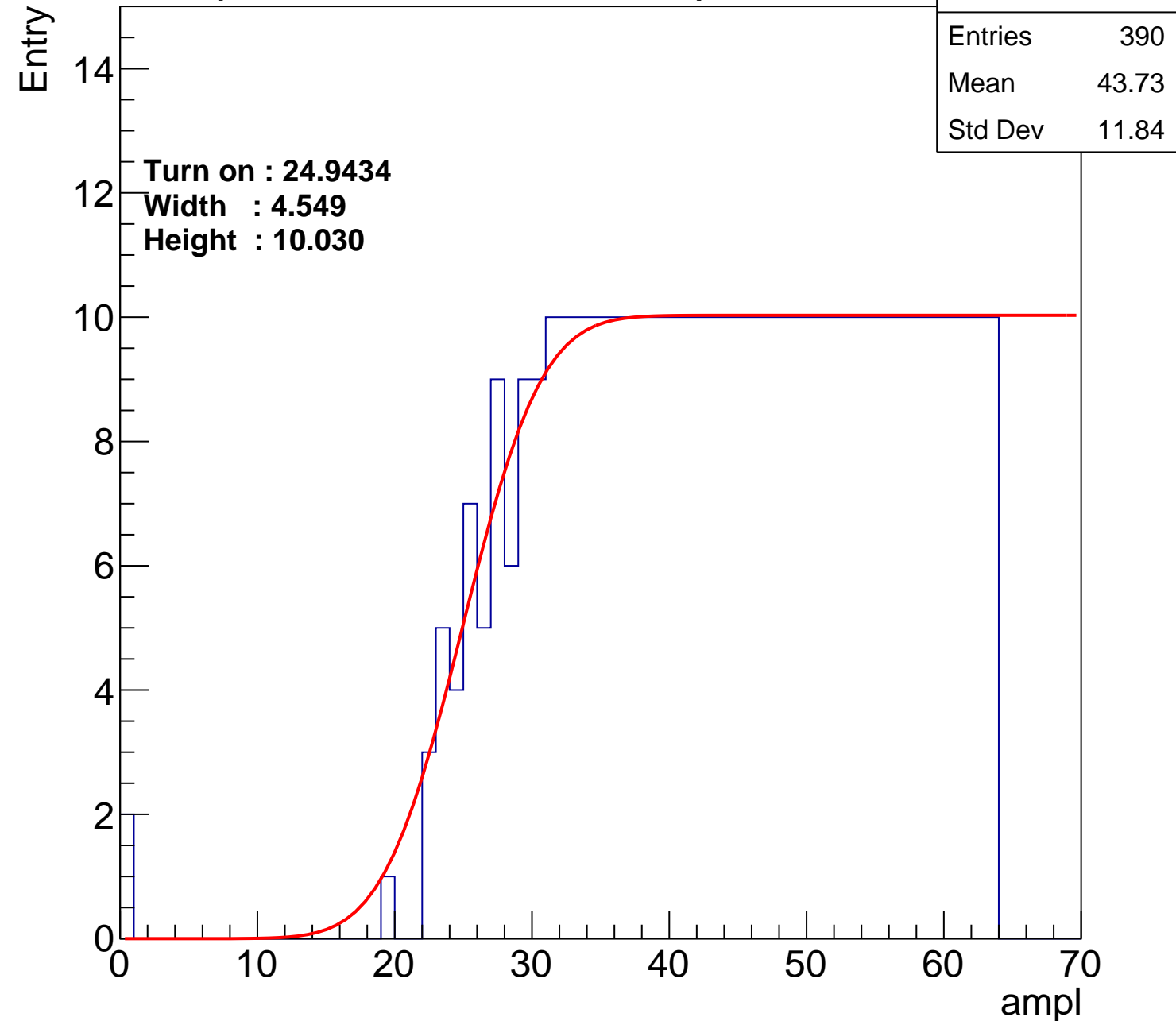
Width : 4.549

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.84
Std Dev	11.78

Turn on : 25.0895

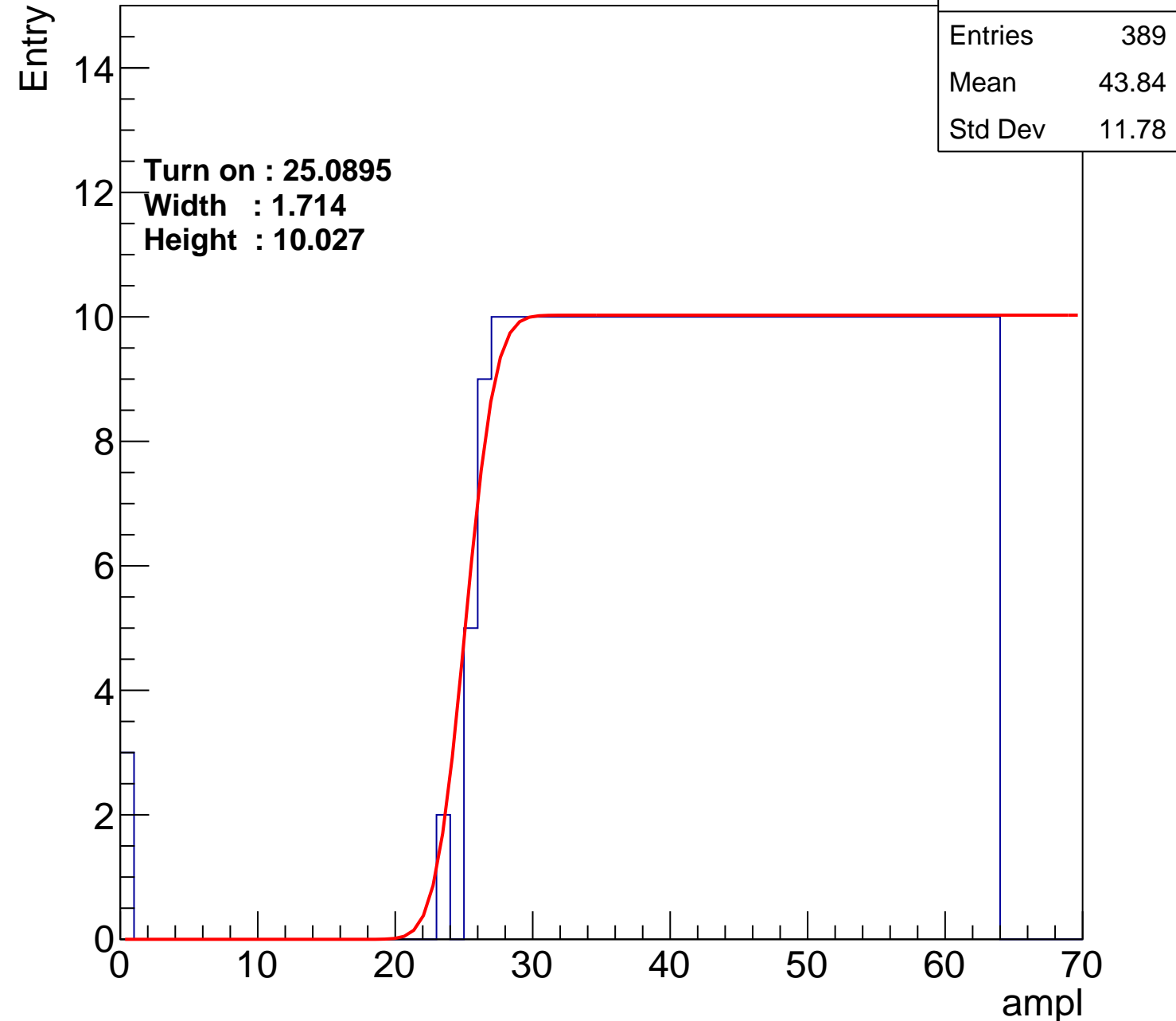
Width : 1.714

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 25.5491

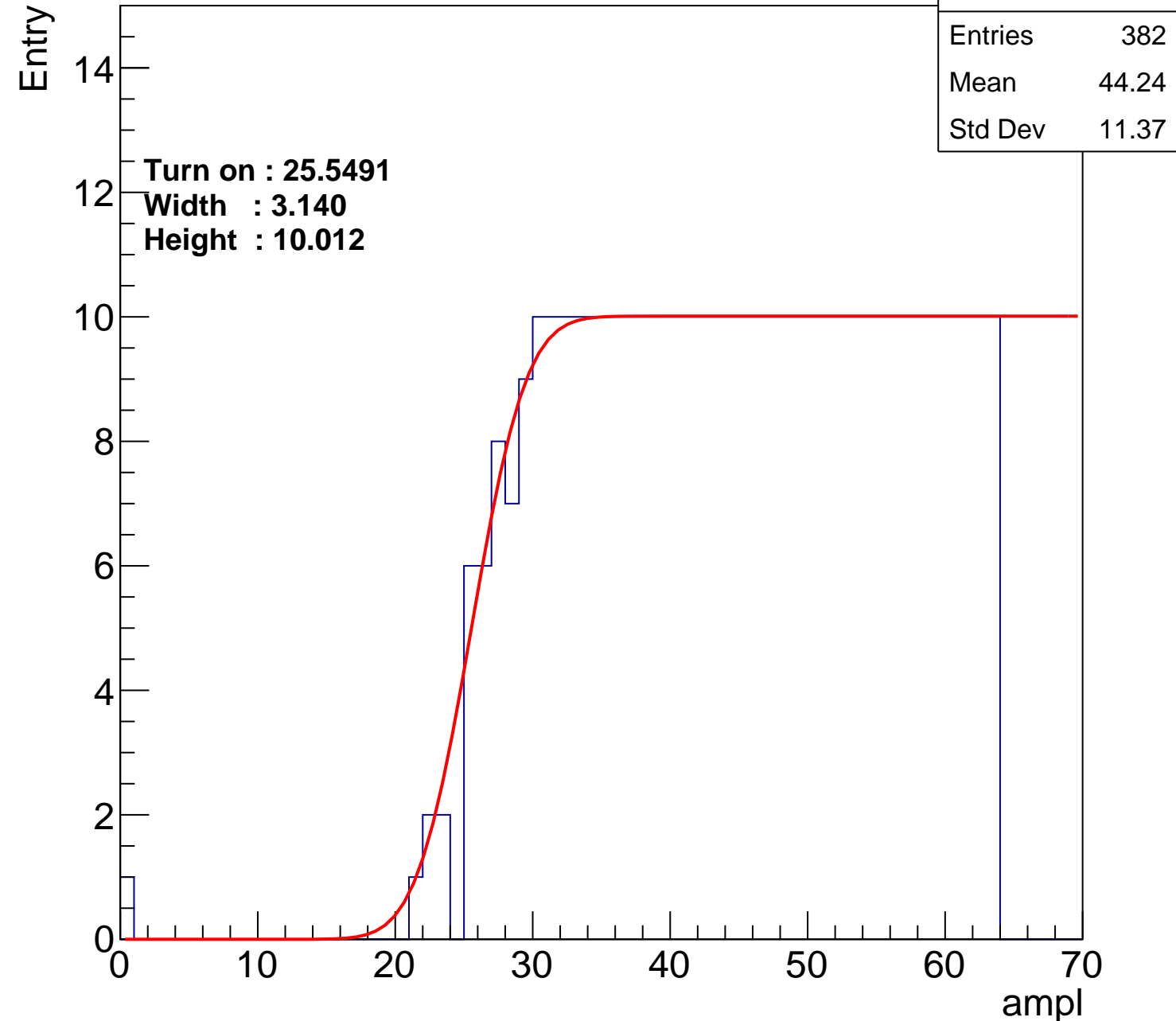
Width : 3.140

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch76

calib_packv5_042523_0143.root, FC#3, port B3

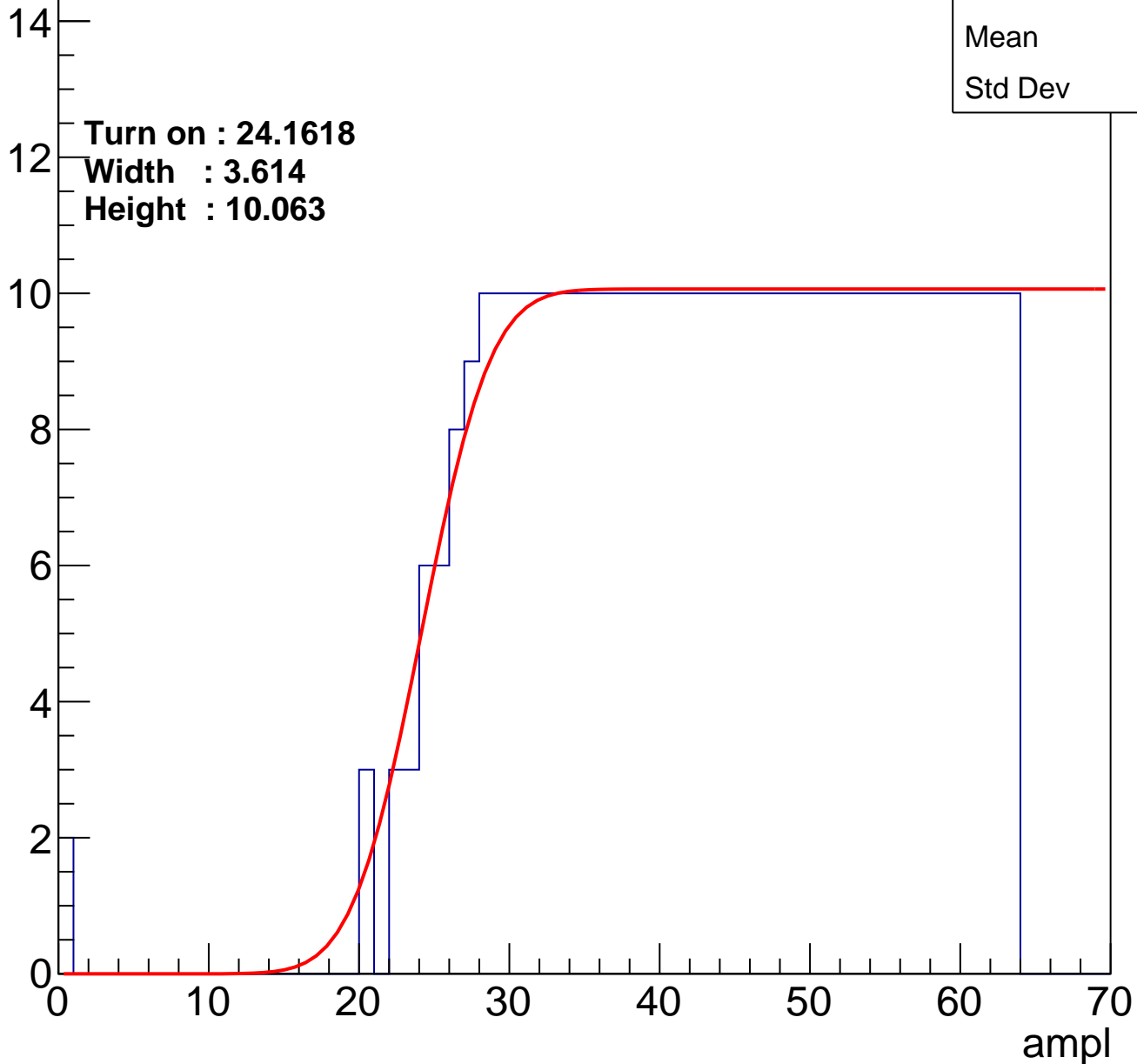
Entries	400
Mean	43.3
Std Dev	12

Turn on : 24.1618

Width : 3.614

Height : 10.063

Entry



B1L000S, U4-ch77

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.96
Std Dev	11.92

Turn on : 26.1988

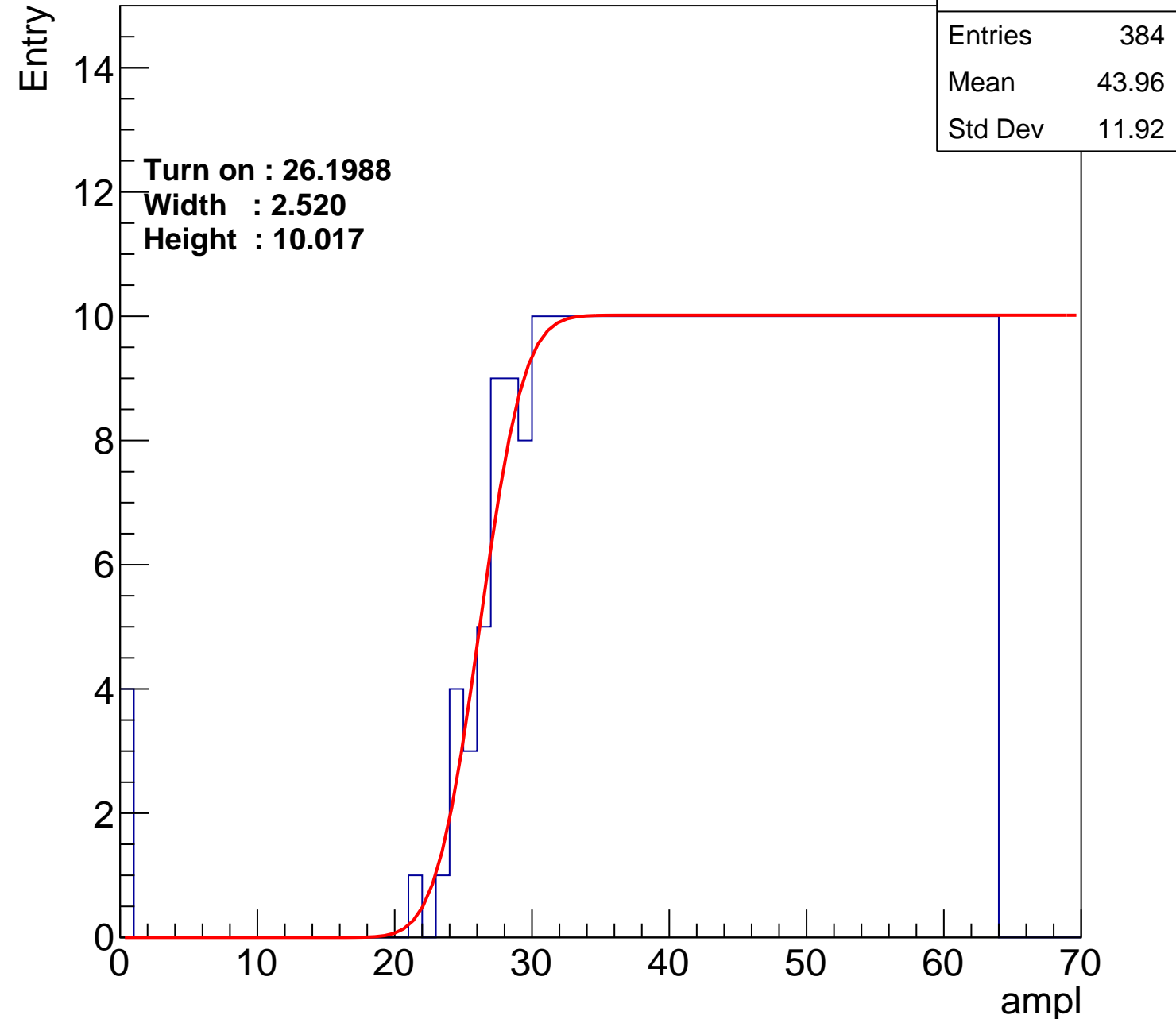
Width : 2.520

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch78

calib_packv5_042523_0143.root, FC#3, port B3

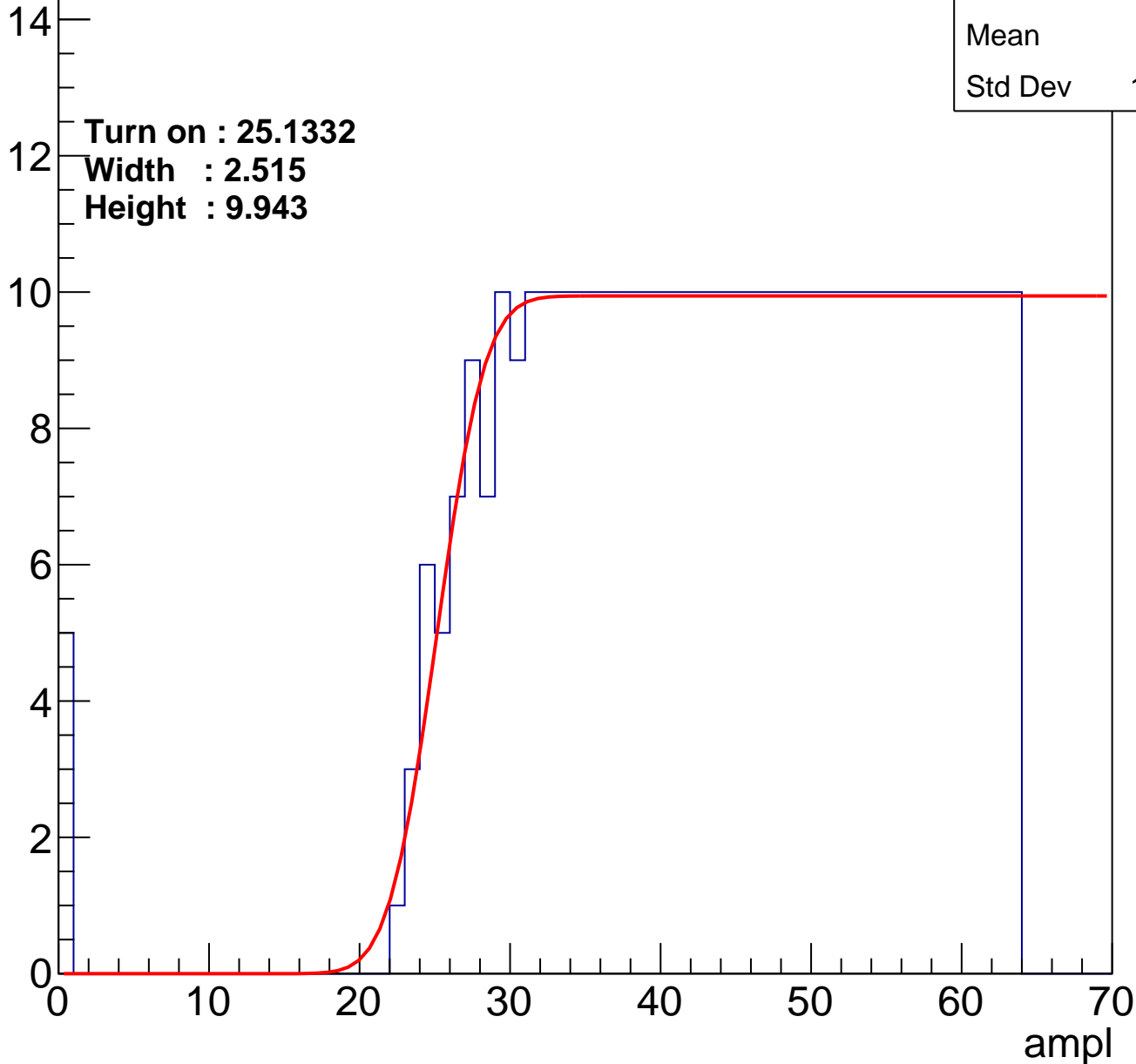
Entry

Entries	392
Mean	43.5
Std Dev	12.28

Turn on : 25.1332

Width : 2.515

Height : 9.943



B1L000S, U4-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.61

Turn on : 25.6758

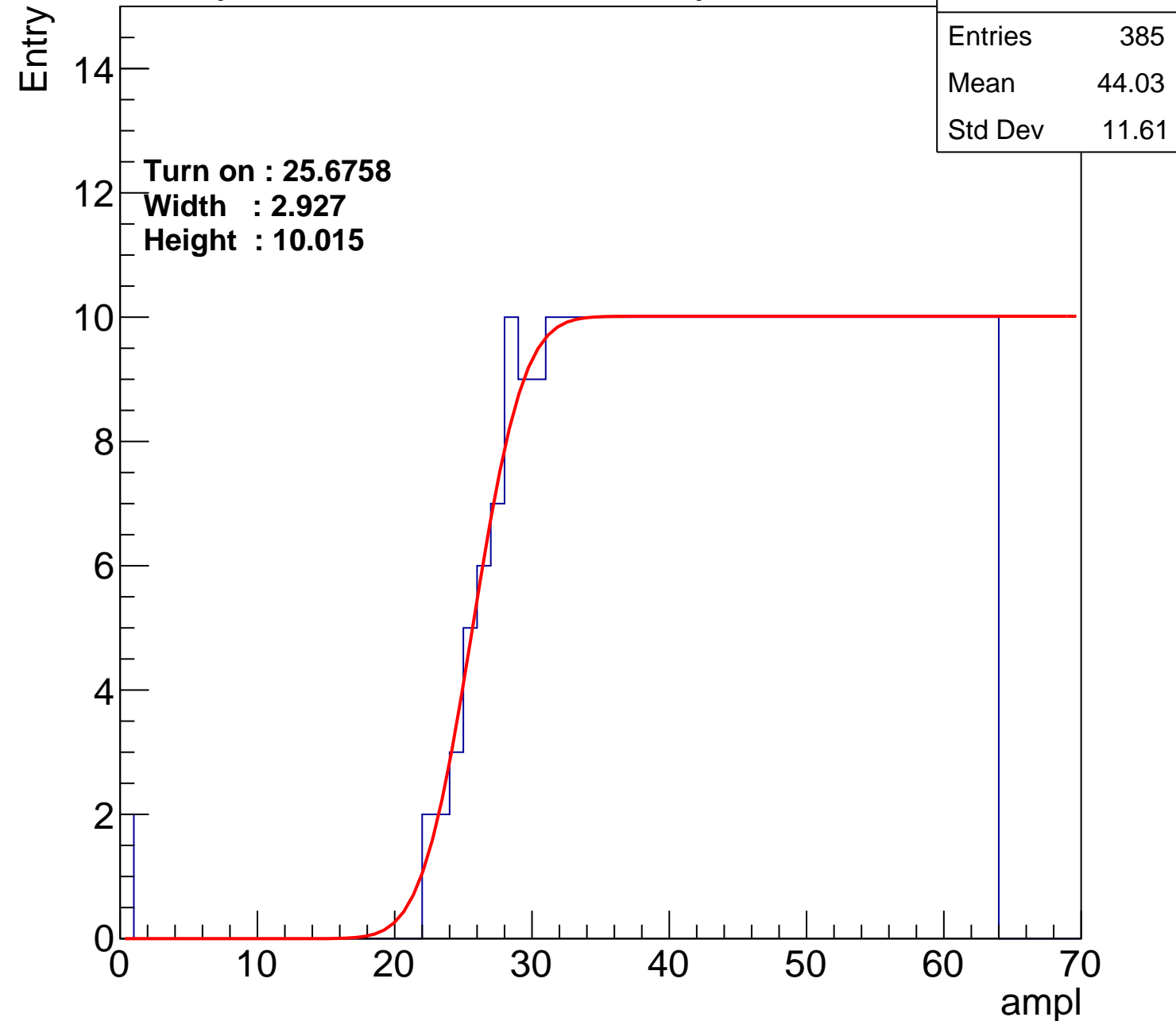
Width : 2.927

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.41
Std Dev	11.92

Turn on : 24.4037

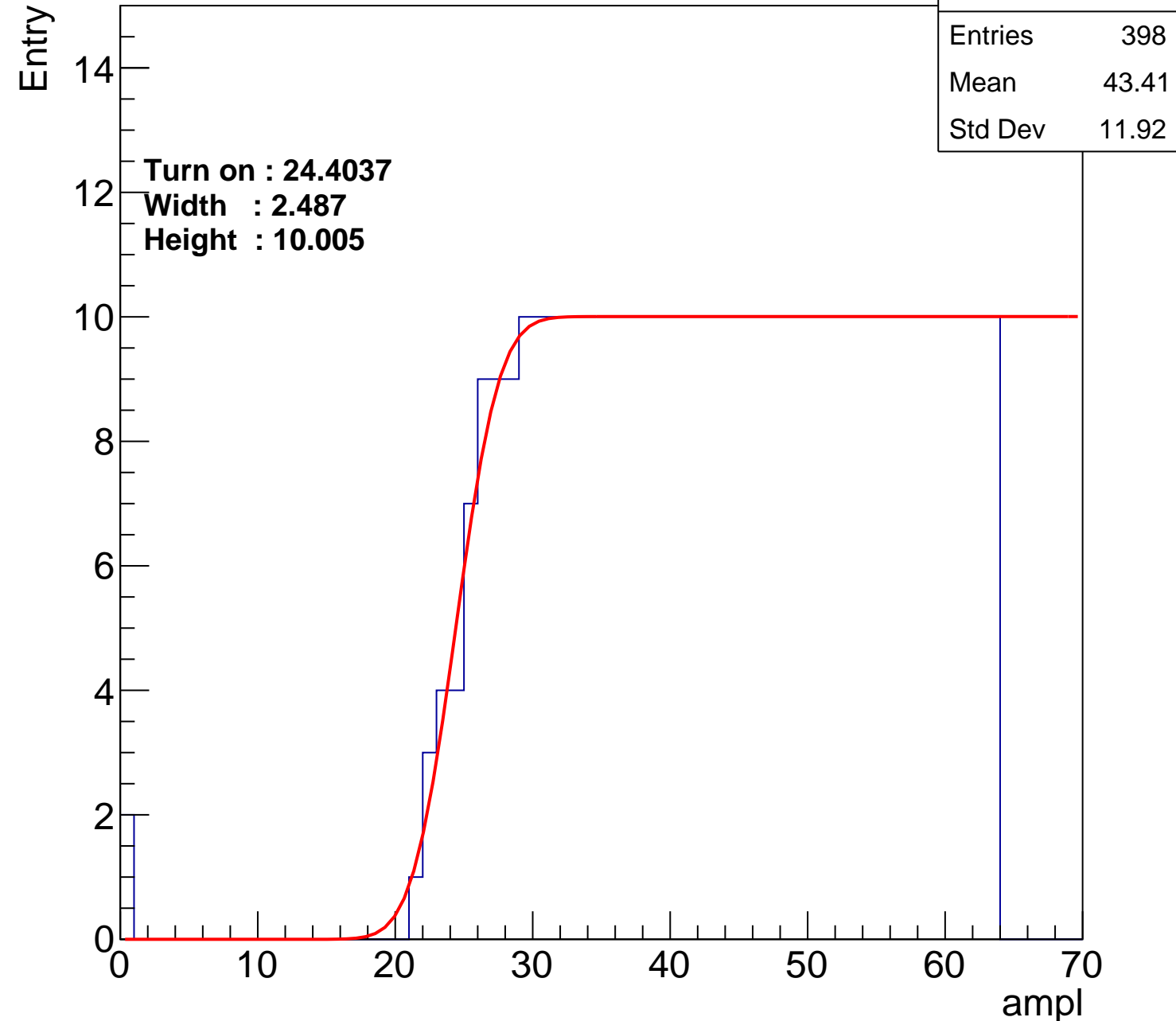
Width : 2.487

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.42
Std Dev	12.09

Turn on : 25.2725

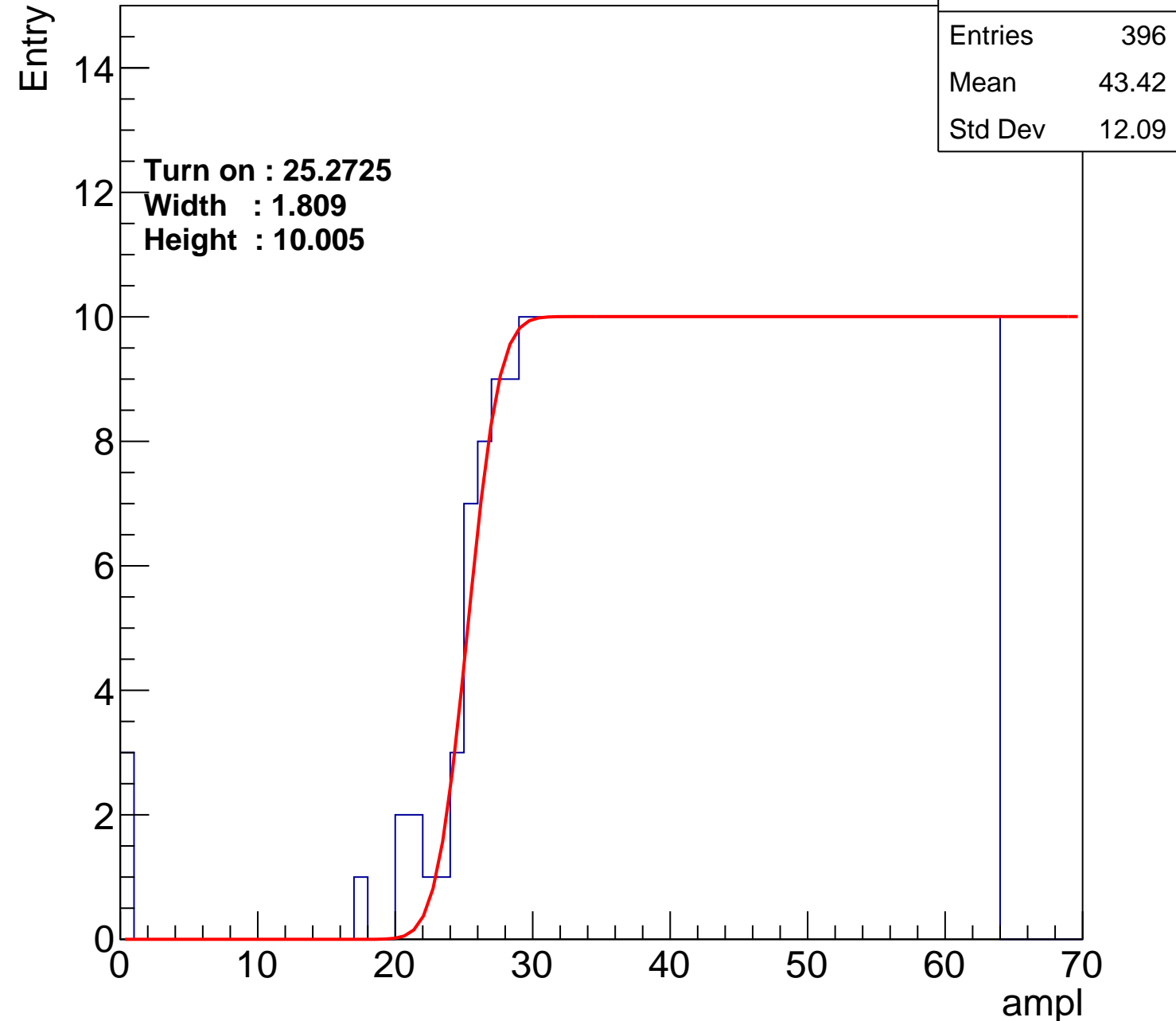
Width : 1.809

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.74
Std Dev	11.6

Turn on : 24.9489

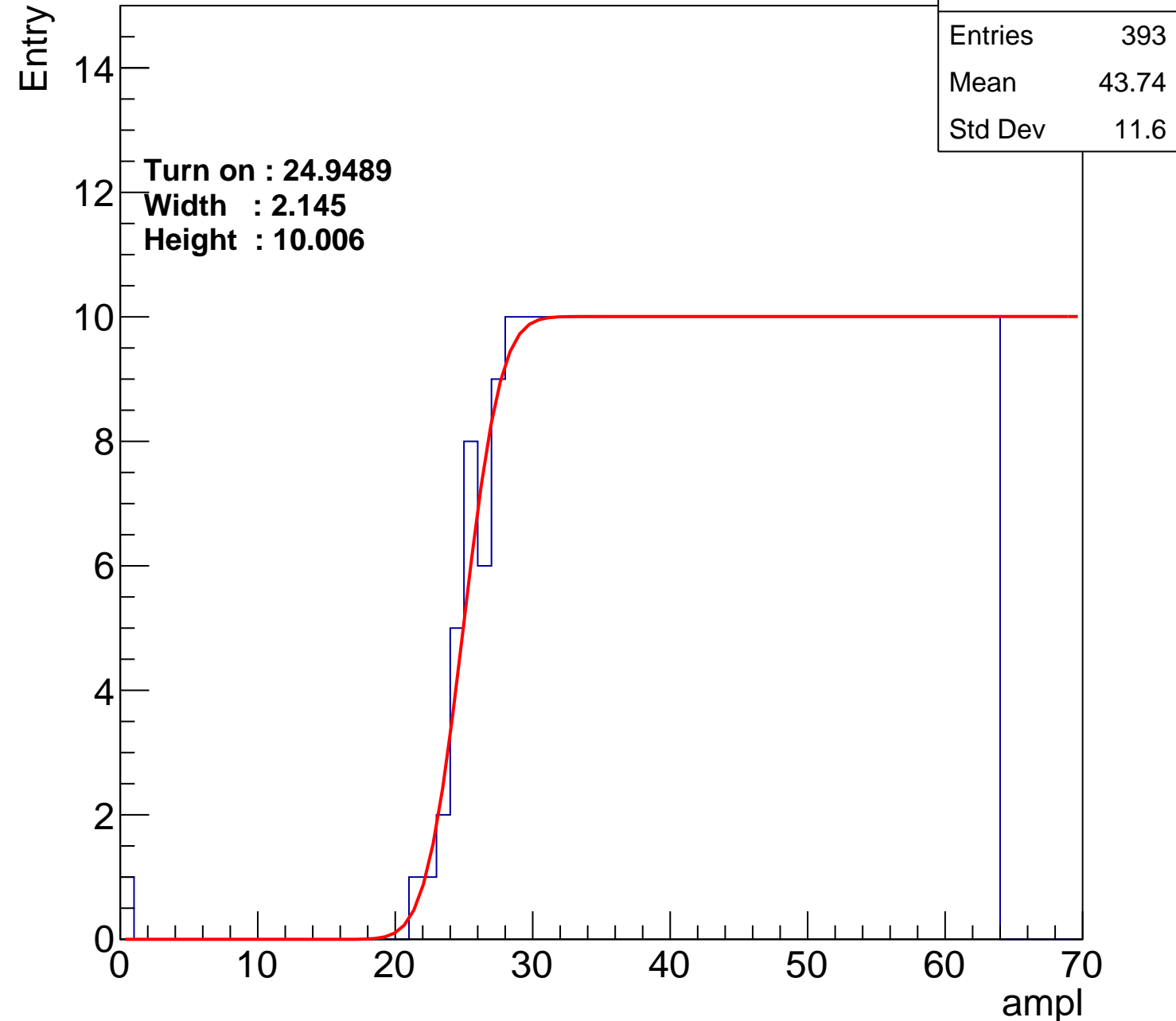
Width : 2.145

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch83

calib_packv5_042523_0143.root, FC#3, port B3

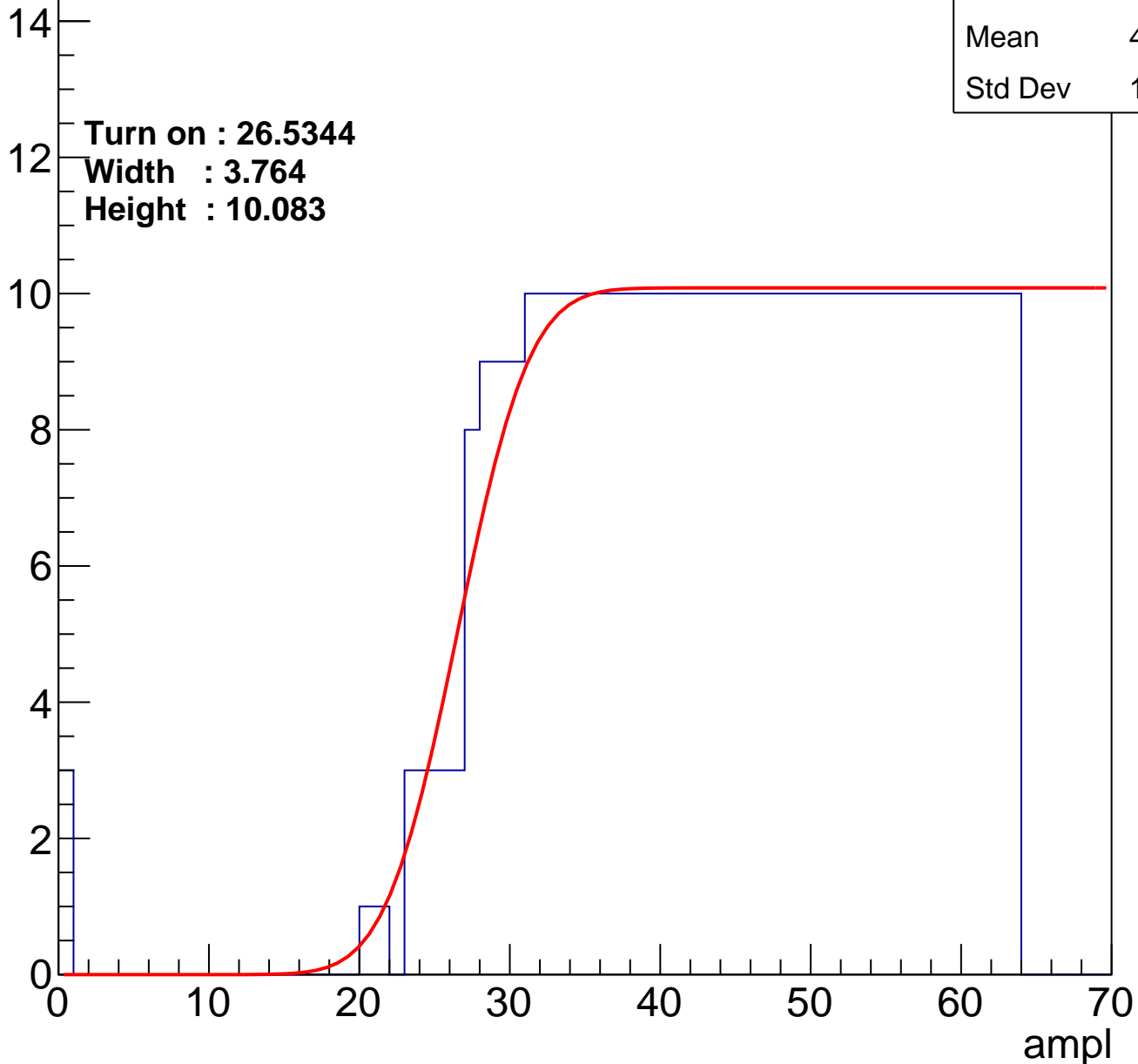
Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 26.5344

Width : 3.764

Height : 10.083

Entry



B1L000S, U4-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.53
Std Dev	11.36

Turn on : 26.3357

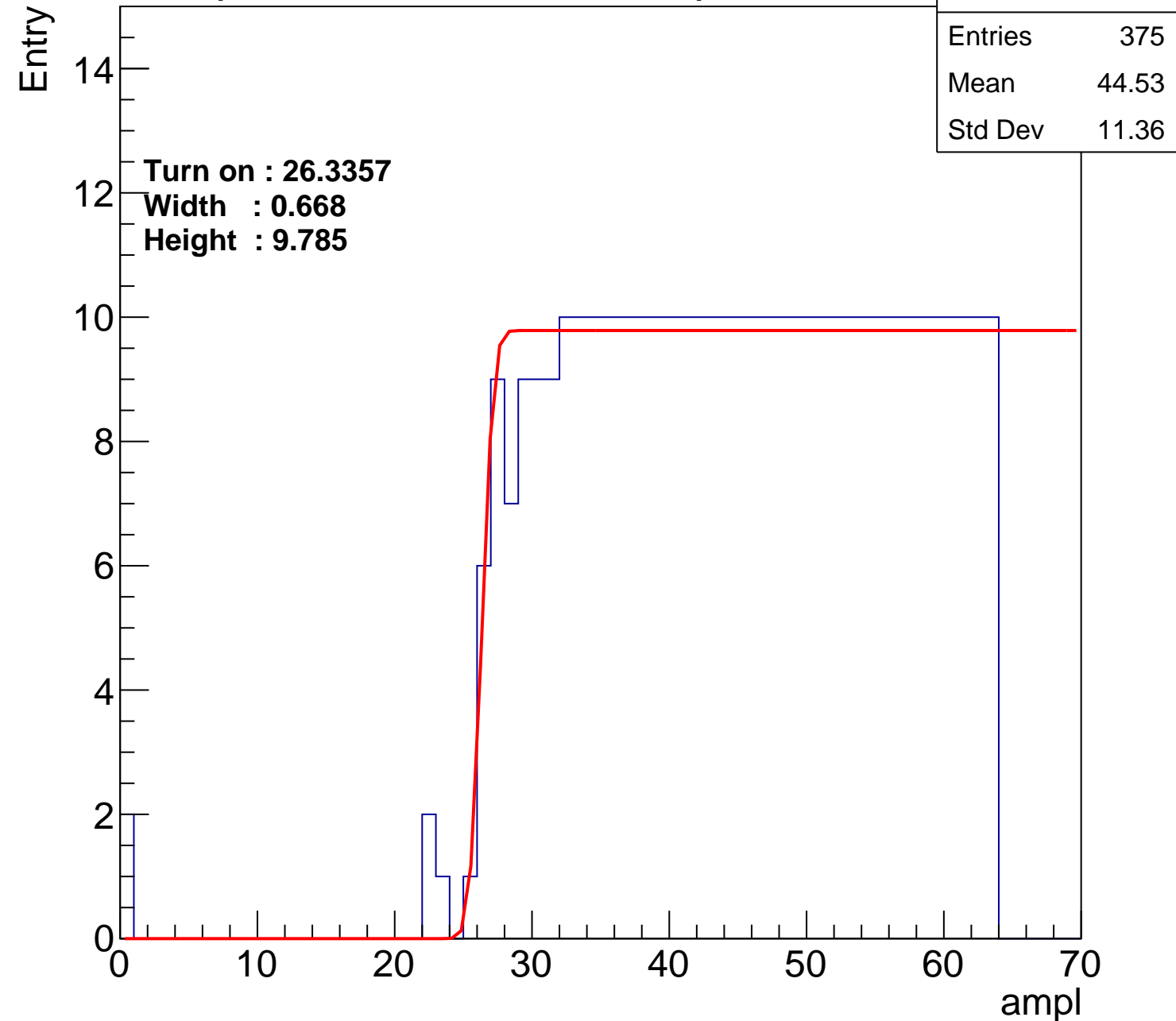
Width : 0.668

Height : 9.785

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.87
Std Dev	12.02

Turn on : 26.0242

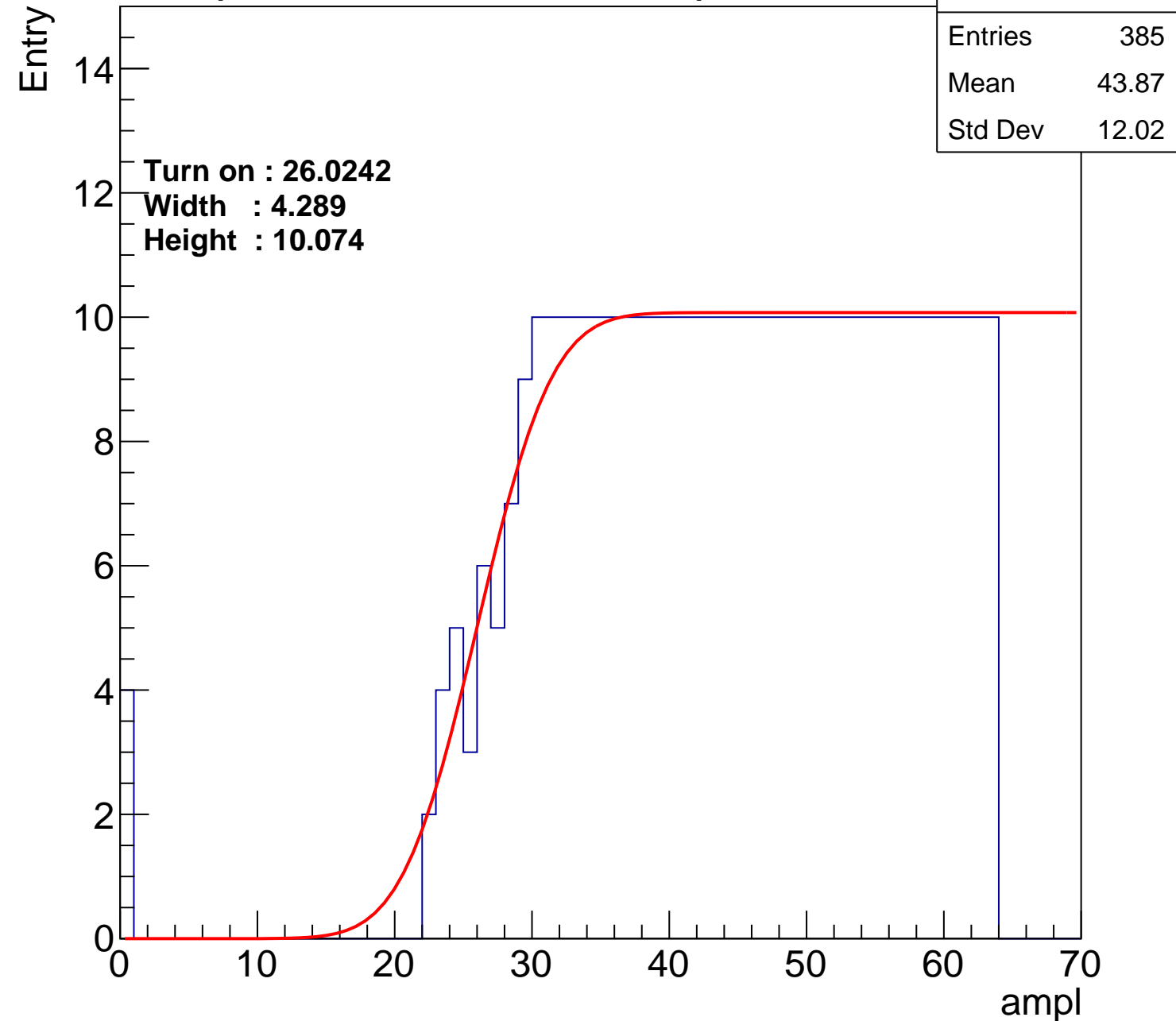
Width : 4.289

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	405
Mean	42.91
Std Dev	12.46

Turn on : 23.9236

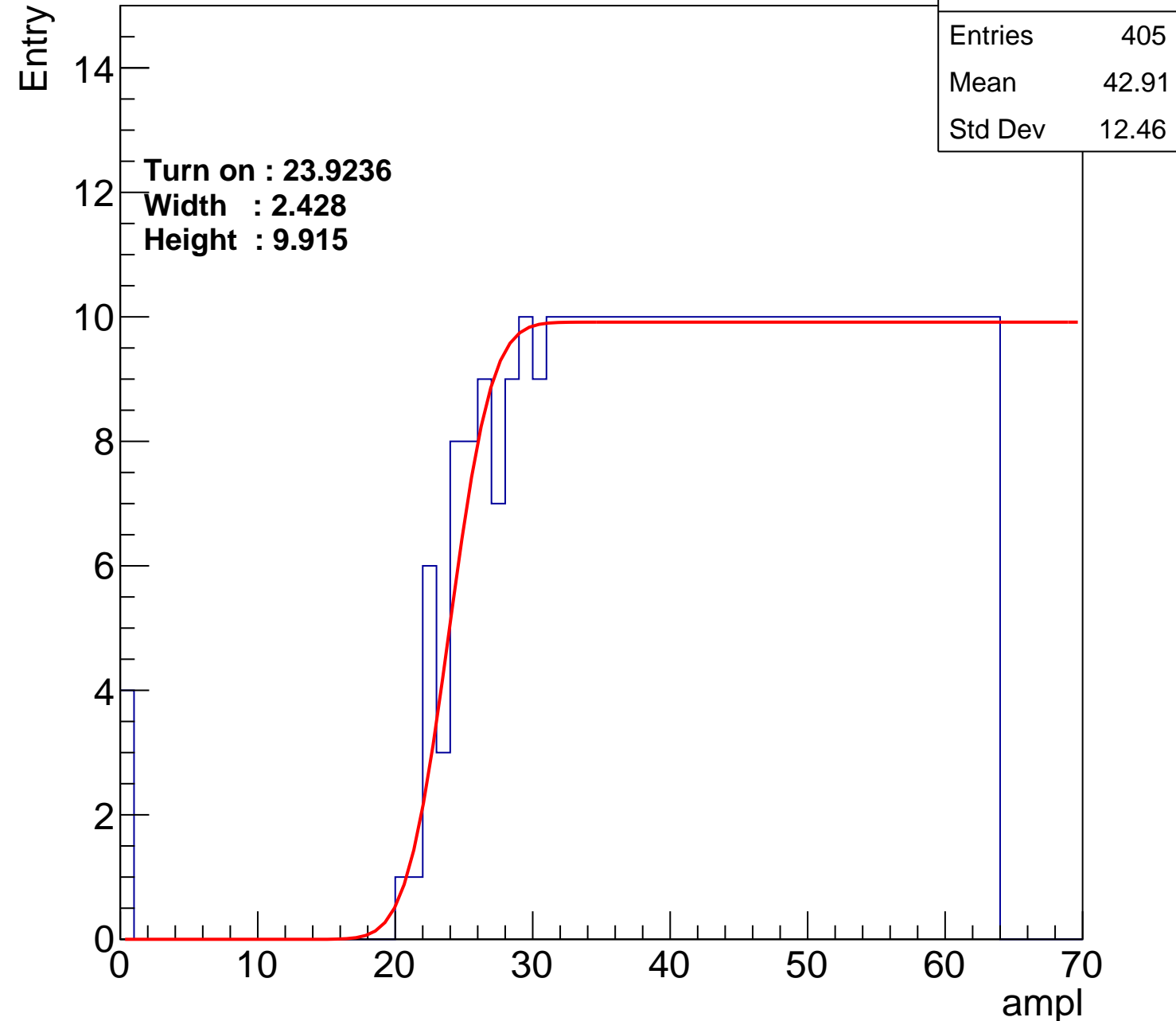
Width : 2.428

Height : 9.915

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.94
Std Dev	11.9

Turn on : 26.1283

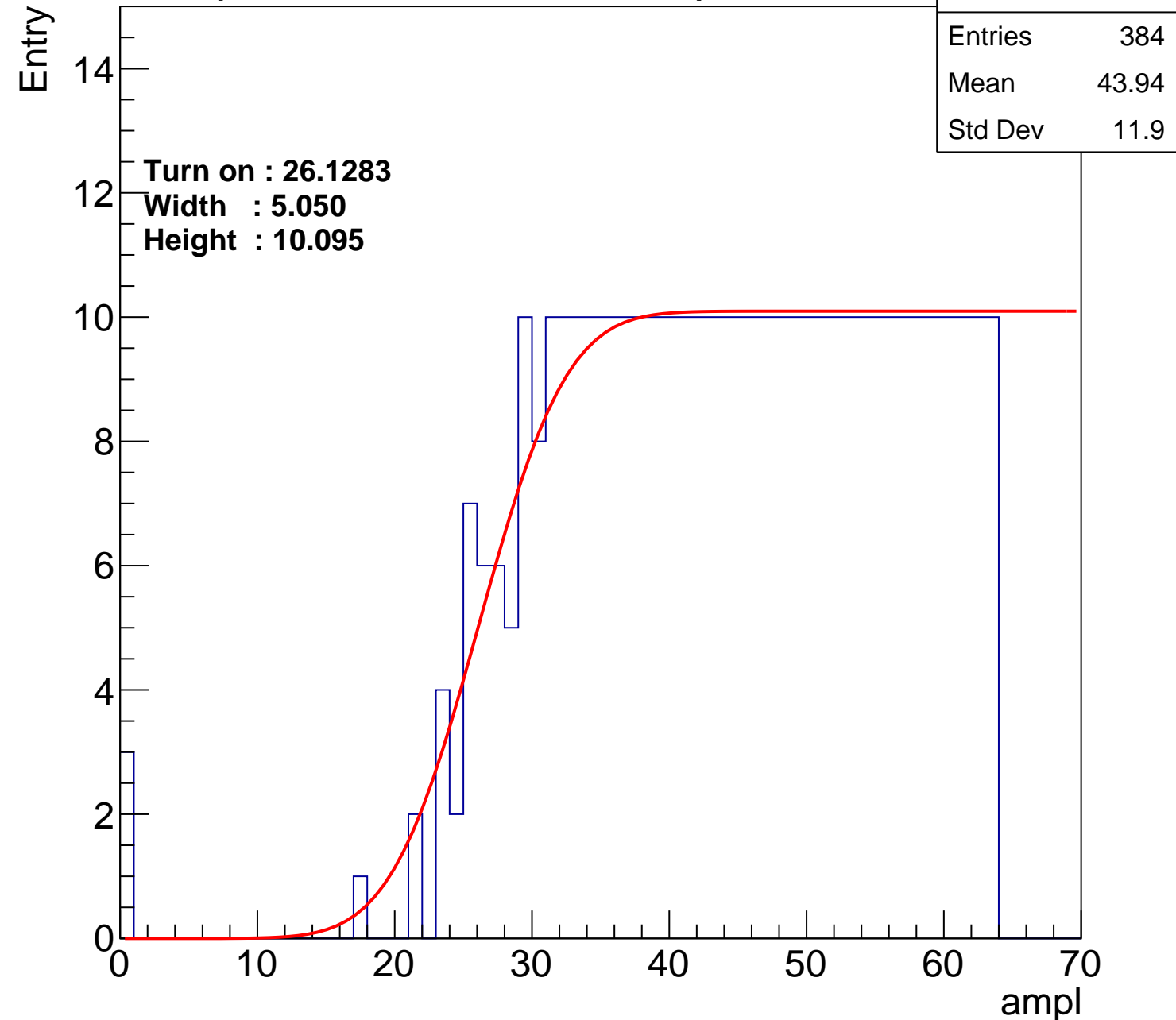
Width : 5.050

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.19
Std Dev	12.06

Turn on : 24.3966

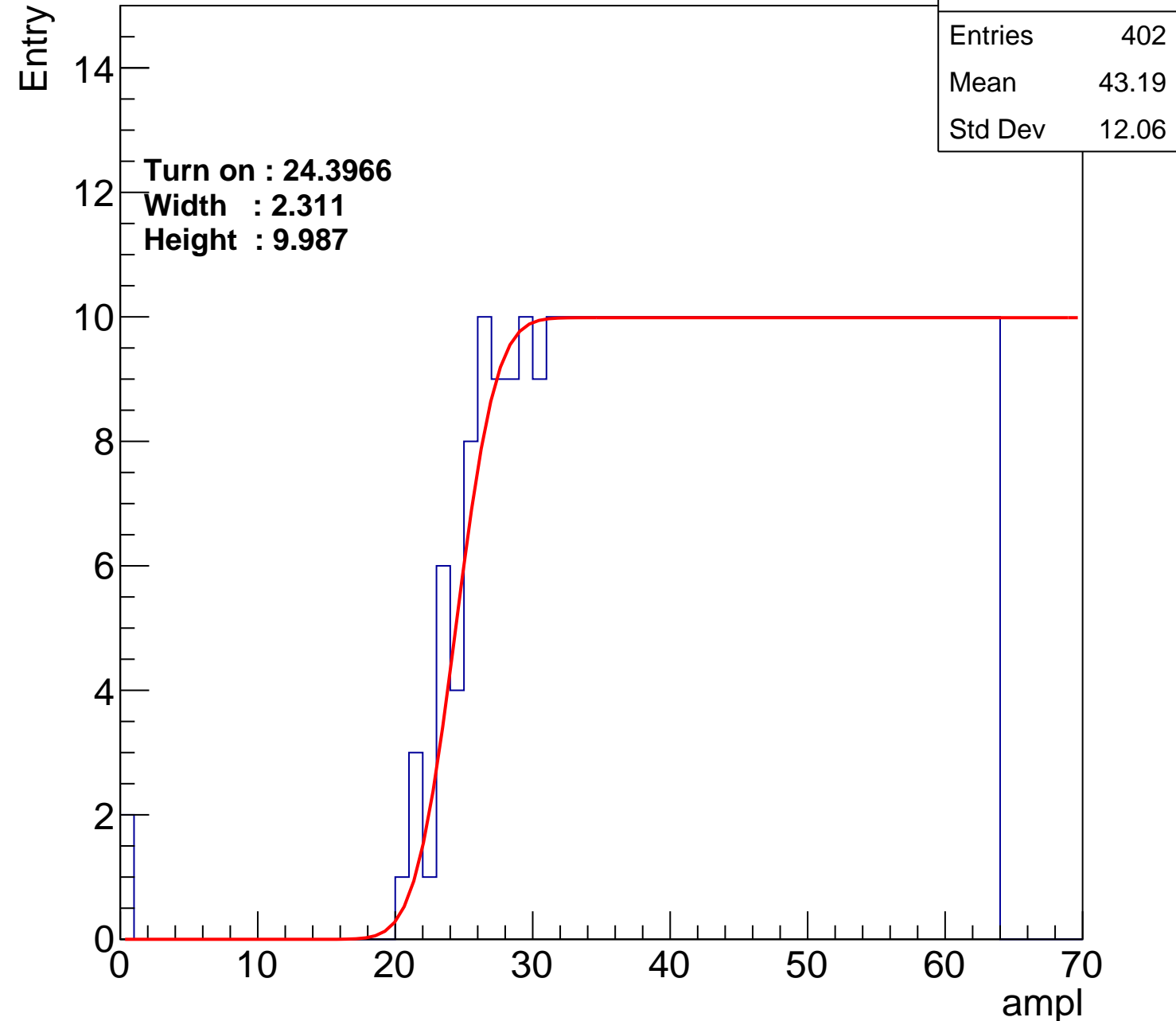
Width : 2.311

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.31
Std Dev	12.03

Turn on : 24.3451

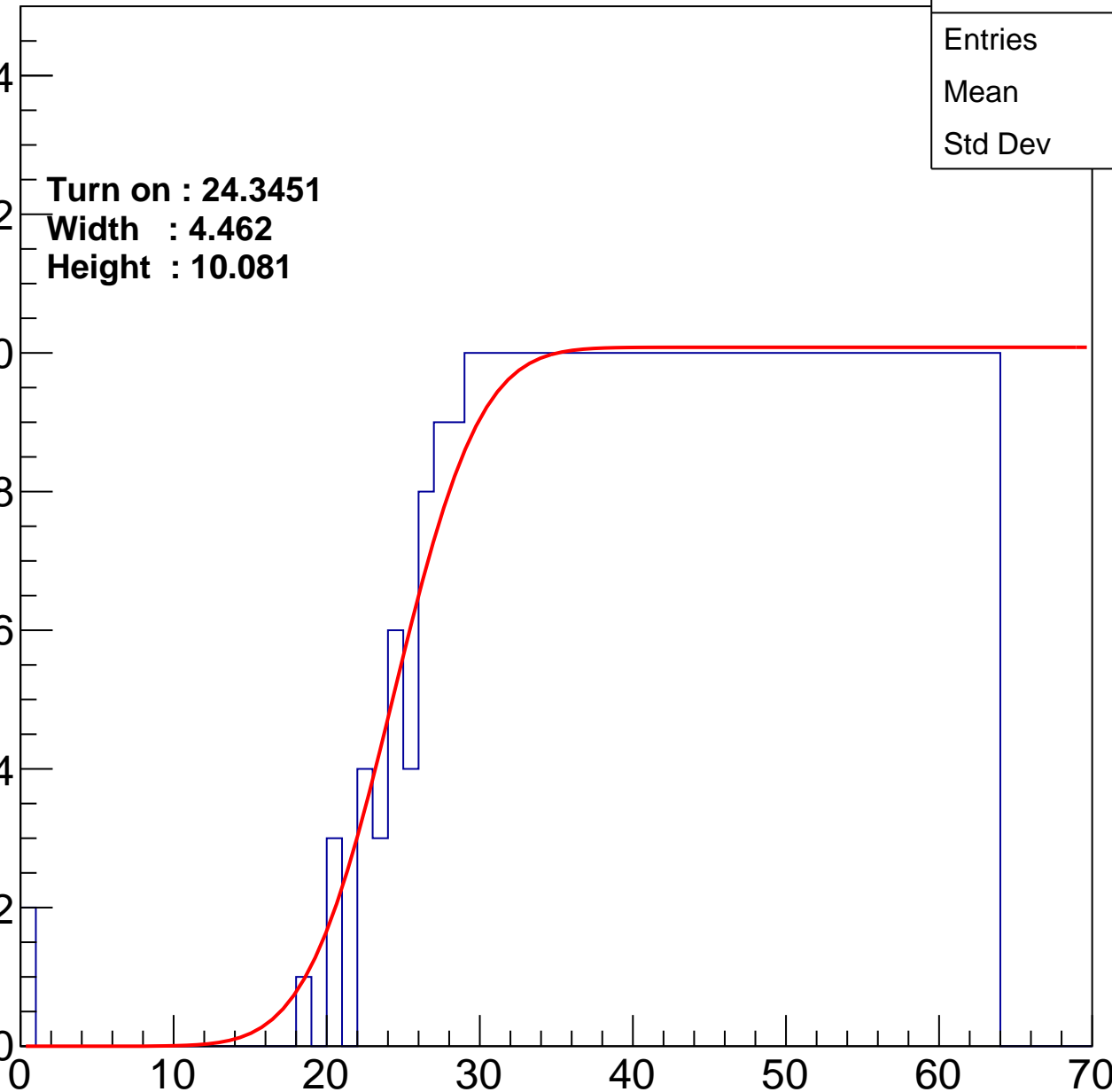
Width : 4.462

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.94
Std Dev	11.72

Turn on : 26.0134

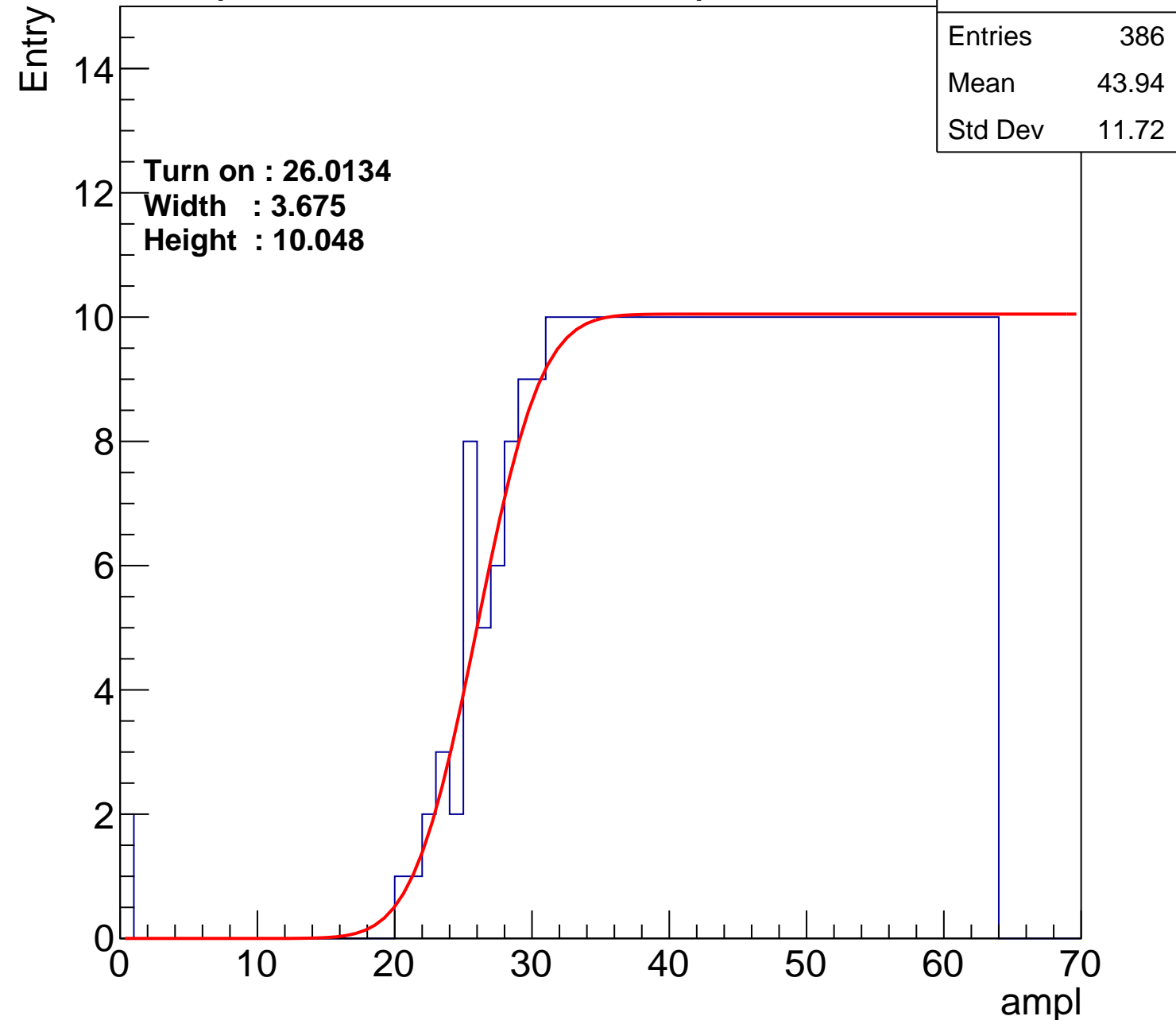
Width : 3.675

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch91

calib_packv5_042523_0143.root, FC#3, port B3

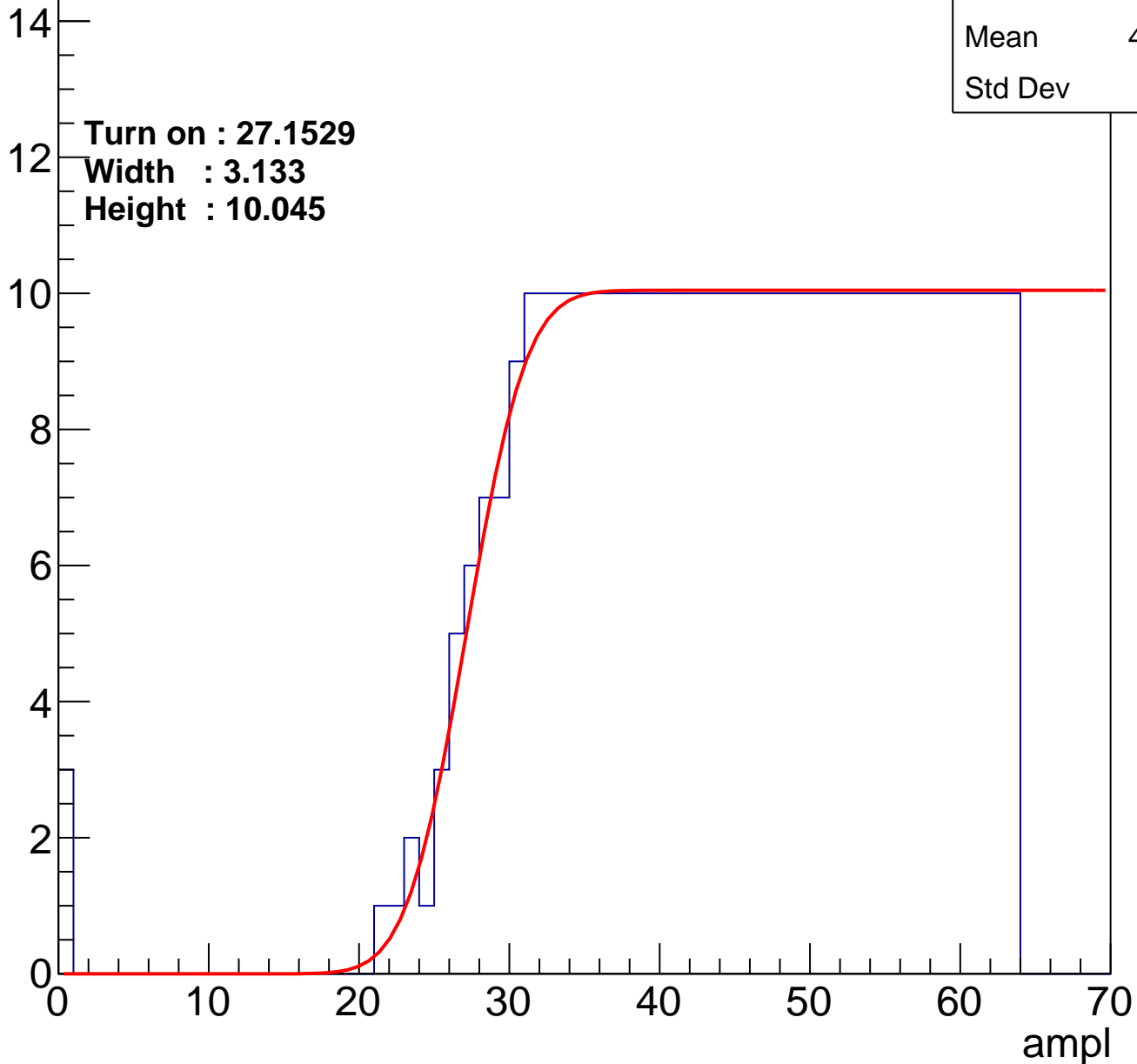
Entries	375
Mean	44.42
Std Dev	11.6

Turn on : 27.1529

Width : 3.133

Height : 10.045

Entry



B1L000S, U4-ch92

calib_packv5_042523_0143.root, FC#3, port B3

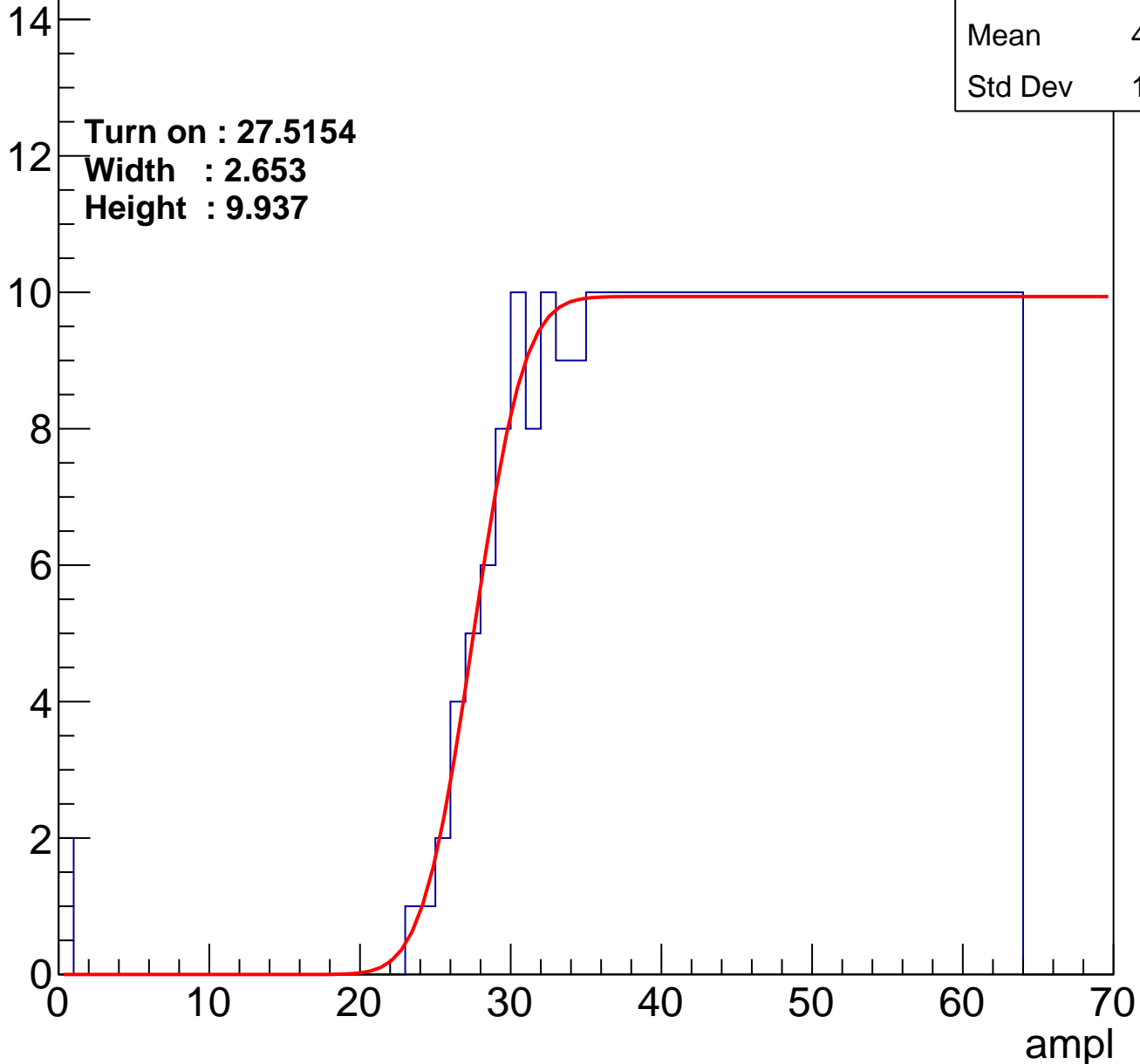
Entries	365
Mean	44.98
Std Dev	11.16

Turn on : 27.5154

Width : 2.653

Height : 9.937

Entry



B1L000S, U4-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.51
Std Dev	11.75

Turn on : 24.5489

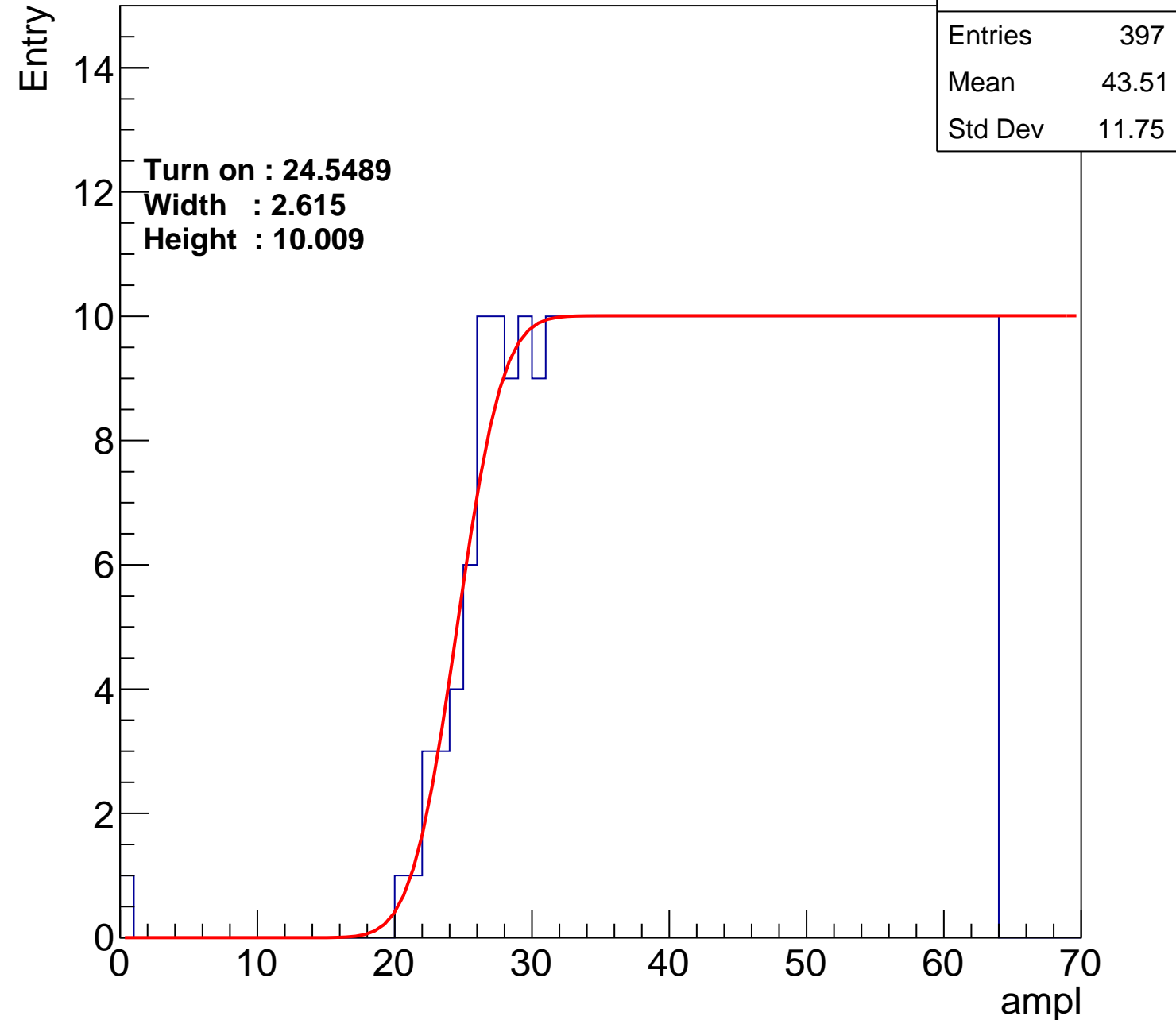
Width : 2.615

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch94

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.58
Std Dev	11.2

Turn on : 27.1686

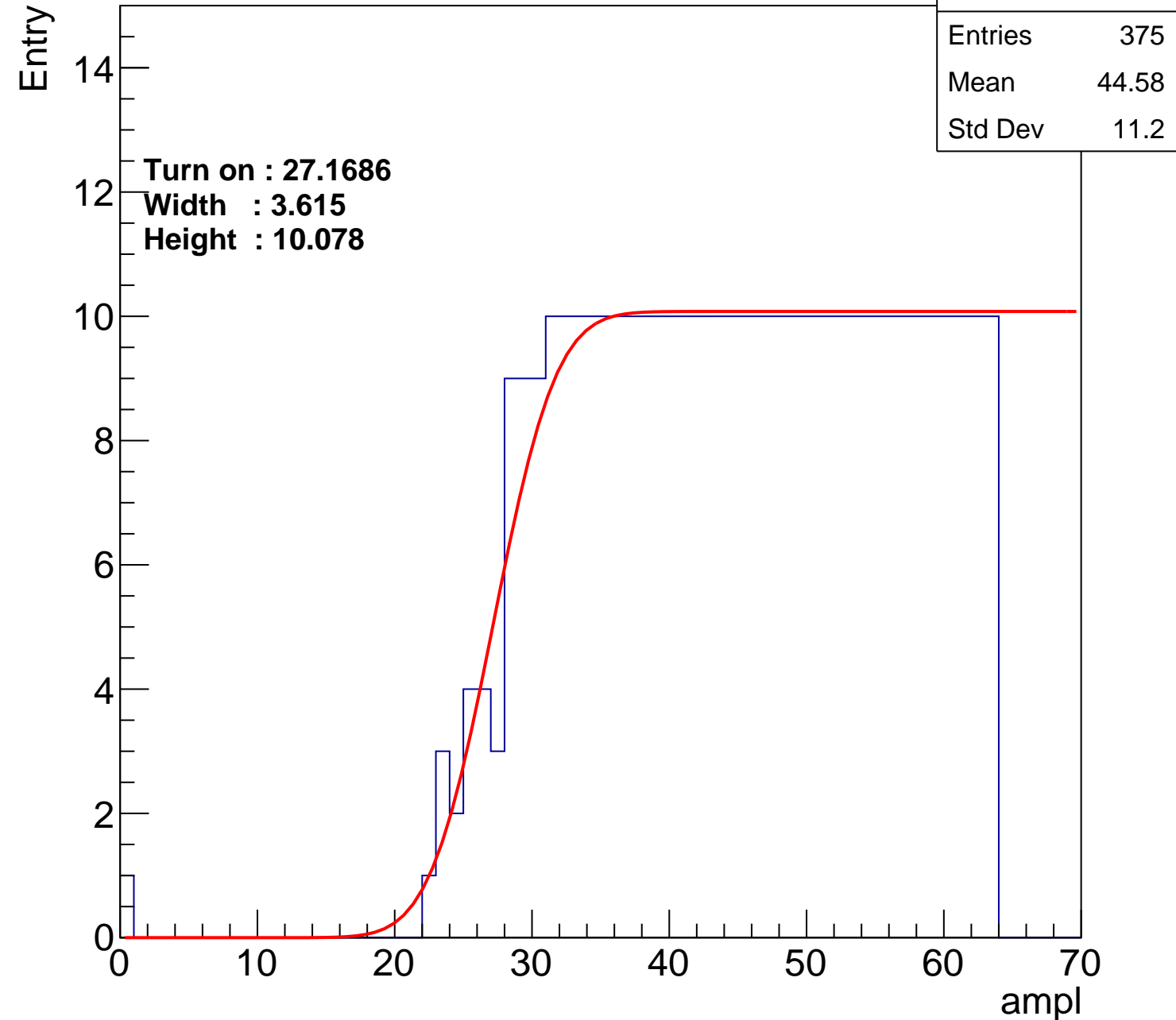
Width : 3.615

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.26
Std Dev	11.53

Turn on : 26.3786

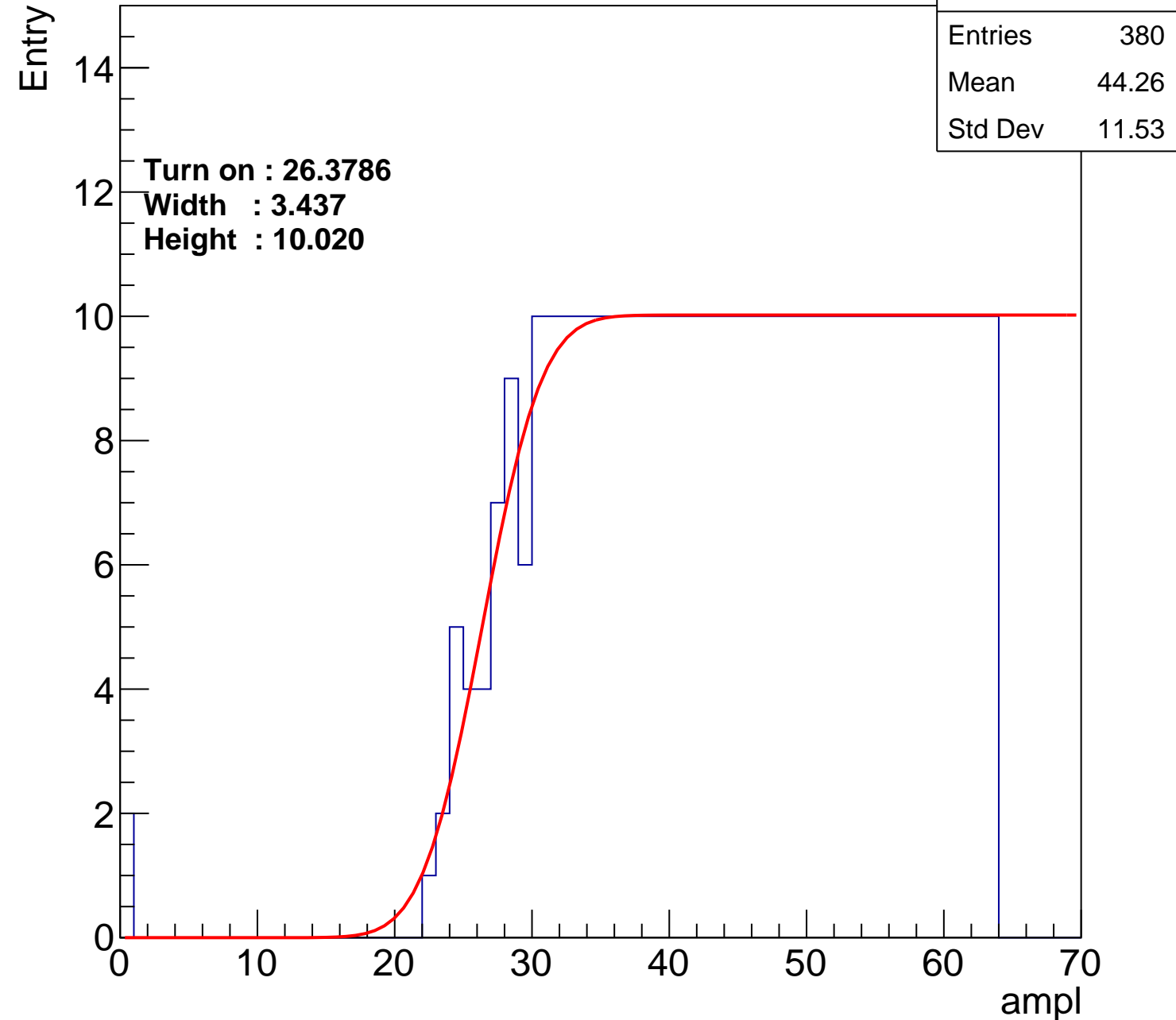
Width : 3.437

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	43.88
Std Dev	12.24

Turn on : 26.3059

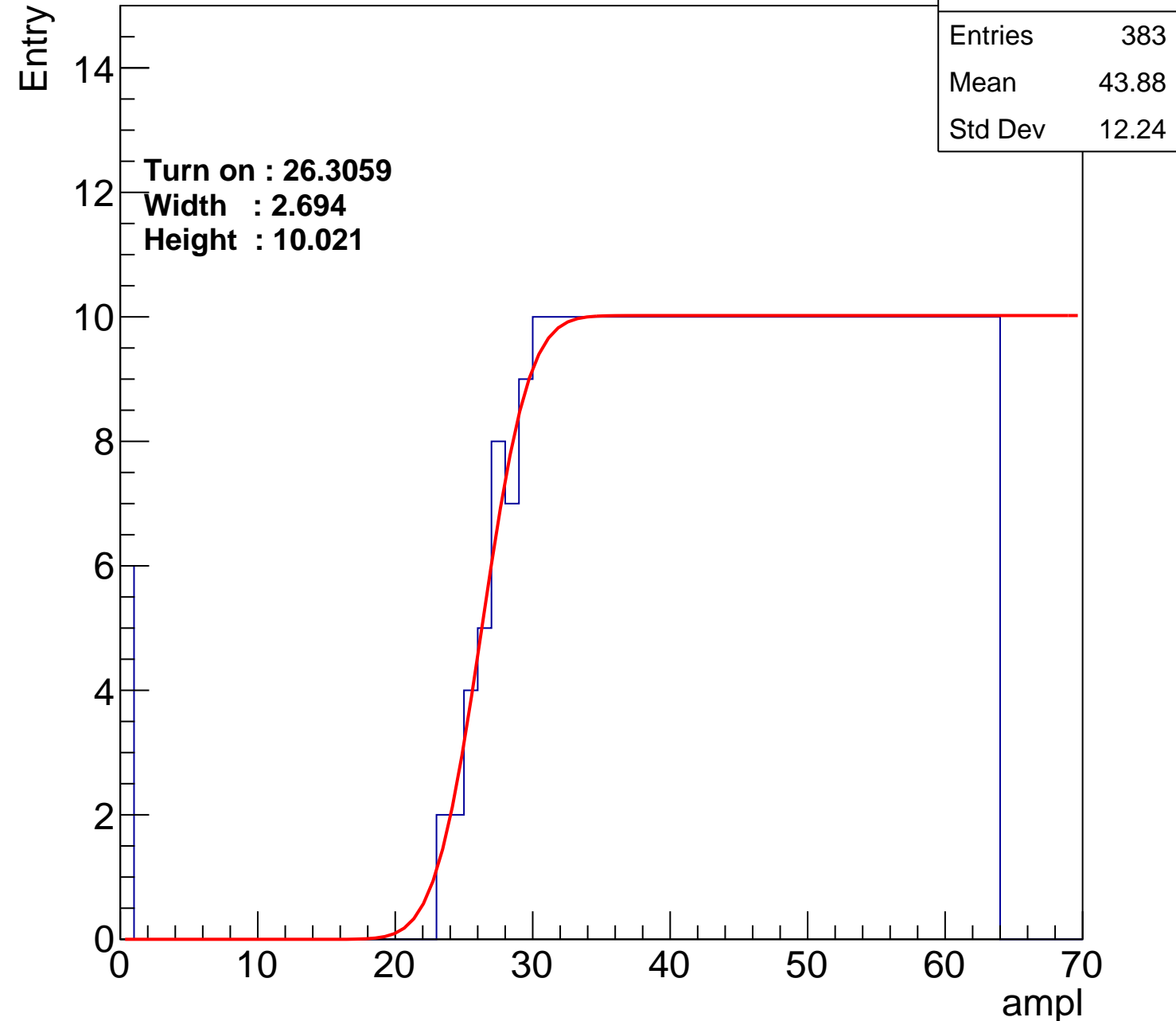
Width : 2.694

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.45
Std Dev	12.43

Turn on : 25.5221

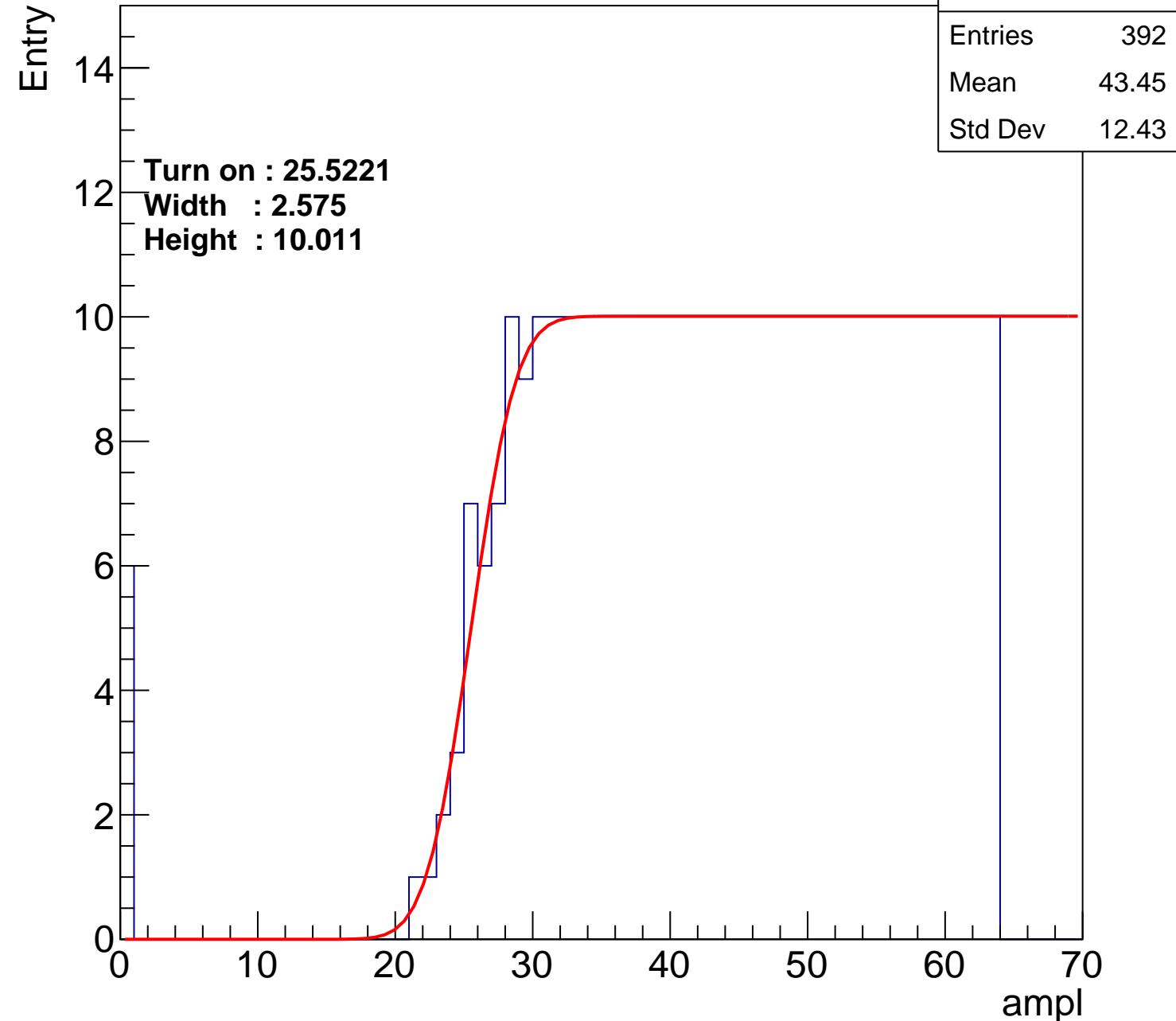
Width : 2.575

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.63
Std Dev	12.12

Turn on : 25.9971

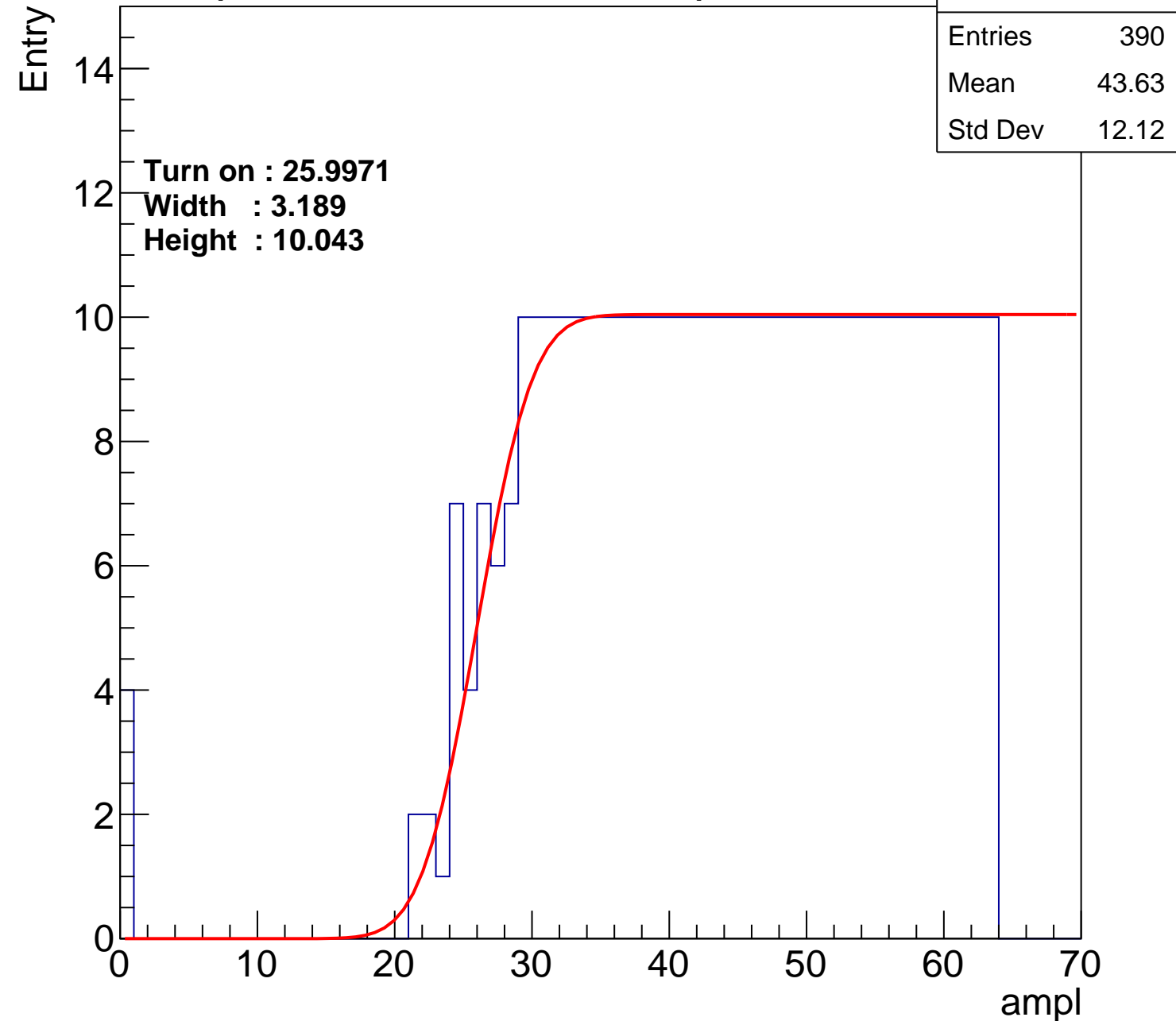
Width : 3.189

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch99

calib_packv5_042523_0143.root, FC#3, port B3

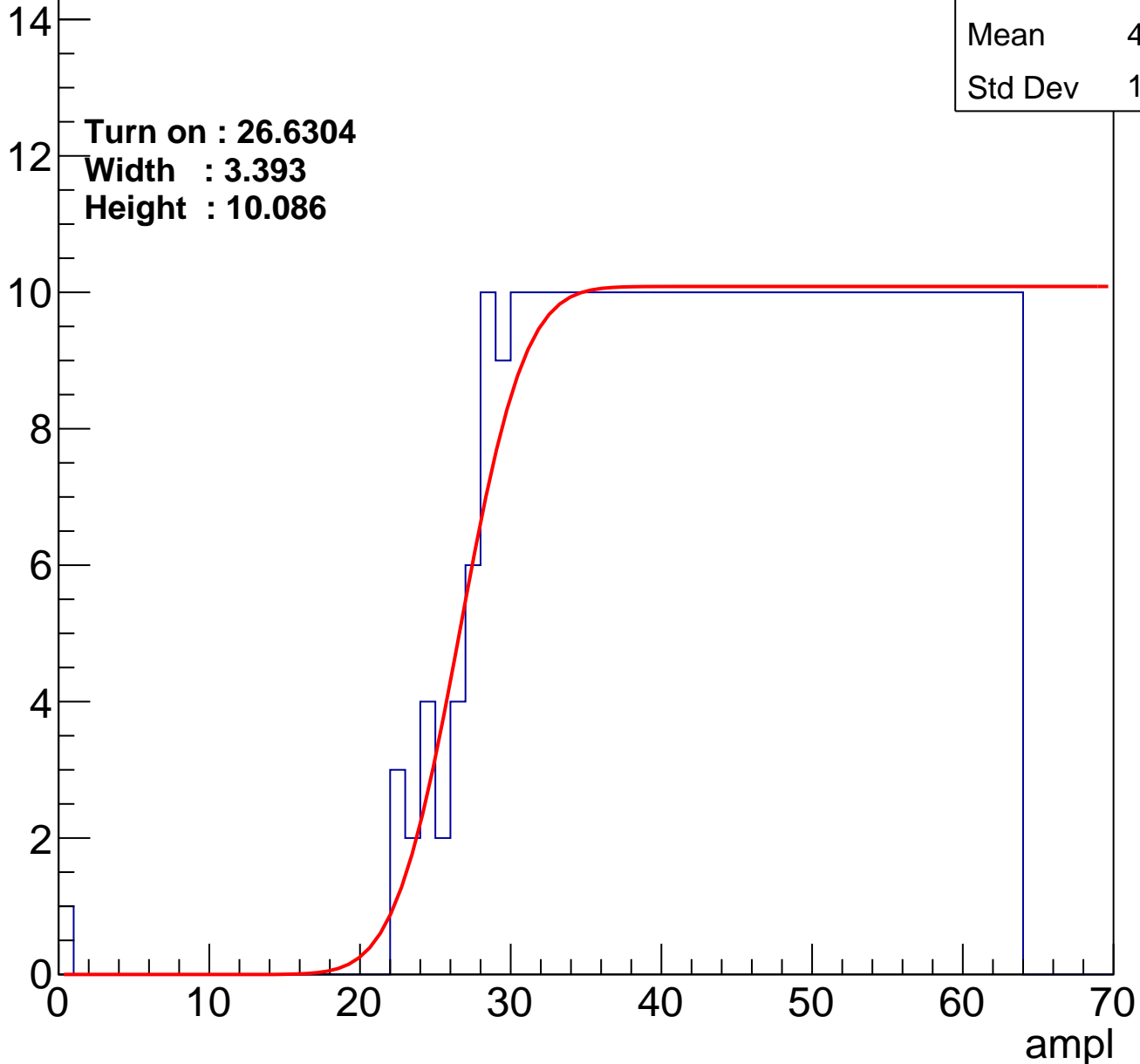
Entries	381
Mean	44.29
Std Dev	11.34

Turn on : 26.6304

Width : 3.393

Height : 10.086

Entry



B1L000S, U4-ch100

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.0491

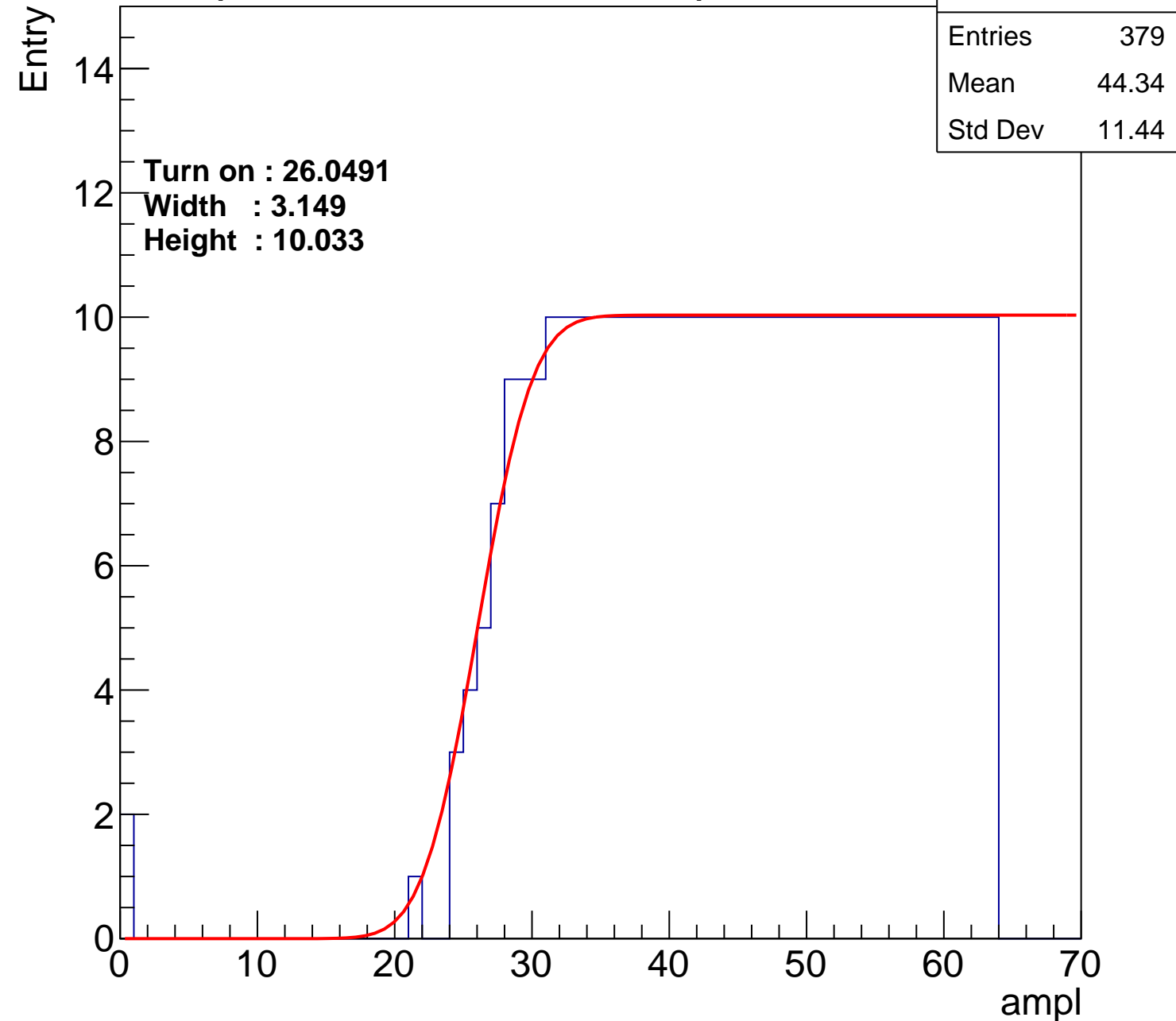
Width : 3.149

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.99
Std Dev	11.81

Turn on : 26.2043

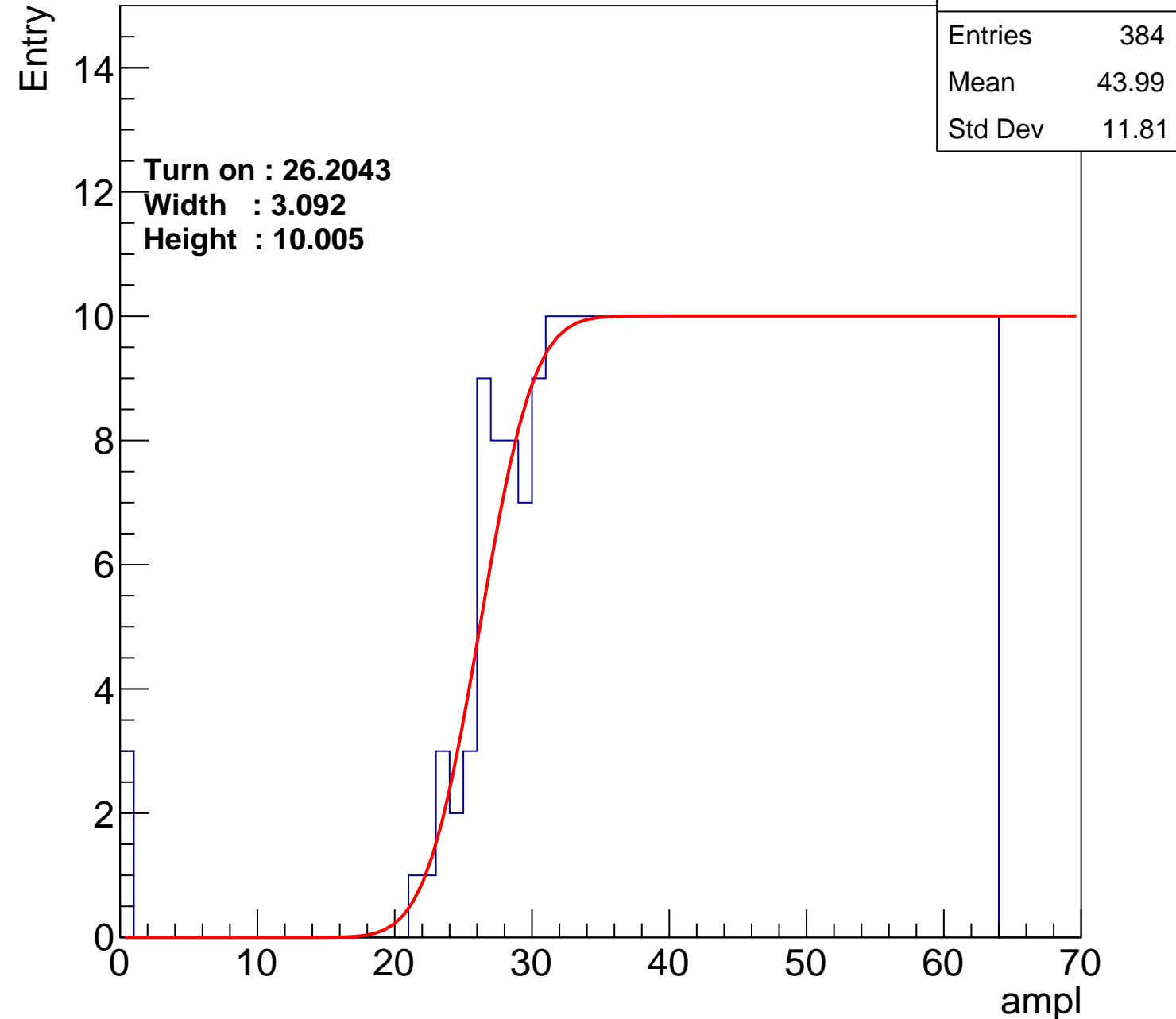
Width : 3.092

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch102

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 26.3441

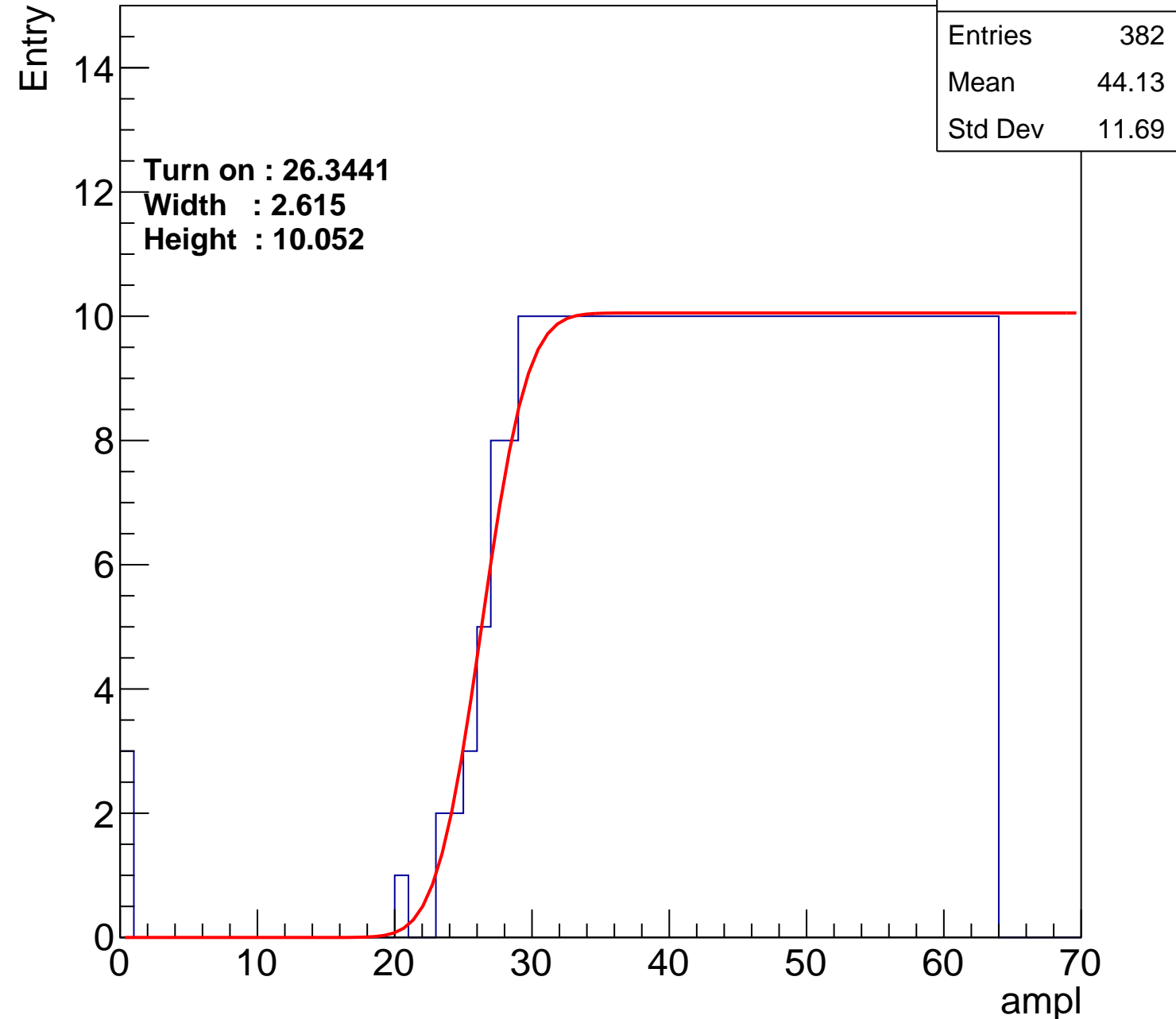
Width : 2.615

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch103

calib_packv5_042523_0143.root, FC#3, port B3

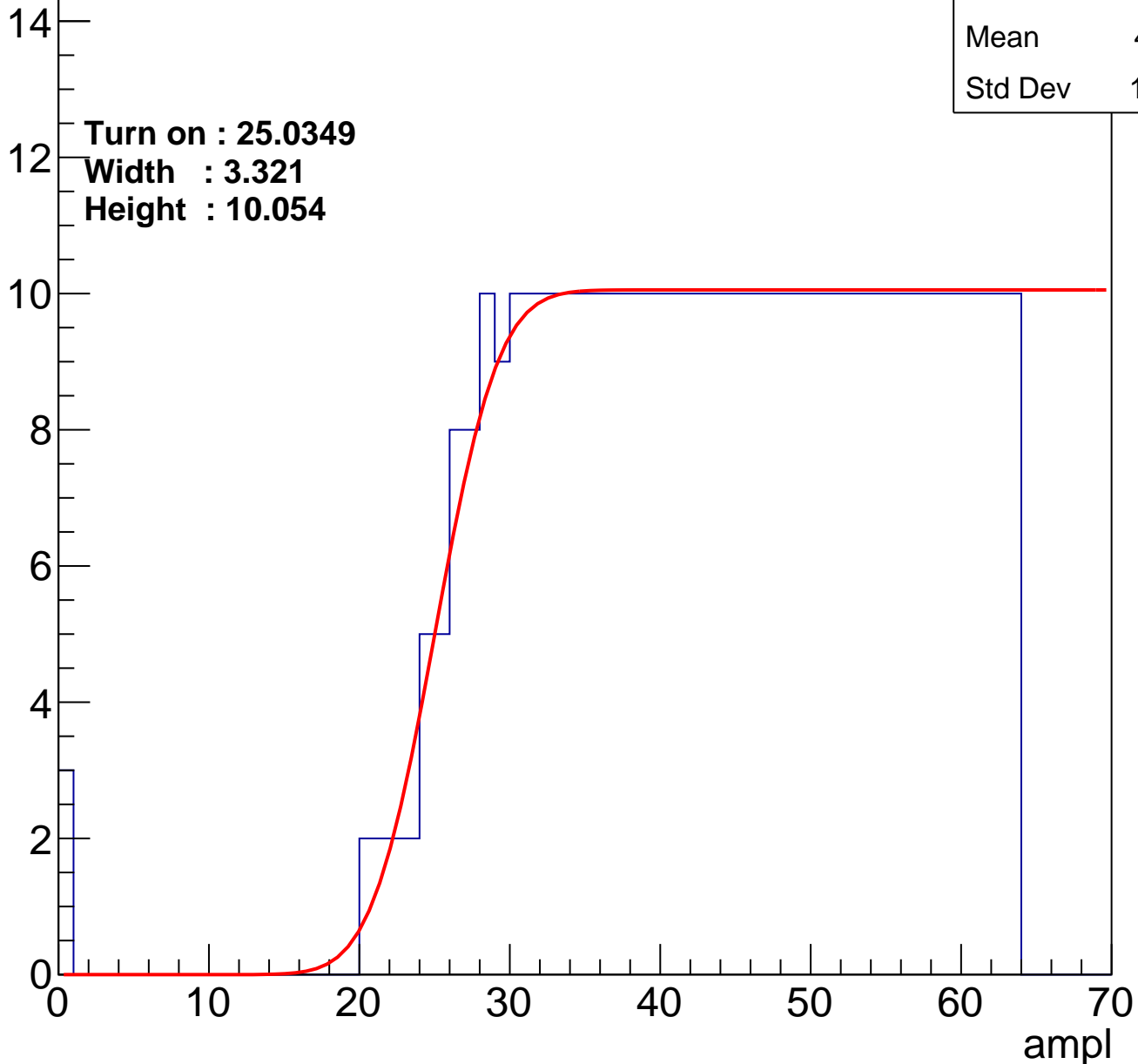
Entries	396
Mean	43.41
Std Dev	12.09

Turn on : 25.0349

Width : 3.321

Height : 10.054

Entry



B1L000S, U4-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.46
Std Dev	12.06

Turn on : 24.8178

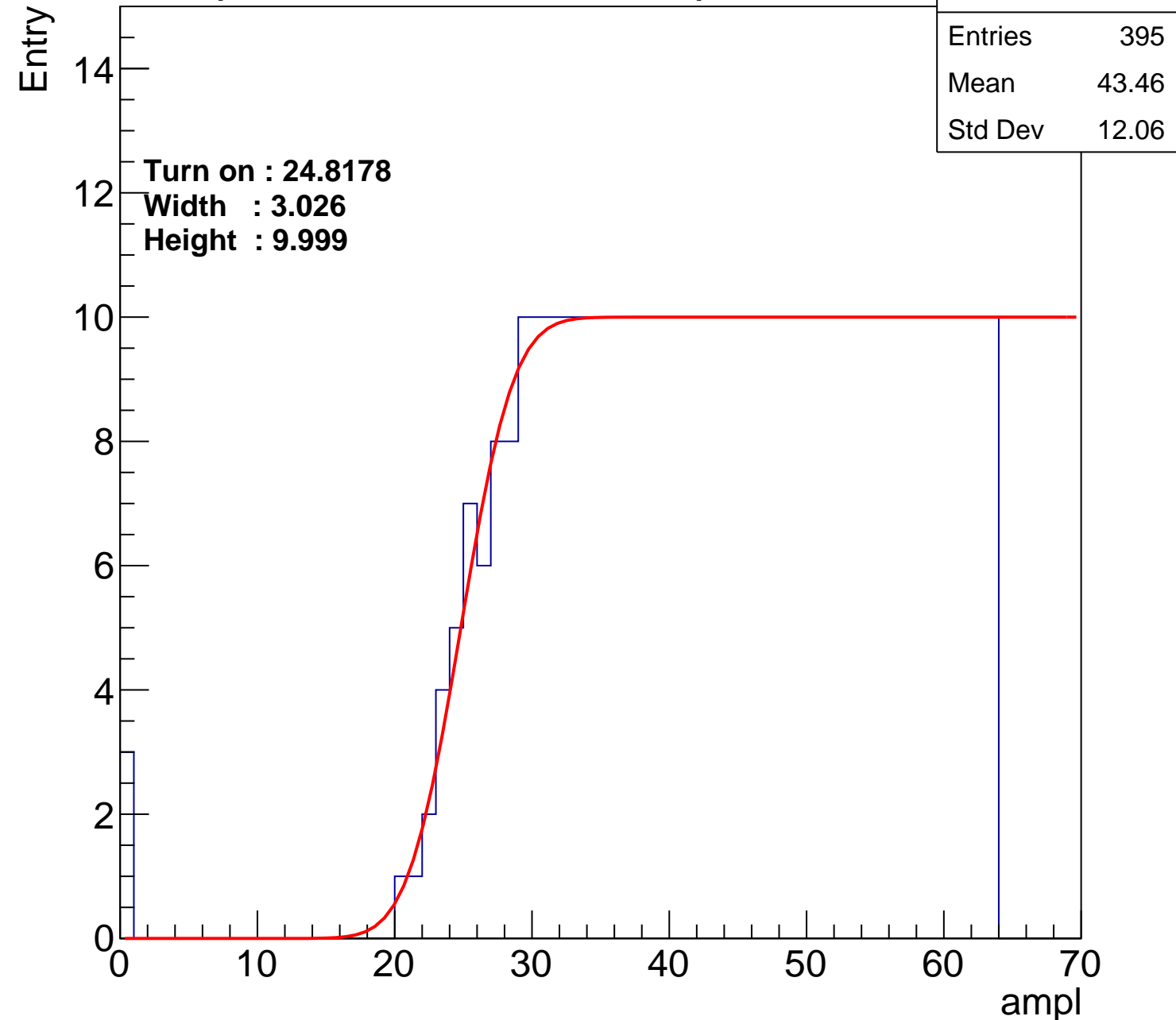
Width : 3.026

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.2690

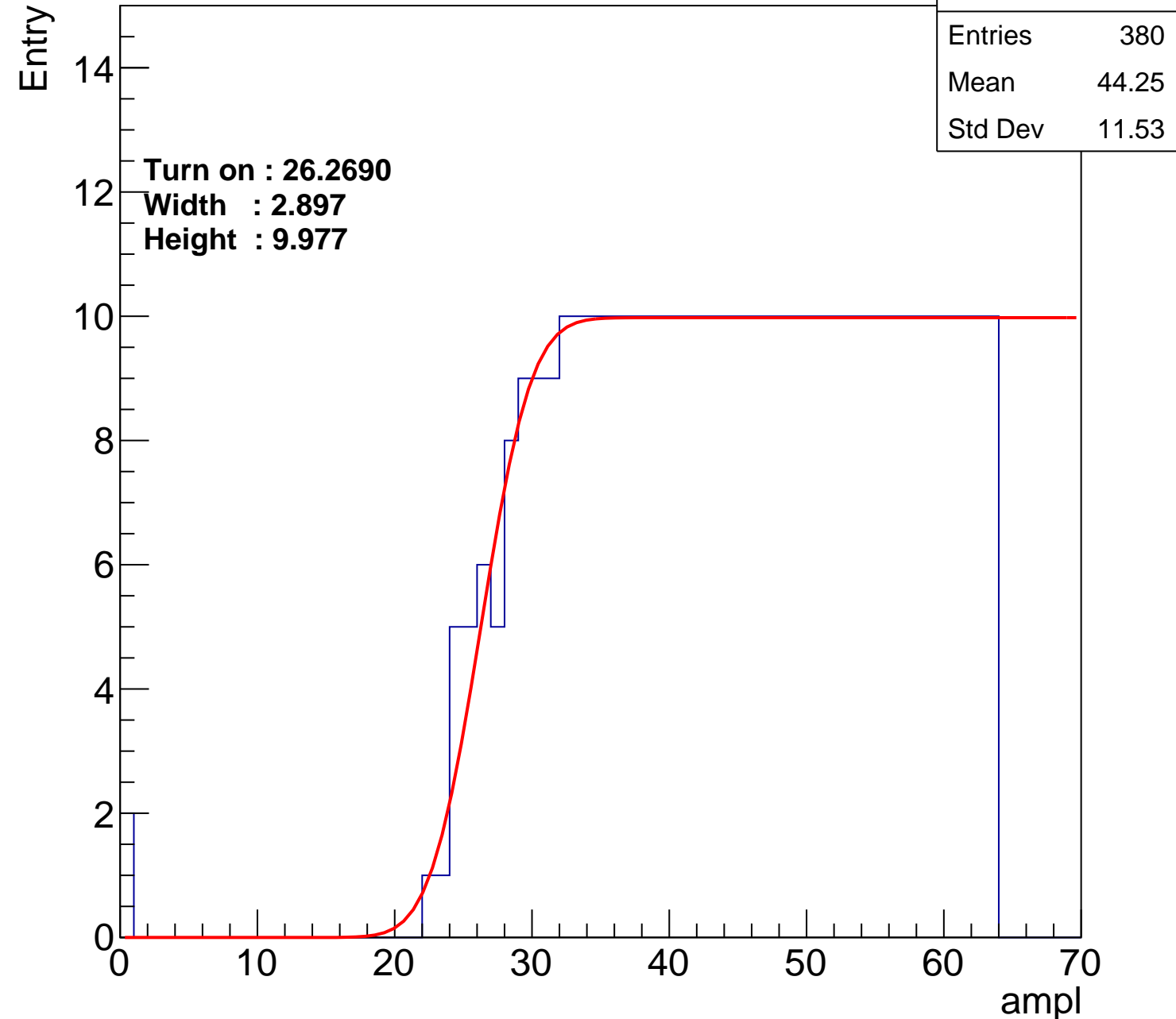
Width : 2.897

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	407
Mean	42.91
Std Dev	12.31

Turn on : 23.1115

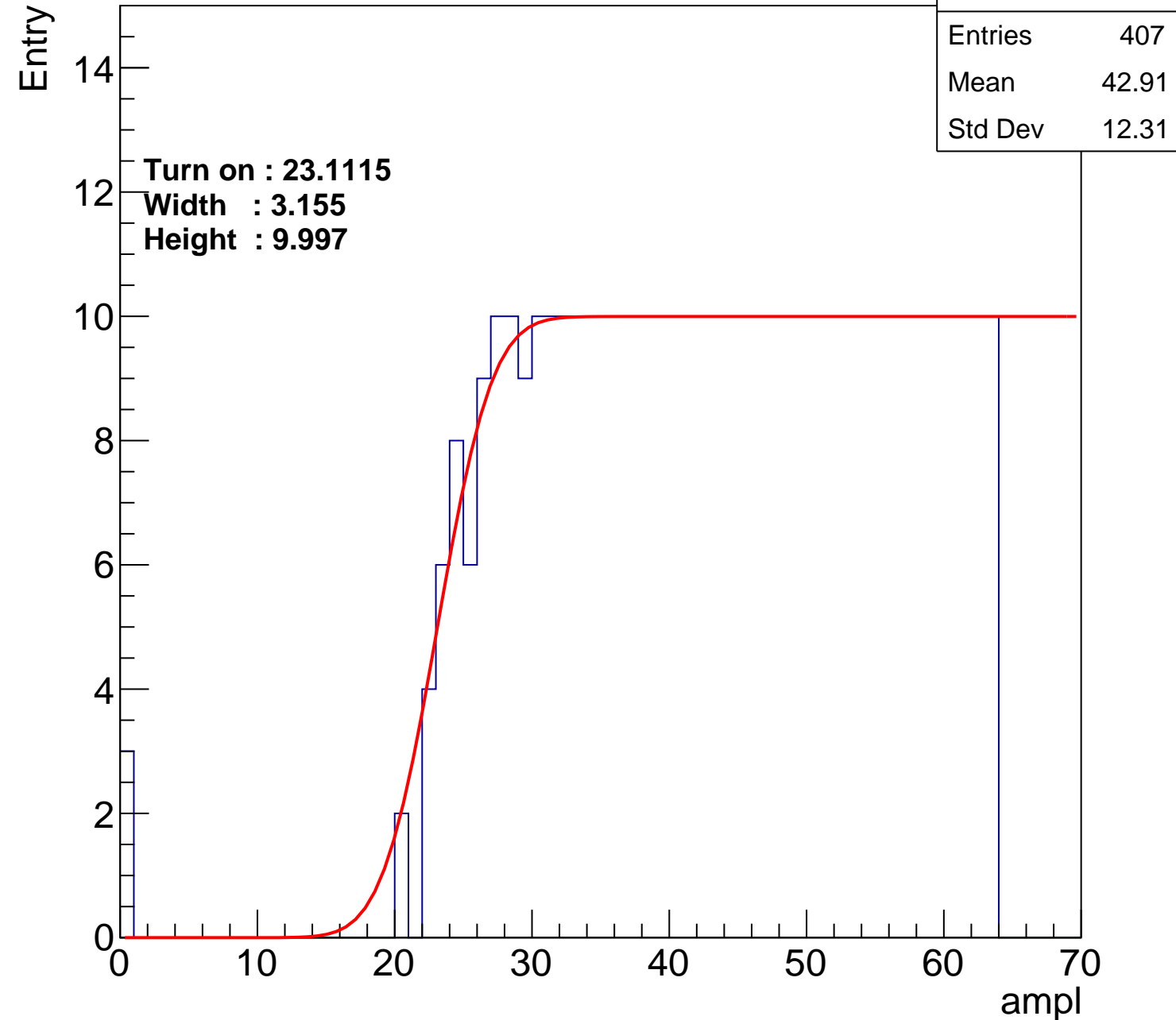
Width : 3.155

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	407
Mean	42.81
Std Dev	12.52

Turn on : 23.5073

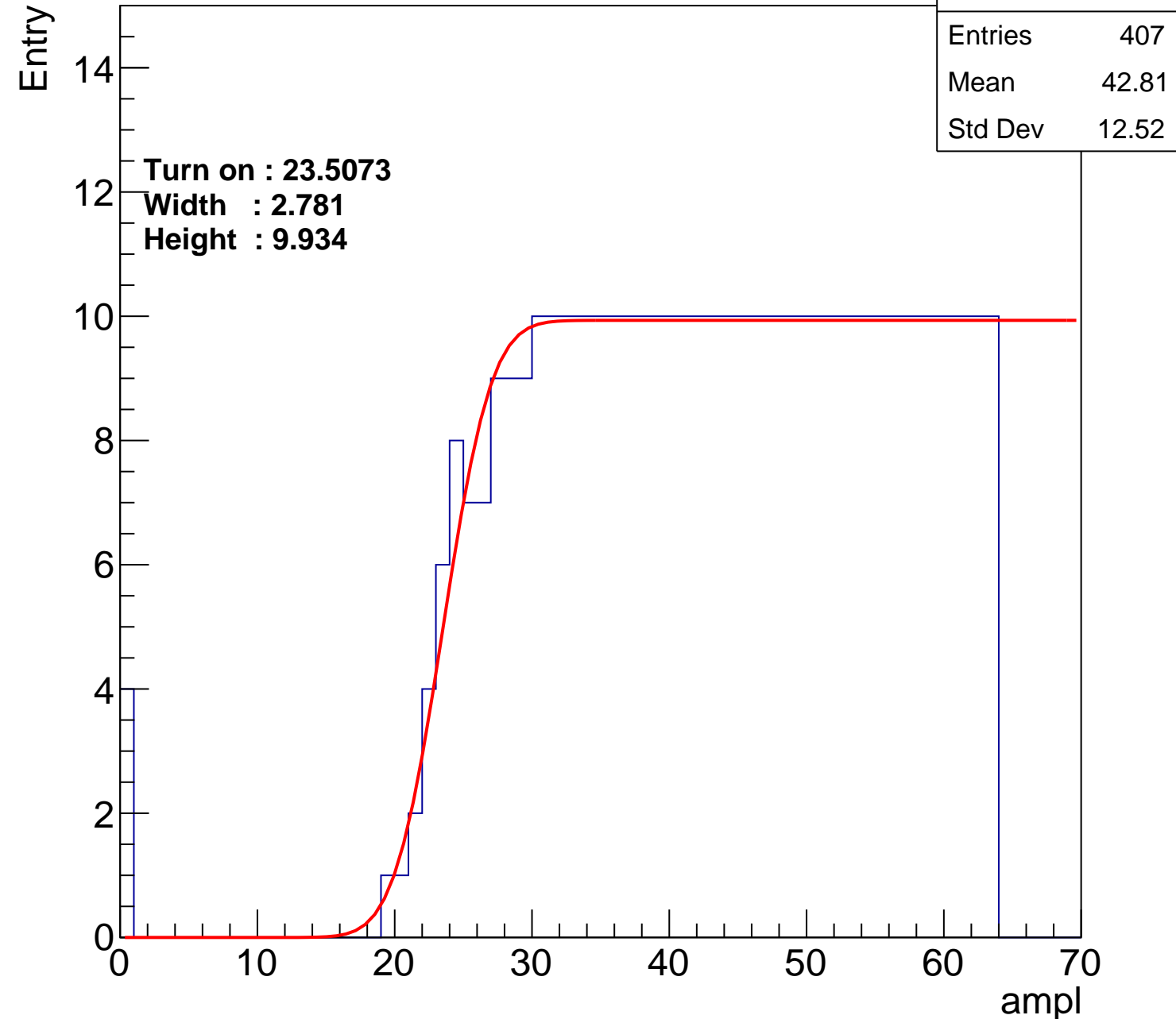
Width : 2.781

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch108

calib_packv5_042523_0143.root, FC#3, port B3

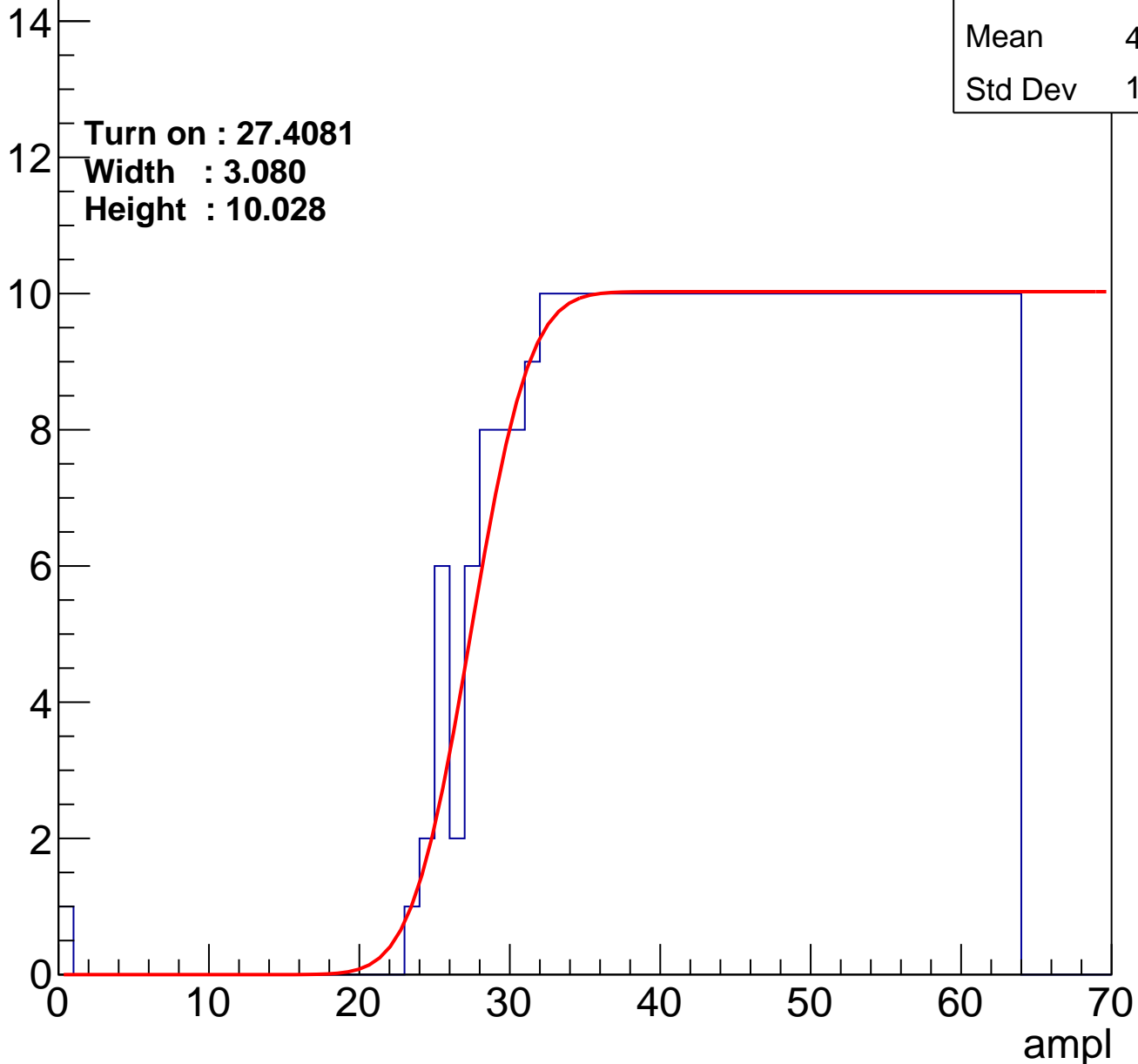
Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 27.4081

Width : 3.080

Height : 10.028

Entry



B1L000S, U4-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.35
Std Dev	12.31

Turn on : 24.3615

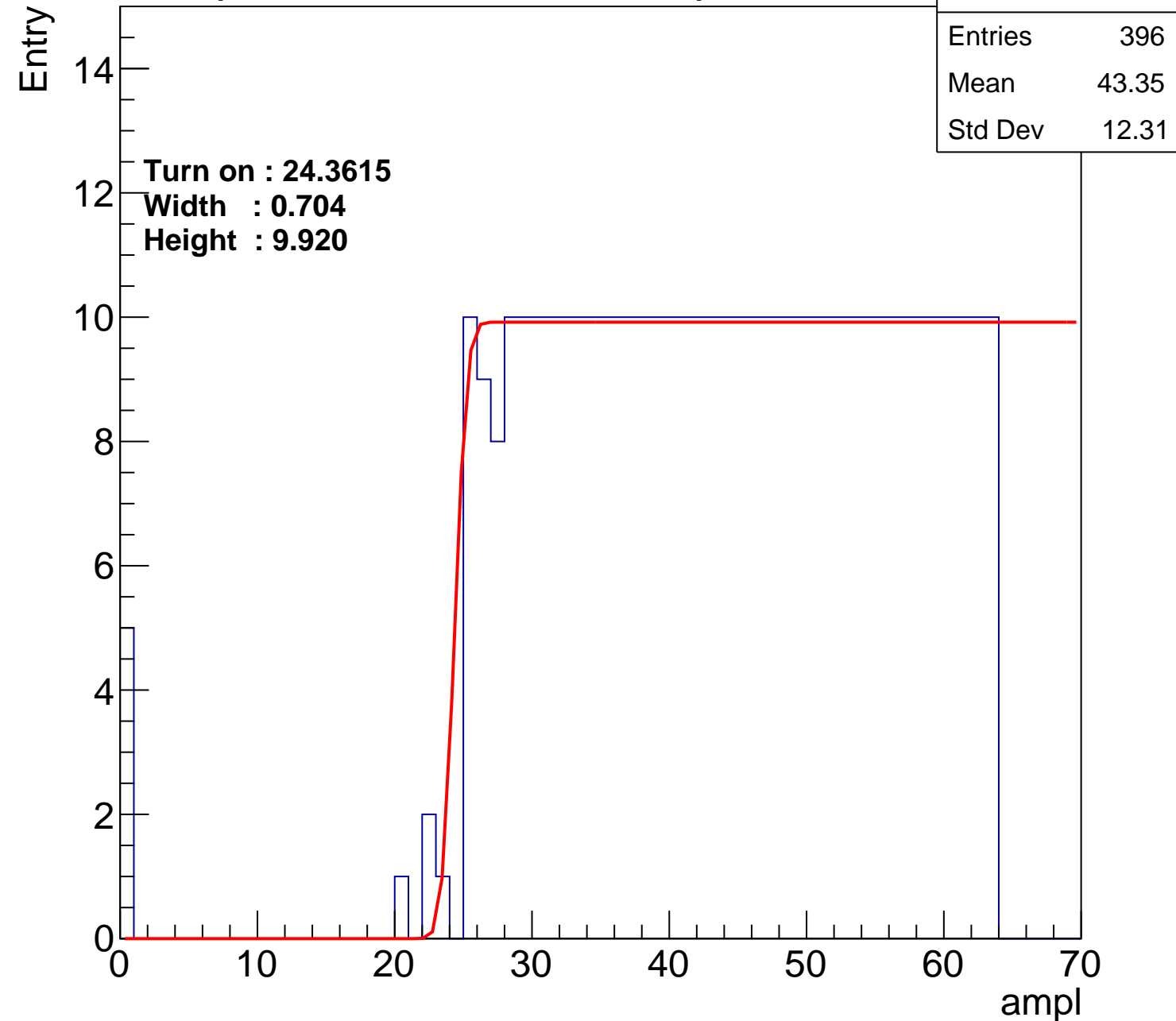
Width : 0.704

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch110

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.22
Std Dev	12.35

Turn on : 25.0726

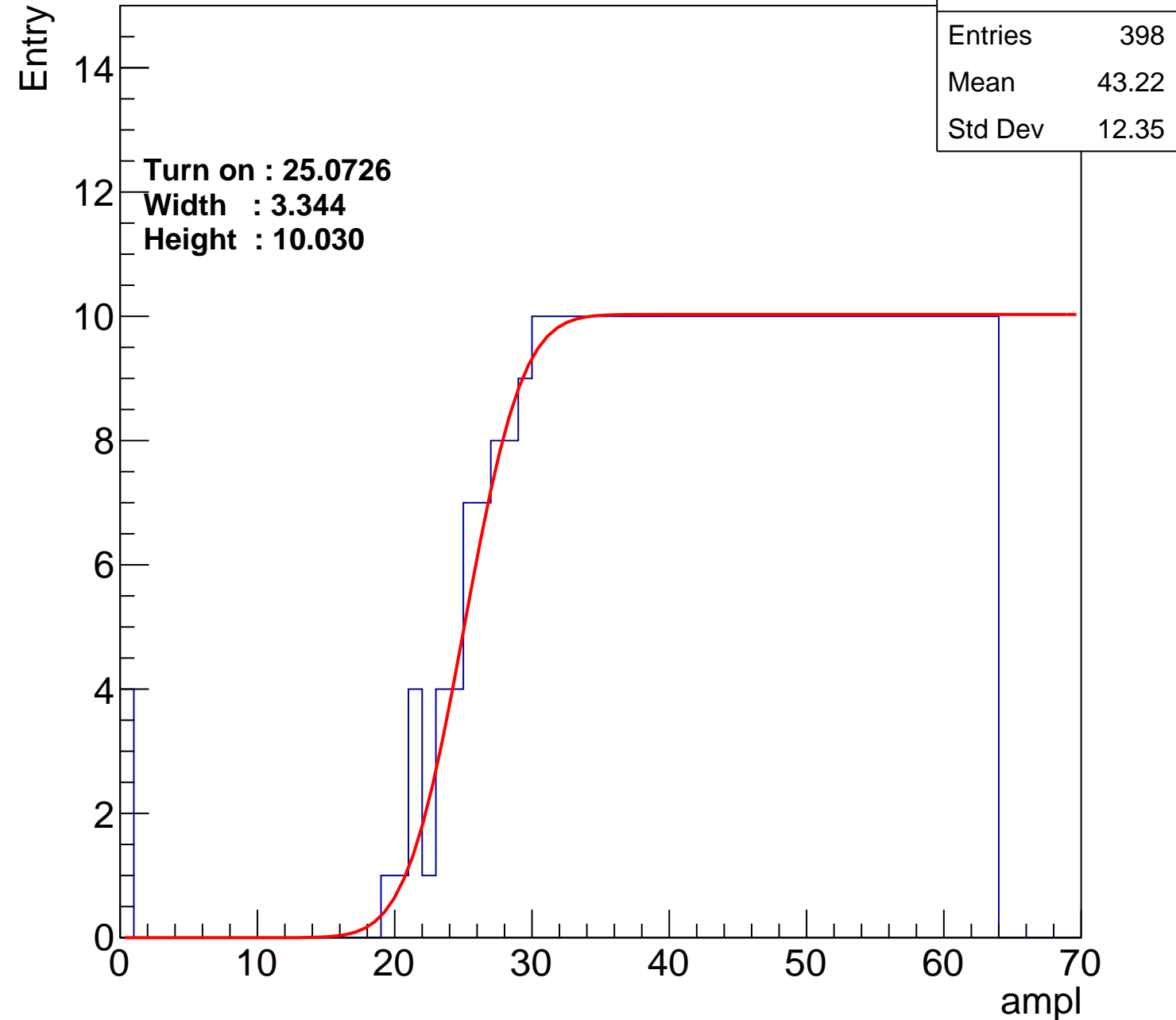
Width : 3.344

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.24
Std Dev	12.27

Turn on : 24.7052

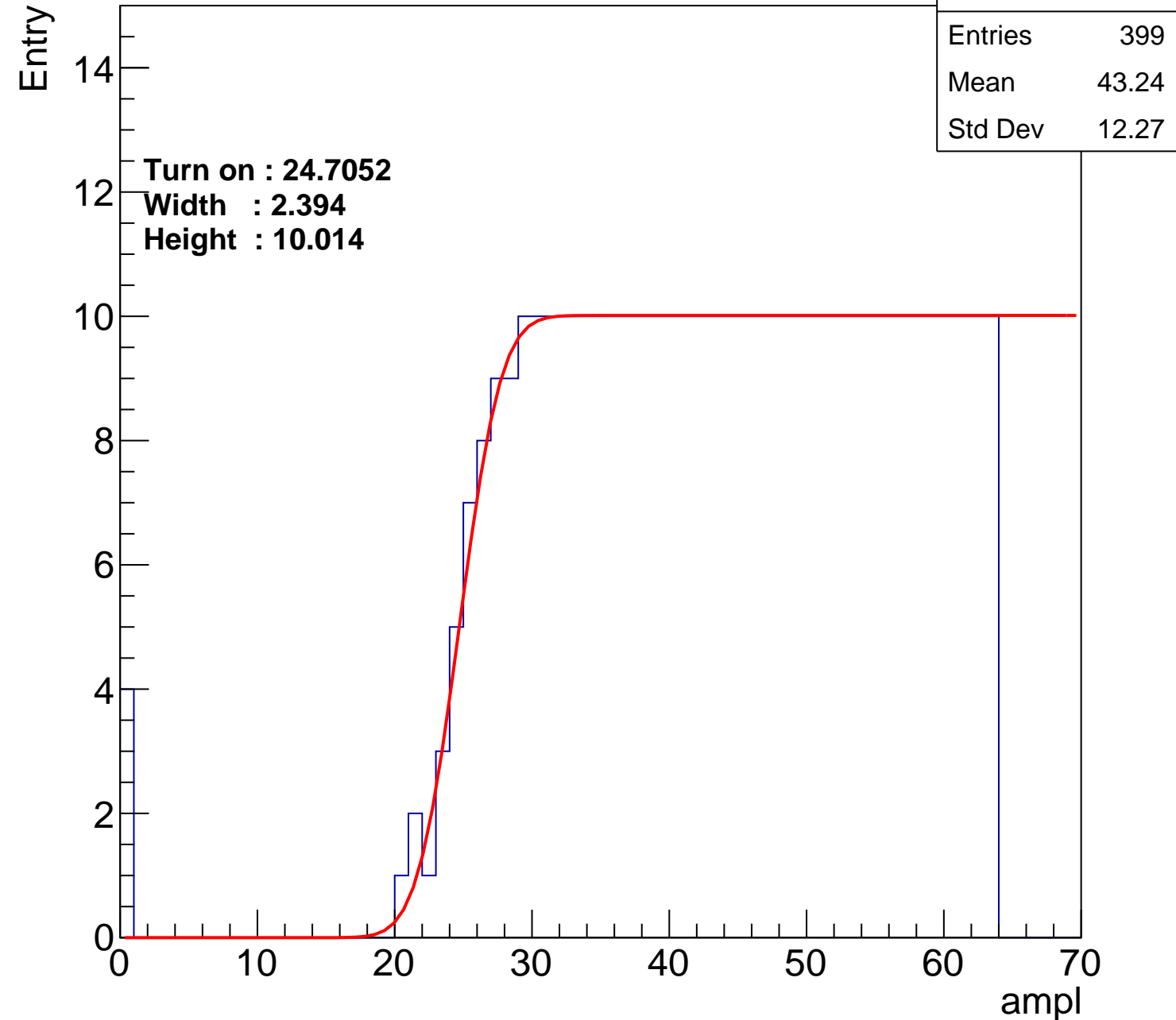
Width : 2.394

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch112

calib_packv5_042523_0143.root, FC#3, port B3

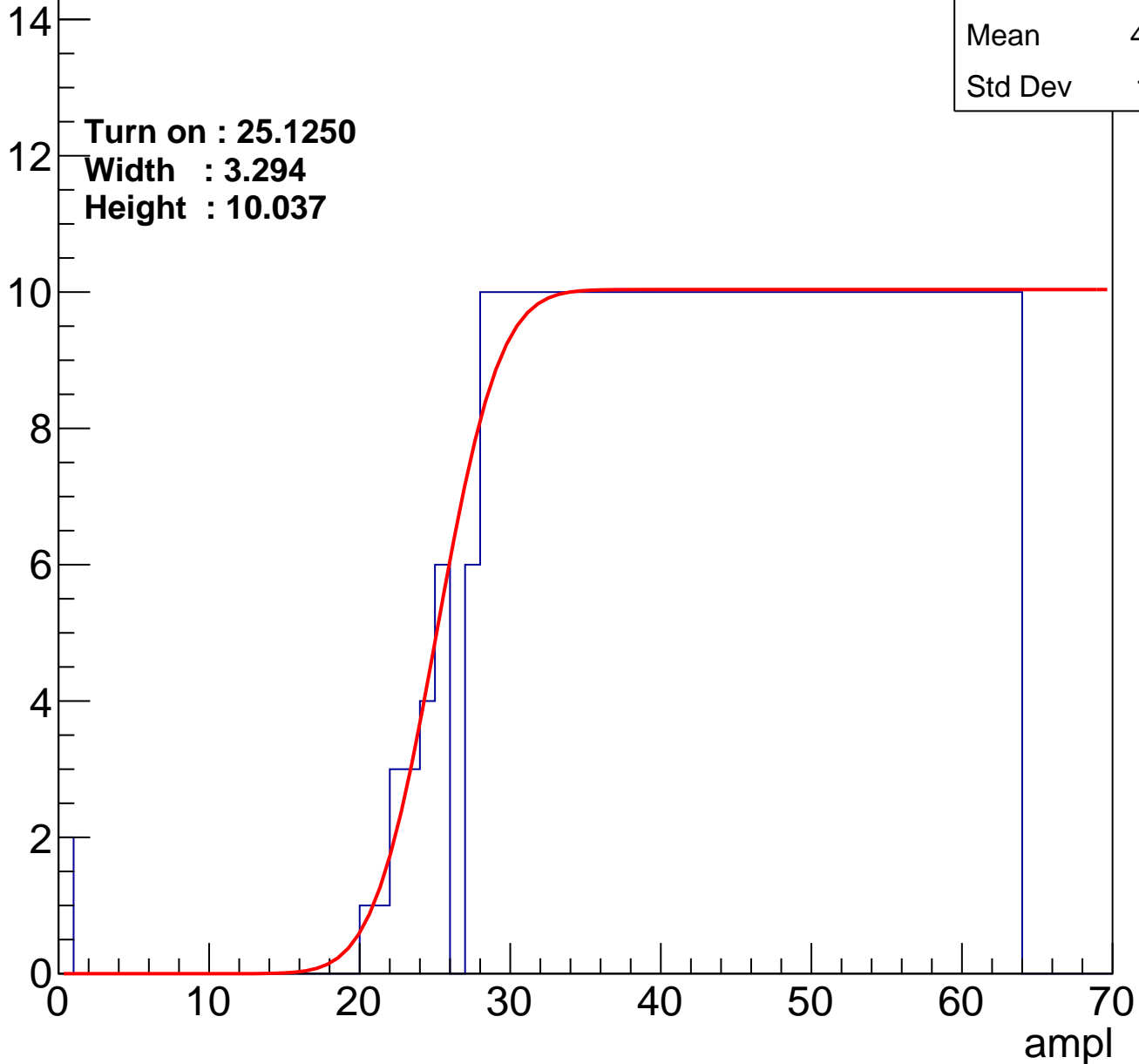
Entries	386
Mean	43.95
Std Dev	11.71

Turn on : 25.1250

Width : 3.294

Height : 10.037

Entry



B1L000S, U4-ch113

calib_packv5_042523_0143.root, FC#3, port B3

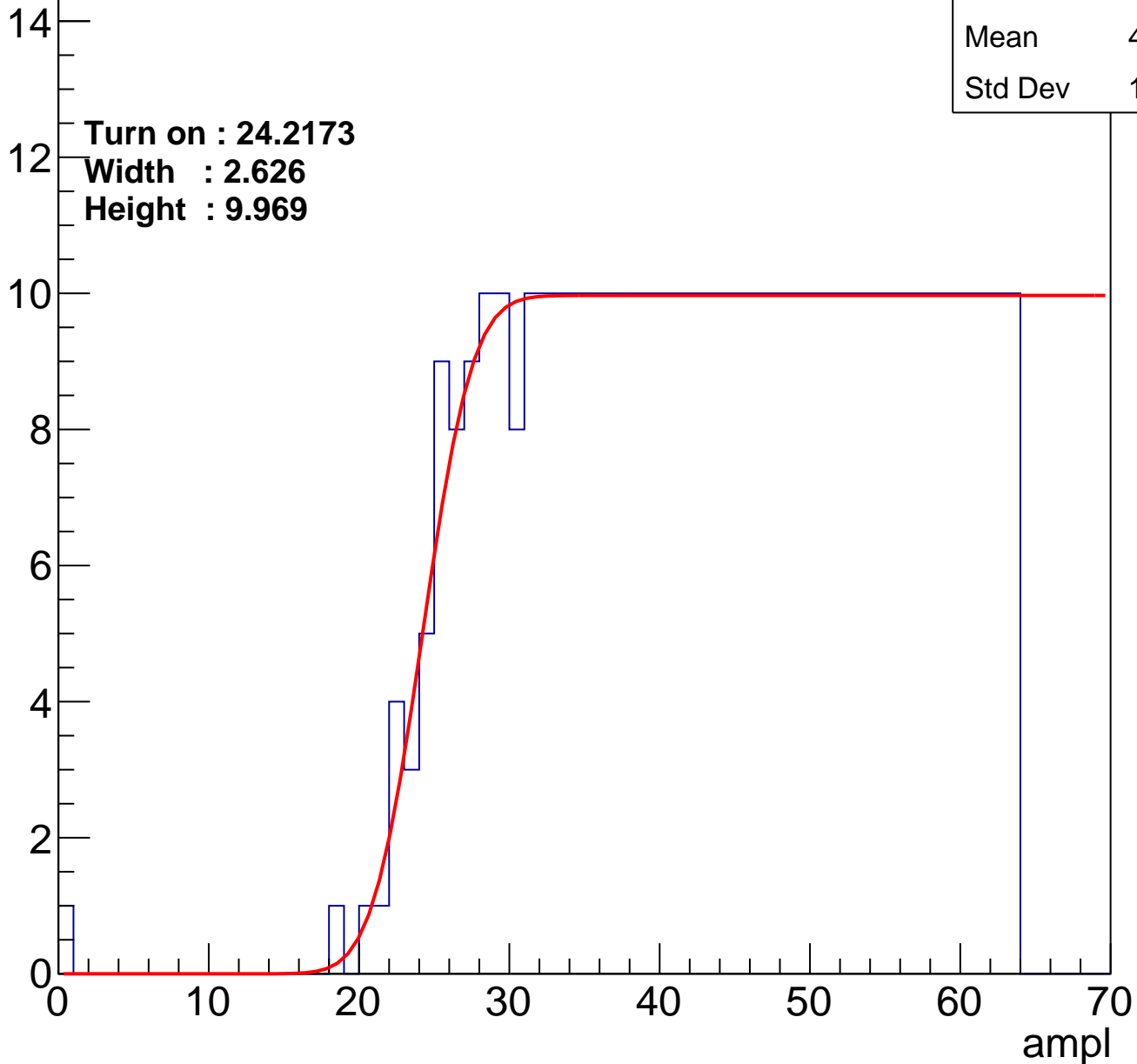
Entries	400
Mean	43.33
Std Dev	11.89

Turn on : 24.2173

Width : 2.626

Height : 9.969

Entry



B1L000S, U4-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.7879

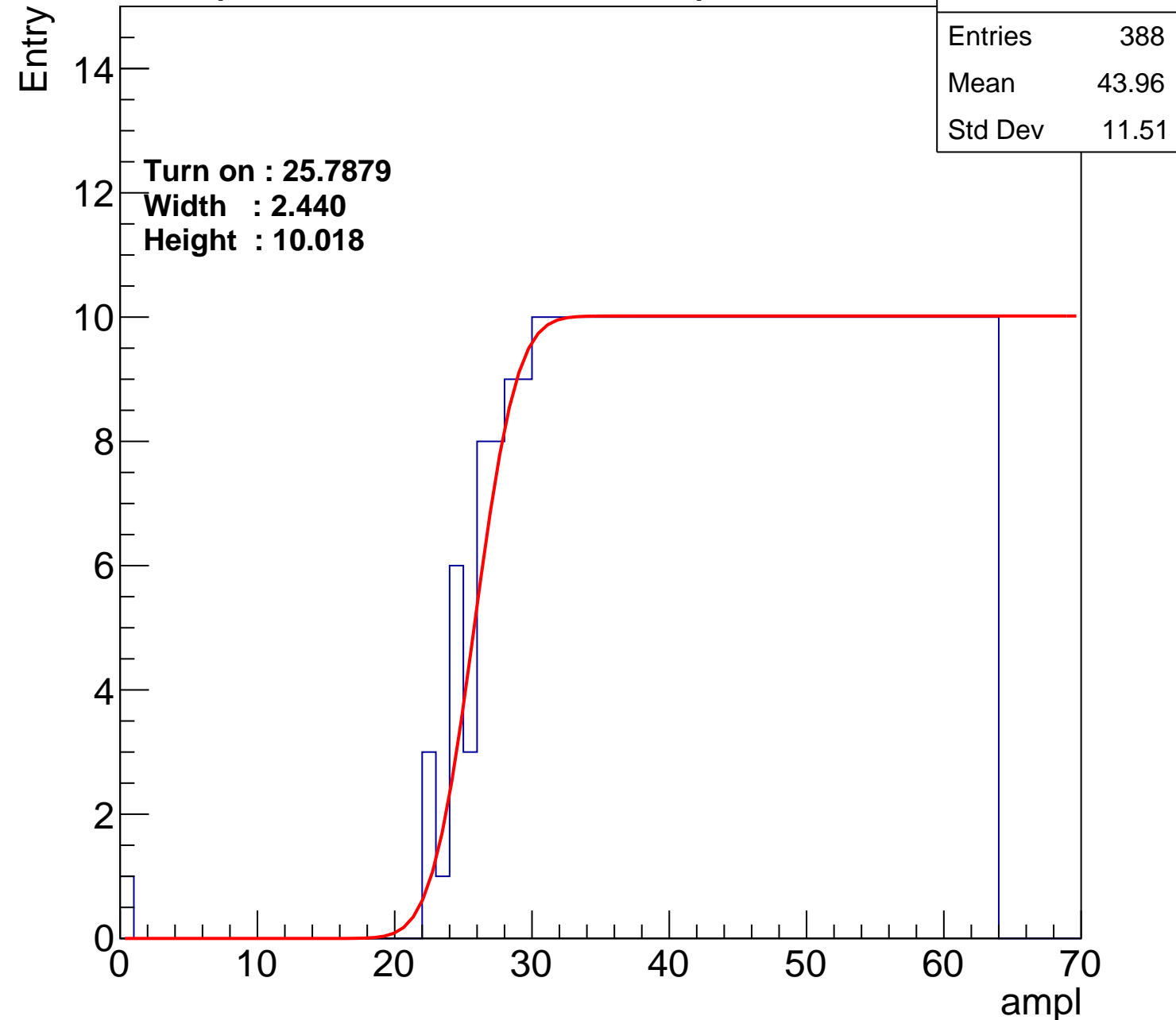
Width : 2.440

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.1
Std Dev	11.92

Turn on : 26.7920

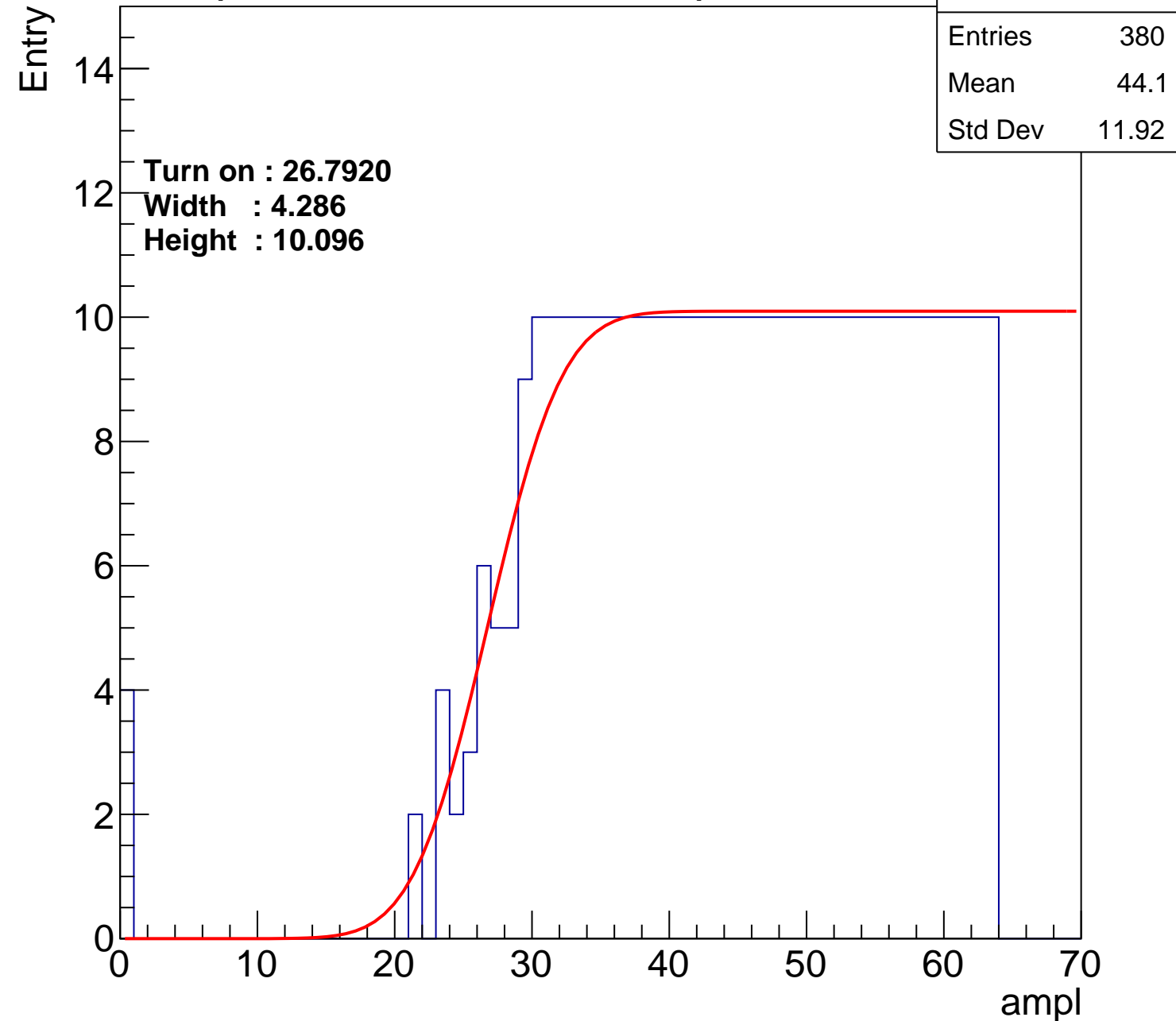
Width : 4.286

Height : 10.096

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.91
Std Dev	11.76

Turn on : 26.4288

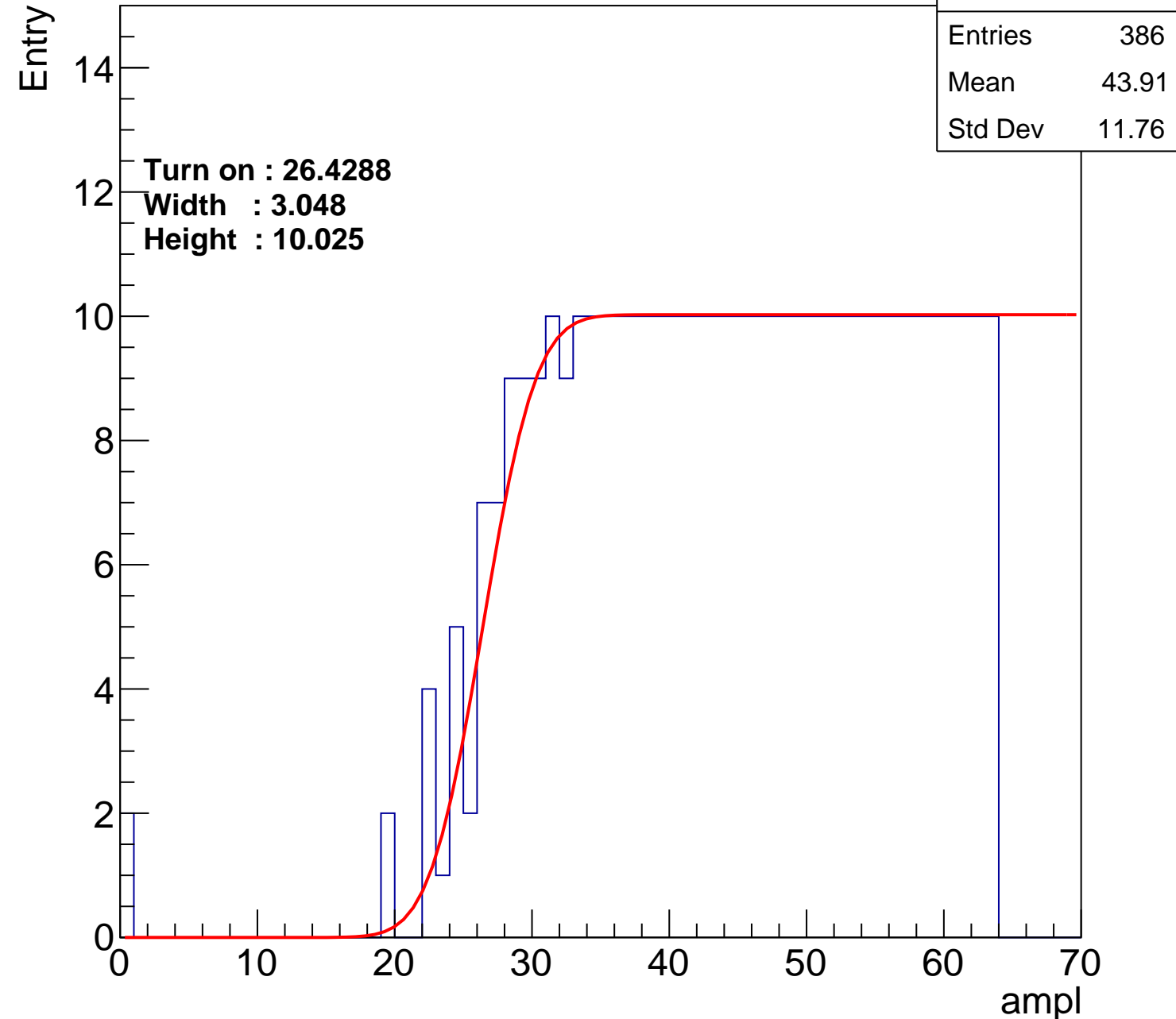
Width : 3.048

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.79
Std Dev	12.04

Turn on : 25.8141

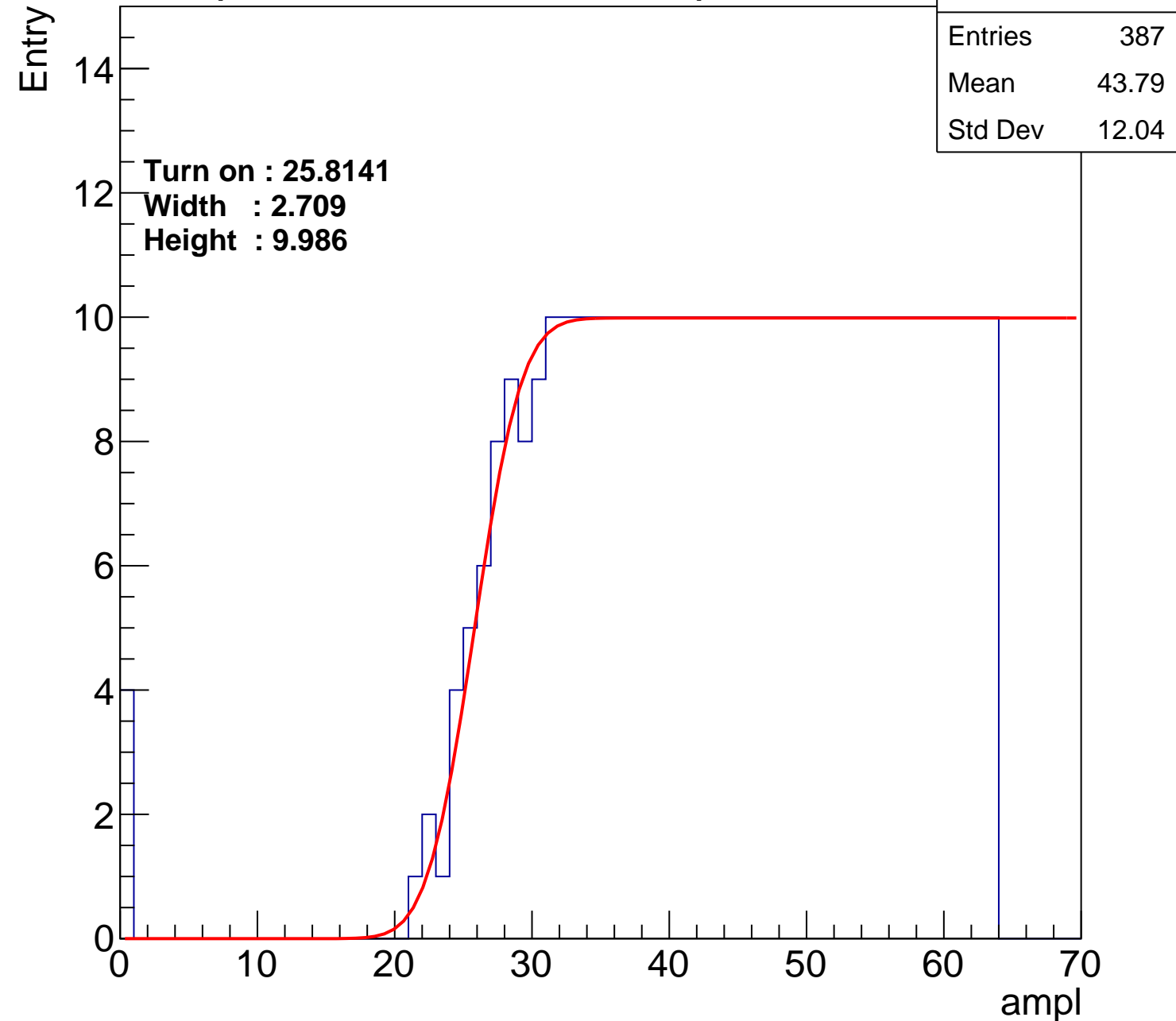
Width : 2.709

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.34
Std Dev	12.1

Turn on : 24.6906

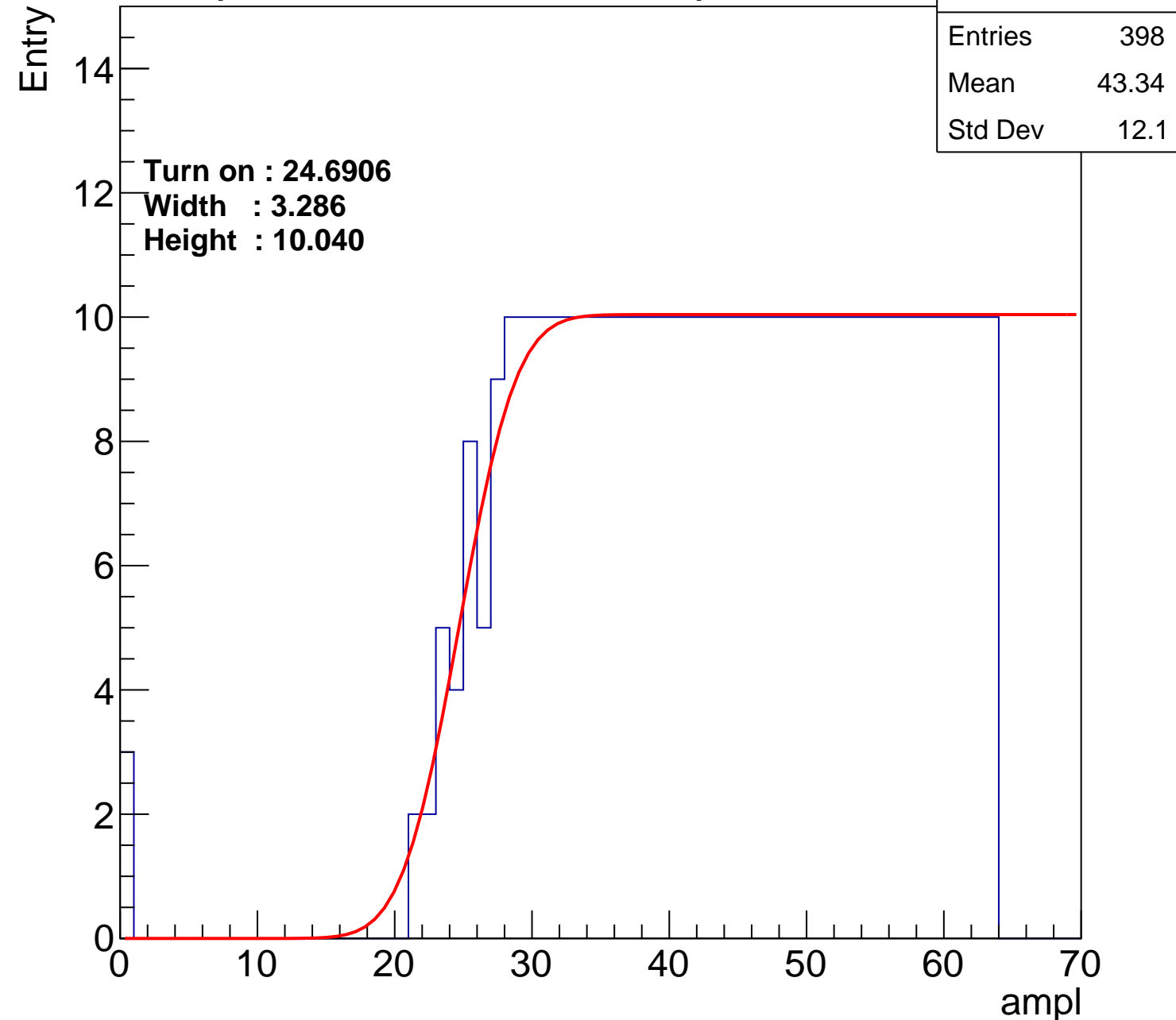
Width : 3.286

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.31
Std Dev	11.62

Turn on : 26.7235

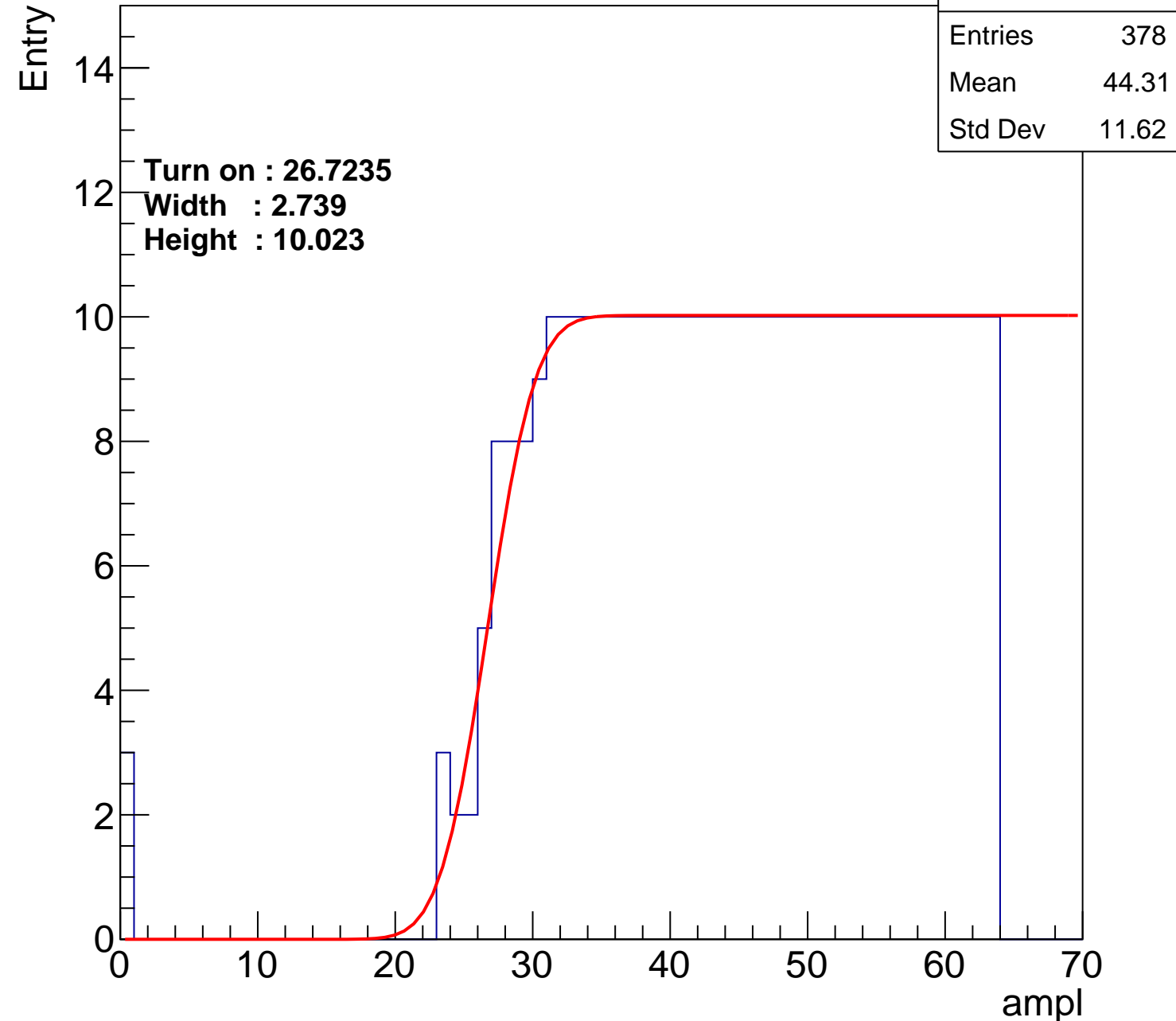
Width : 2.739

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch120

calib_packv5_042523_0143.root, FC#3, port B3

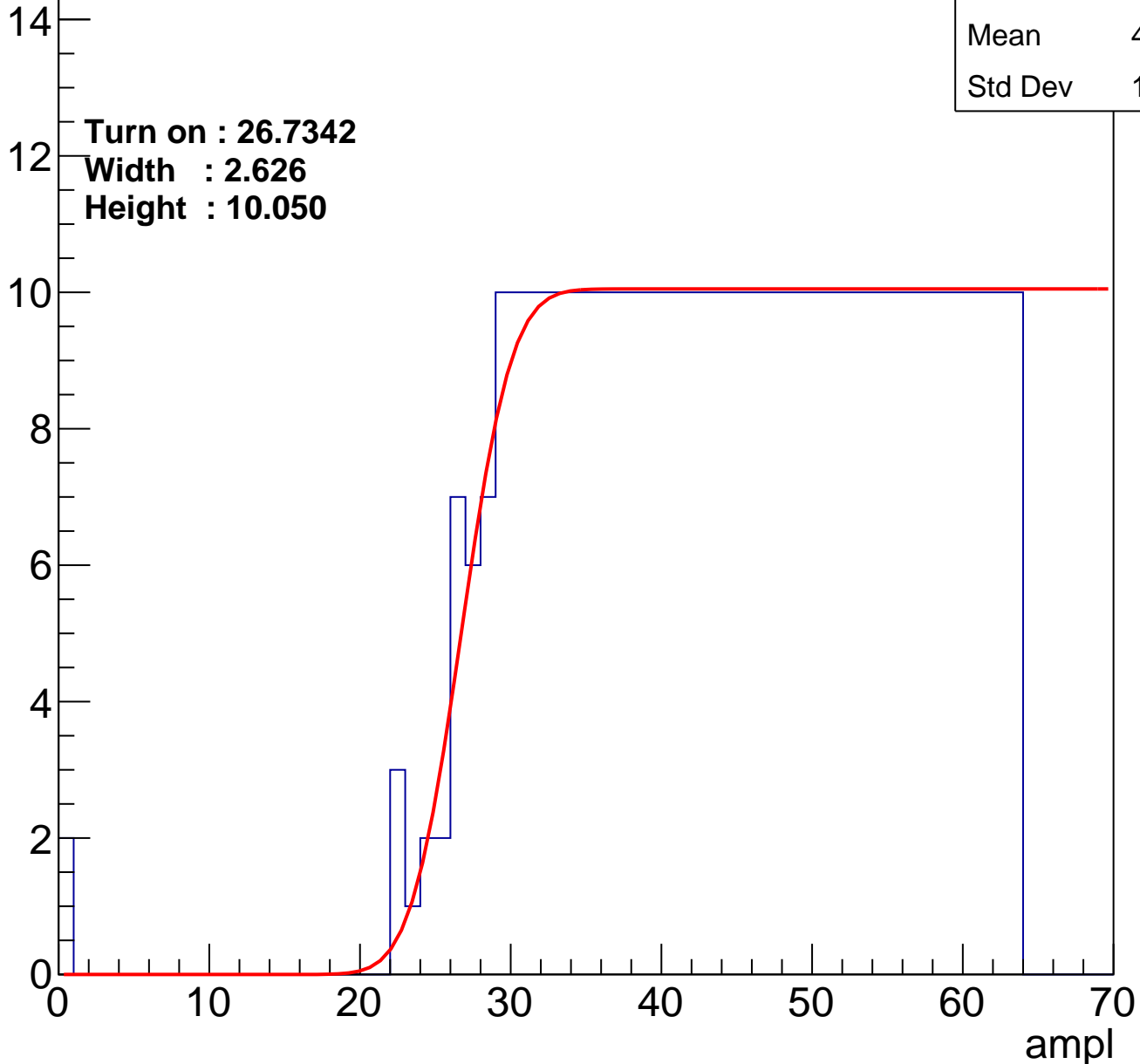
Entries	380
Mean	44.28
Std Dev	11.49

Turn on : 26.7342

Width : 2.626

Height : 10.050

Entry



B1L000S, U4-ch121

calib_packv5_042523_0143.root, FC#3, port B3

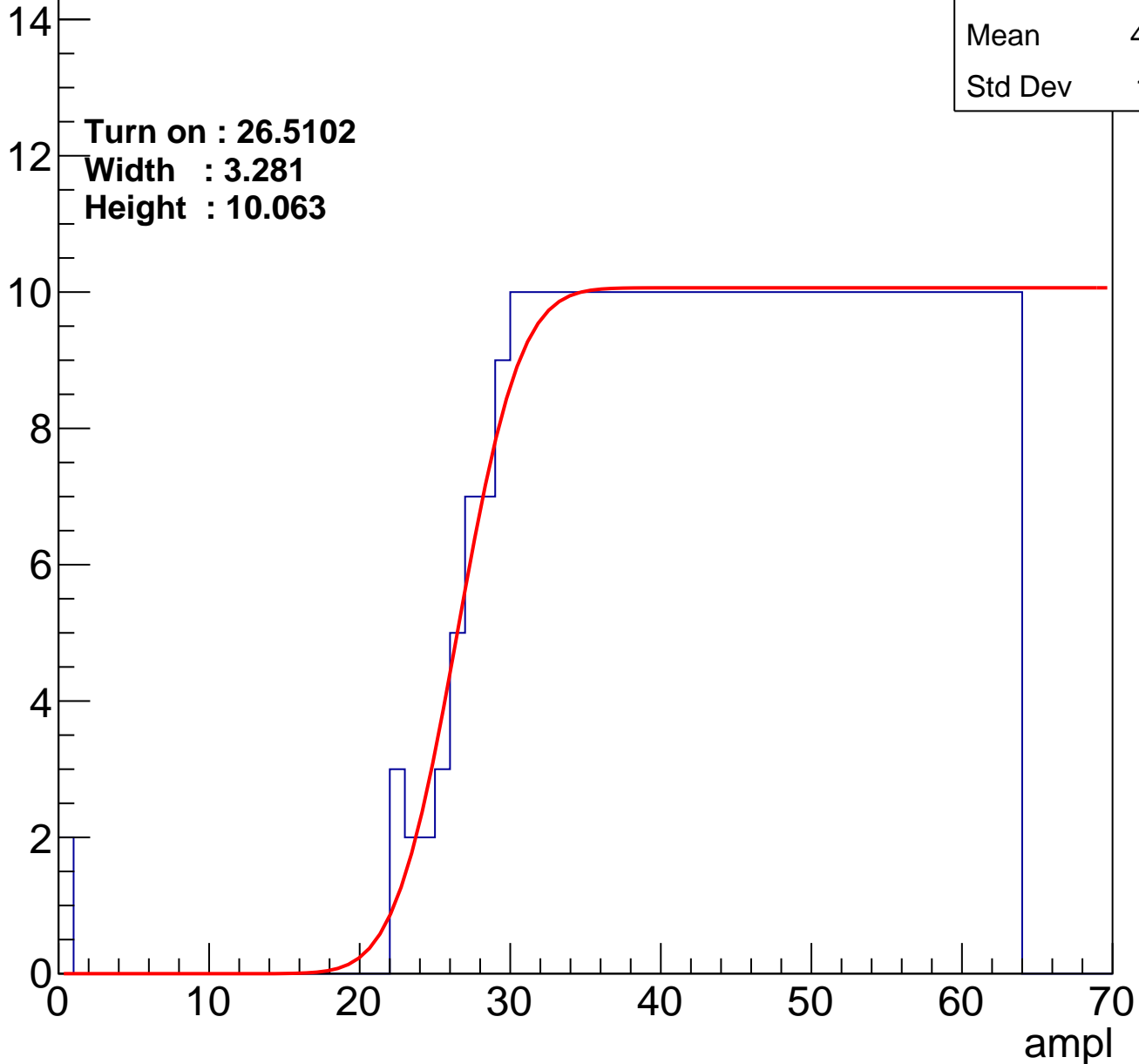
Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.5102

Width : 3.281

Height : 10.063

Entry



B1L000S, U4-ch122

calib_packv5_042523_0143.root, FC#3, port B3

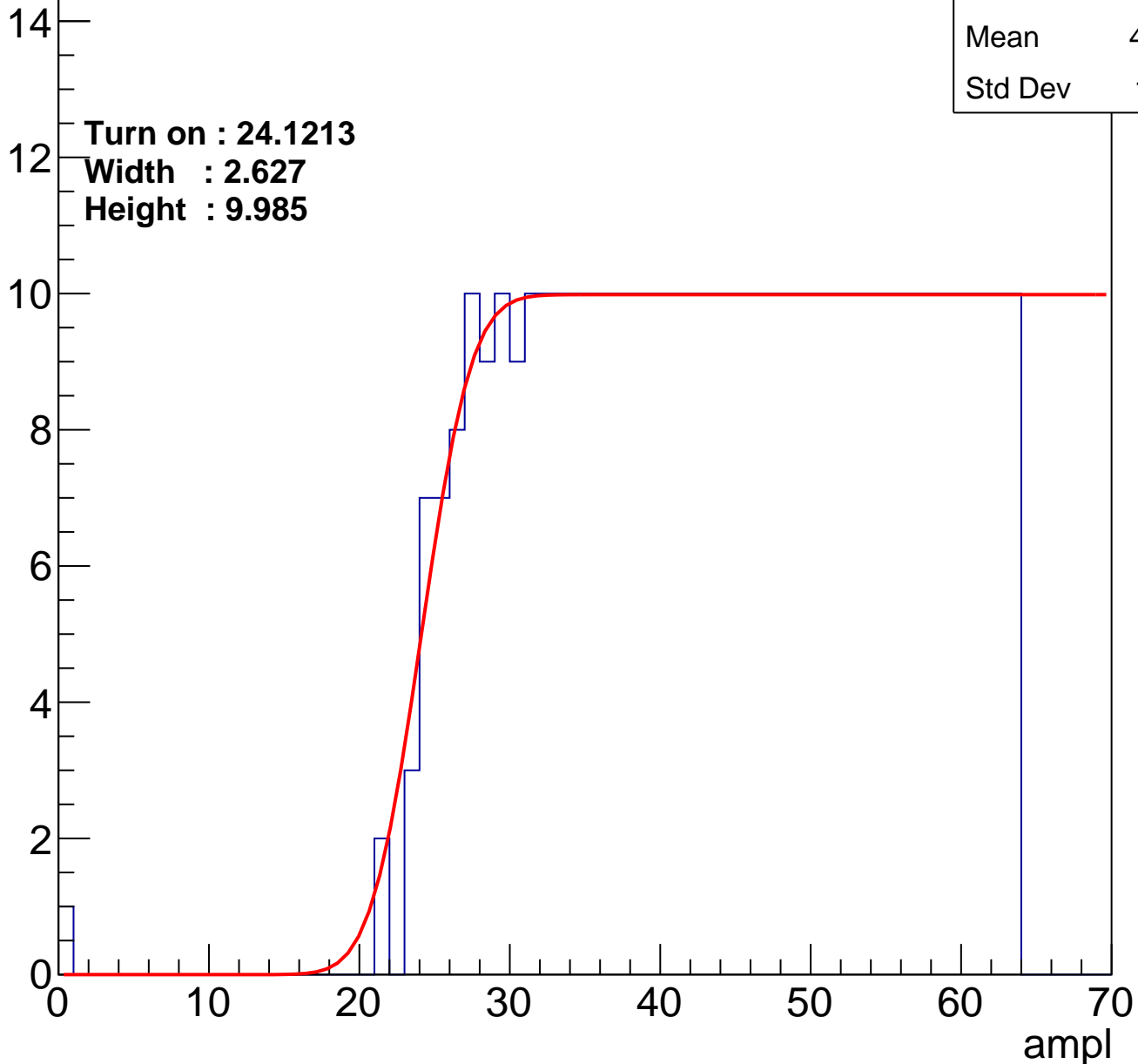
Entries	396
Mean	43.57
Std Dev	11.71

Turn on : 24.1213

Width : 2.627

Height : 9.985

Entry



B1L000S, U4-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.52
Std Dev	11.56

Turn on : 27.0842

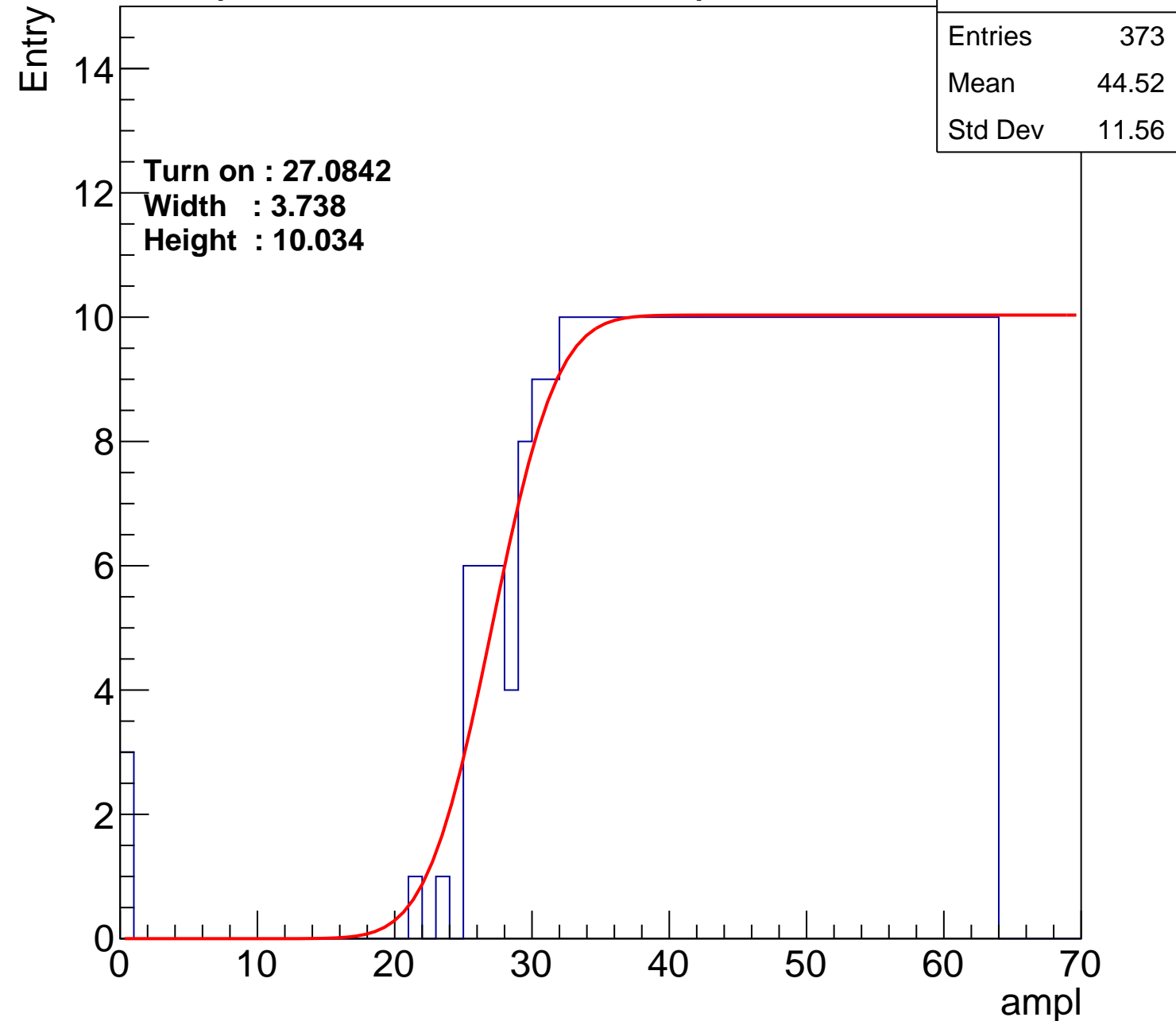
Width : 3.738

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.45
Std Dev	11.81

Turn on : 24.3373

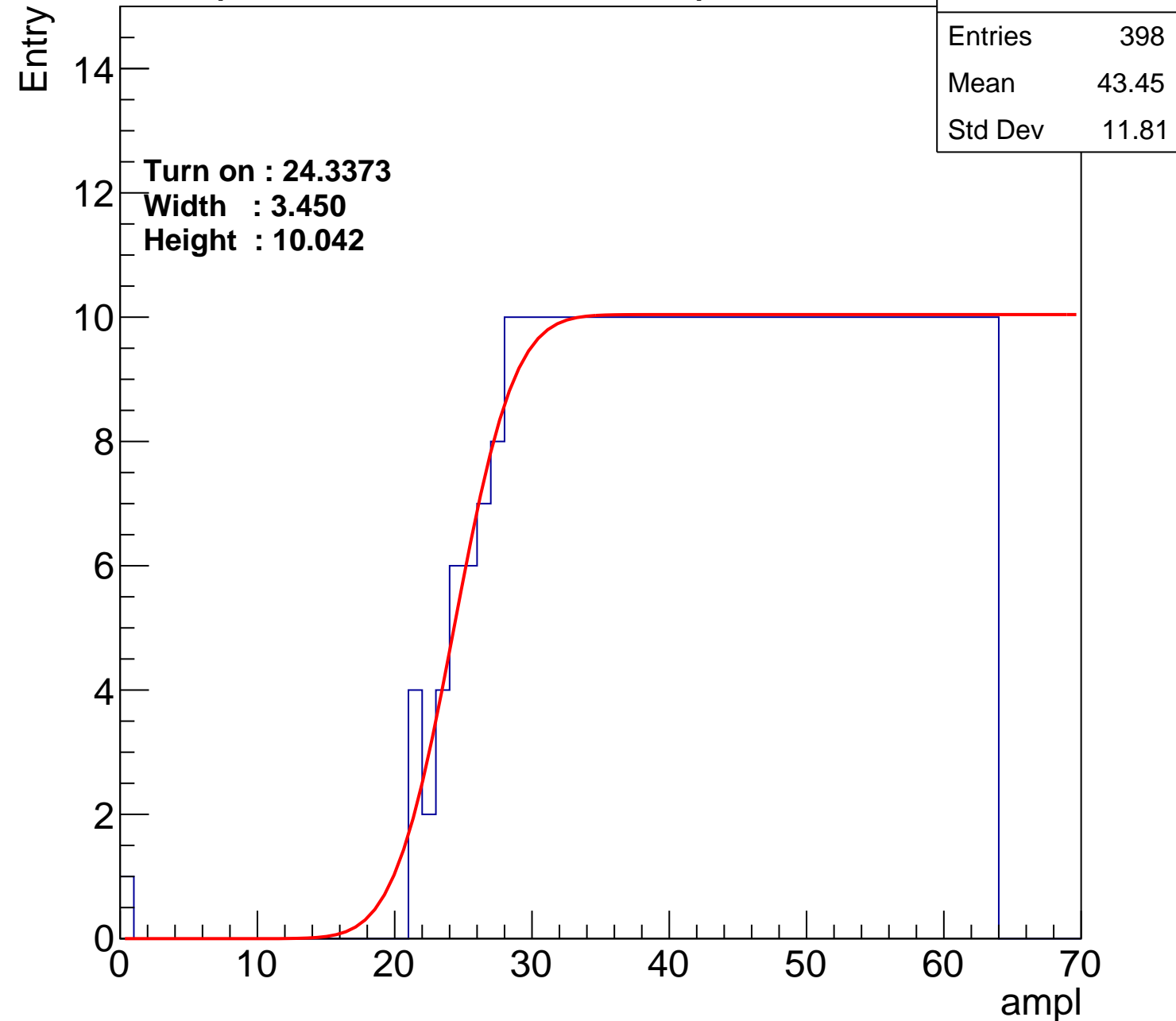
Width : 3.450

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch125

calib_packv5_042523_0143.root, FC#3, port B3

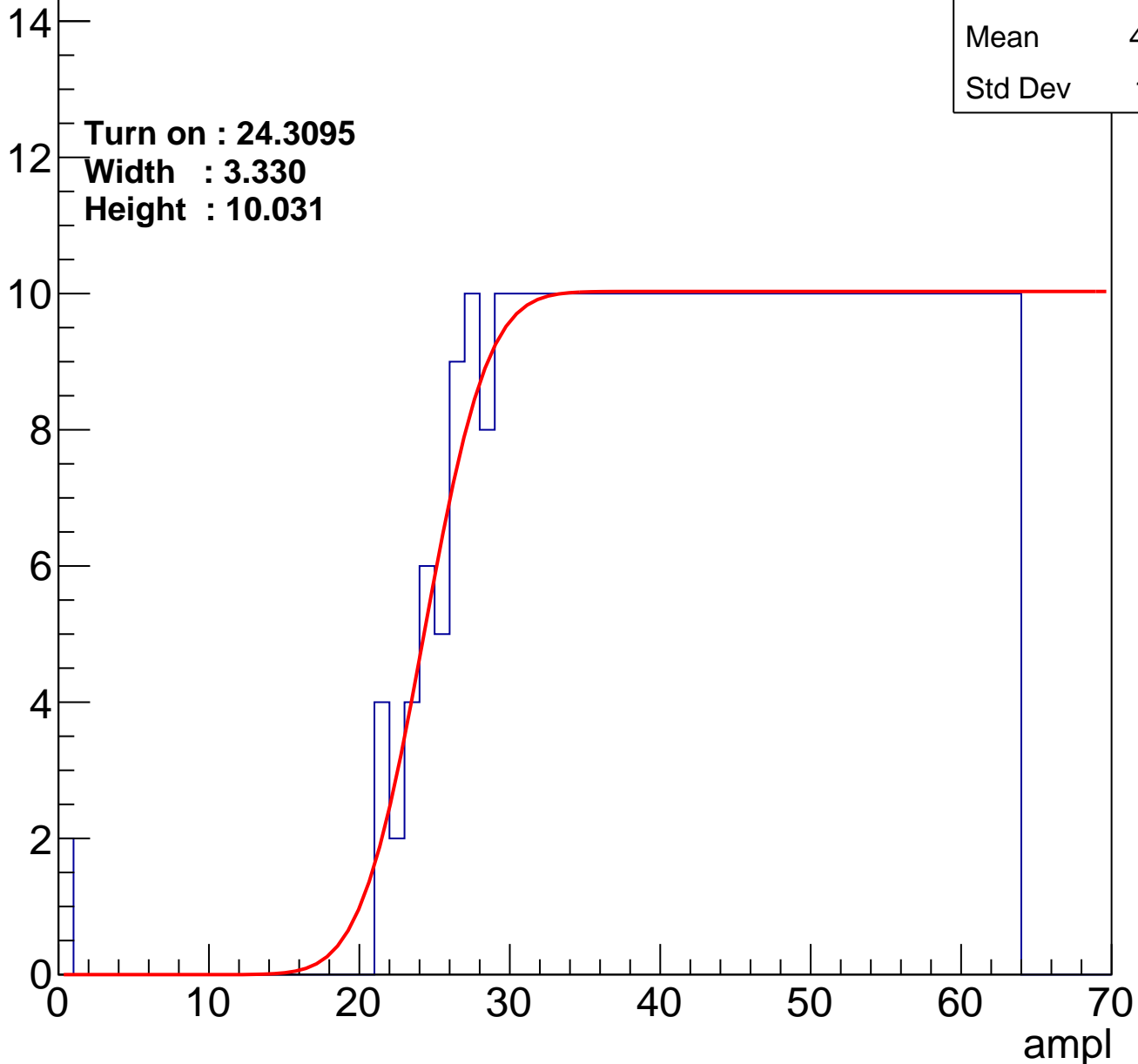
Entries	400
Mean	43.29
Std Dev	12.01

Turn on : 24.3095

Width : 3.330

Height : 10.031

Entry



B1L000S, U4-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	43.22
Std Dev	12.18

Turn on : 24.4570

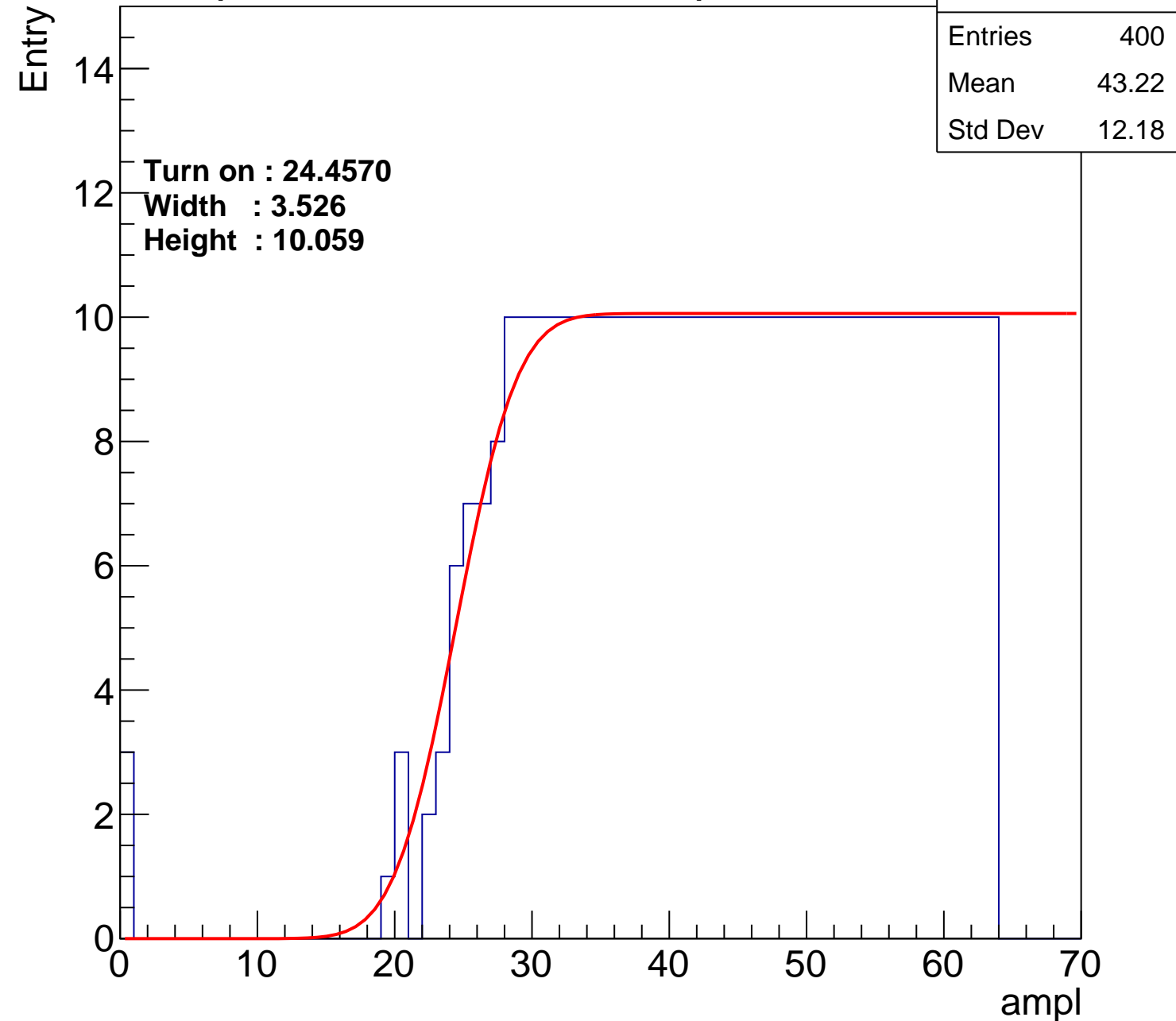
Width : 3.526

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.56
Std Dev	12.37

Turn on : 25.7844

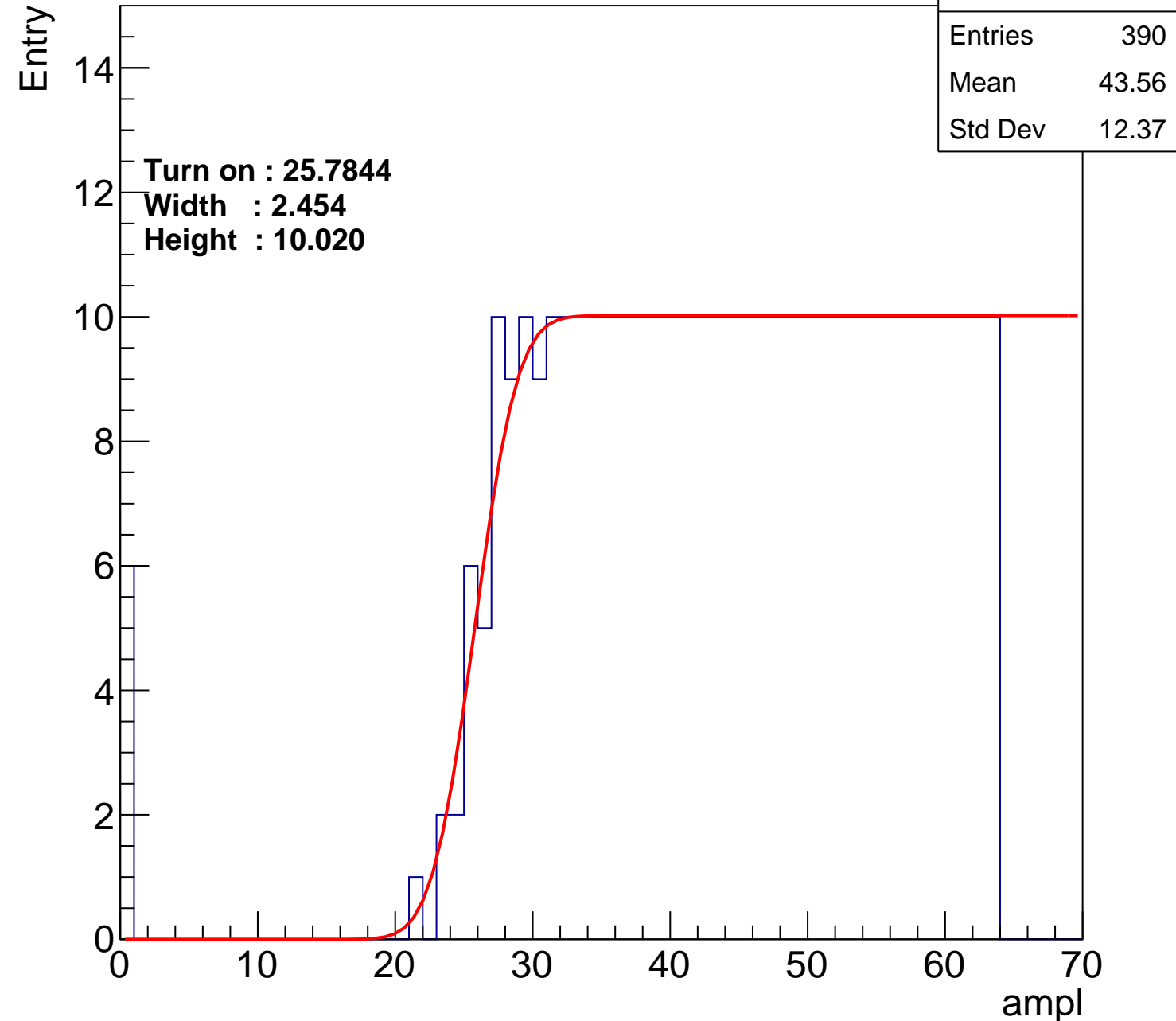
Width : 2.454

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U4-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.56
Std Dev	12.37

Turn on : 25.7844

Width : 2.454

Height : 10.020

Entry

