

B1L103S, U17-ch0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	419
Mean	41.99
Std Dev	13.23

Turn on : 23.3818

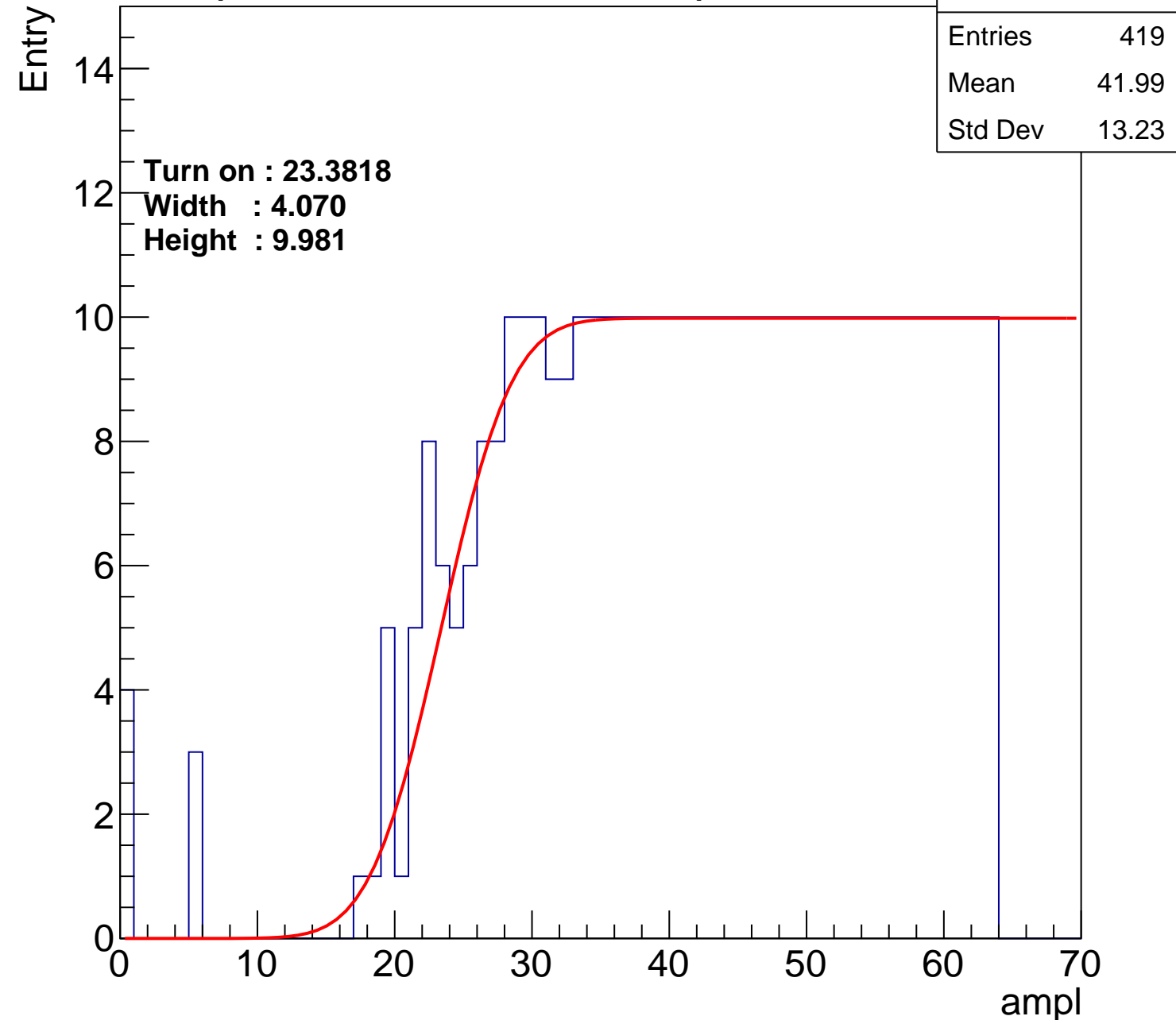
Width : 4.070

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	380
Mean	44.1
Std Dev	11.92

Turn on : 26.4629

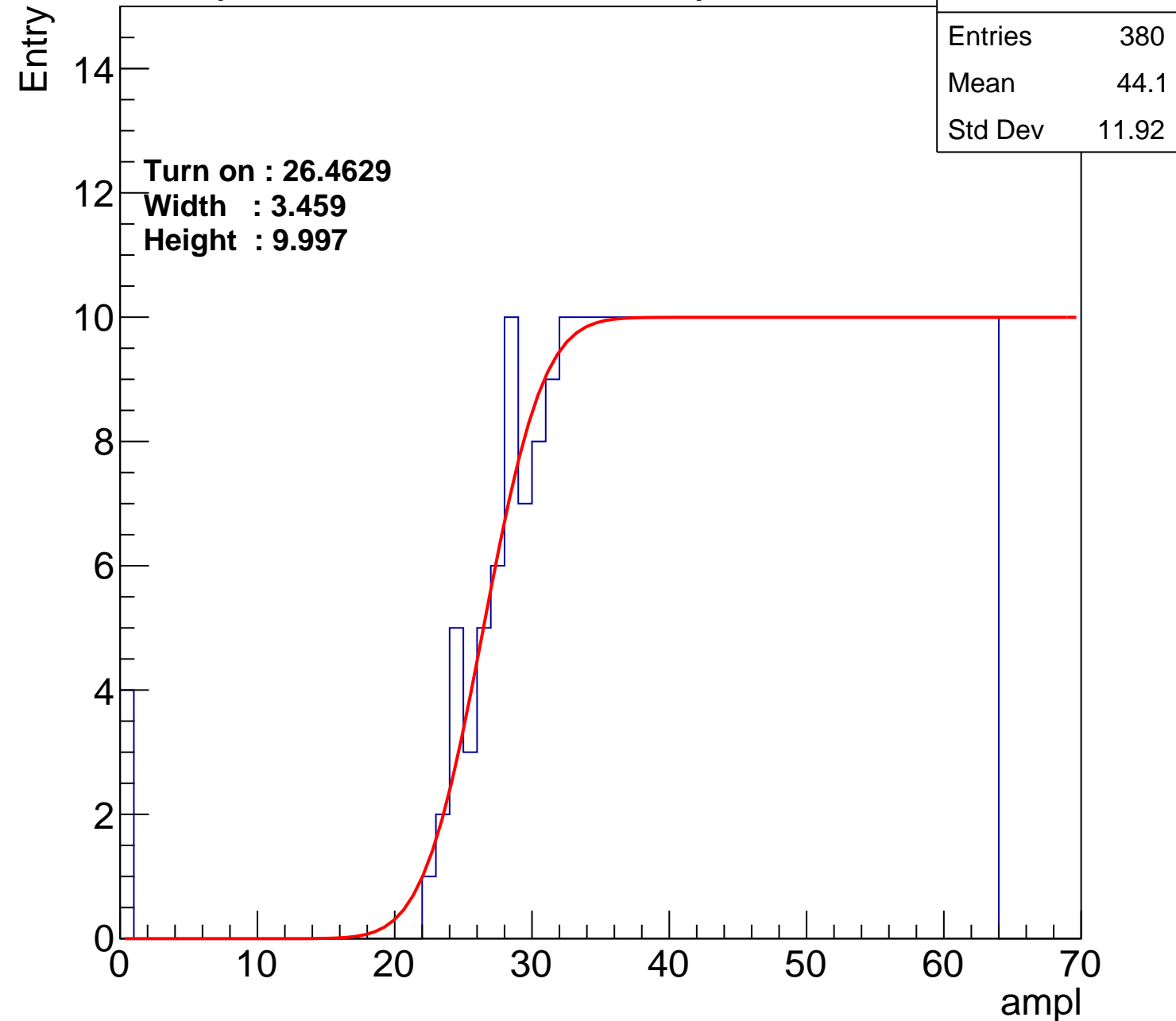
Width : 3.459

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.37
Std Dev	12.03

Turn on : 24.3187

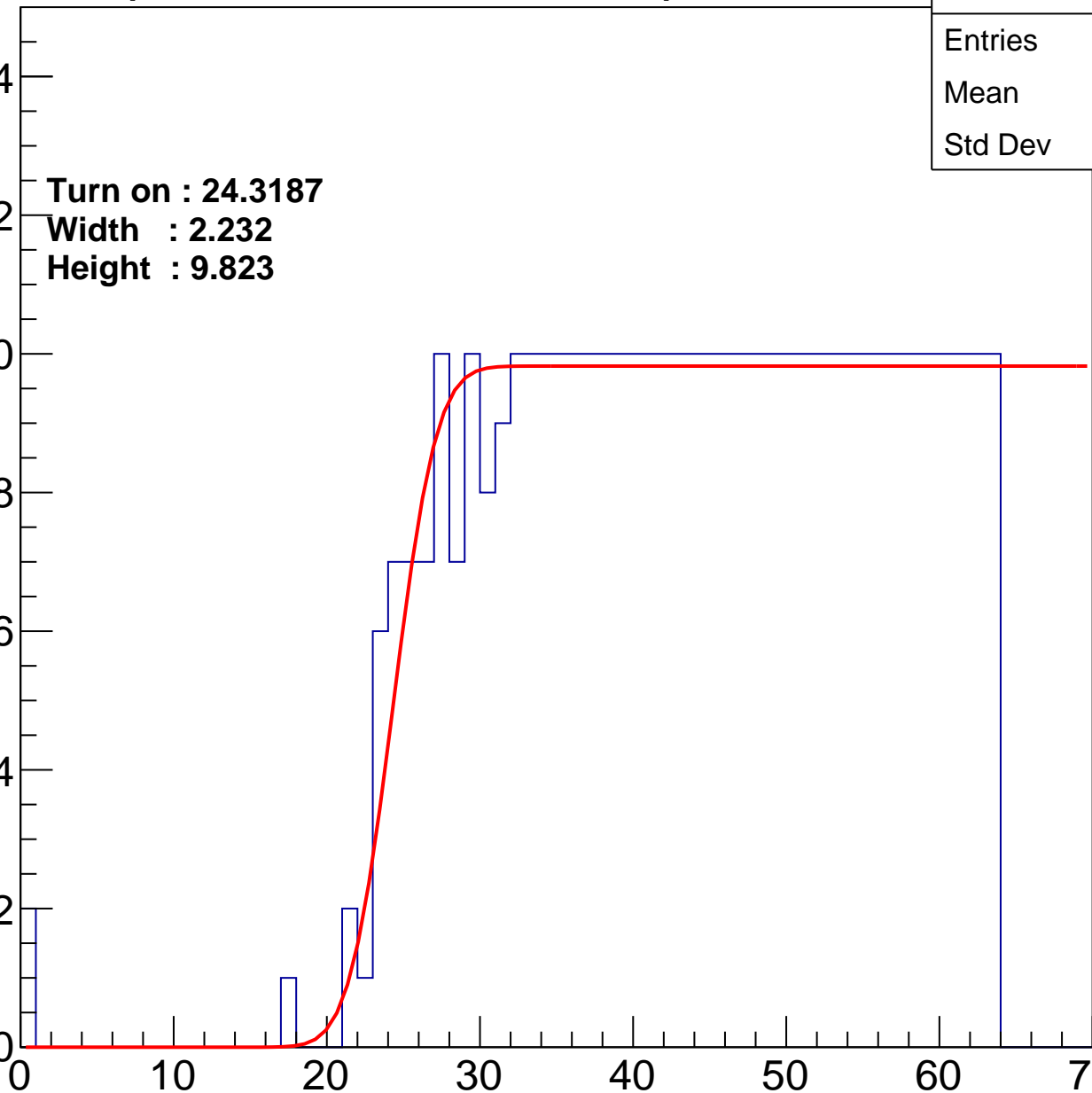
Width : 2.232

Height : 9.823

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.65
Std Dev	10.83

Turn on : 28.8609

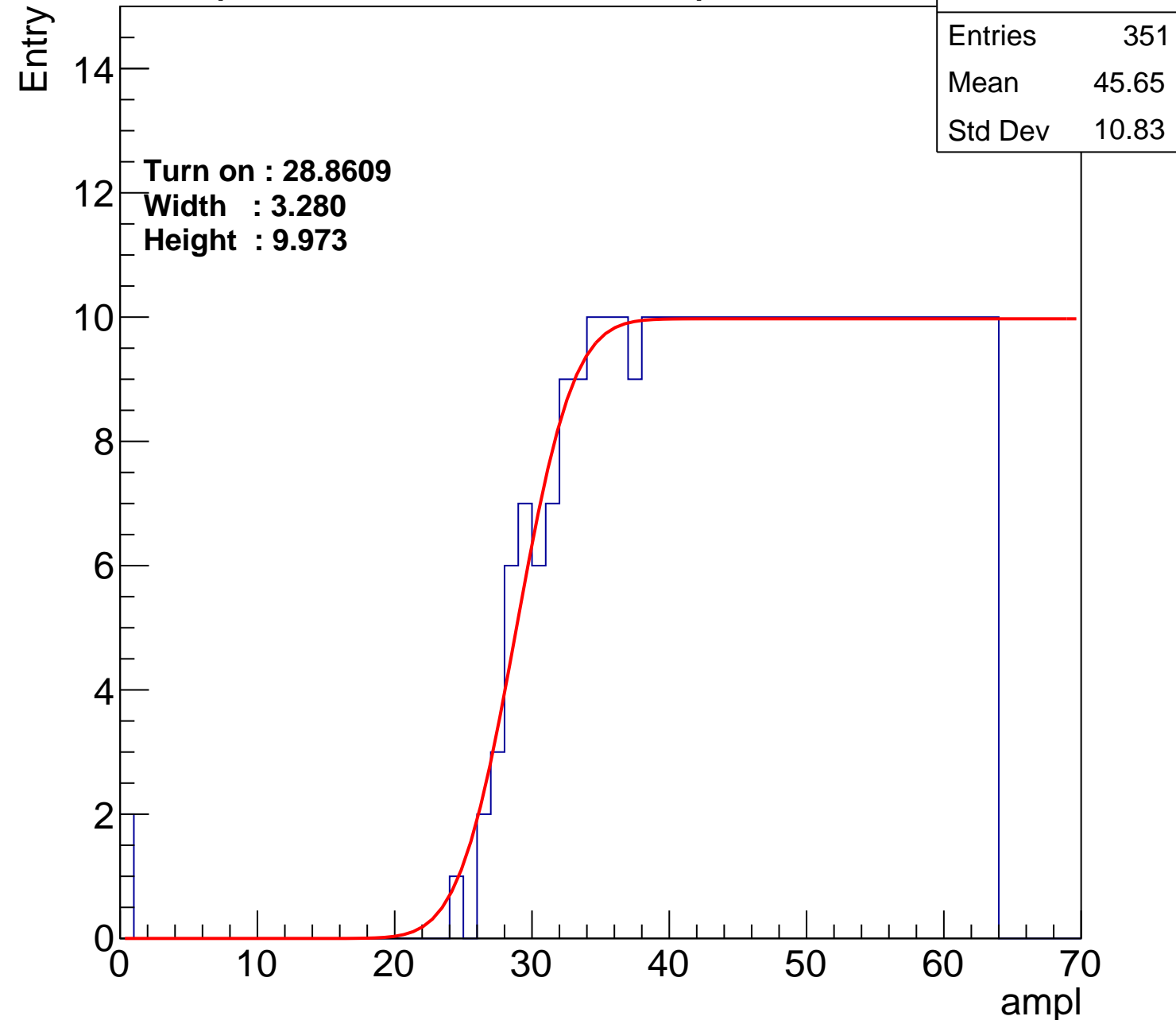
Width : 3.280

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.1
Std Dev	11.49

Turn on : 26.4113

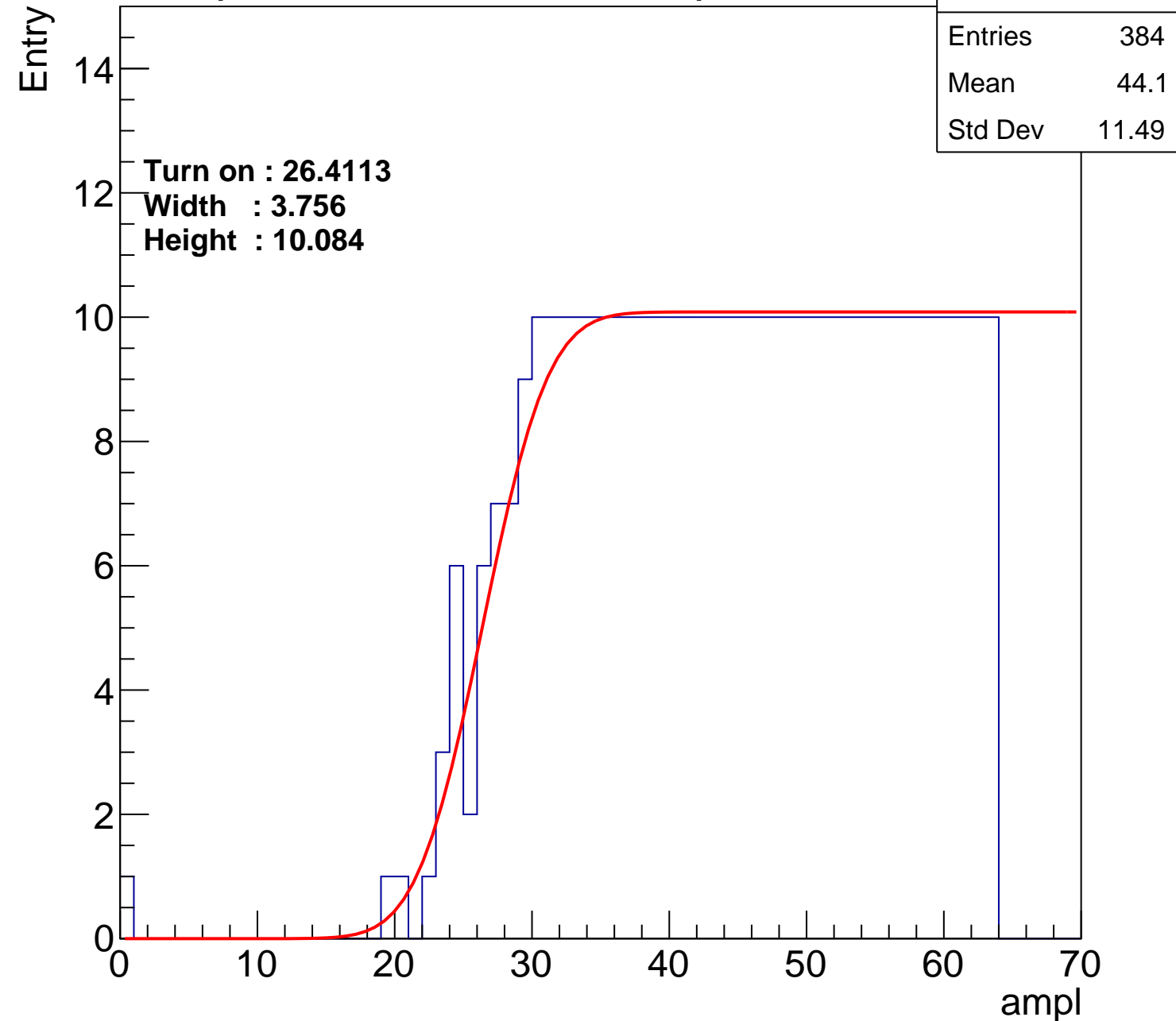
Width : 3.756

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch5

calib_packv5_042523_0143.root, FC#7, port C2

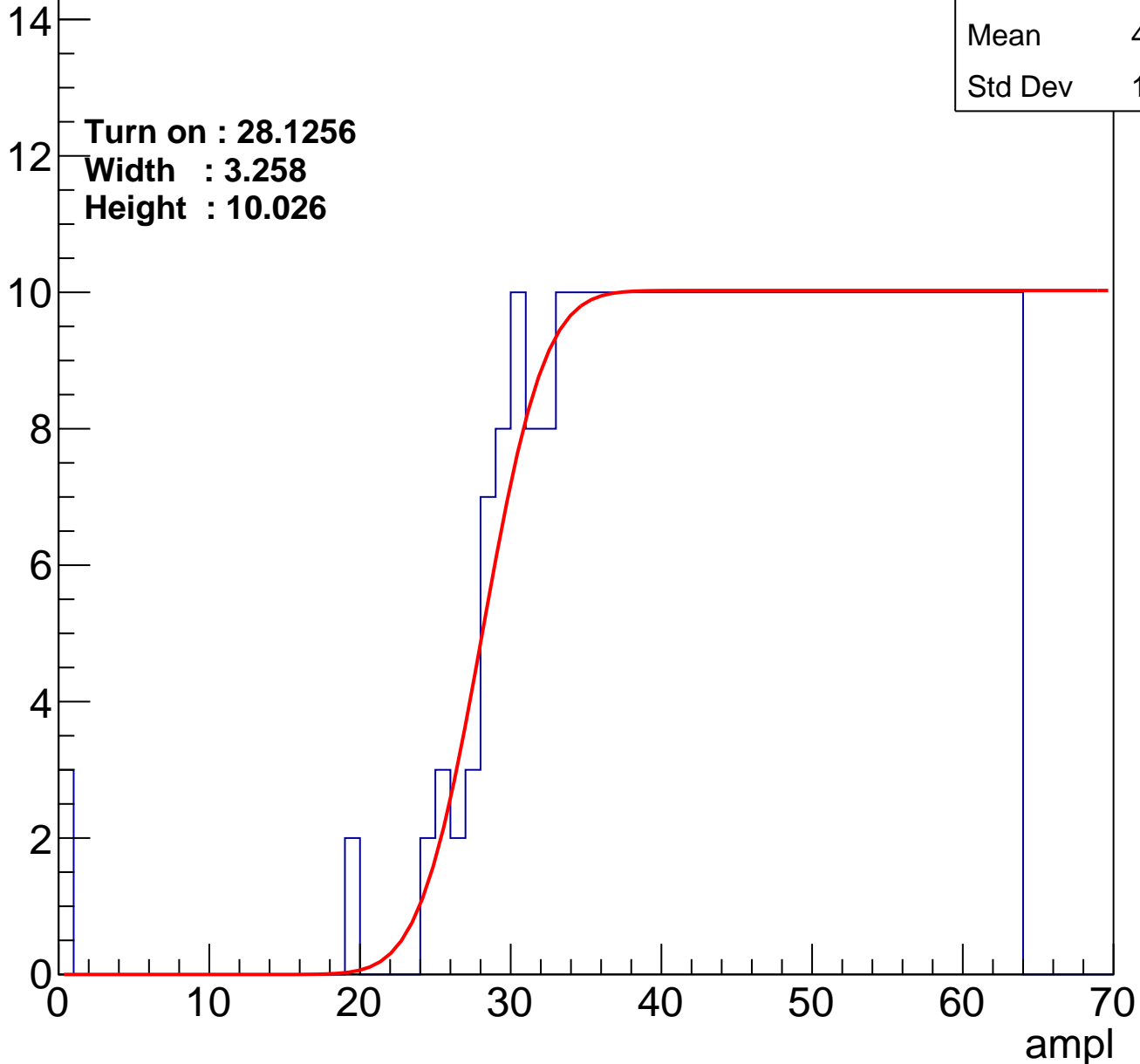
Entries	366
Mean	44.83
Std Dev	11.45

Turn on : 28.1256

Width : 3.258

Height : 10.026

Entry



B1L103S, U17-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.84
Std Dev	11.42

Turn on : 27.7722

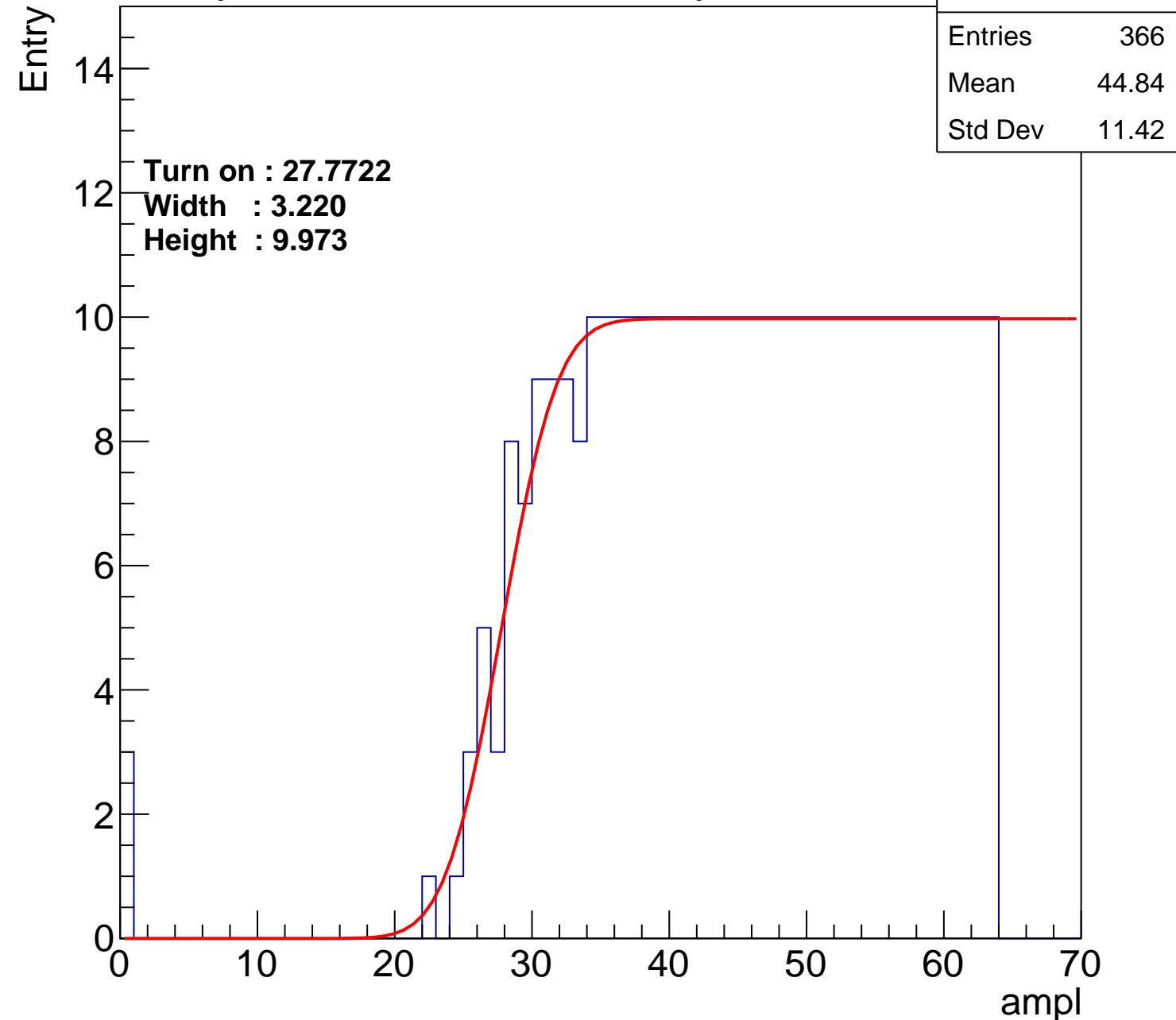
Width : 3.220

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch7

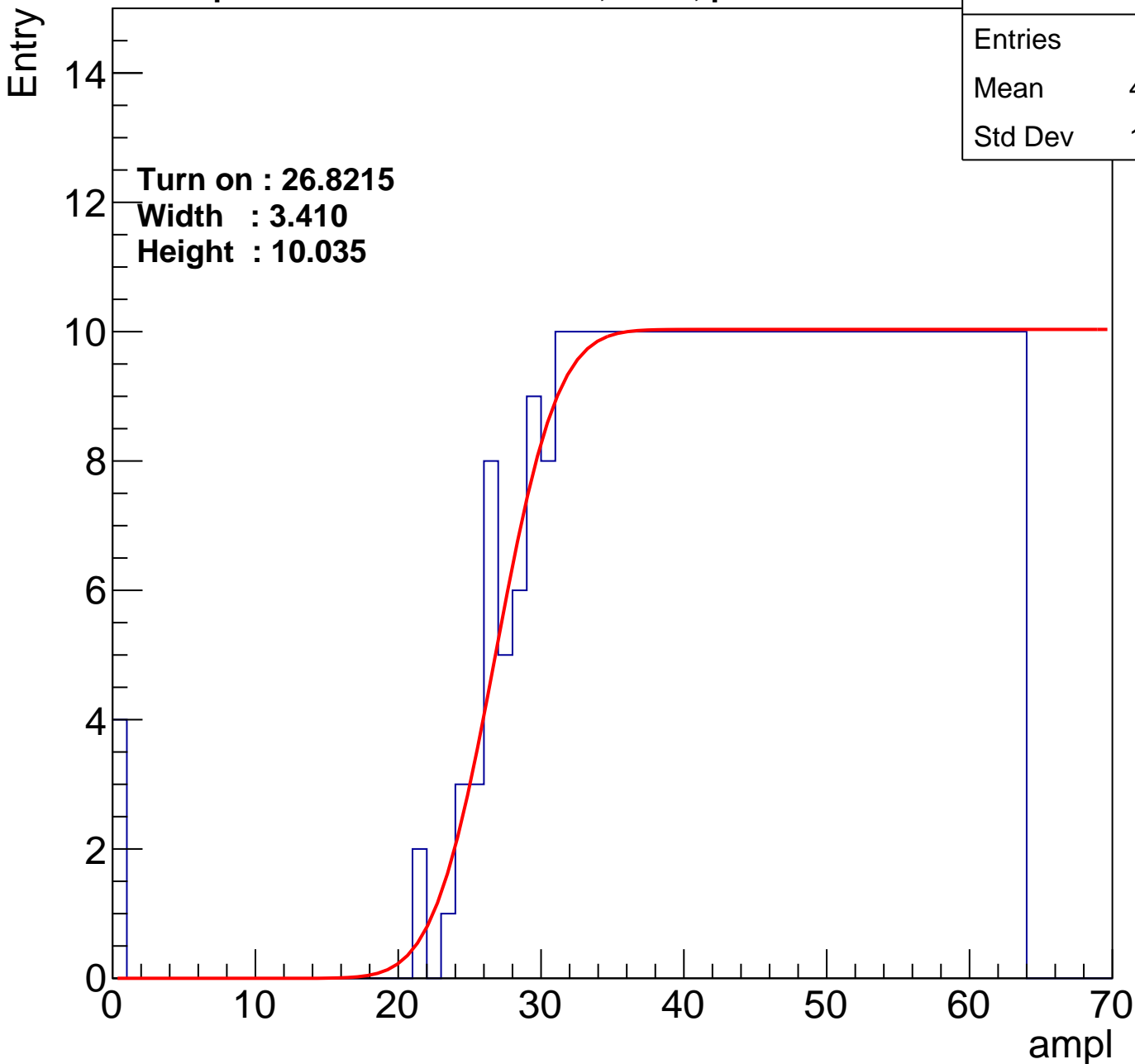
calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.15
Std Dev	11.89

Turn on : 26.8215

Width : 3.410

Height : 10.035



B1L103S, U17-ch8

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.32
Std Dev	11.65

Turn on : 26.8834

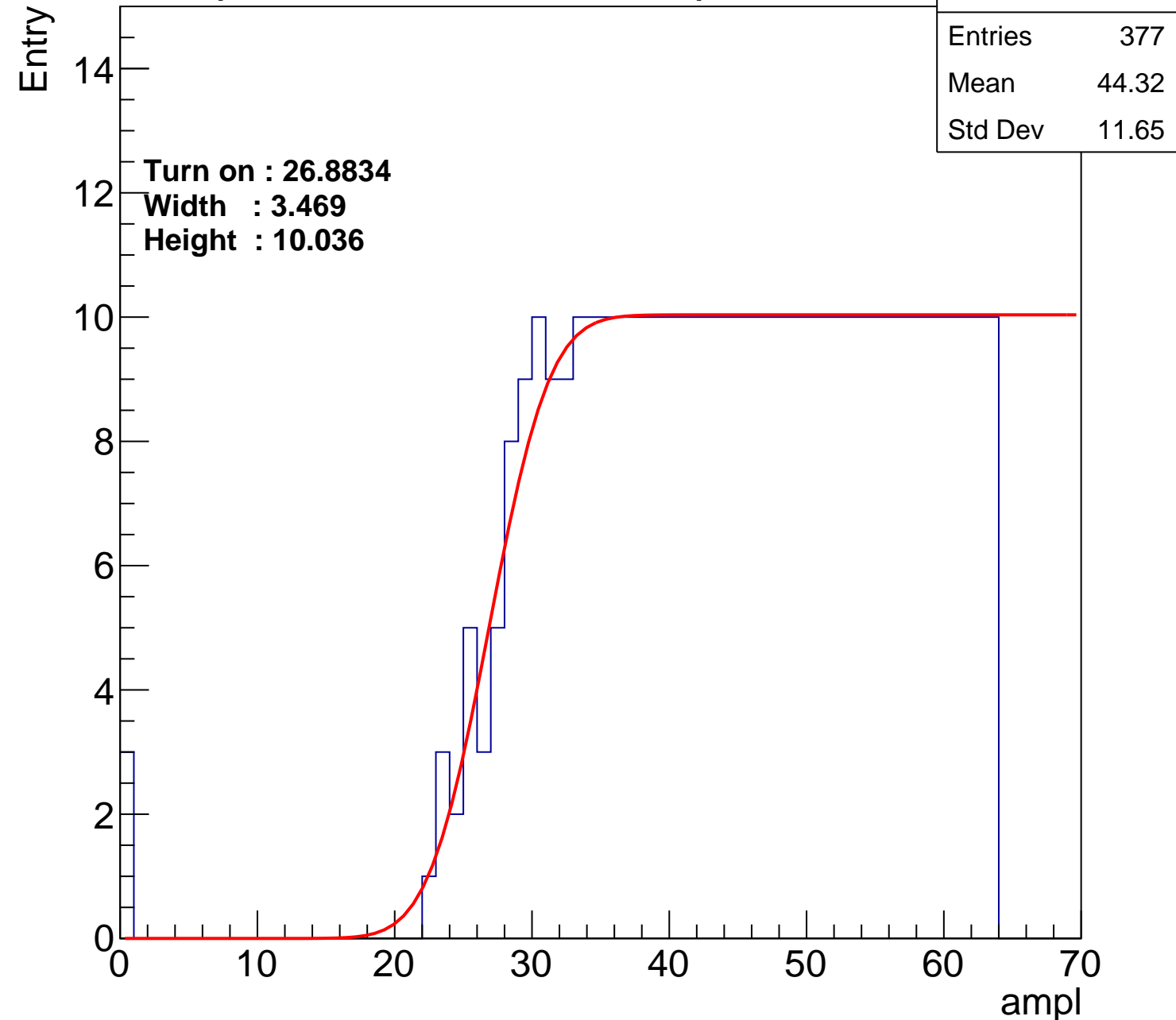
Width : 3.469

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.57
Std Dev	11.35

Turn on : 26.9389

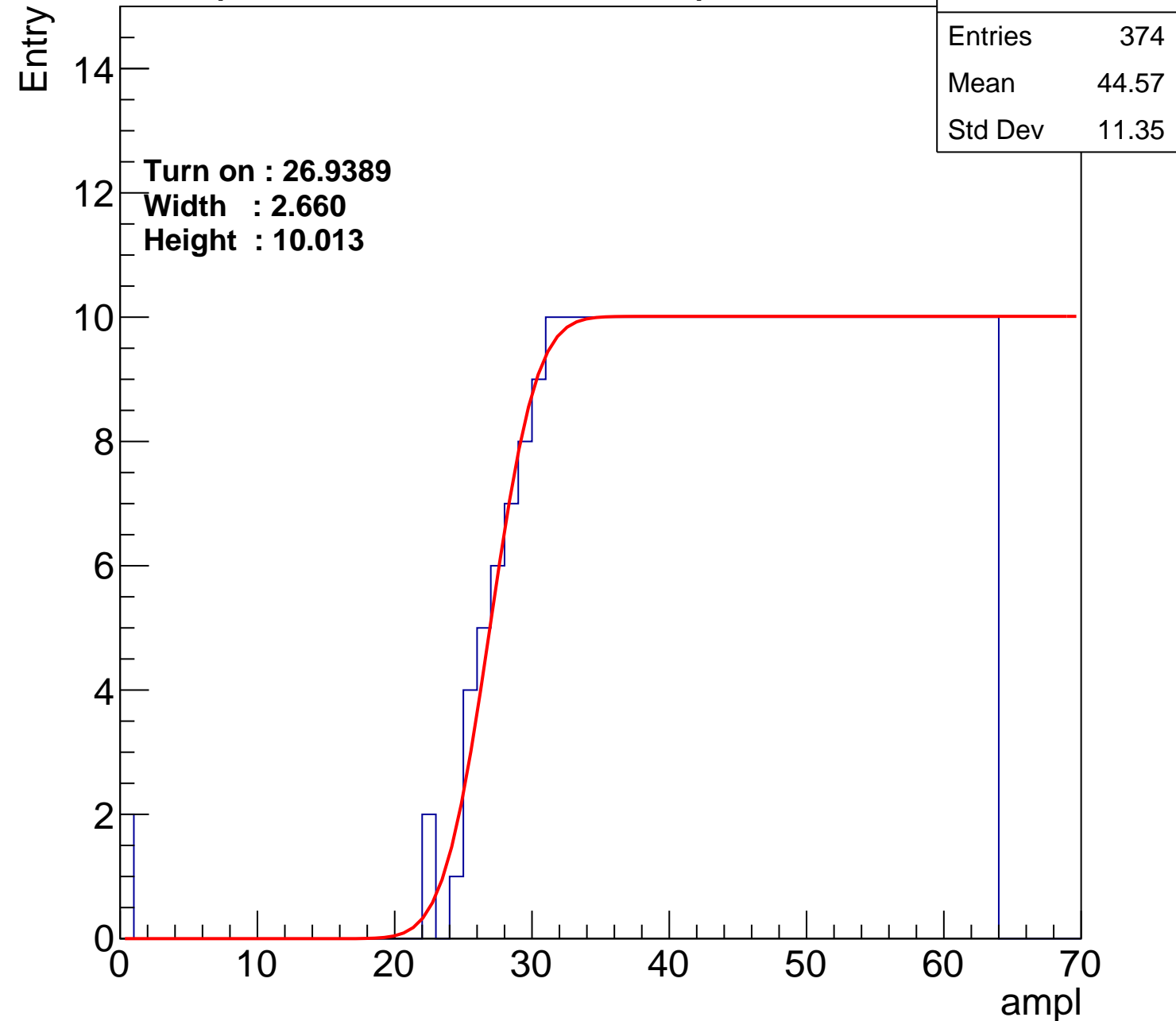
Width : 2.660

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch10

calib_packv5_042523_0143.root, FC#7, port C2

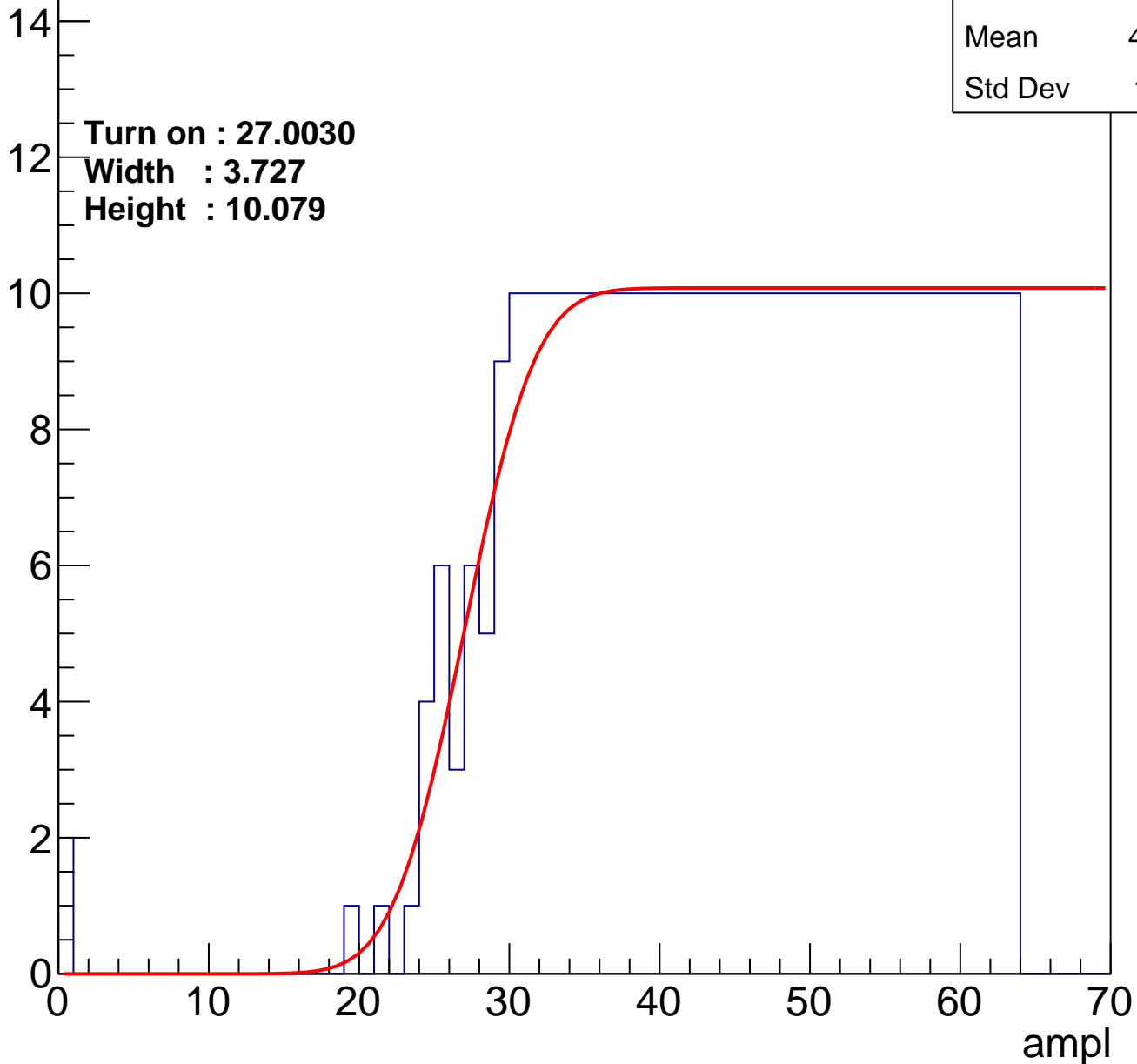
Entries	378
Mean	44.34
Std Dev	11.51

Turn on : 27.0030

Width : 3.727

Height : 10.079

Entry



B1L103S, U17-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.87
Std Dev	11.18

Turn on : 28.8500

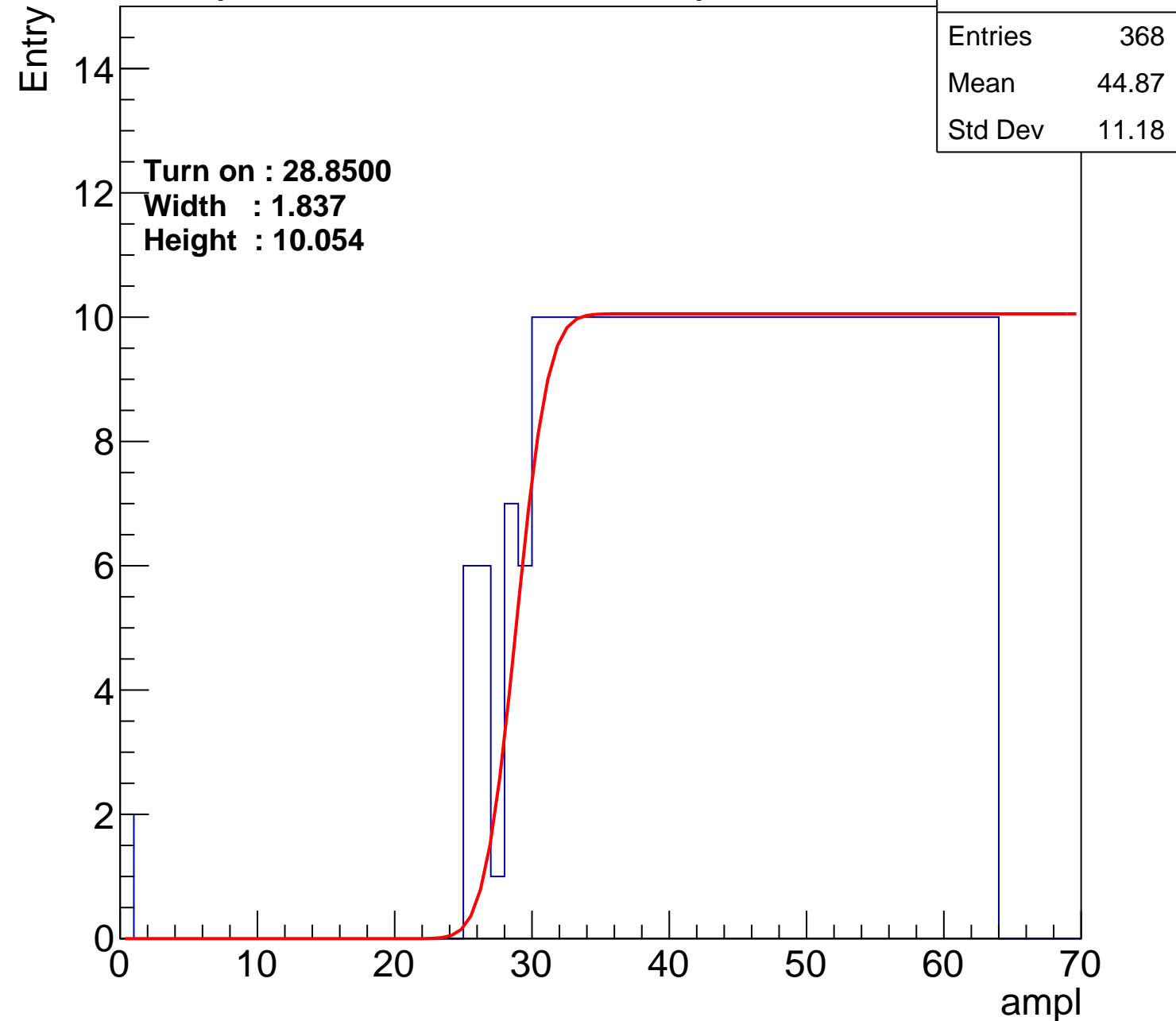
Width : 1.837

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.08
Std Dev	11.46

Turn on : 28.7638

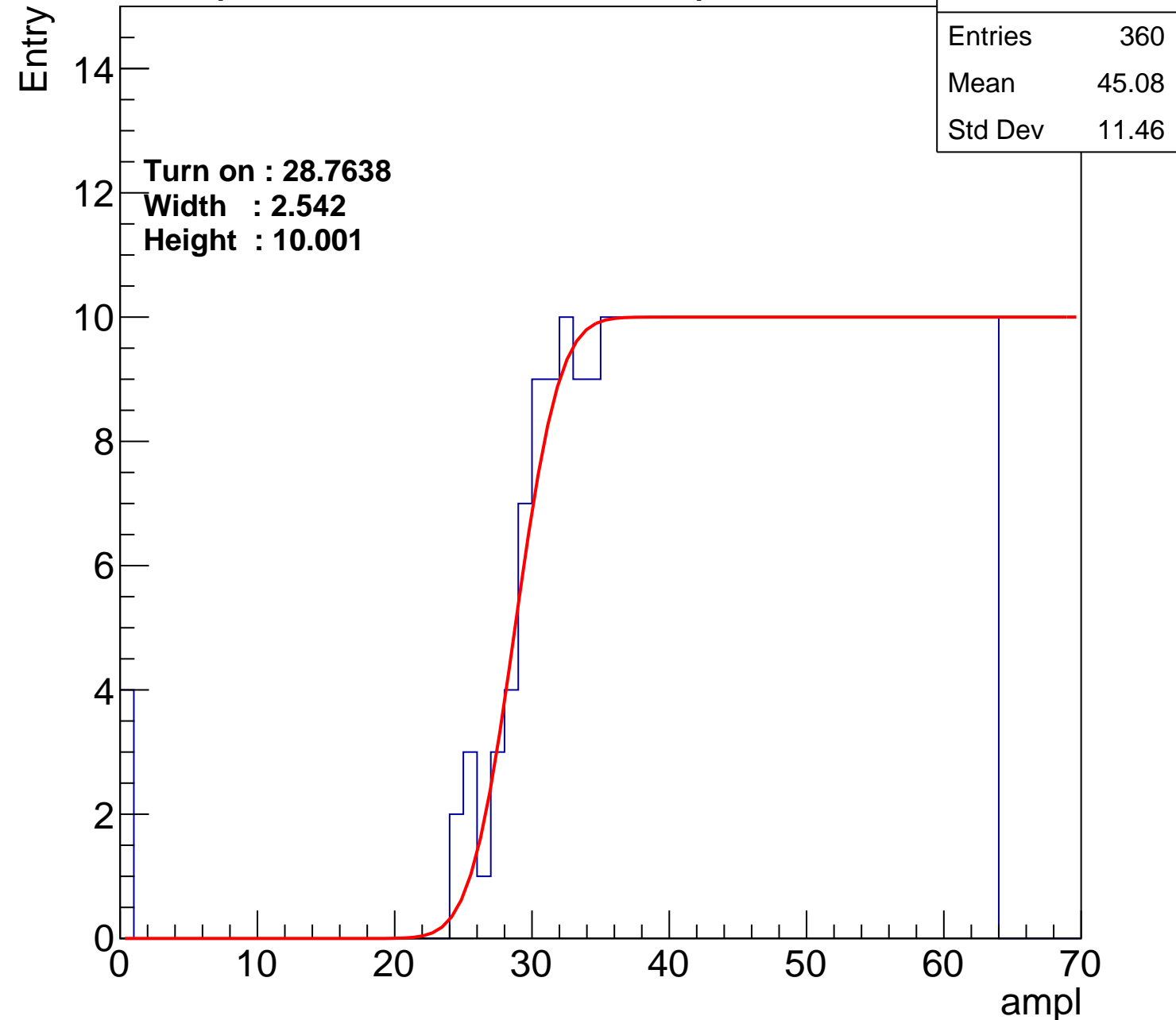
Width : 2.542

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.21
Std Dev	11.99

Turn on : 26.6738

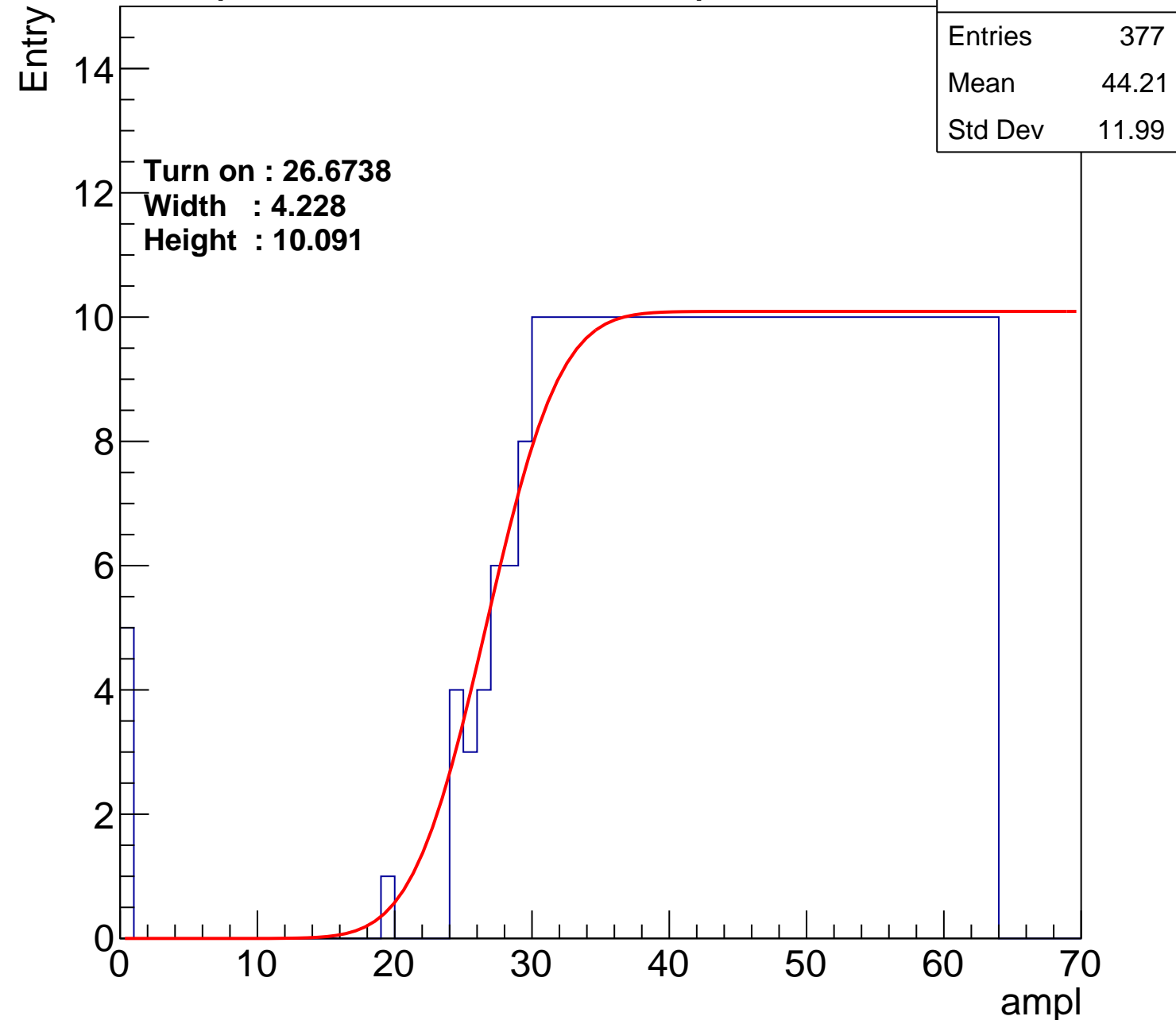
Width : 4.228

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch14

calib_packv5_042523_0143.root, FC#7, port C2

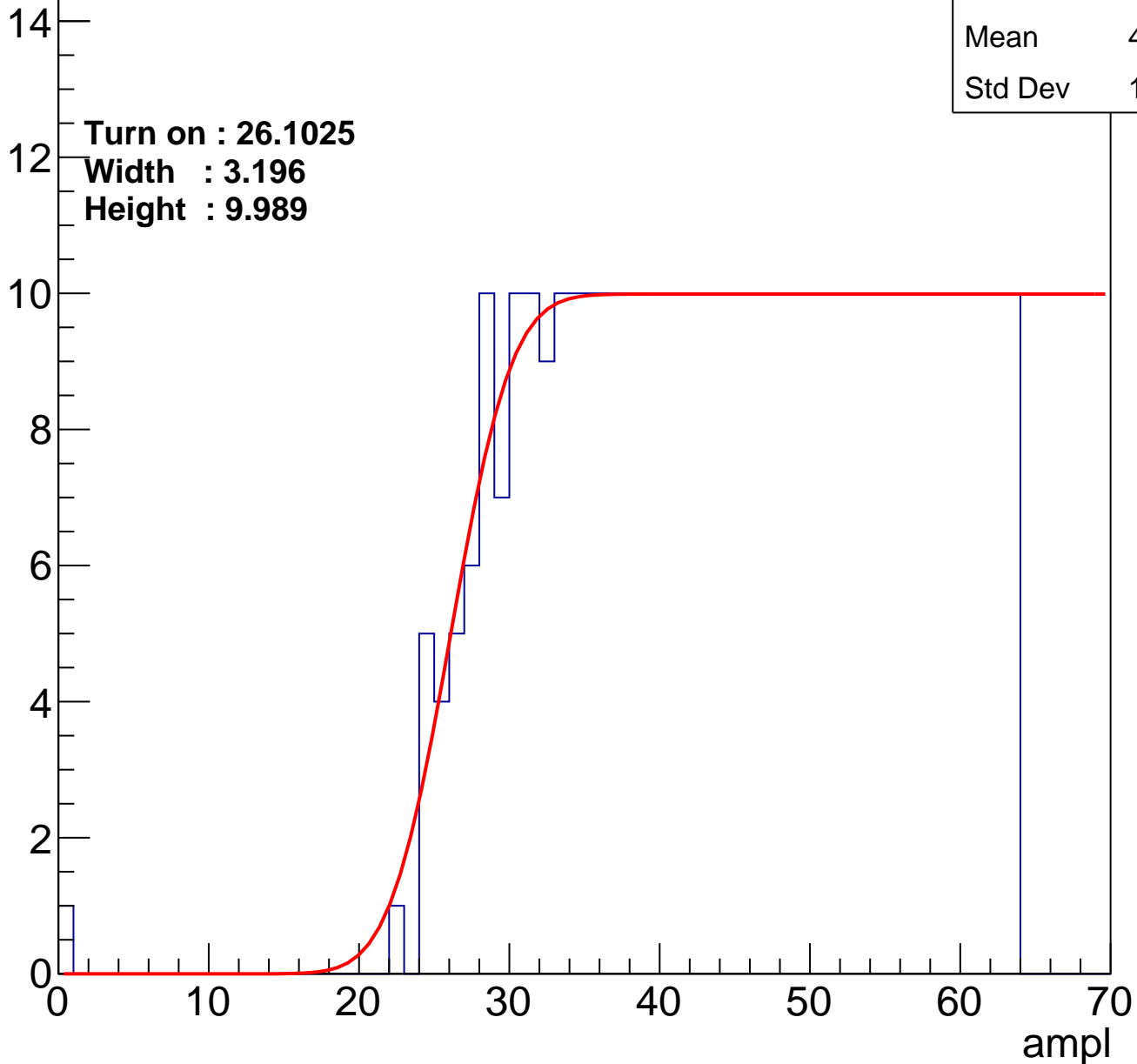
Entries	378
Mean	44.43
Std Dev	11.27

Turn on : 26.1025

Width : 3.196

Height : 9.989

Entry



B1L103S, U17-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.62
Std Dev	11.82

Turn on : 28.6237

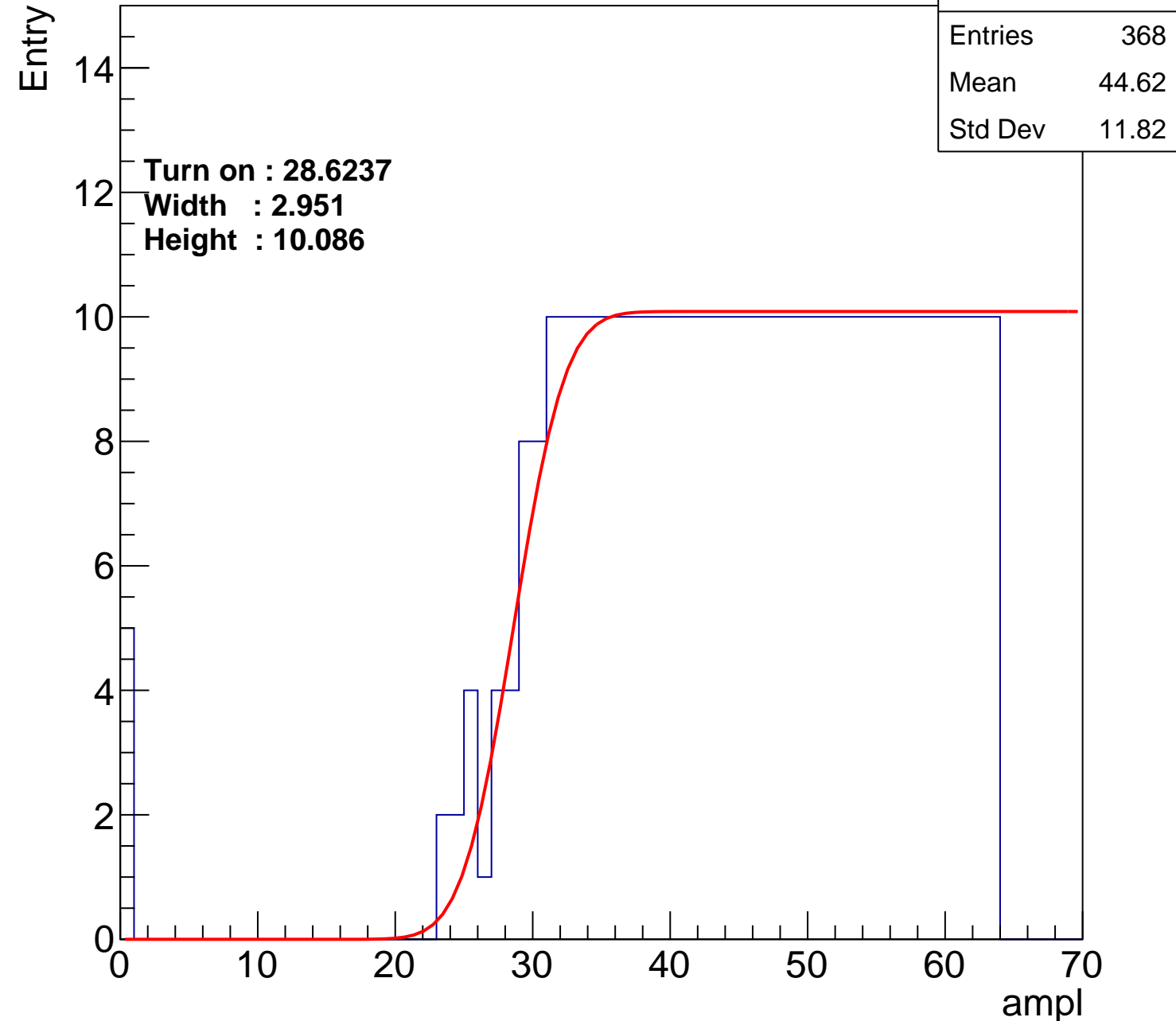
Width : 2.951

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch16

calib_packv5_042523_0143.root, FC#7, port C2

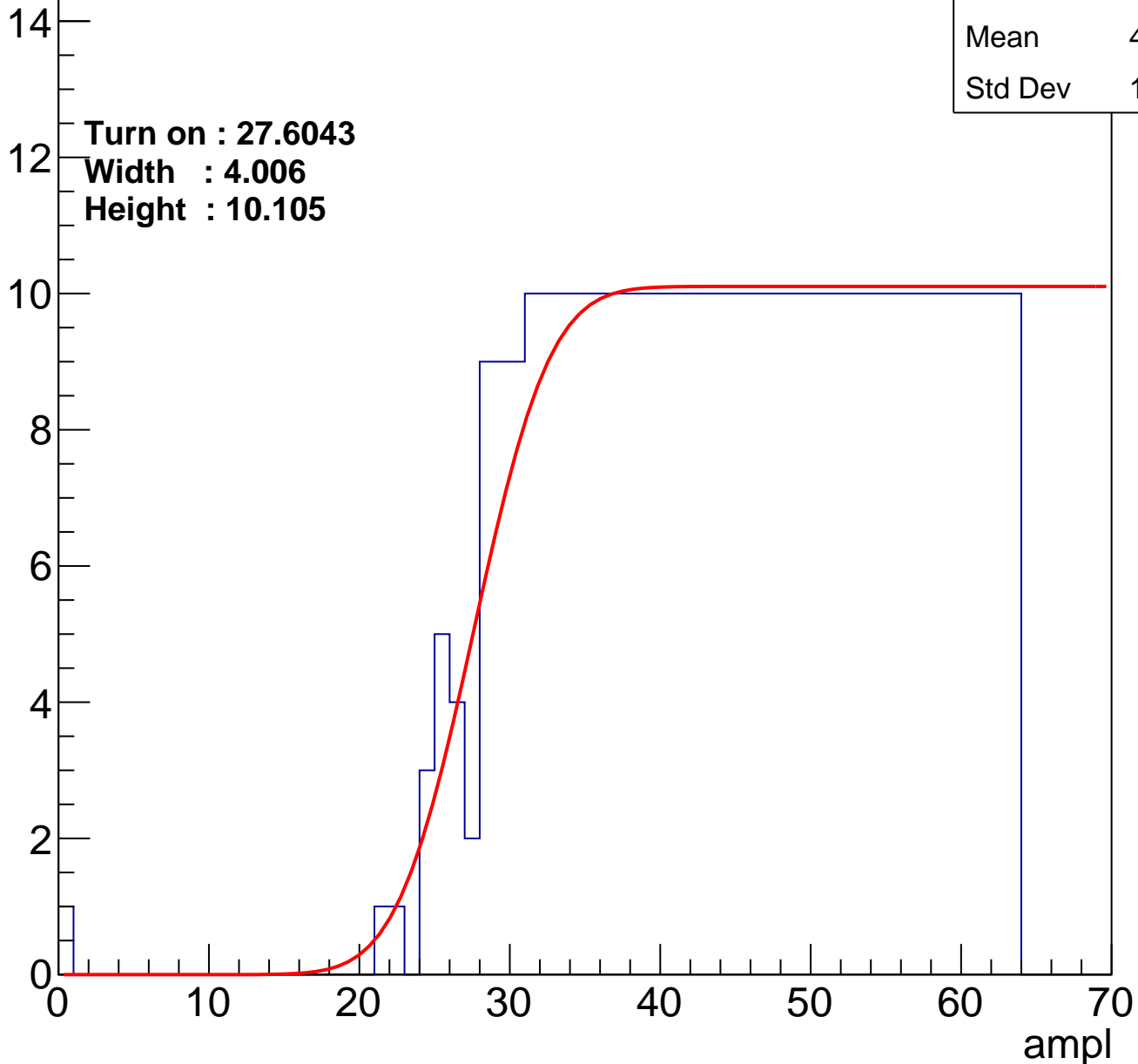
Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 27.6043

Width : 4.006

Height : 10.105

Entry



B1L103S, U17-ch17

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.25
Std Dev	11.71

Turn on : 26.6725

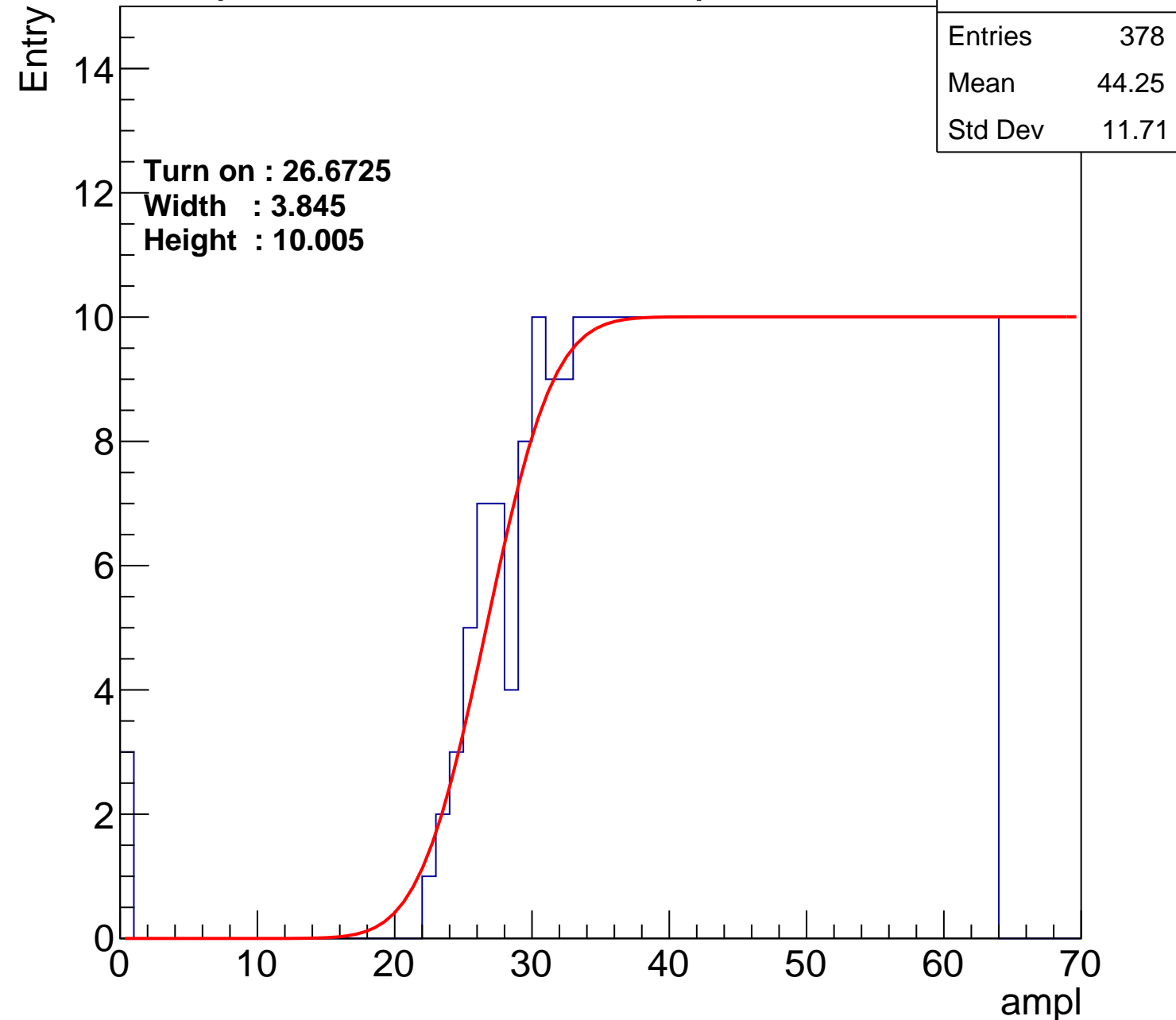
Width : 3.845

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch18

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.24
Std Dev	11.37

Turn on : 26.0147

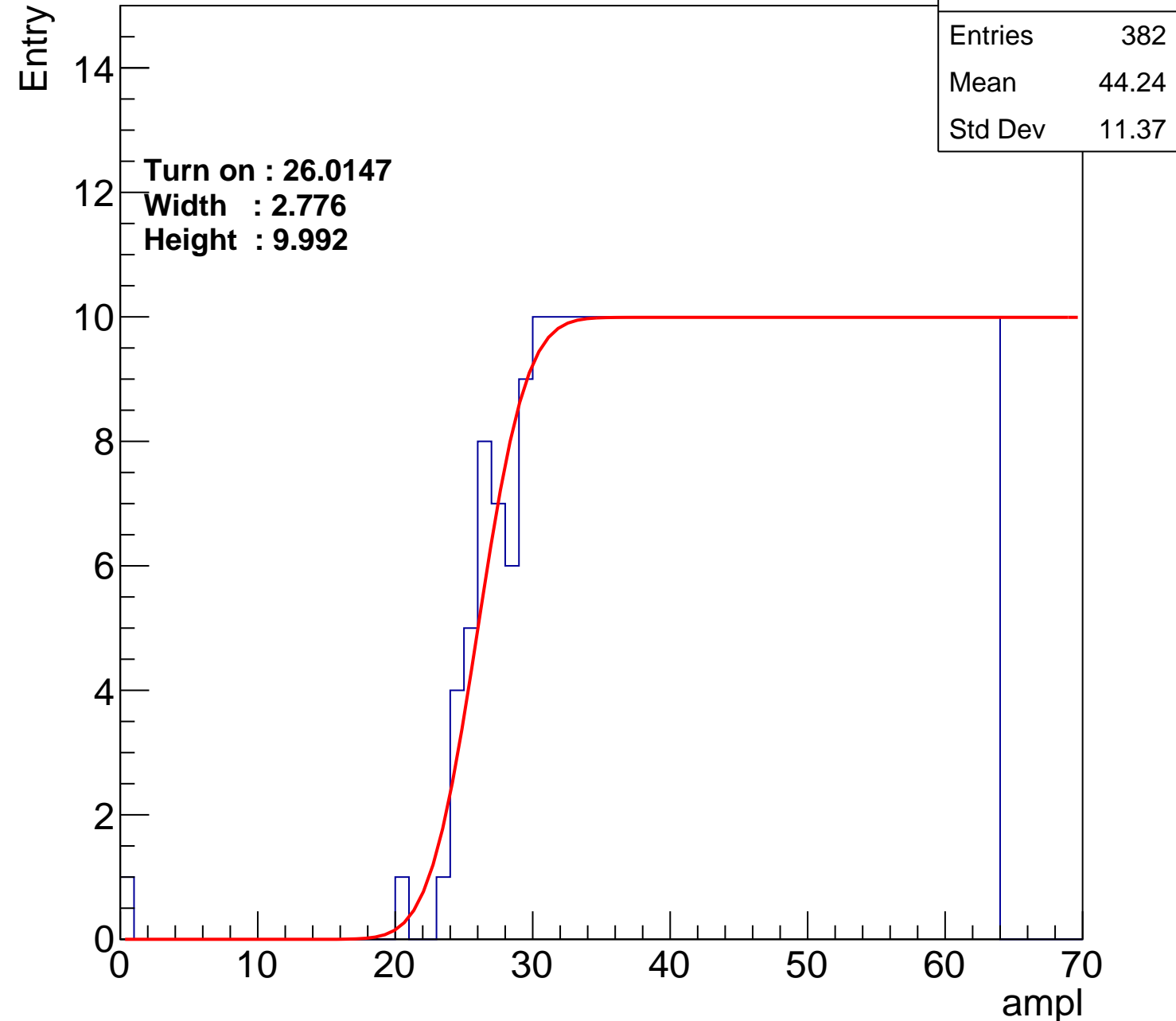
Width : 2.776

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch19

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.75
Std Dev	12.01

Turn on : 25.8212

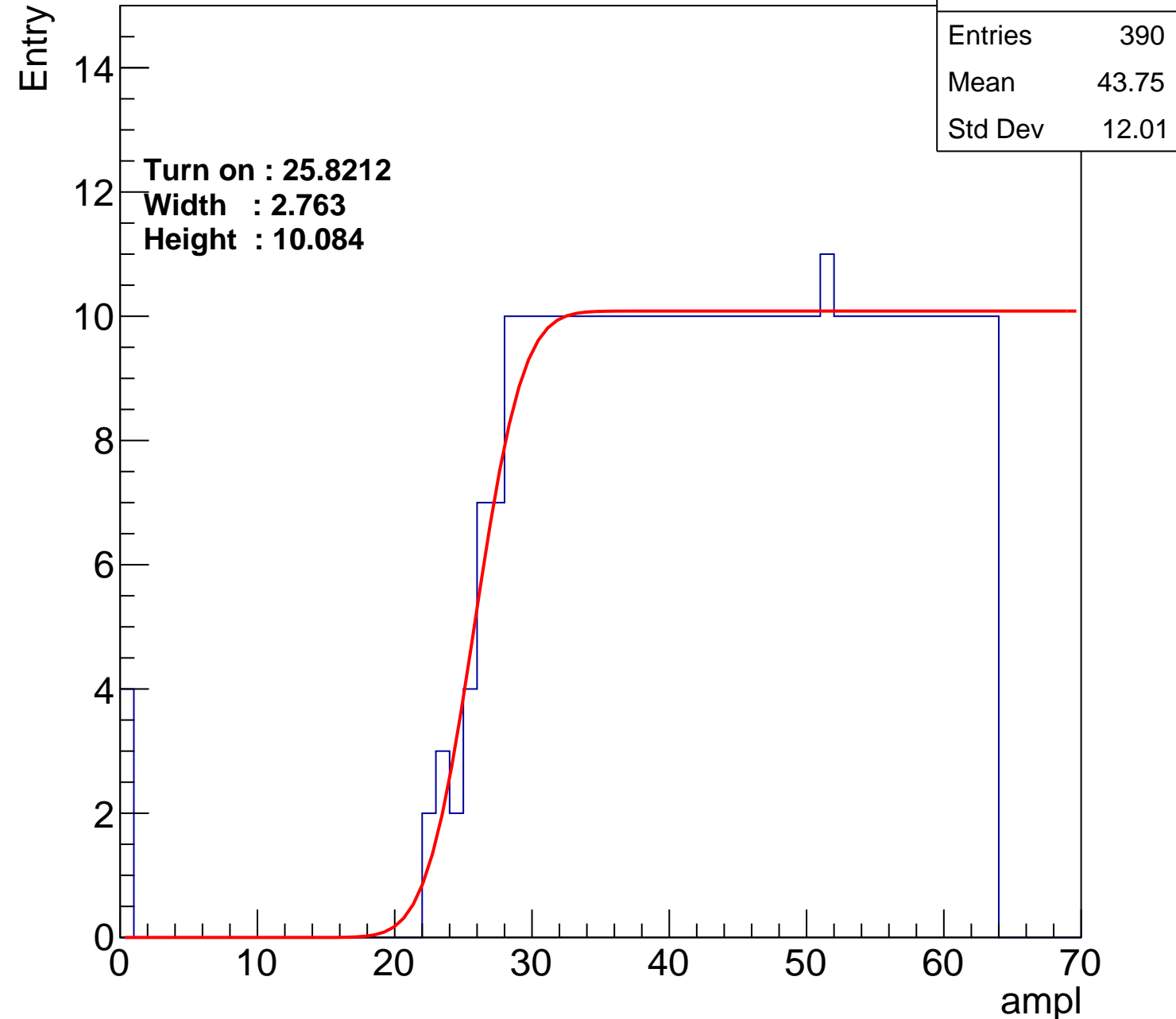
Width : 2.763

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch20

calib_packv5_042523_0143.root, FC#7, port C2

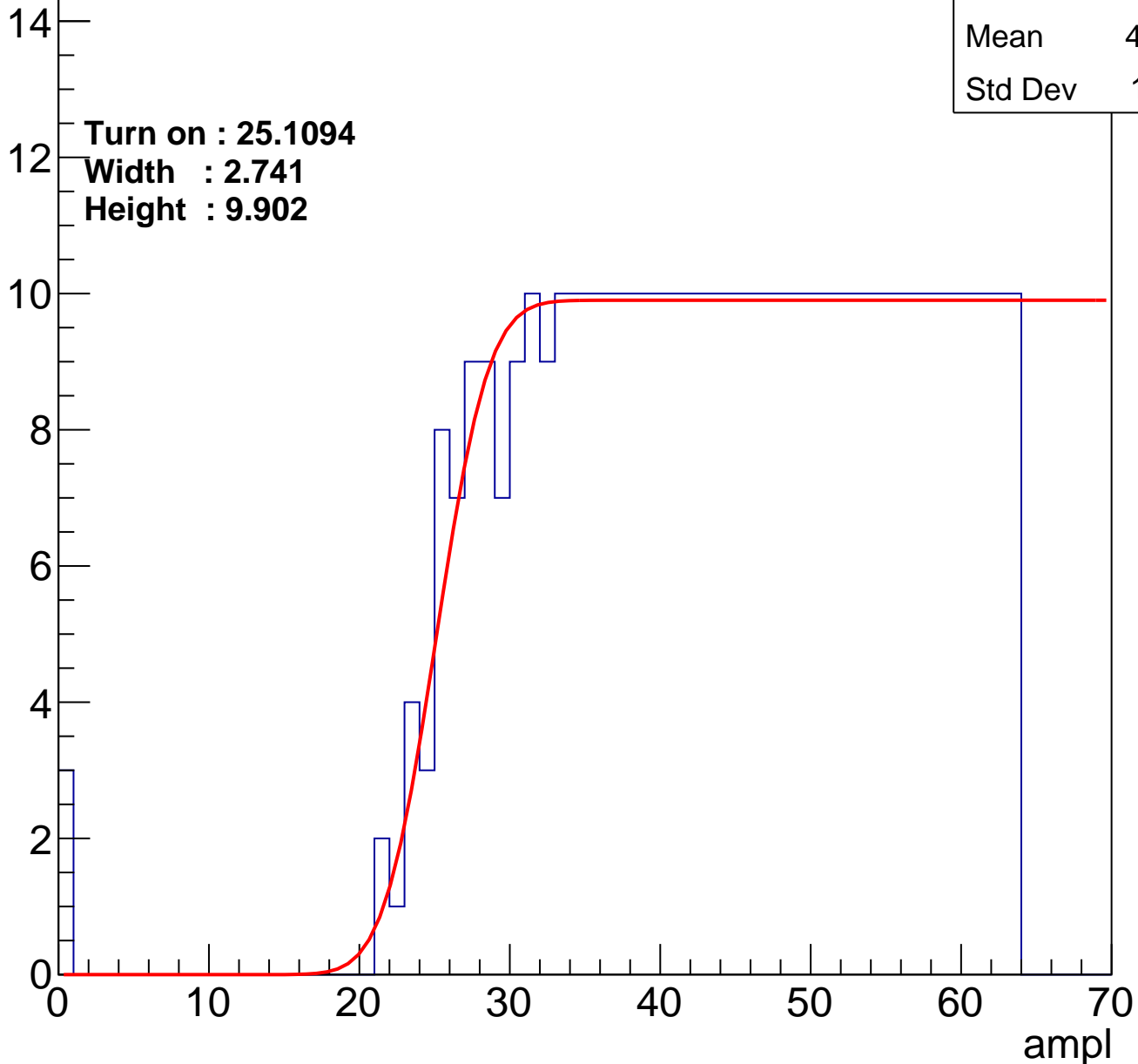
Entries	391
Mean	43.62
Std Dev	12.01

Turn on : 25.1094

Width : 2.741

Height : 9.902

Entry



B1L103S, U17-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.61
Std Dev	11.97

Turn on : 28.4742

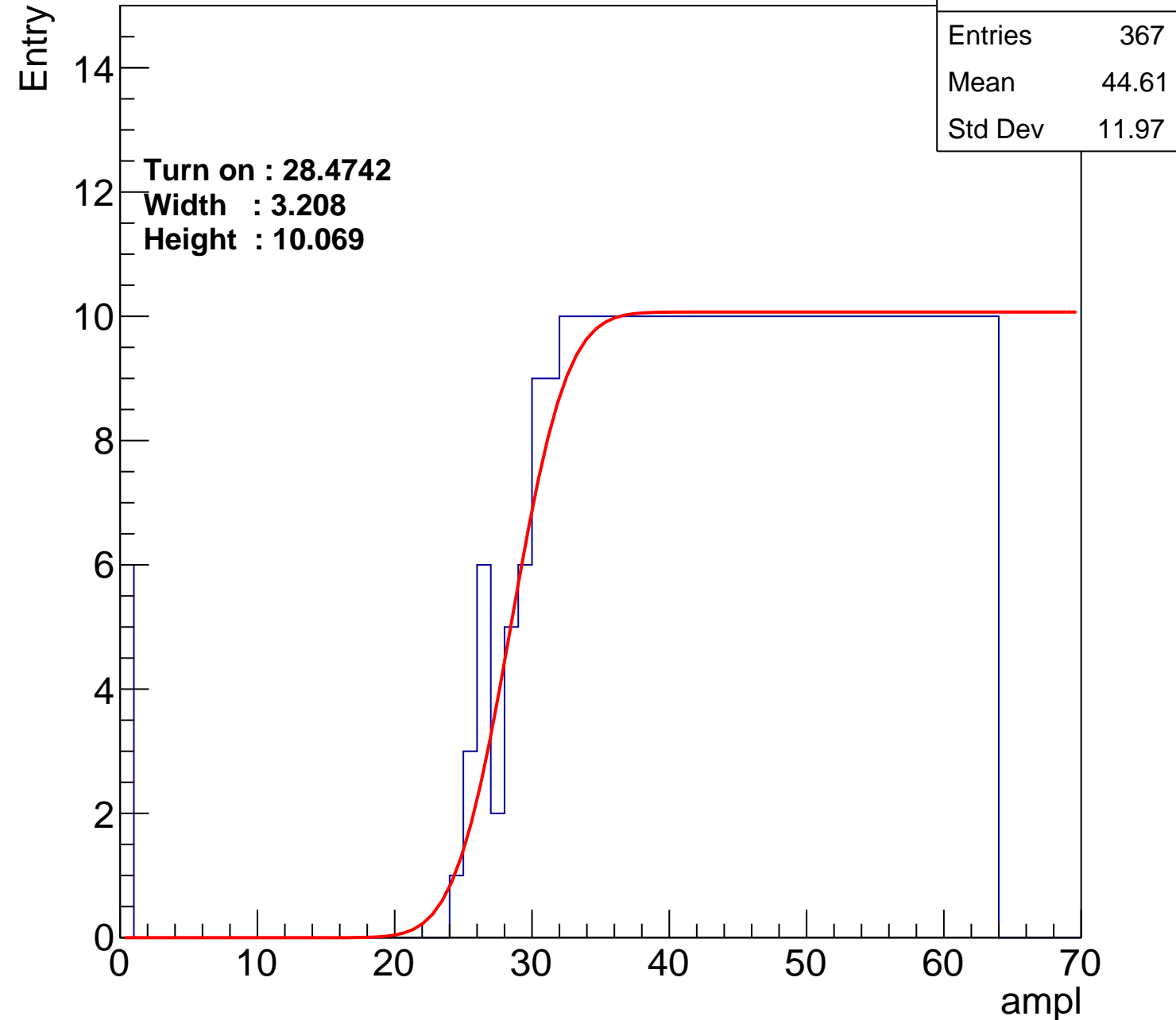
Width : 3.208

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch22

calib_packv5_042523_0143.root, FC#7, port C2

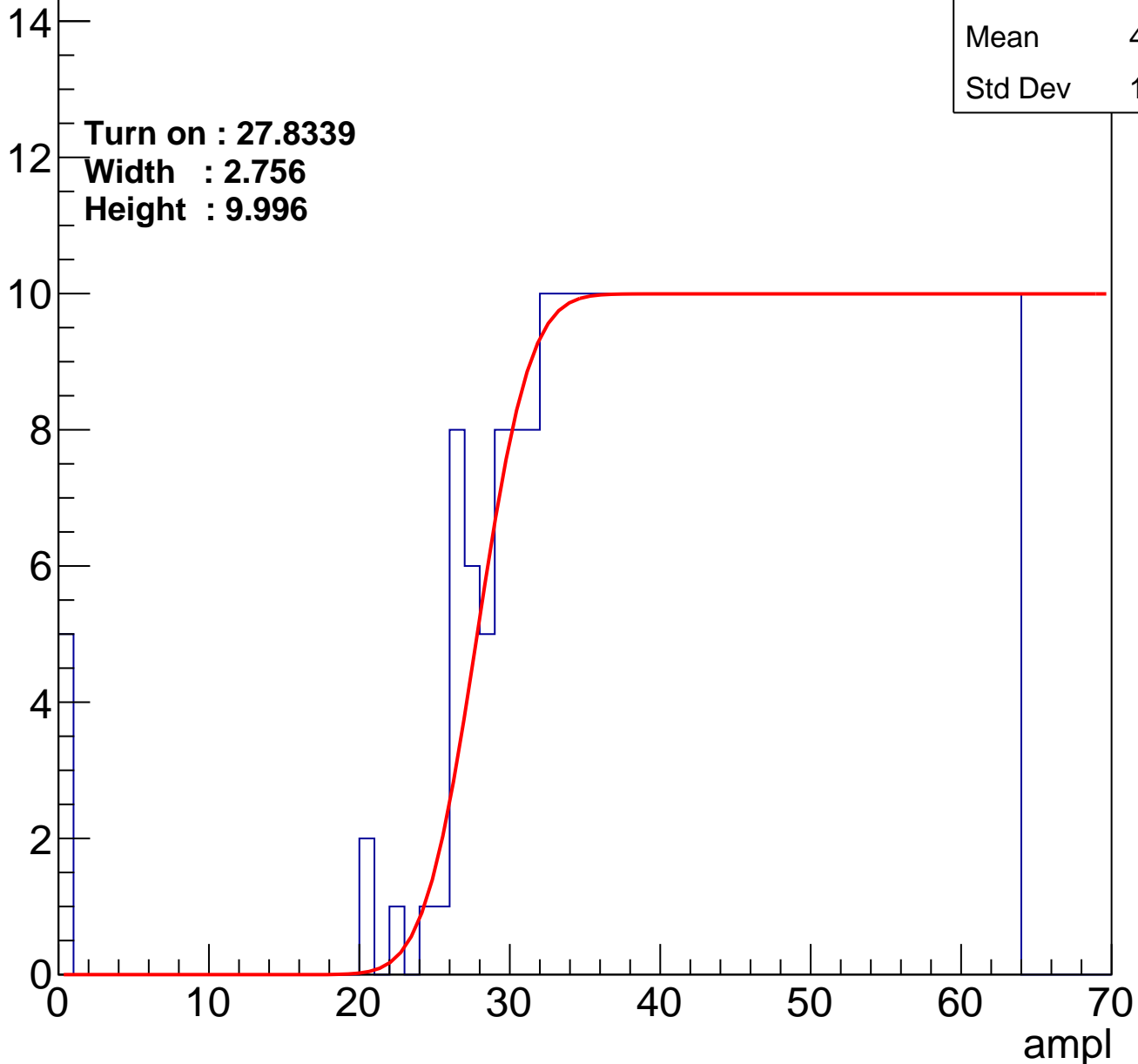
Entries	373
Mean	44.35
Std Dev	11.98

Turn on : 27.8339

Width : 2.756

Height : 9.996

Entry



B1L103S, U17-ch23

calib_packv5_042523_0143.root, FC#7, port C2

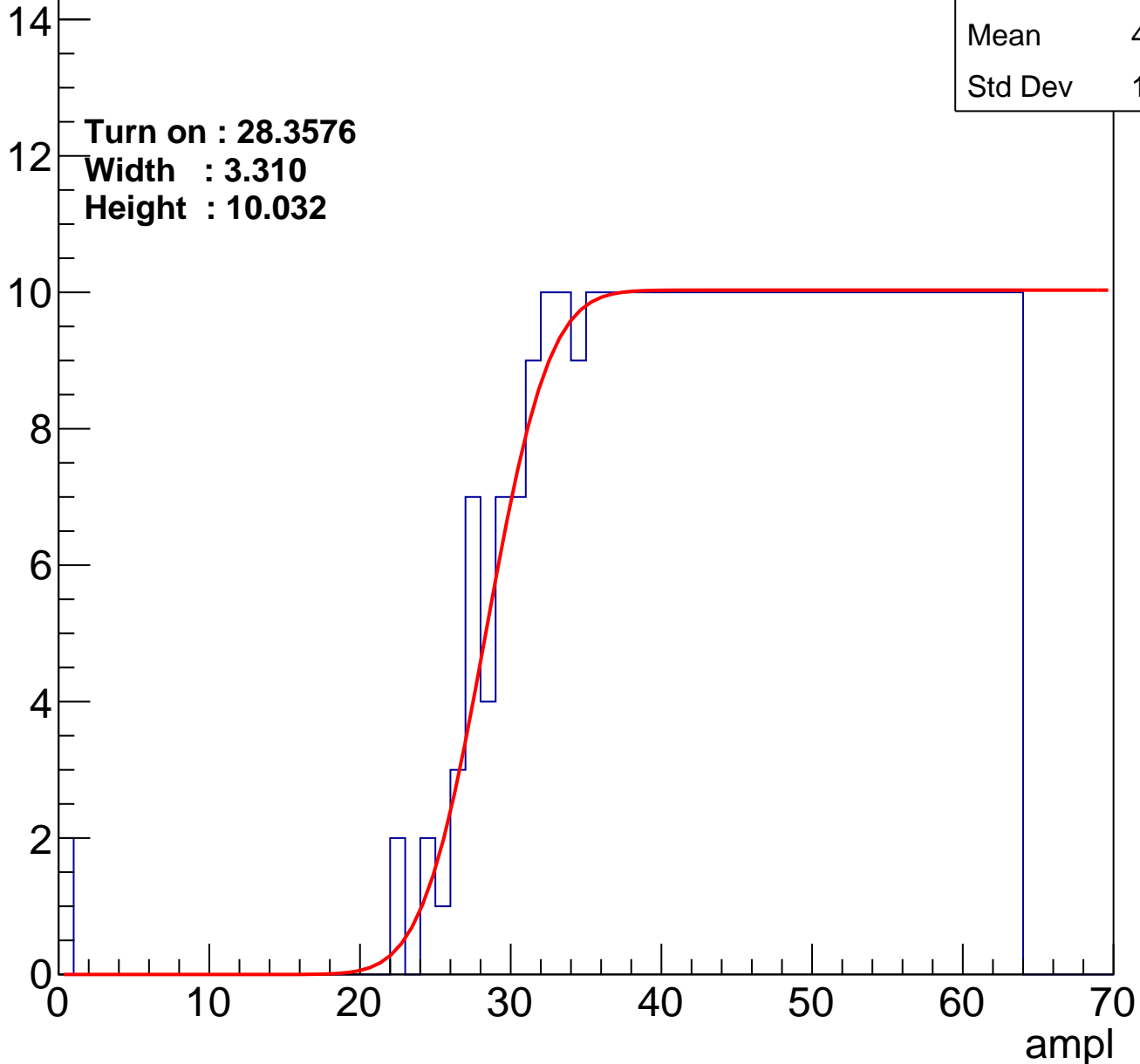
Entries	363
Mean	45.05
Std Dev	11.16

Turn on : 28.3576

Width : 3.310

Height : 10.032

Entry



B1L103S, U17-ch24

calib_packv5_042523_0143.root, FC#7, port C2

Entries	362
Mean	45.02
Std Dev	11.45

Turn on : 28.8490

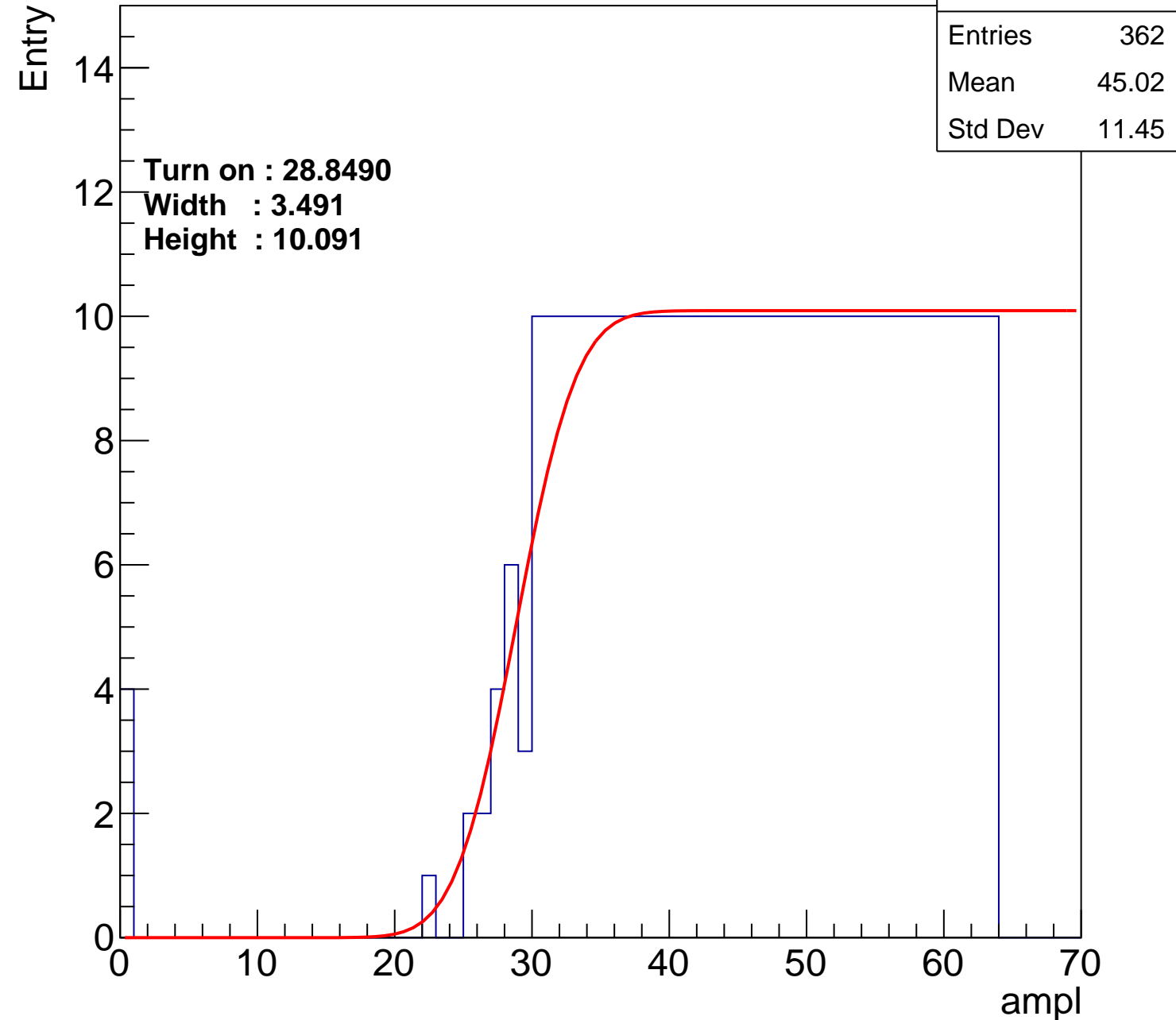
Width : 3.491

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	395
Mean	43.53
Std Dev	11.89

Turn on : 25.3566

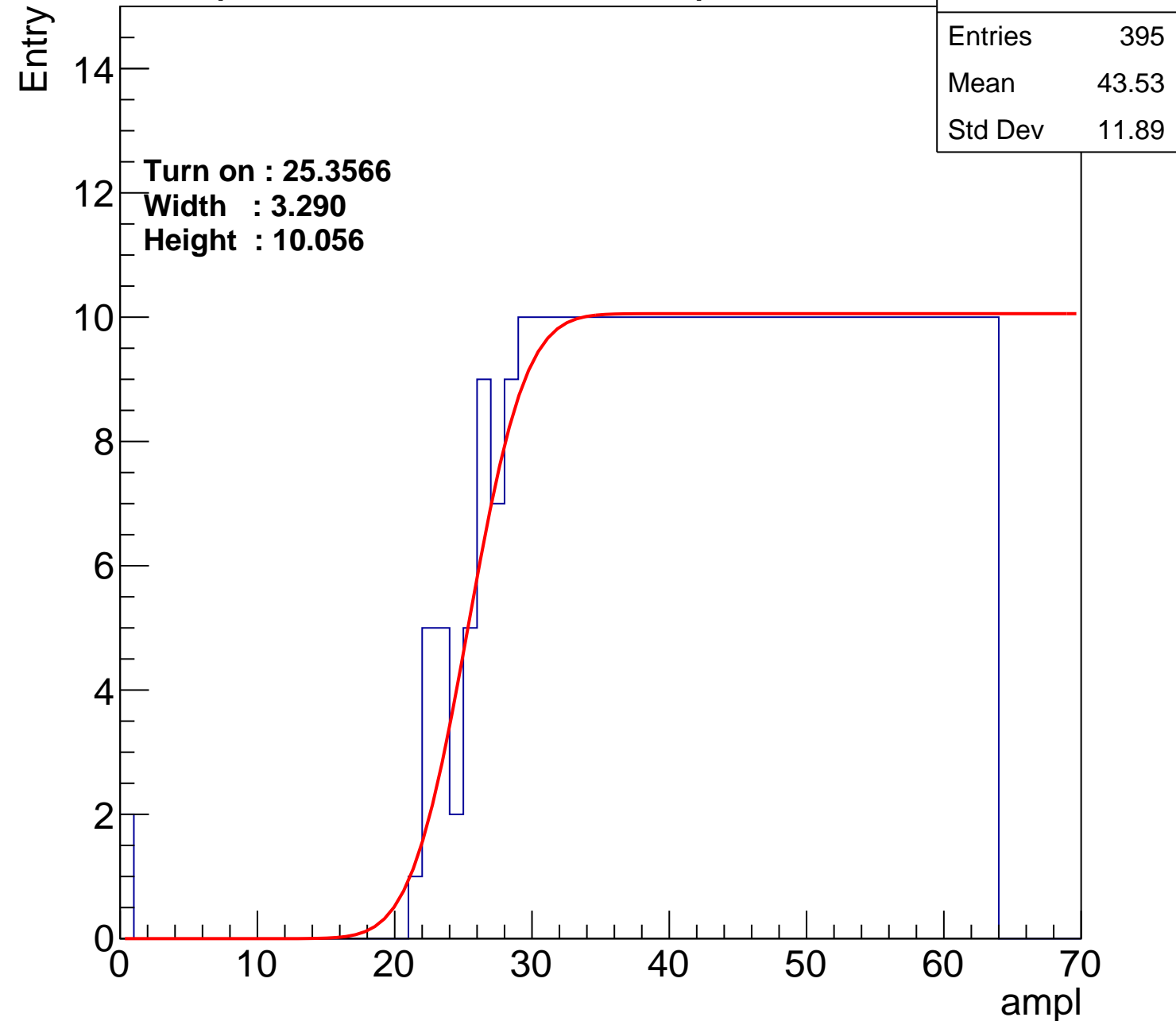
Width : 3.290

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch26

calib_packv5_042523_0143.root, FC#7, port C2

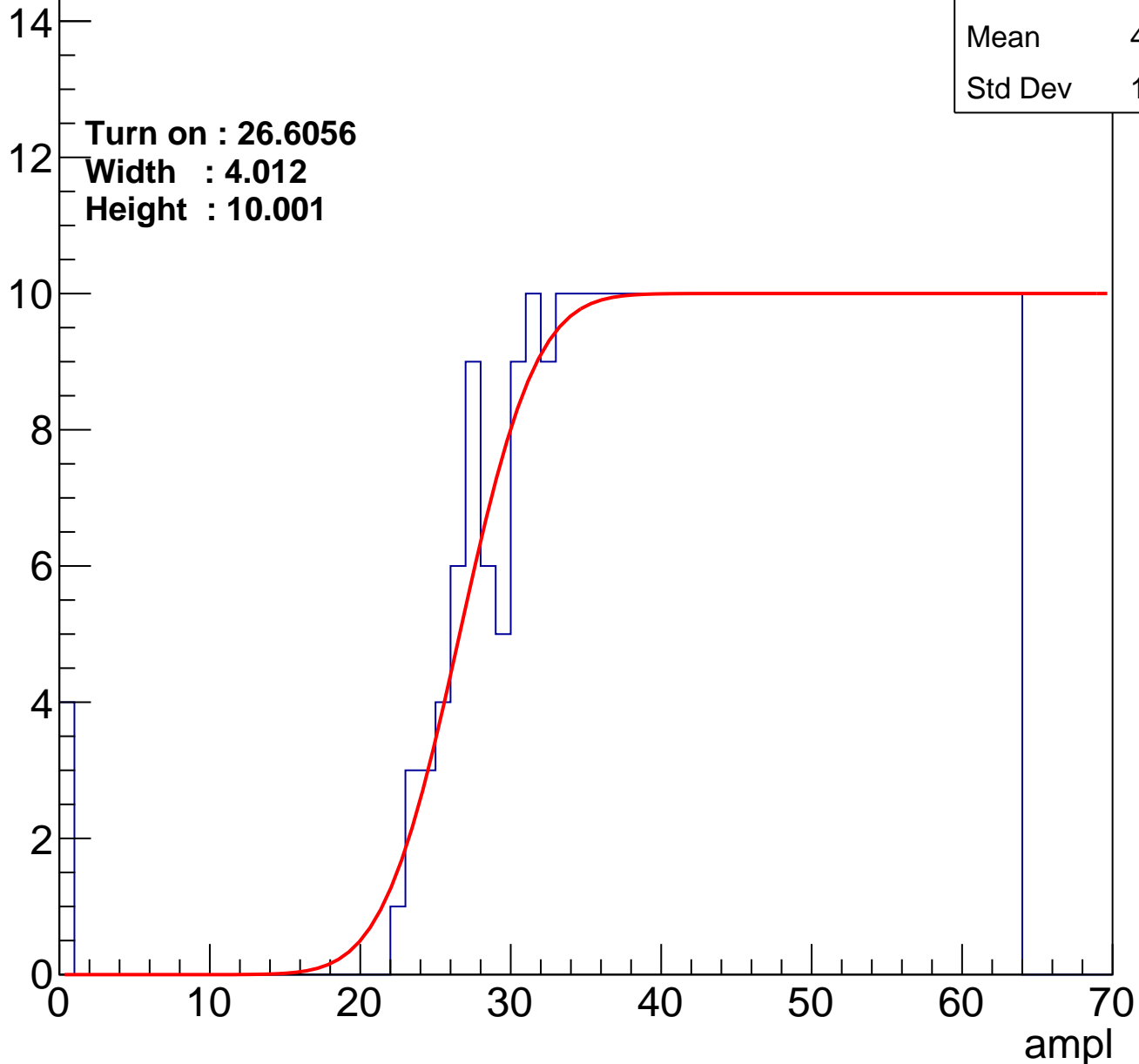
Entries	379
Mean	44.12
Std Dev	11.93

Turn on : 26.6056

Width : 4.012

Height : 10.001

Entry



B1L103S, U17-ch27

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.81
Std Dev	11.65

Turn on : 25.4506

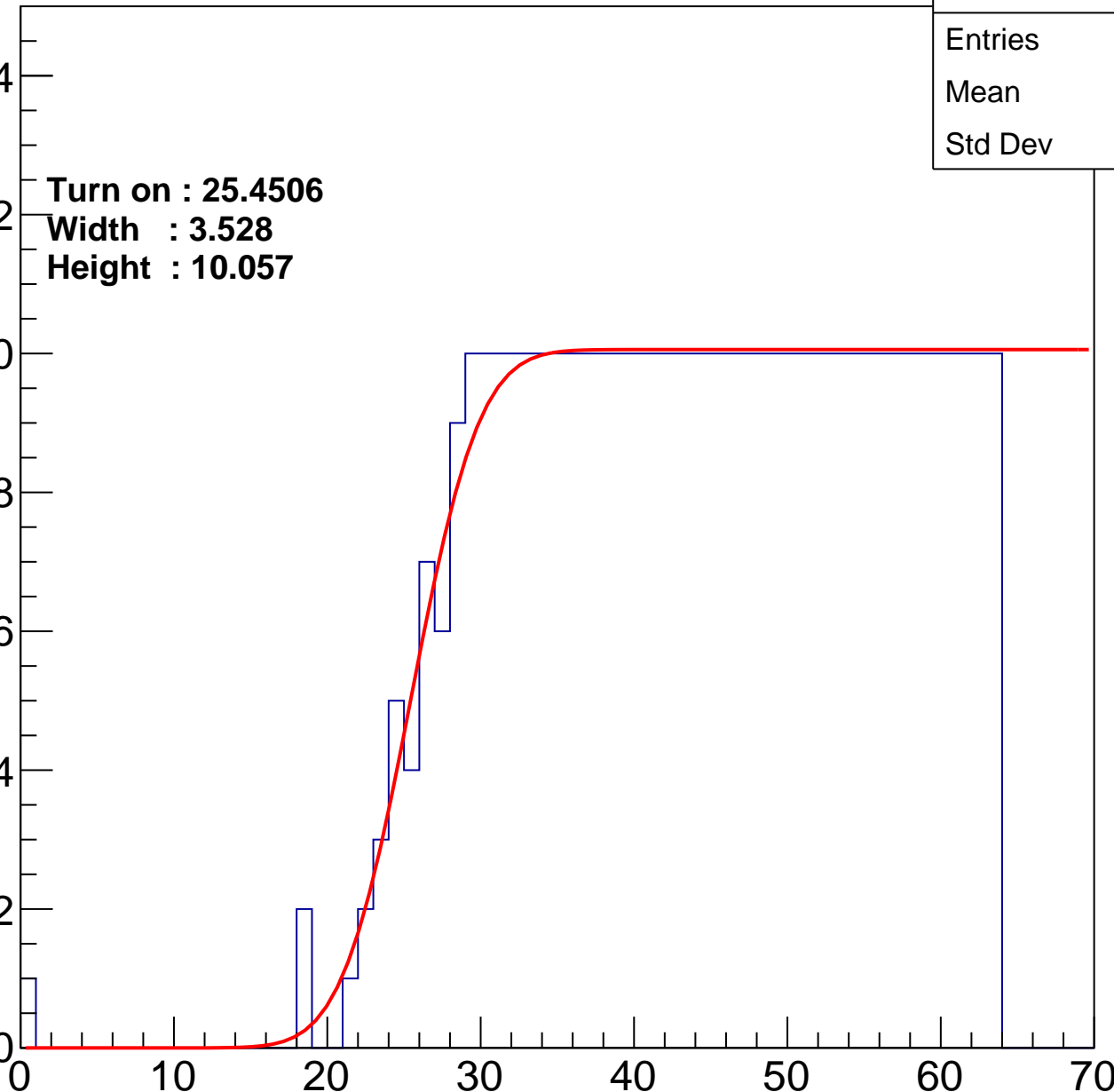
Width : 3.528

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch28

calib_packv5_042523_0143.root, FC#7, port C2

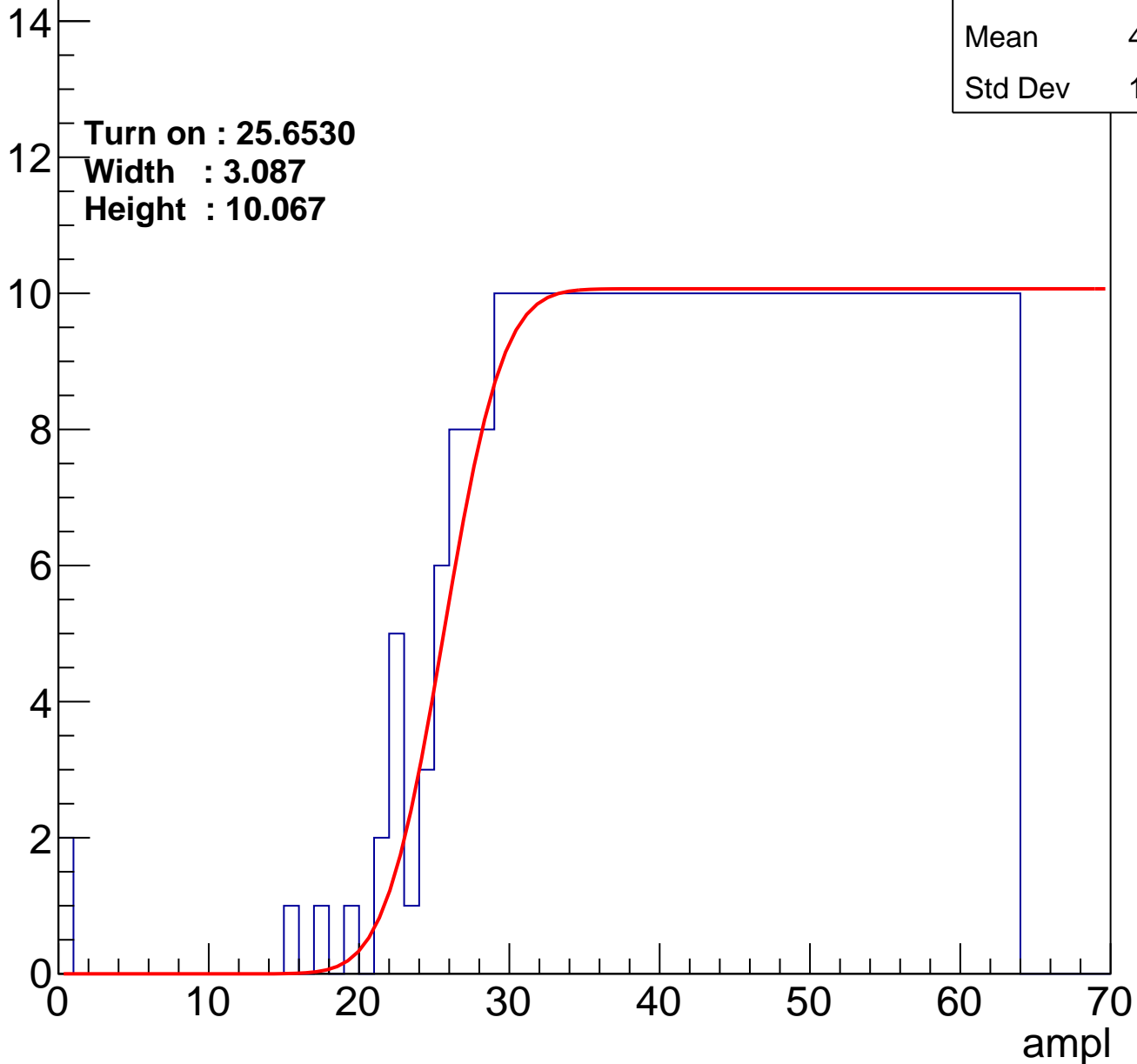
Entries	396
Mean	43.42
Std Dev	12.03

Turn on : 25.6530

Width : 3.087

Height : 10.067

Entry



B1L103S, U17-ch29

calib_packv5_042523_0143.root, FC#7, port C2

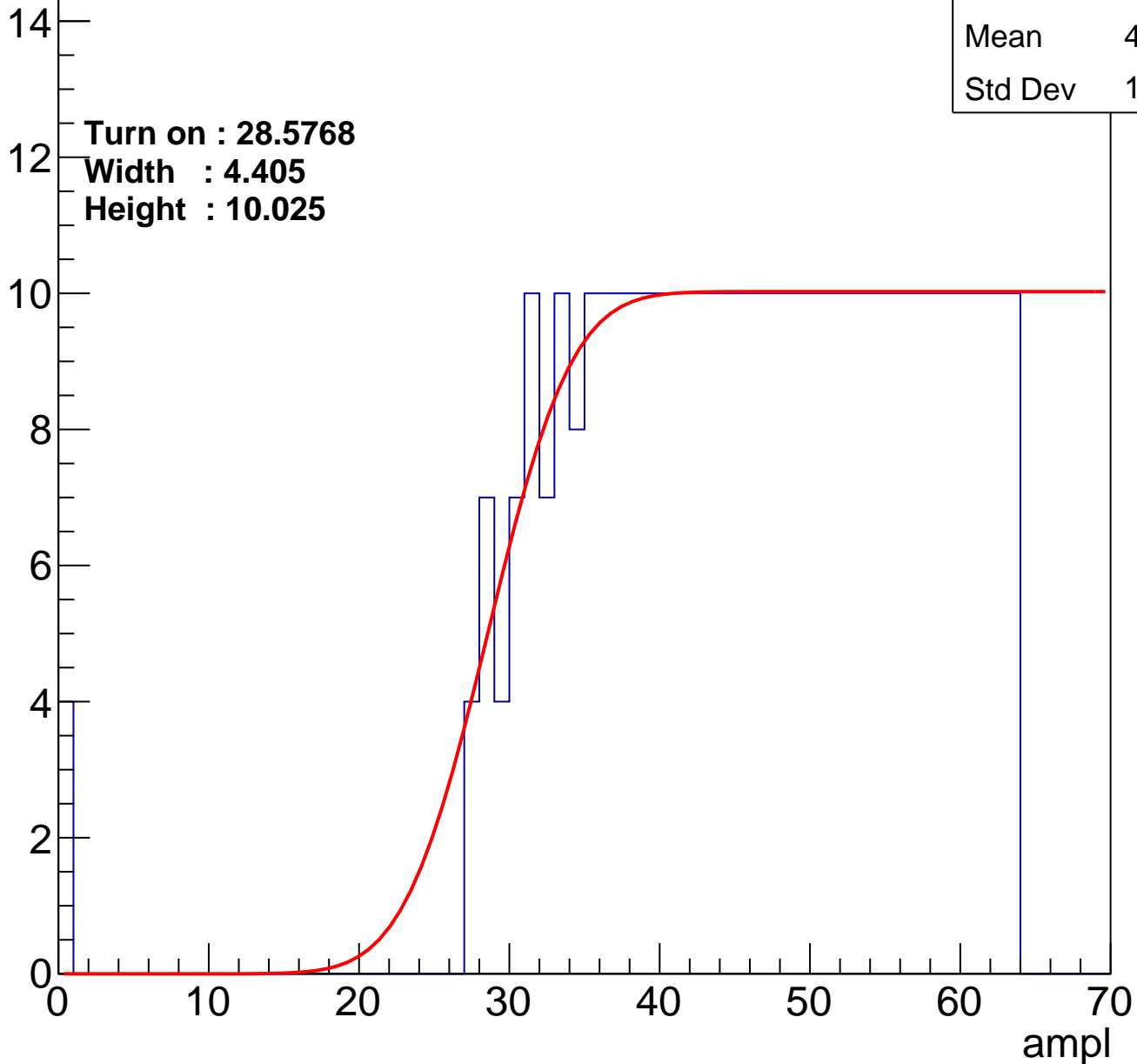
Entries	351
Mean	45.52
Std Dev	11.25

Turn on : 28.5768

Width : 4.405

Height : 10.025

Entry



B1L103S, U17-ch30

calib_packv5_042523_0143.root, FC#7, port C2

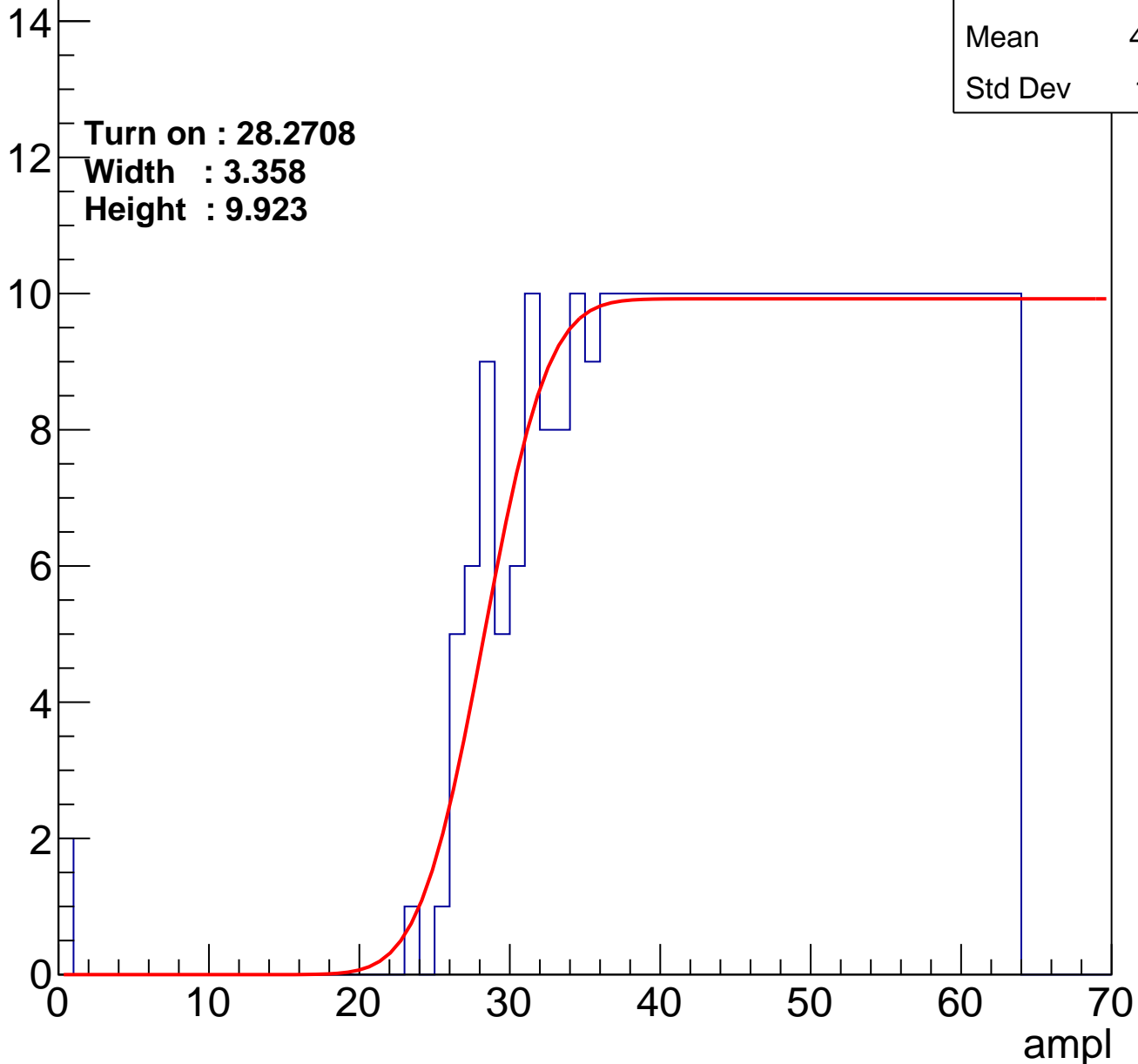
Entries	360
Mean	45.17
Std Dev	11.11

Turn on : 28.2708

Width : 3.358

Height : 9.923

Entry



B1L103S, U17-ch31

calib_packv5_042523_0143.root, FC#7, port C2

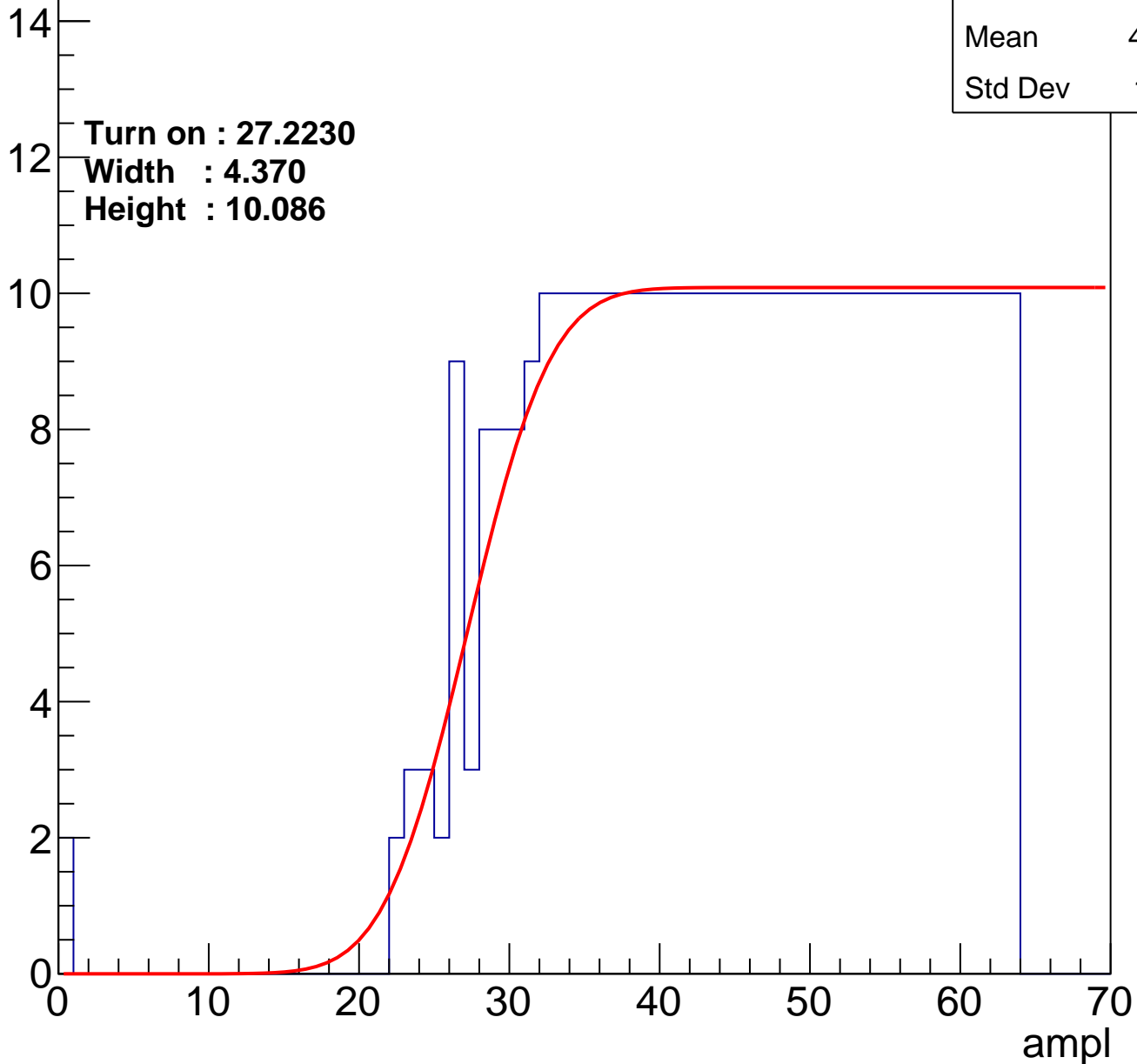
Entries	377
Mean	44.36
Std Dev	11.51

Turn on : 27.2230

Width : 4.370

Height : 10.086

Entry



B1L103S, U17-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.17
Std Dev	11.85

Turn on : 26.8065

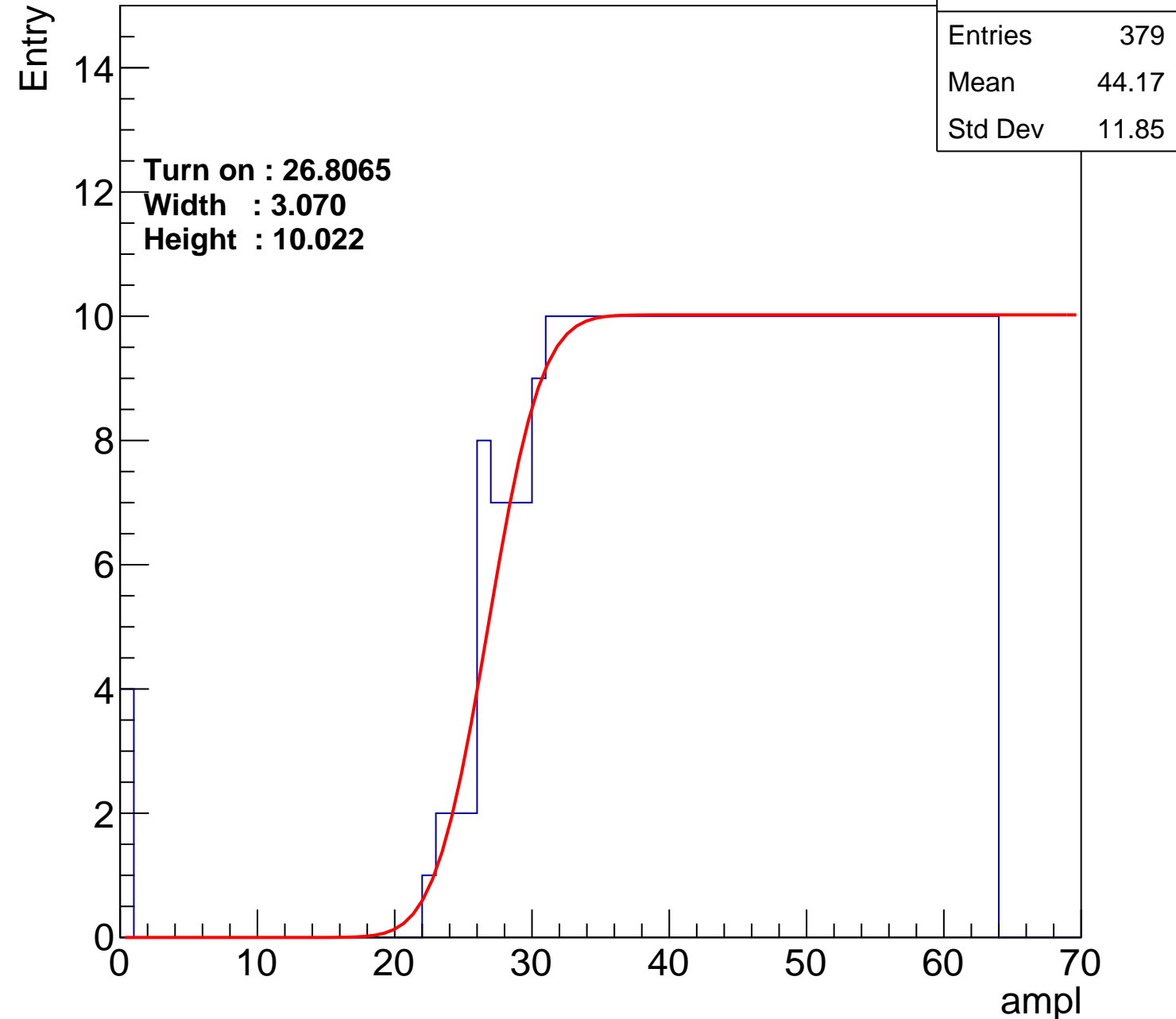
Width : 3.070

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch33

calib_packv5_042523_0143.root, FC#7, port C2

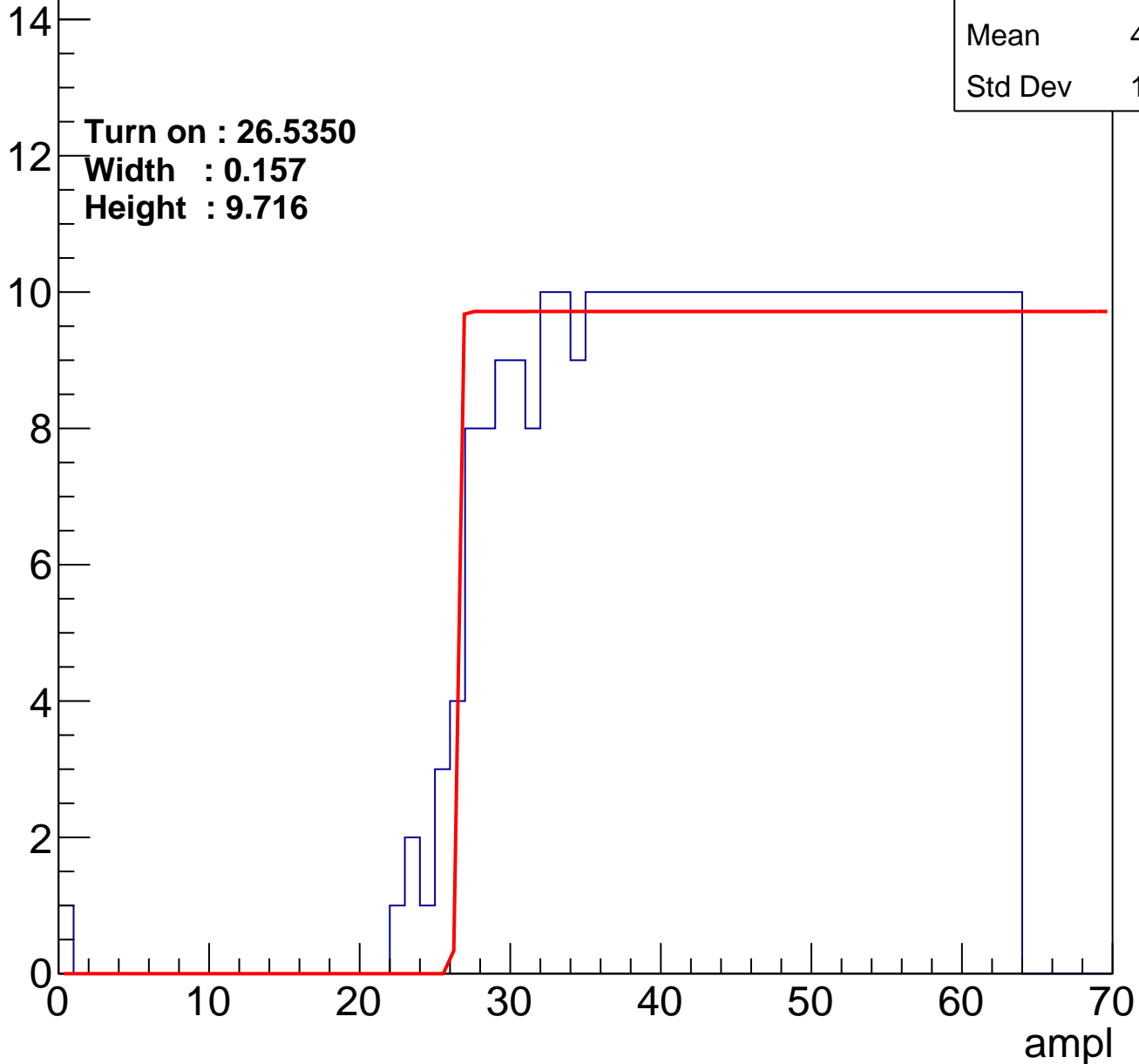
Entries	373
Mean	44.65
Std Dev	11.17

Turn on : 26.5350

Width : 0.157

Height : 9.716

Entry



B1L103S, U17-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.95
Std Dev	11.22

Turn on : 28.0005

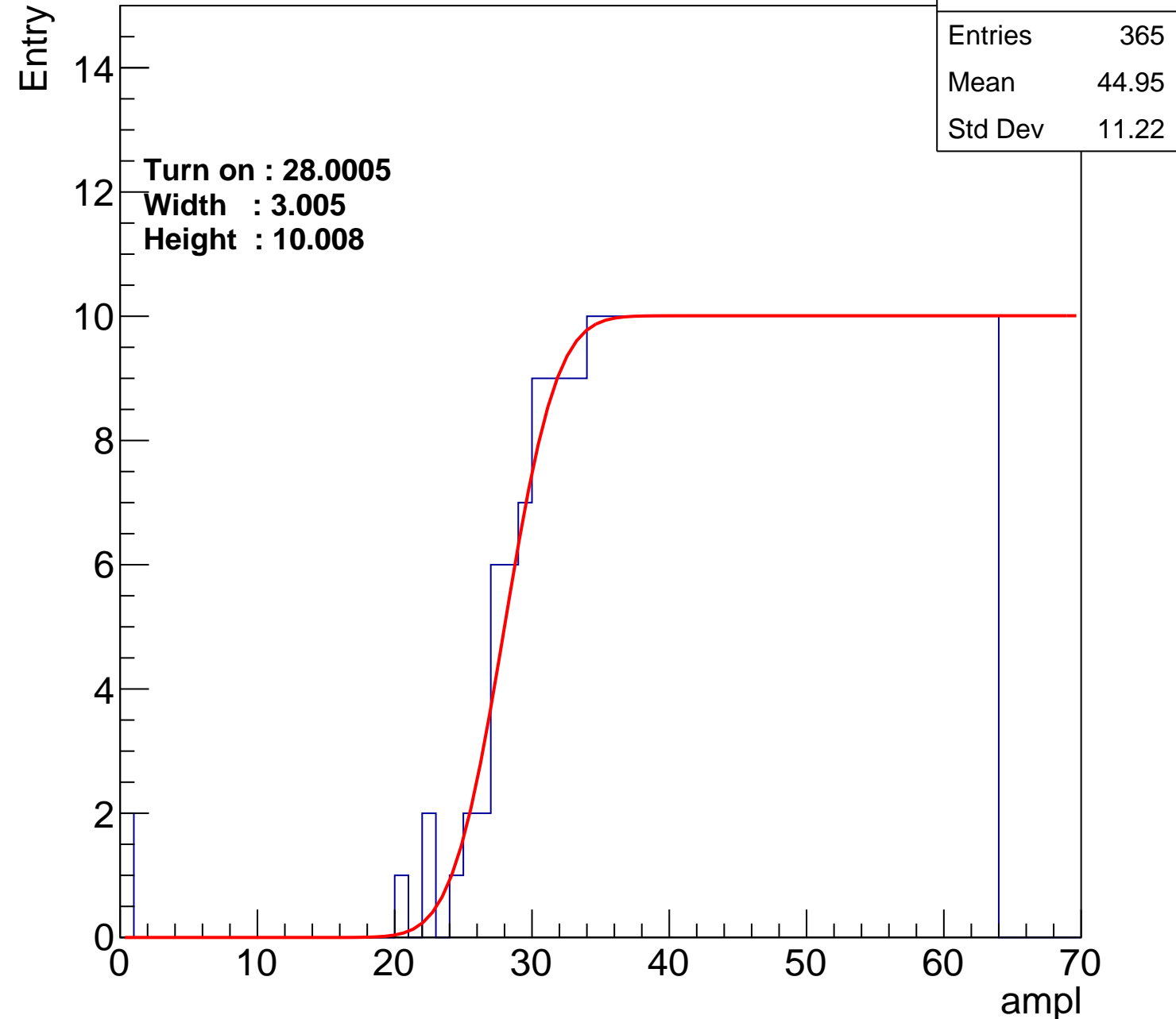
Width : 3.005

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch35

calib_packv5_042523_0143.root, FC#7, port C2

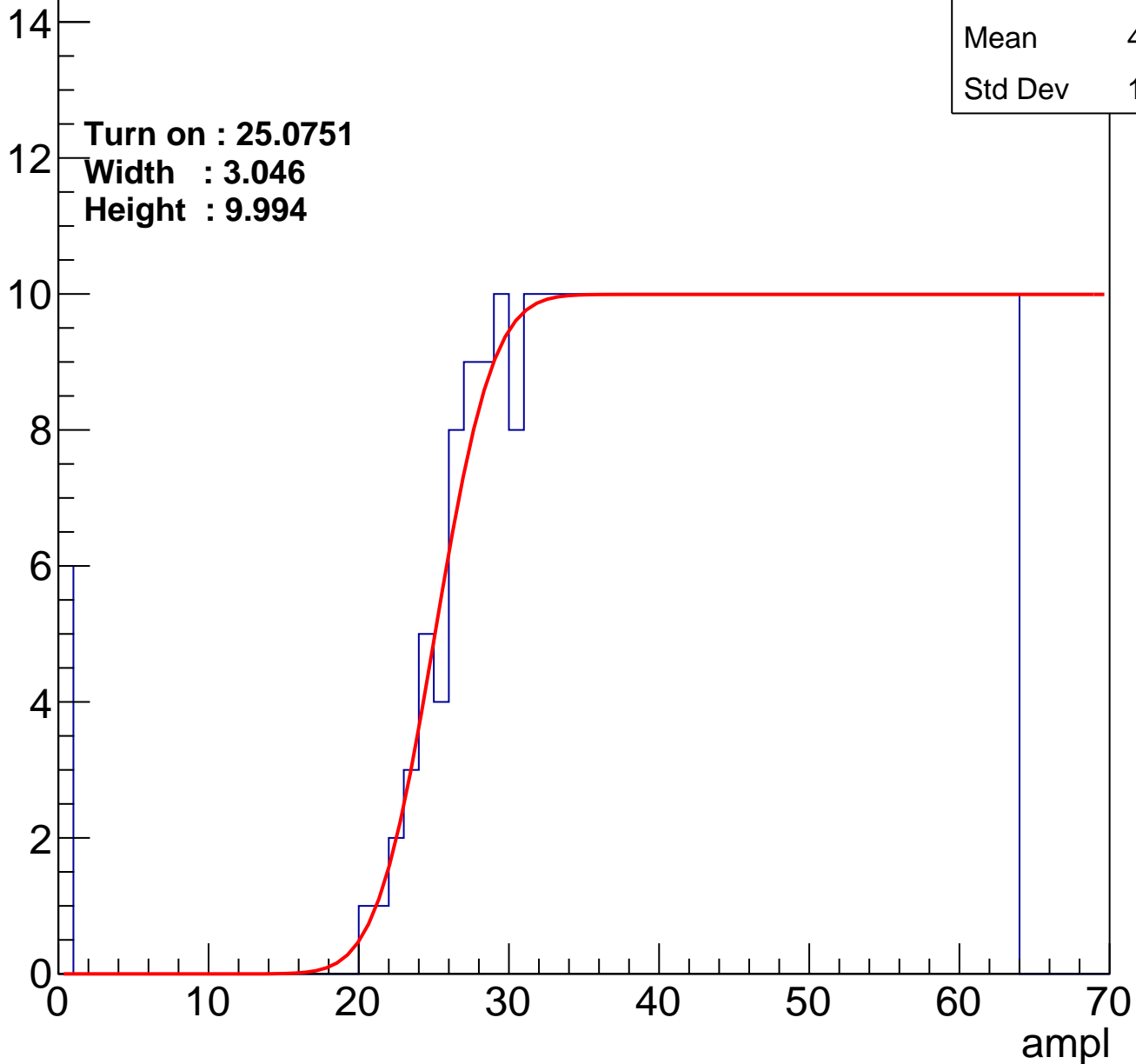
Entries	396
Mean	43.22
Std Dev	12.56

Turn on : 25.0751

Width : 3.046

Height : 9.994

Entry



B1L103S, U17-ch36

calib_packv5_042523_0143.root, FC#7, port C2

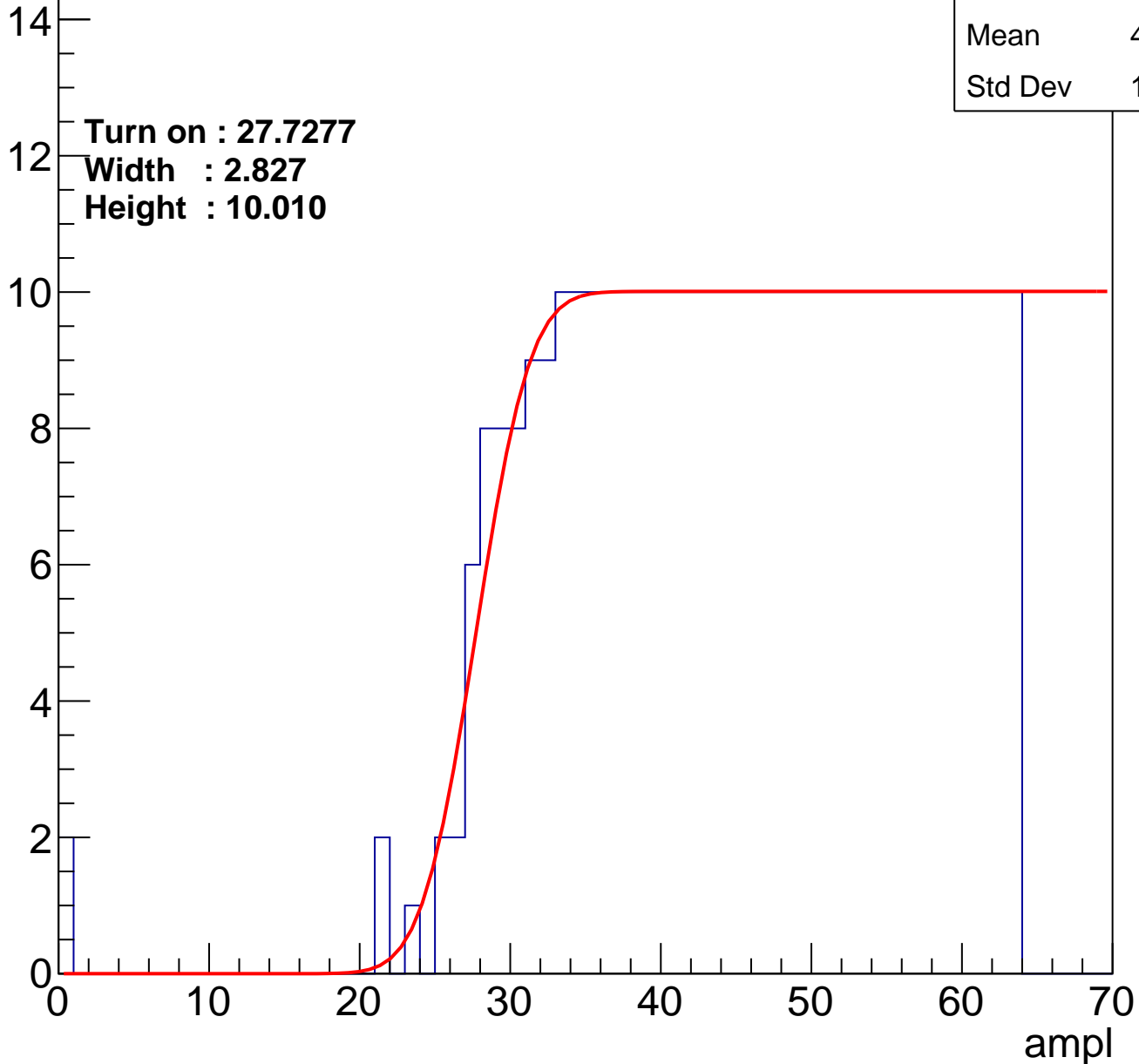
Entries	367
Mean	44.88
Std Dev	11.22

Turn on : 27.7277

Width : 2.827

Height : 10.010

Entry



B1L103S, U17-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.74
Std Dev	11.15

Turn on : 27.2353

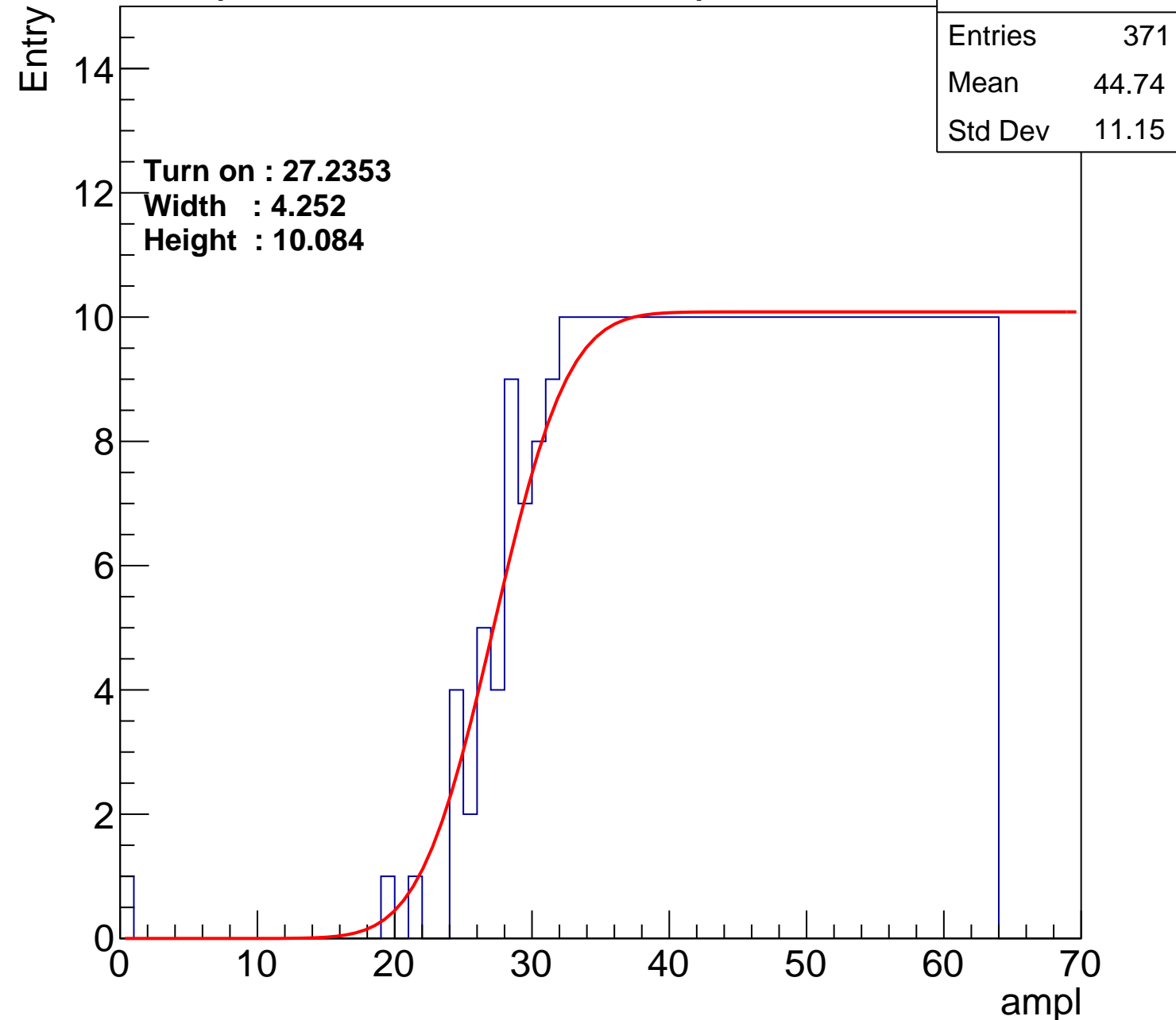
Width : 4.252

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch38

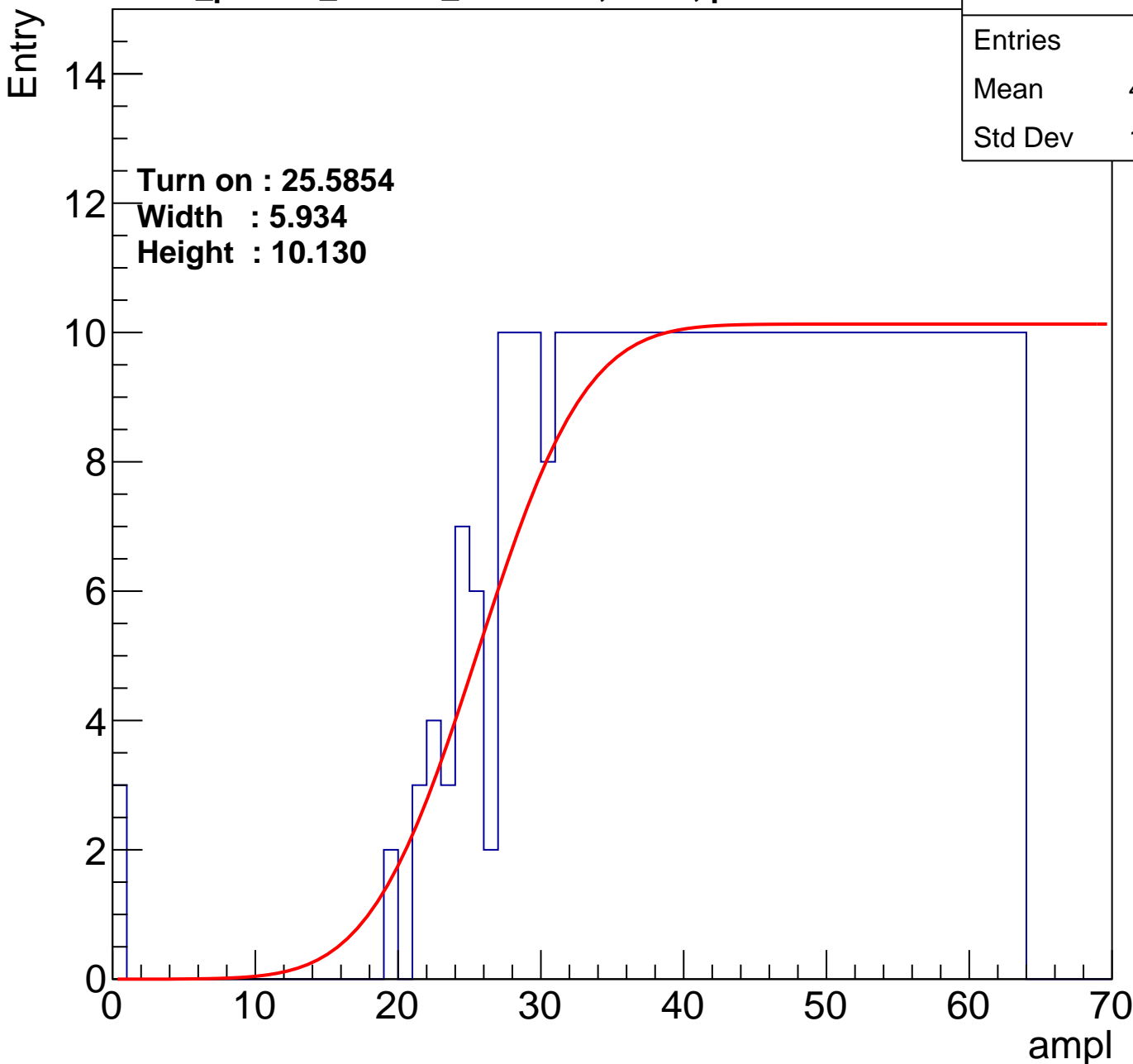
calib_packv5_042523_0143.root, FC#7, port C2

Entries	398
Mean	43.26
Std Dev	12.22

Turn on : 25.5854

Width : 5.934

Height : 10.130



B1L103S, U17-ch39

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.29
Std Dev	11.83

Turn on : 27.5805

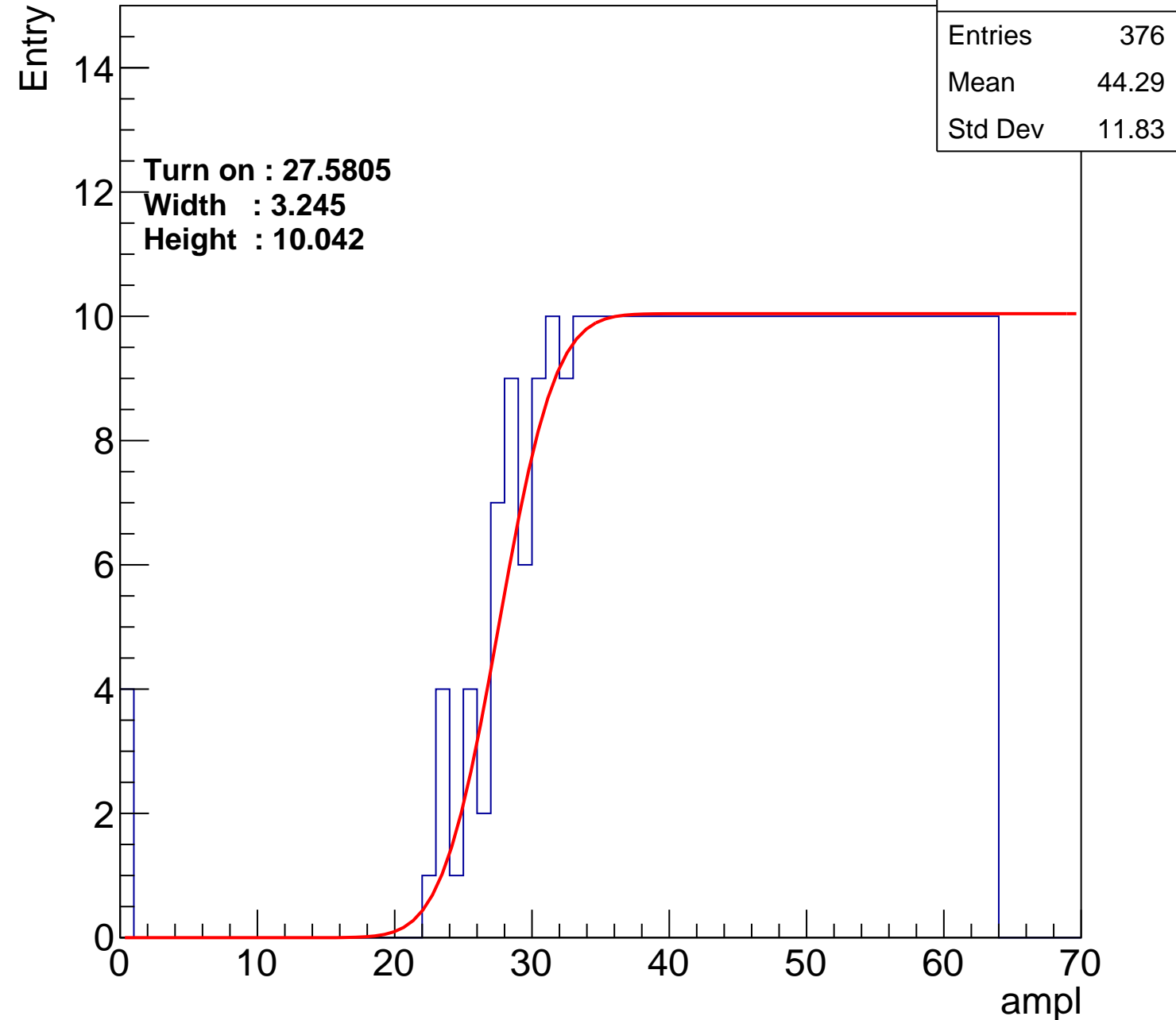
Width : 3.245

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch40

calib_packv5_042523_0143.root, FC#7, port C2

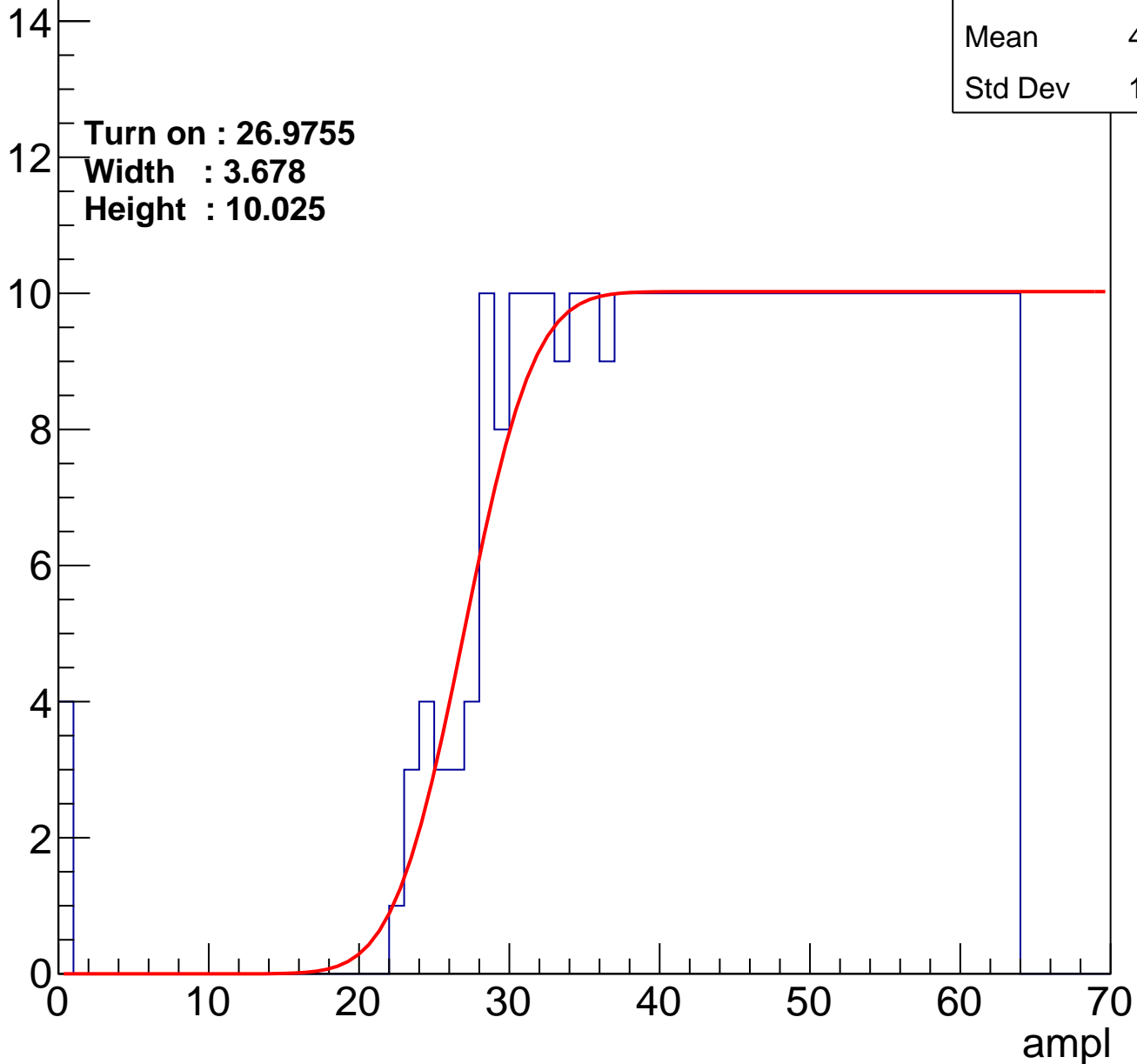
Entries	378
Mean	44.18
Std Dev	11.88

Turn on : 26.9755

Width : 3.678

Height : 10.025

Entry



B1L103S, U17-ch41

calib_packv5_042523_0143.root, FC#7, port C2

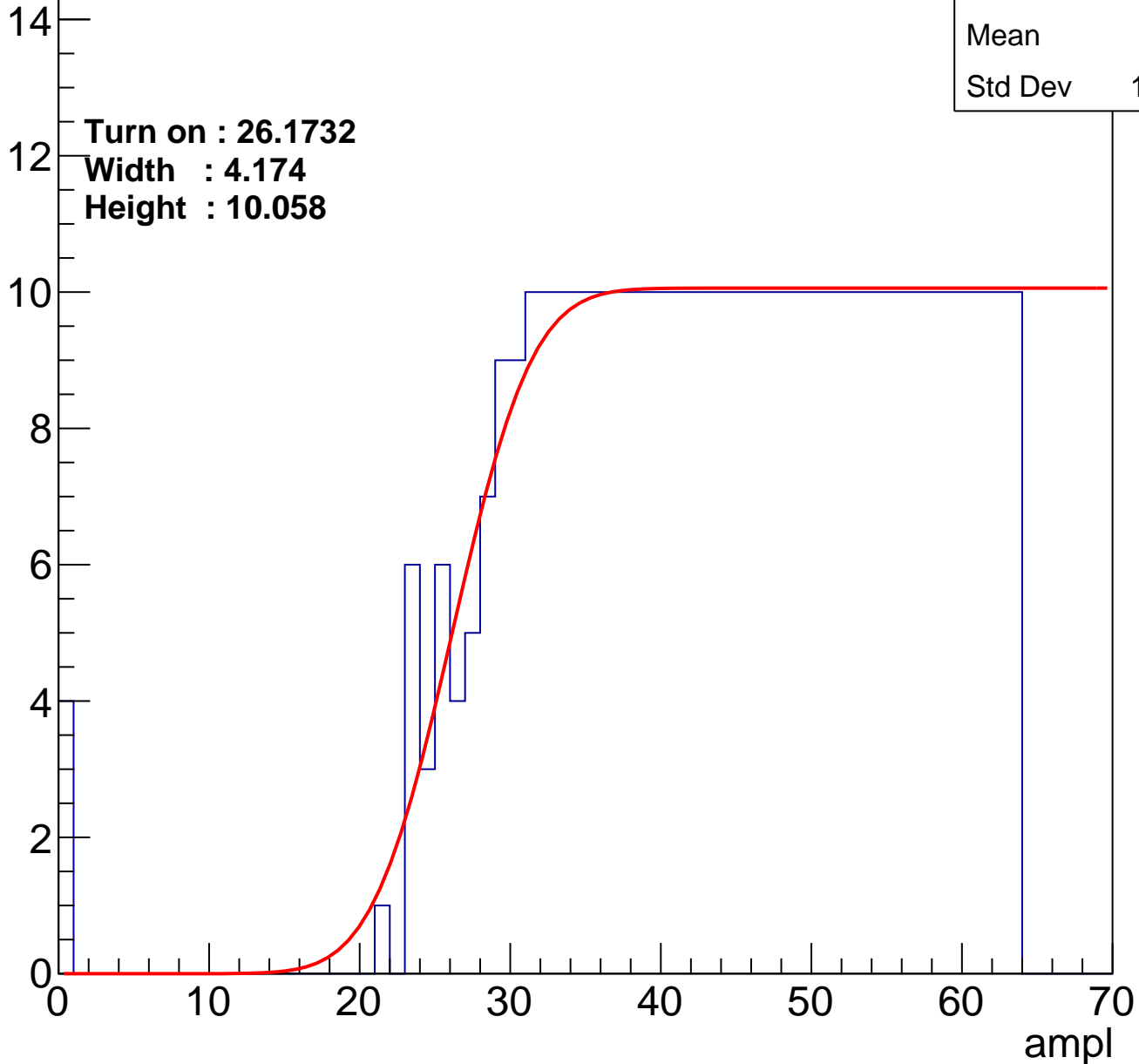
Entries	384
Mean	43.9
Std Dev	12.02

Turn on : 26.1732

Width : 4.174

Height : 10.058

Entry



B1L103S, U17-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.18
Std Dev	11.47

Turn on : 26.4141

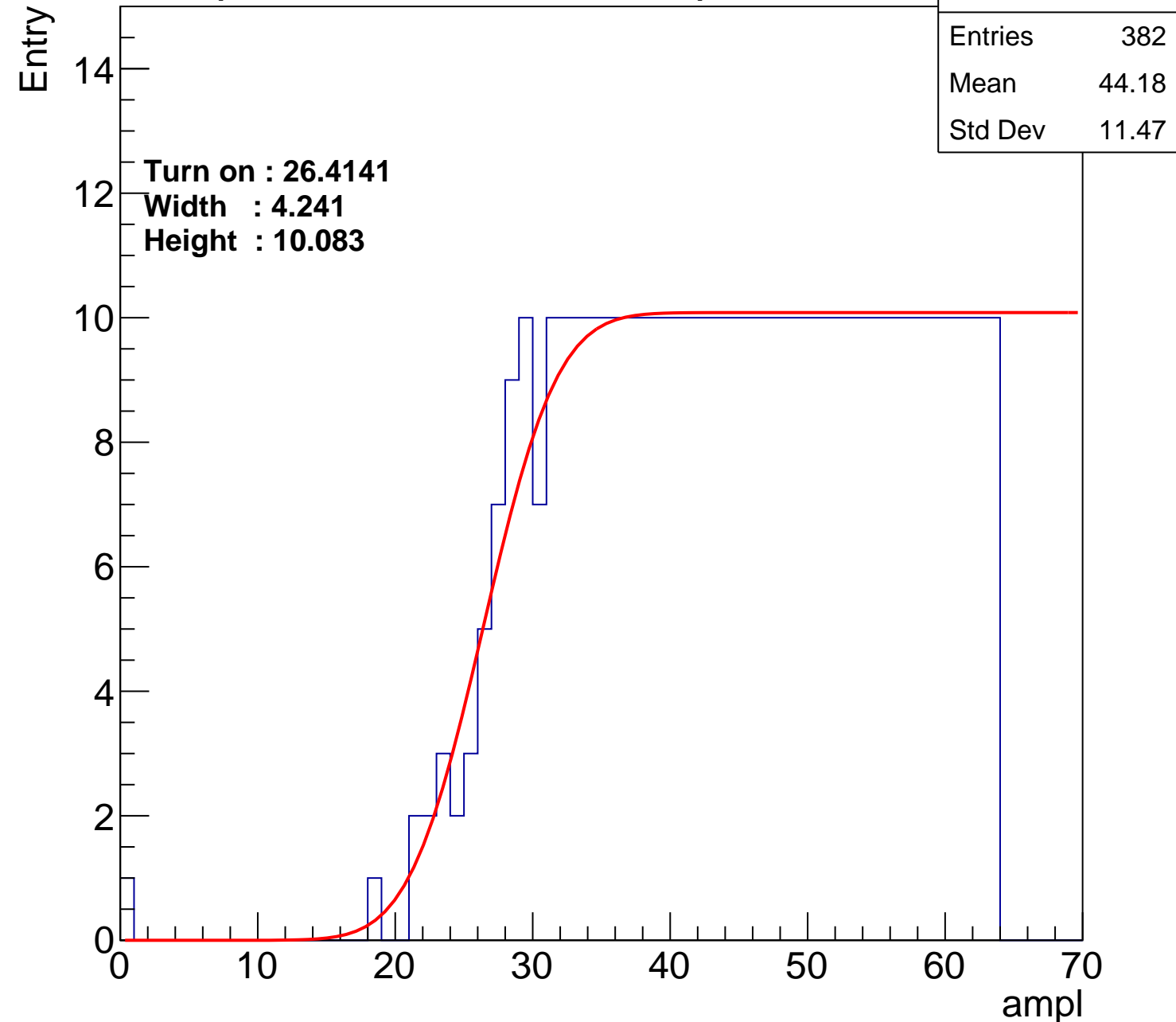
Width : 4.241

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	406
Mean	42.95
Std Dev	12.25

Turn on : 23.6923

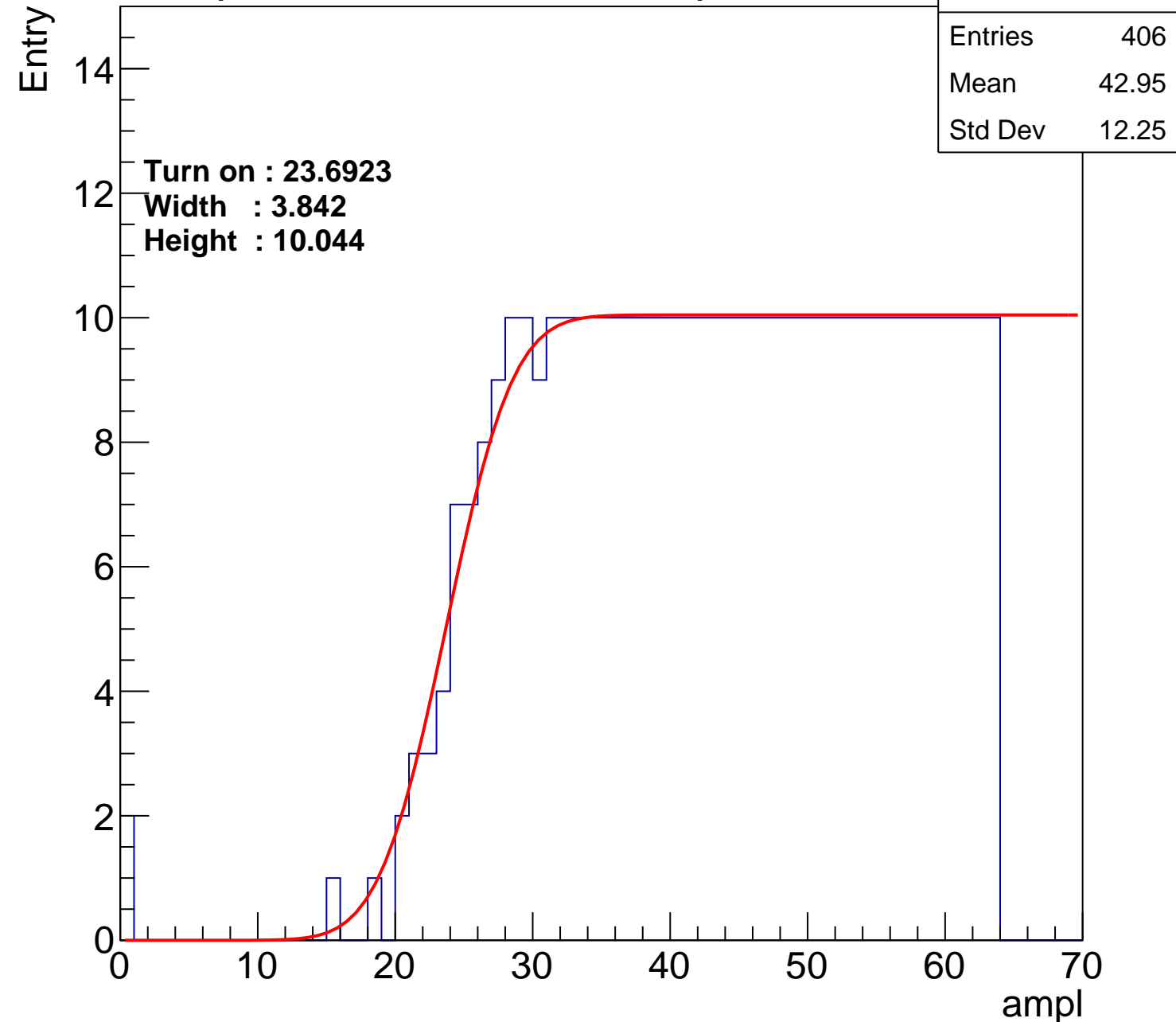
Width : 3.842

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch44

calib_packv5_042523_0143.root, FC#7, port C2

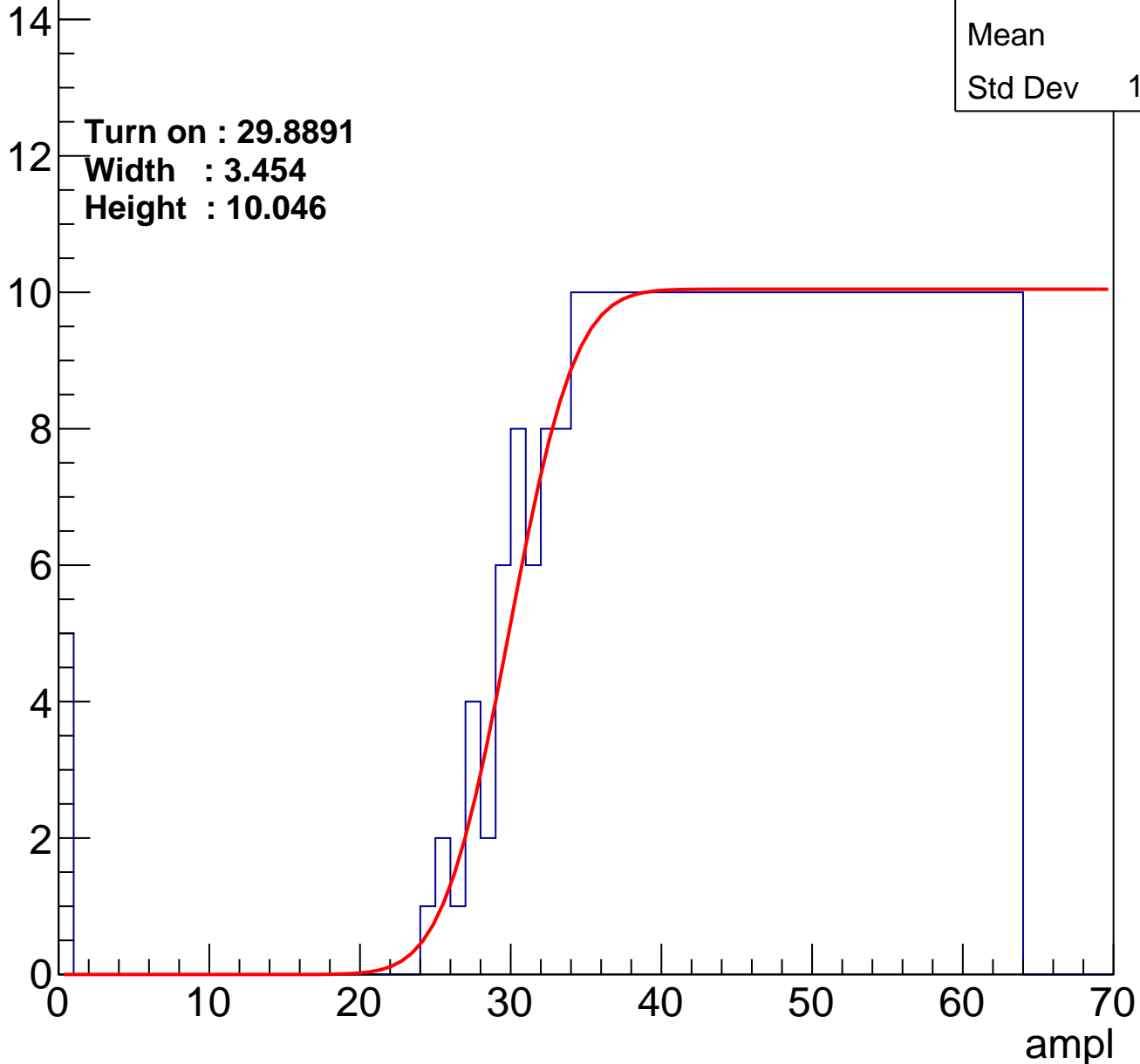
Entries	351
Mean	45.4
Std Dev	11.54

Turn on : 29.8891

Width : 3.454

Height : 10.046

Entry



B1L103S, U17-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.96
Std Dev	11.57

Turn on : 26.1669

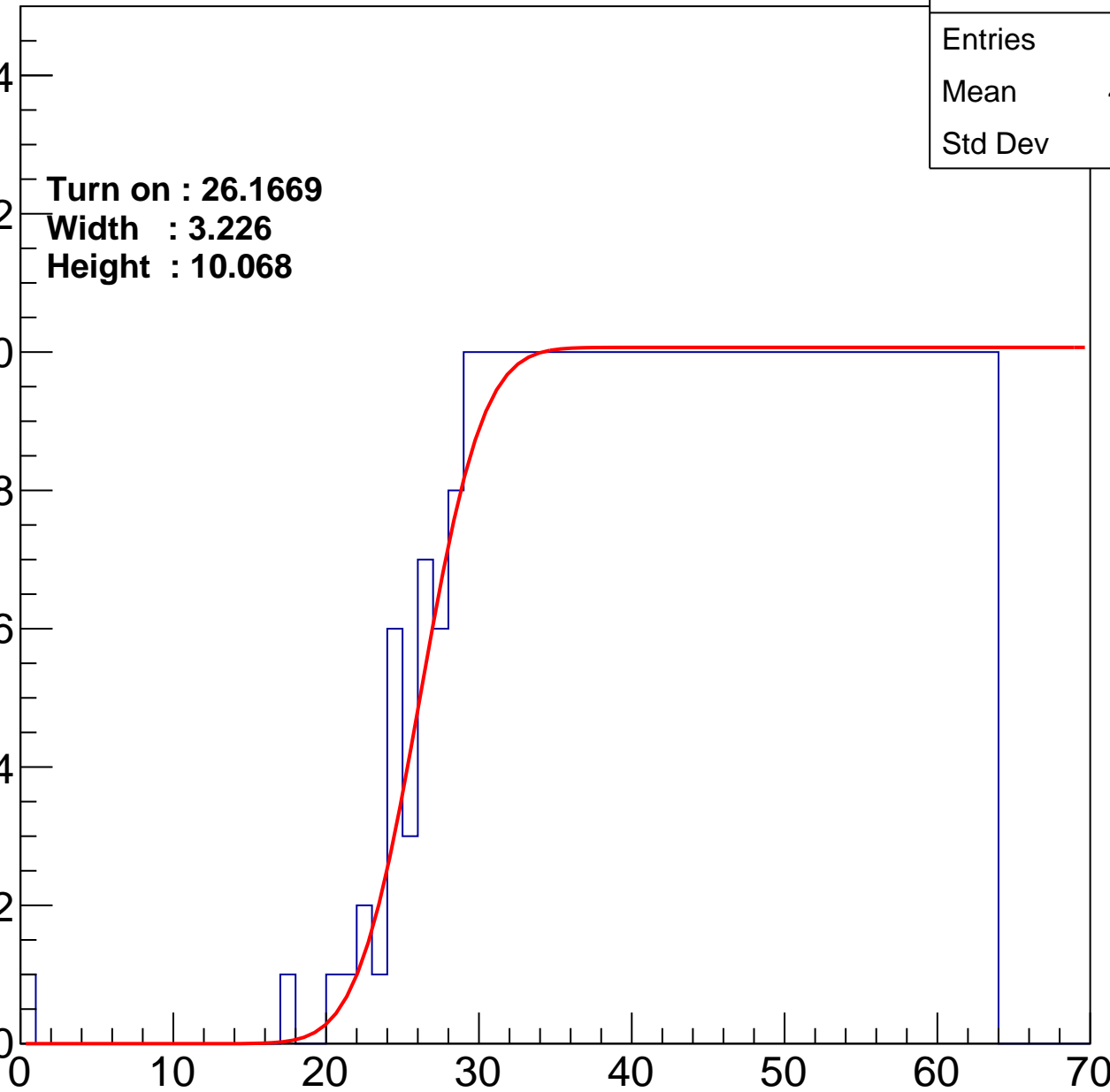
Width : 3.226

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch46

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.41
Std Dev	11.44

Turn on : 26.9245

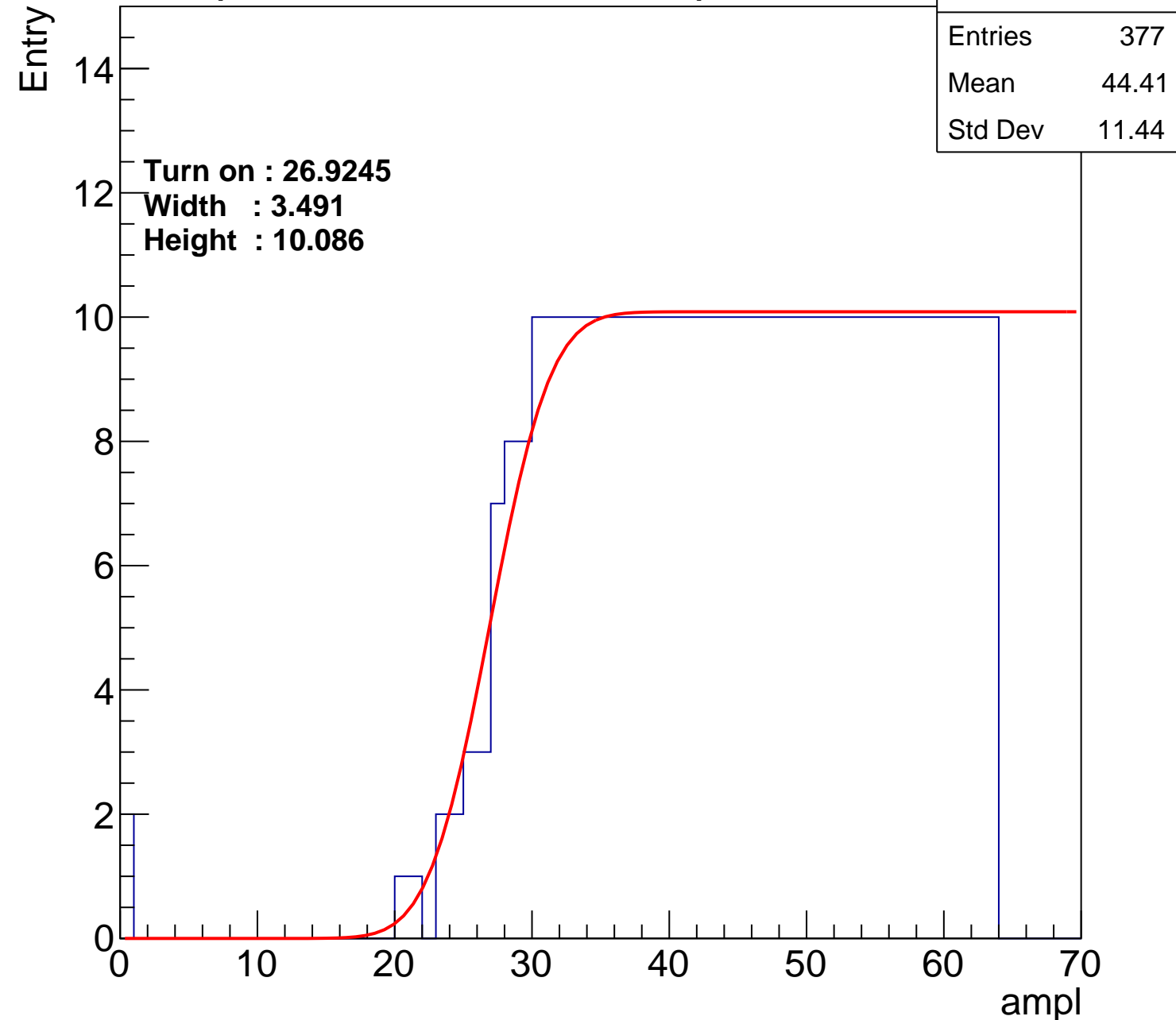
Width : 3.491

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45
Std Dev	11.28

Turn on : 28.5151

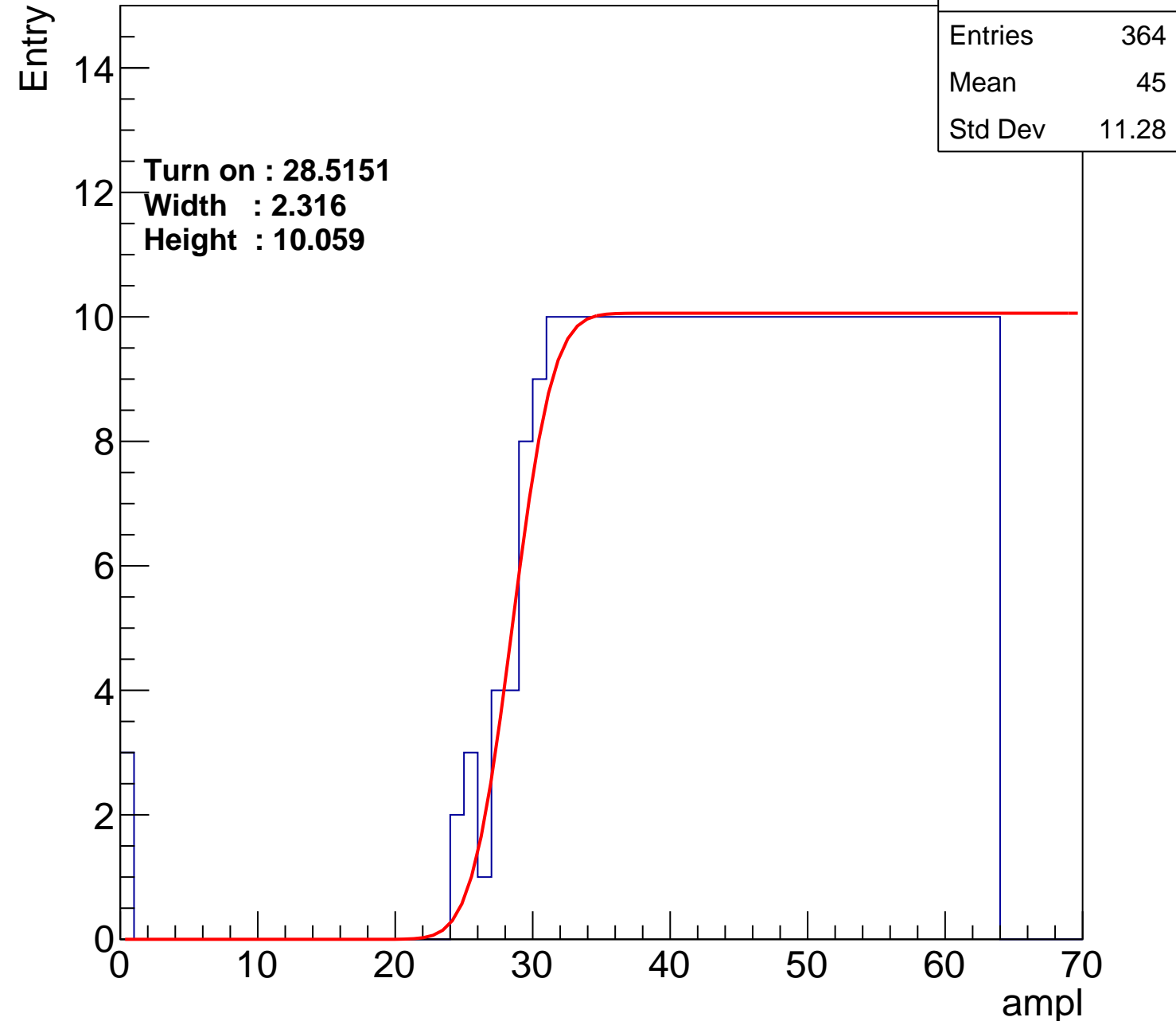
Width : 2.316

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.83
Std Dev	11.92

Turn on : 25.6624

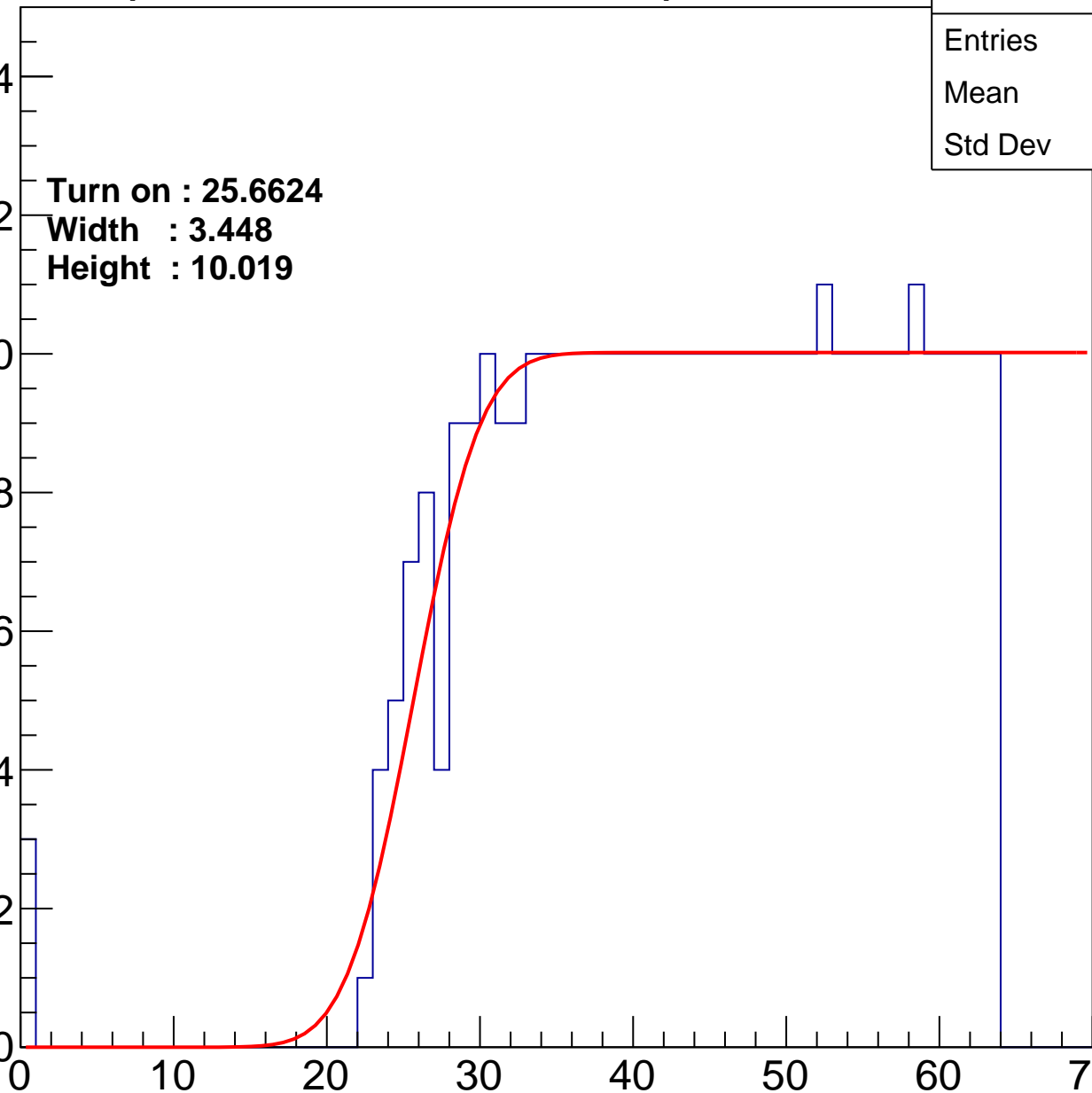
Width : 3.448

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch49

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	44.8
Std Dev	11.9

Turn on : 28.9264

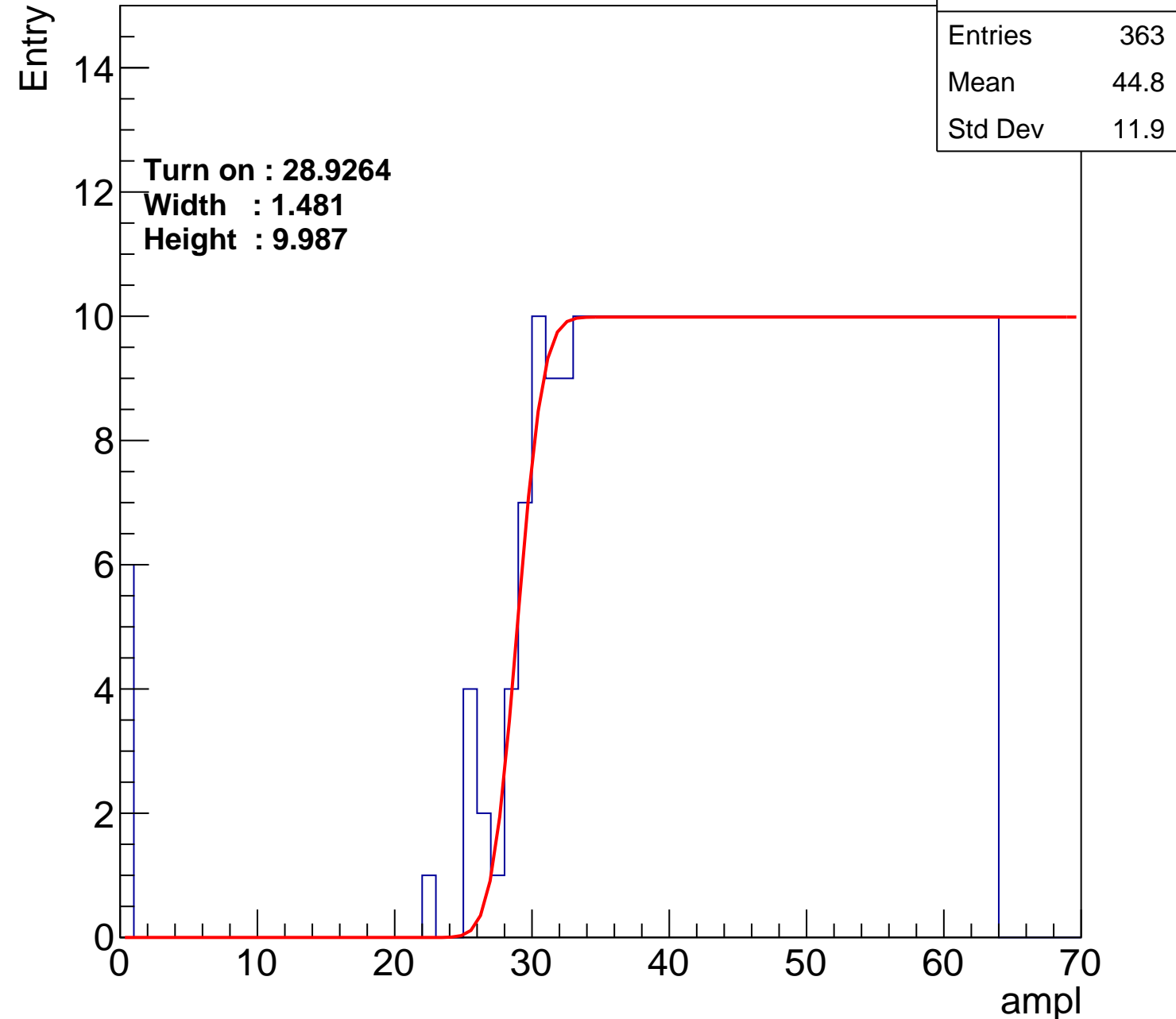
Width : 1.481

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch50

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.43
Std Dev	12.34

Turn on : 25.3607

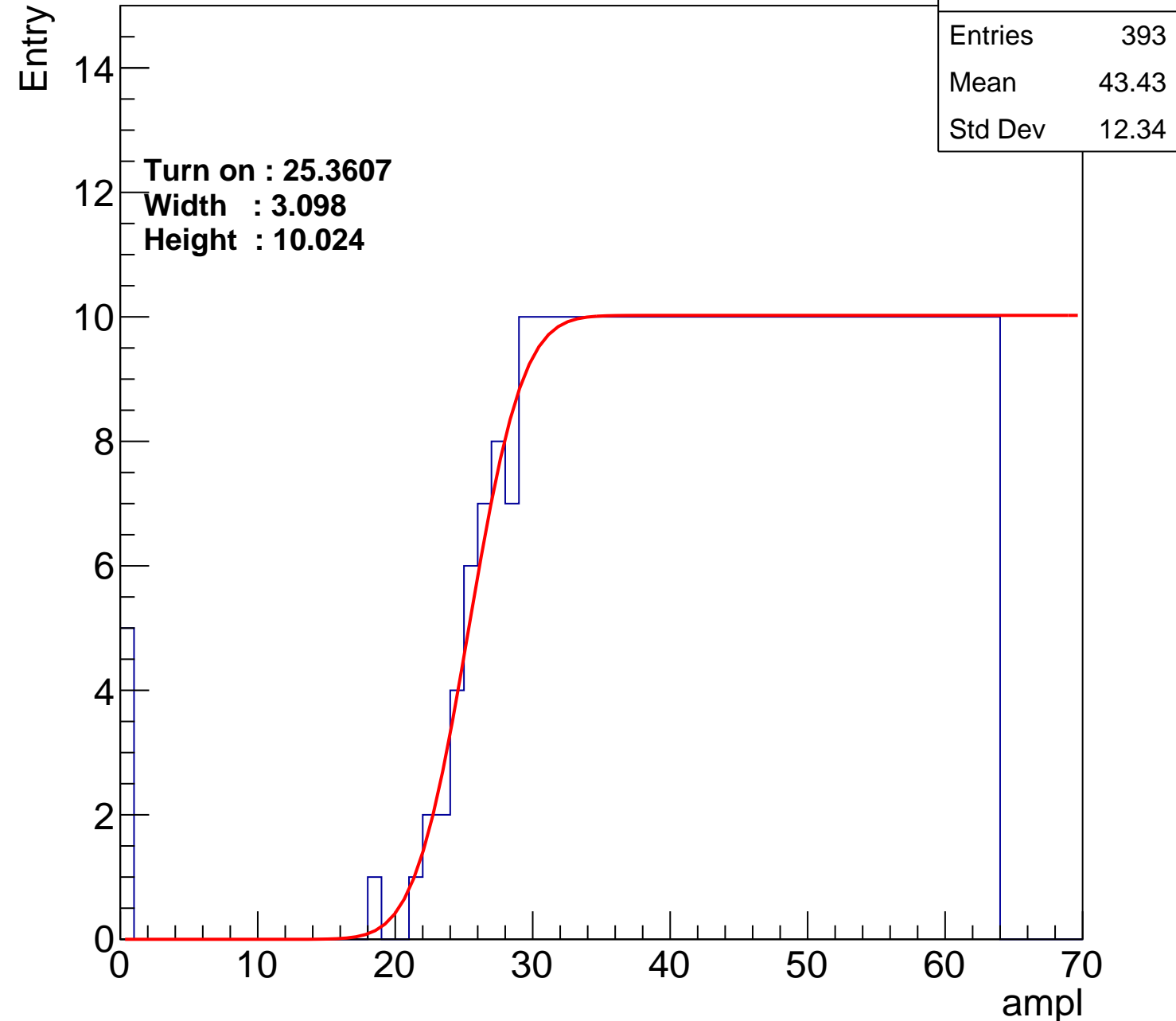
Width : 3.098

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.59
Std Dev	11.55

Turn on : 27.4089

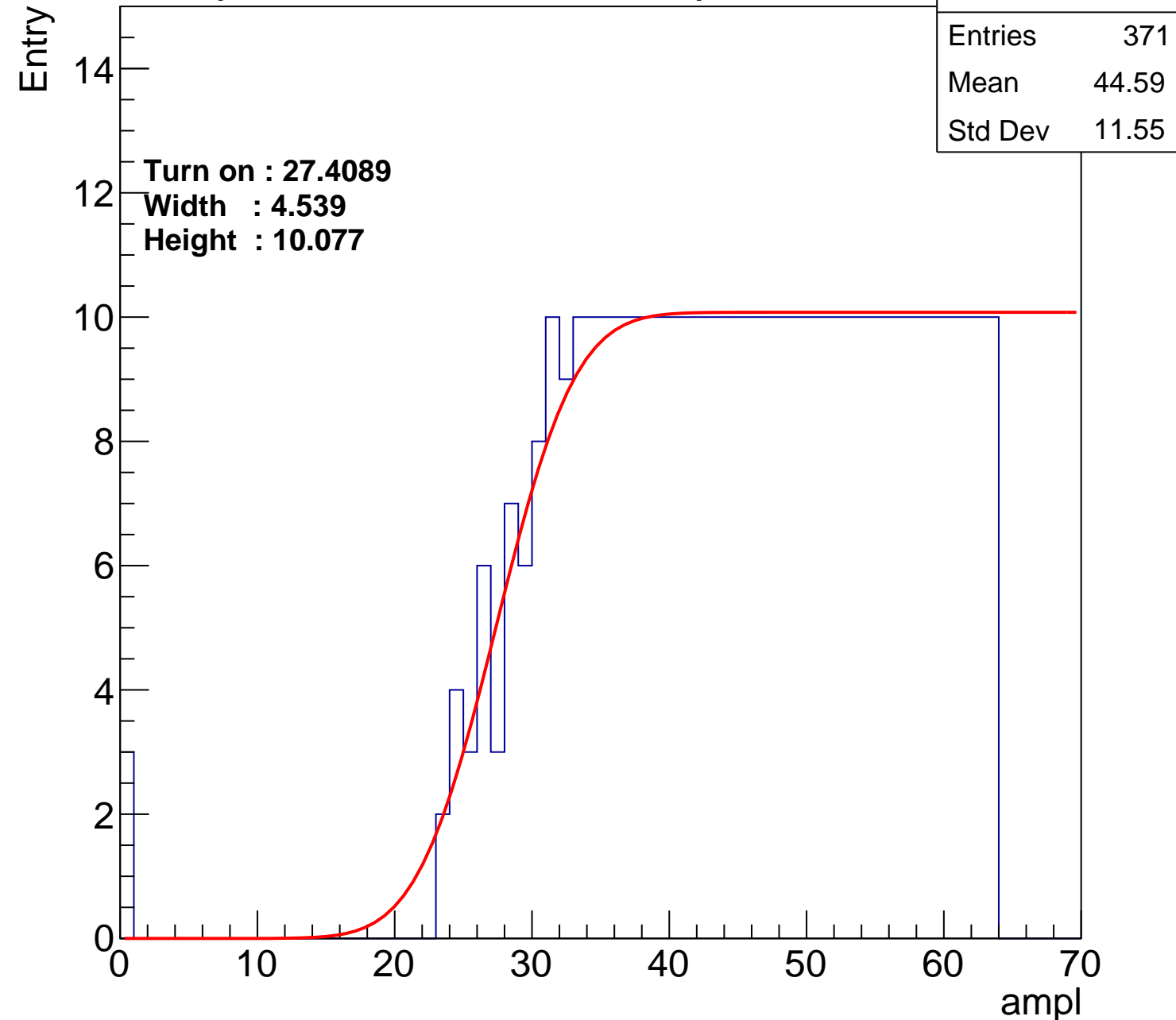
Width : 4.539

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.23
Std Dev	12.44

Turn on : 25.0247

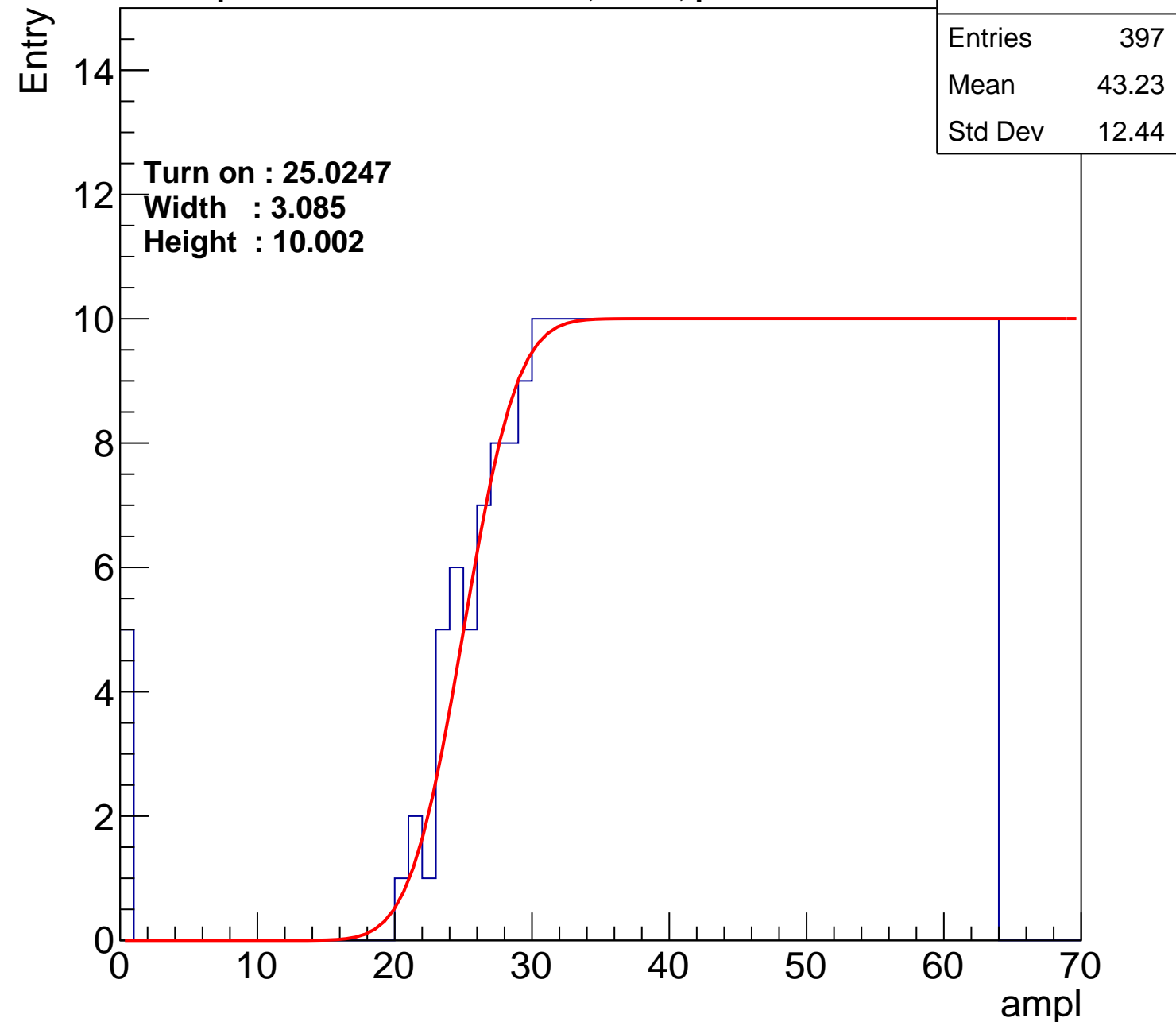
Width : 3.085

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.24
Std Dev	12.28

Turn on : 27.7258

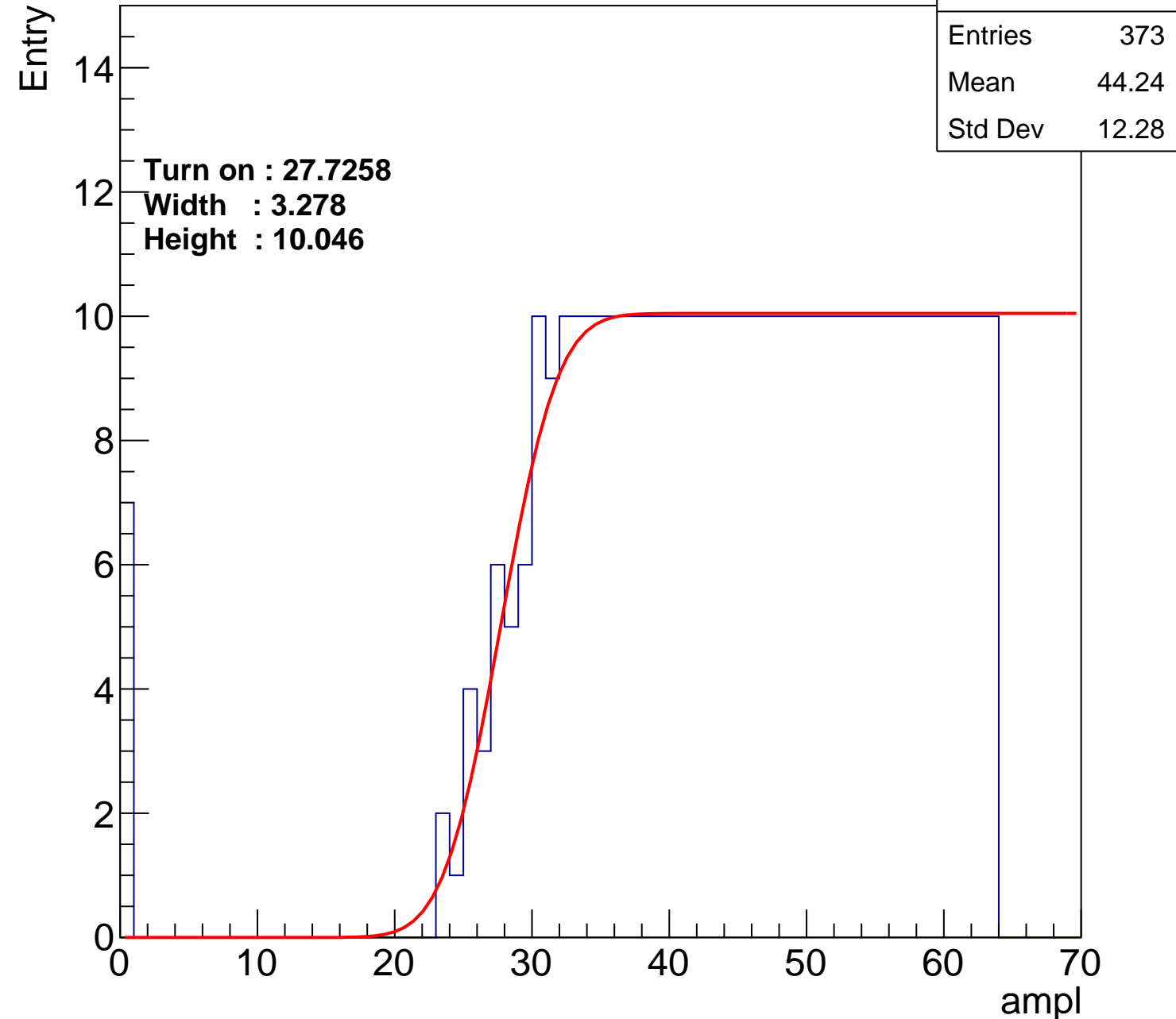
Width : 3.278

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 26.1595

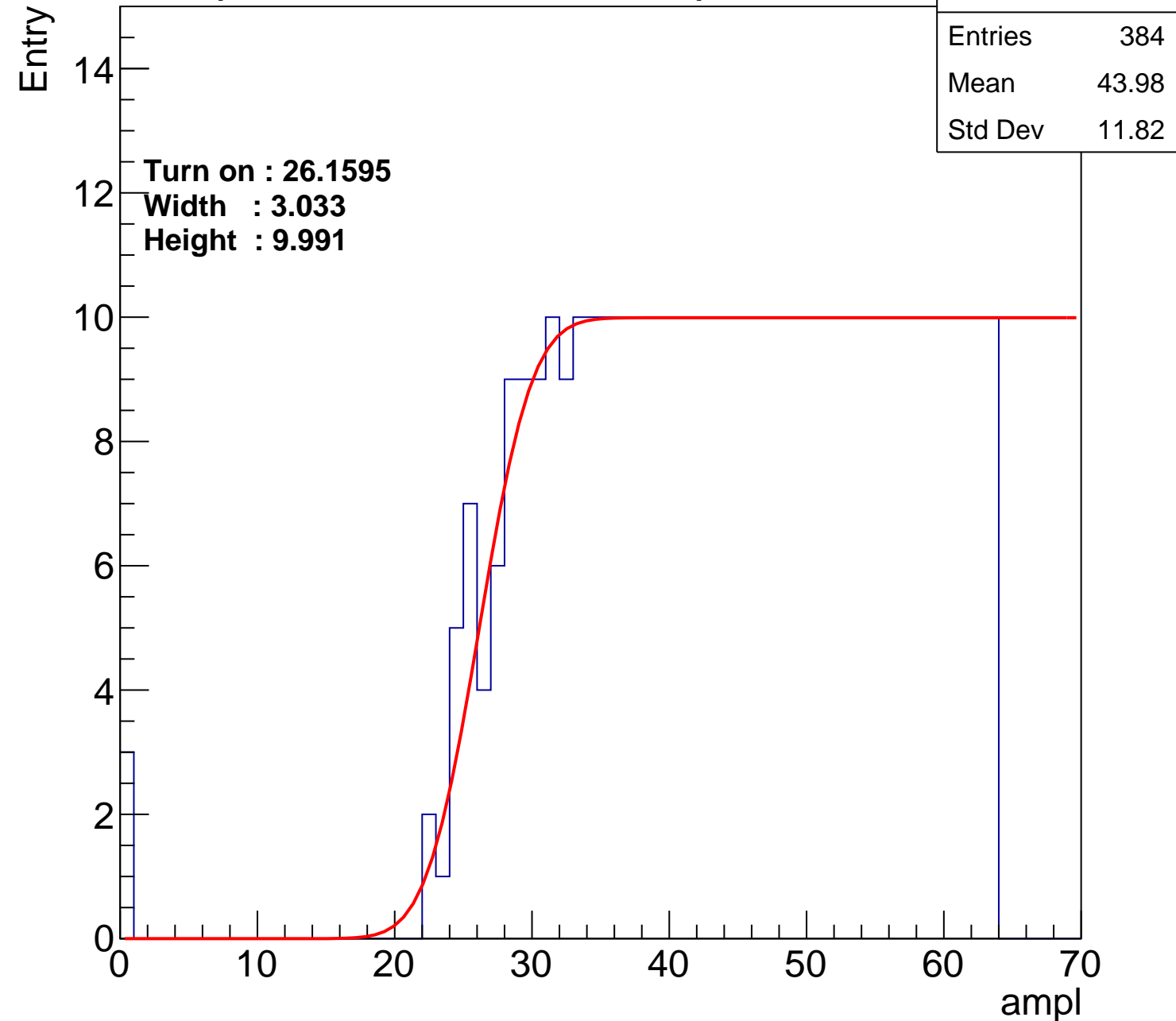
Width : 3.033

Height : 9.991

Entry

14
12
10
8
6
4
2
0

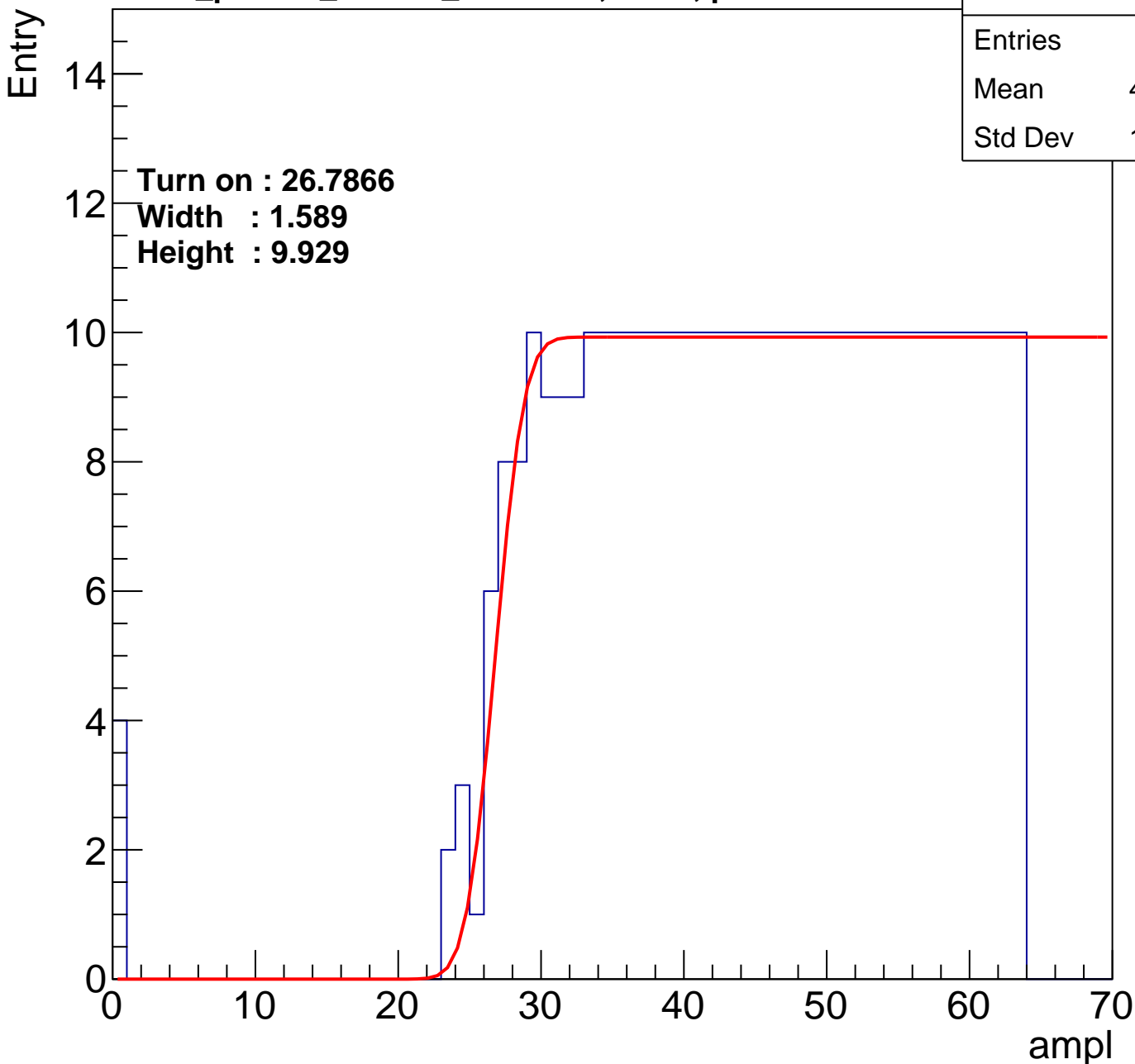
ampl



calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Turn on : 26.7866
Width : 1.589
Height : 9.929



B1L103S, U17-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.12
Std Dev	11.45

Turn on : 25.9760

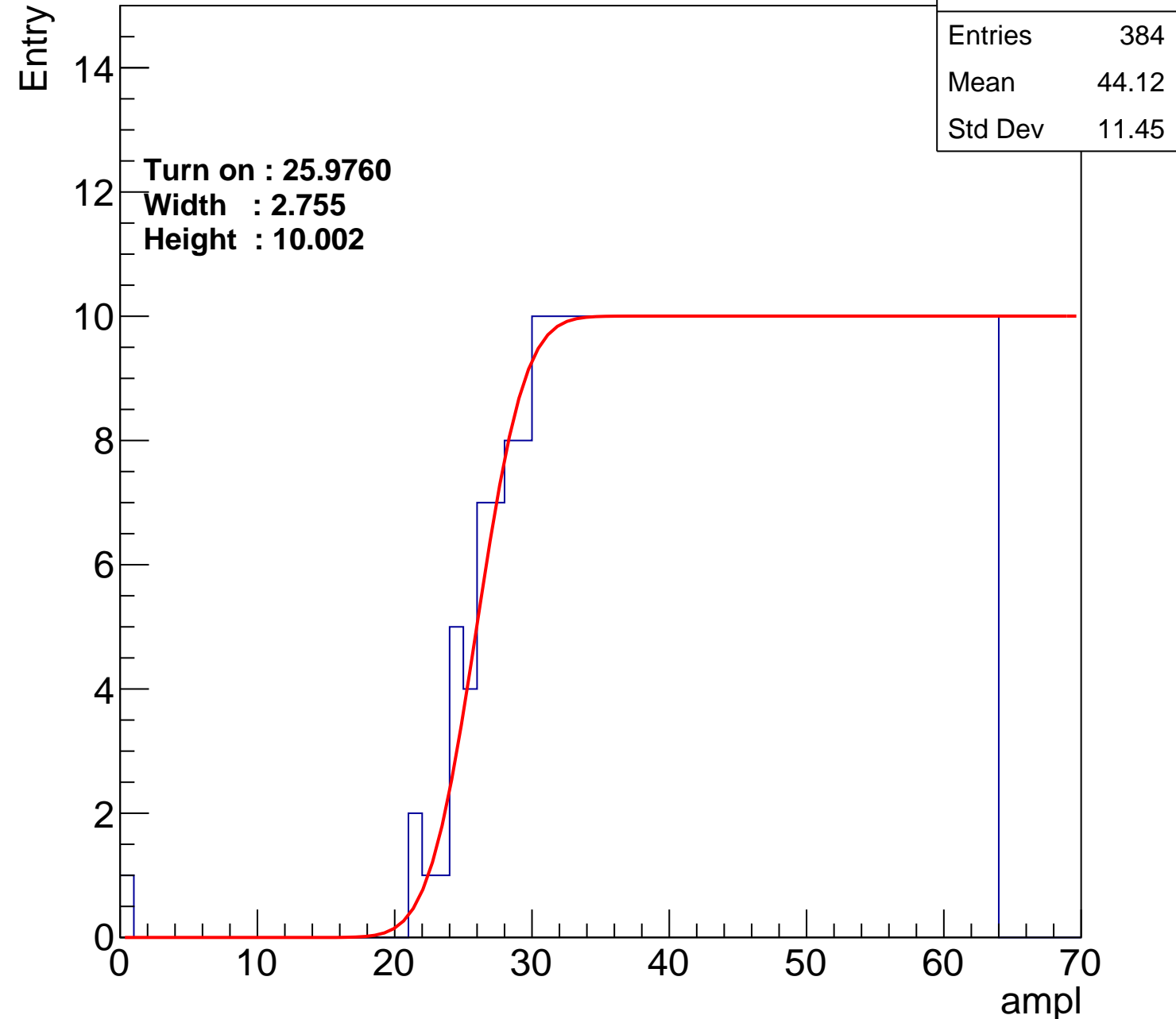
Width : 2.755

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#7, port C2

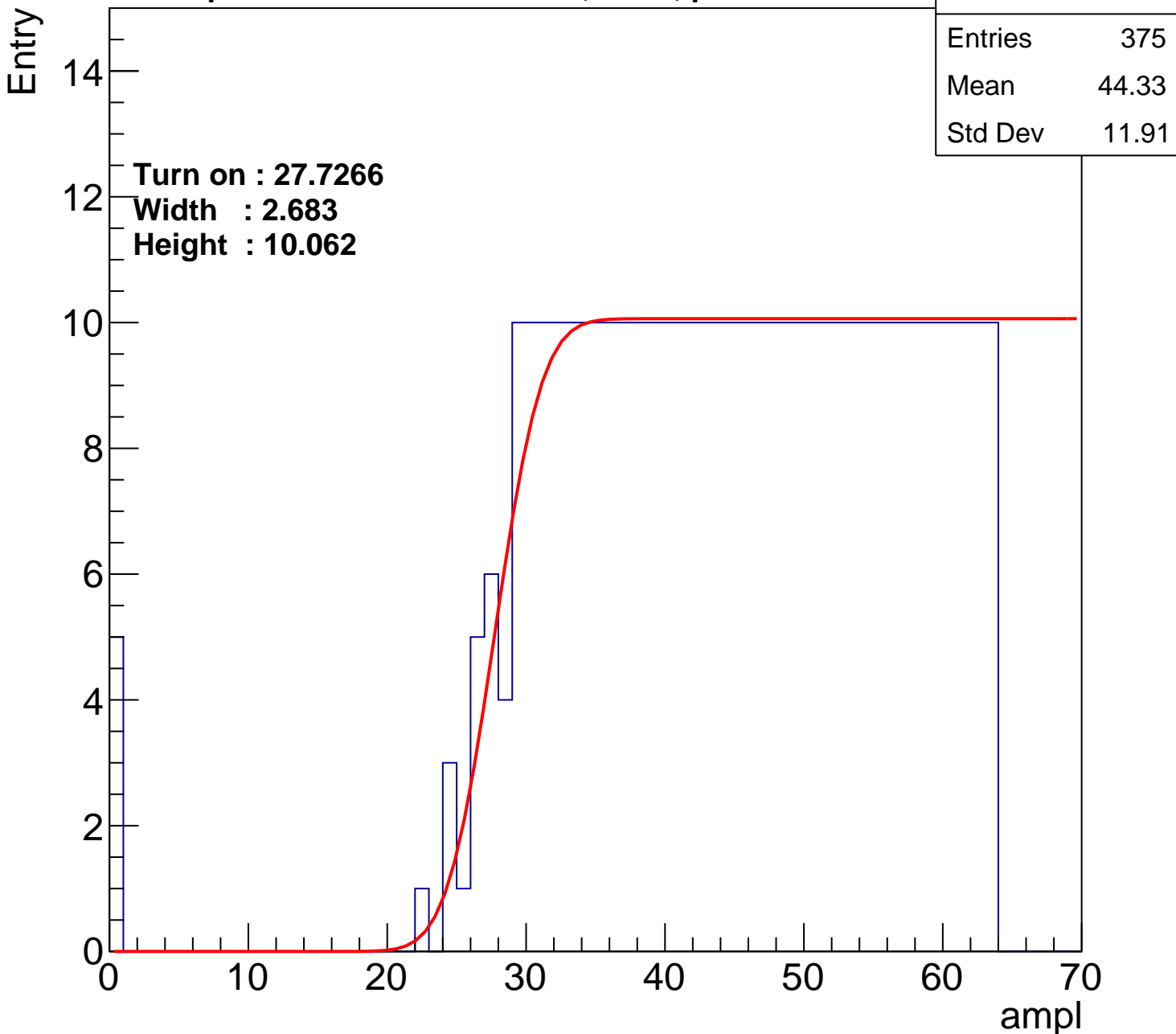
Entries	375
Mean	44.33
Std Dev	11.91

Mean	44.33
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Std Dev	11.91
---------	-------

Width : 2.683

Height : 10.062



B1L103S, U17-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.47
Std Dev	11.62

Turn on : 27.5184

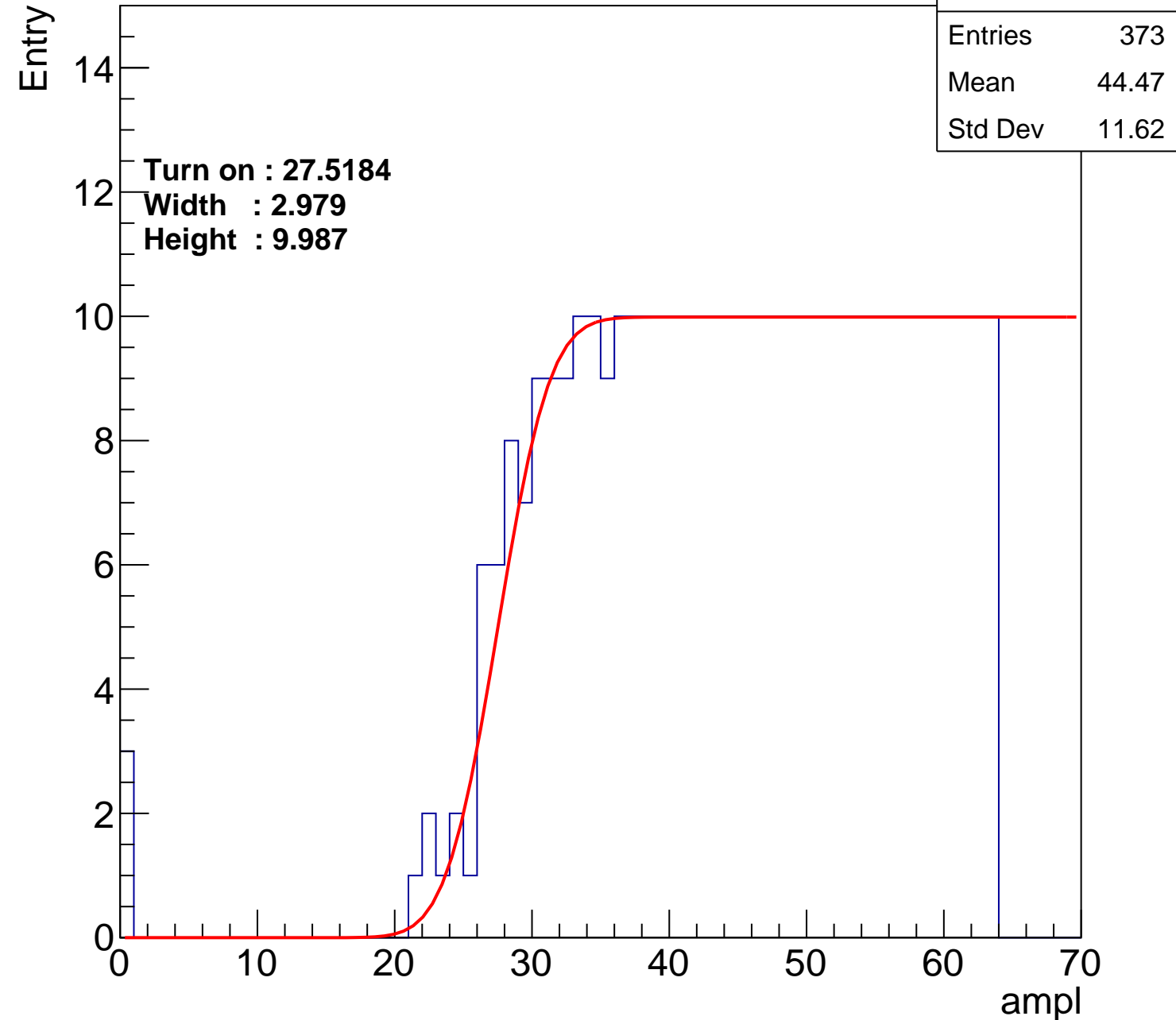
Width : 2.979

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	349
Mean	45.57
Std Dev	11.28

Turn on : 29.5331

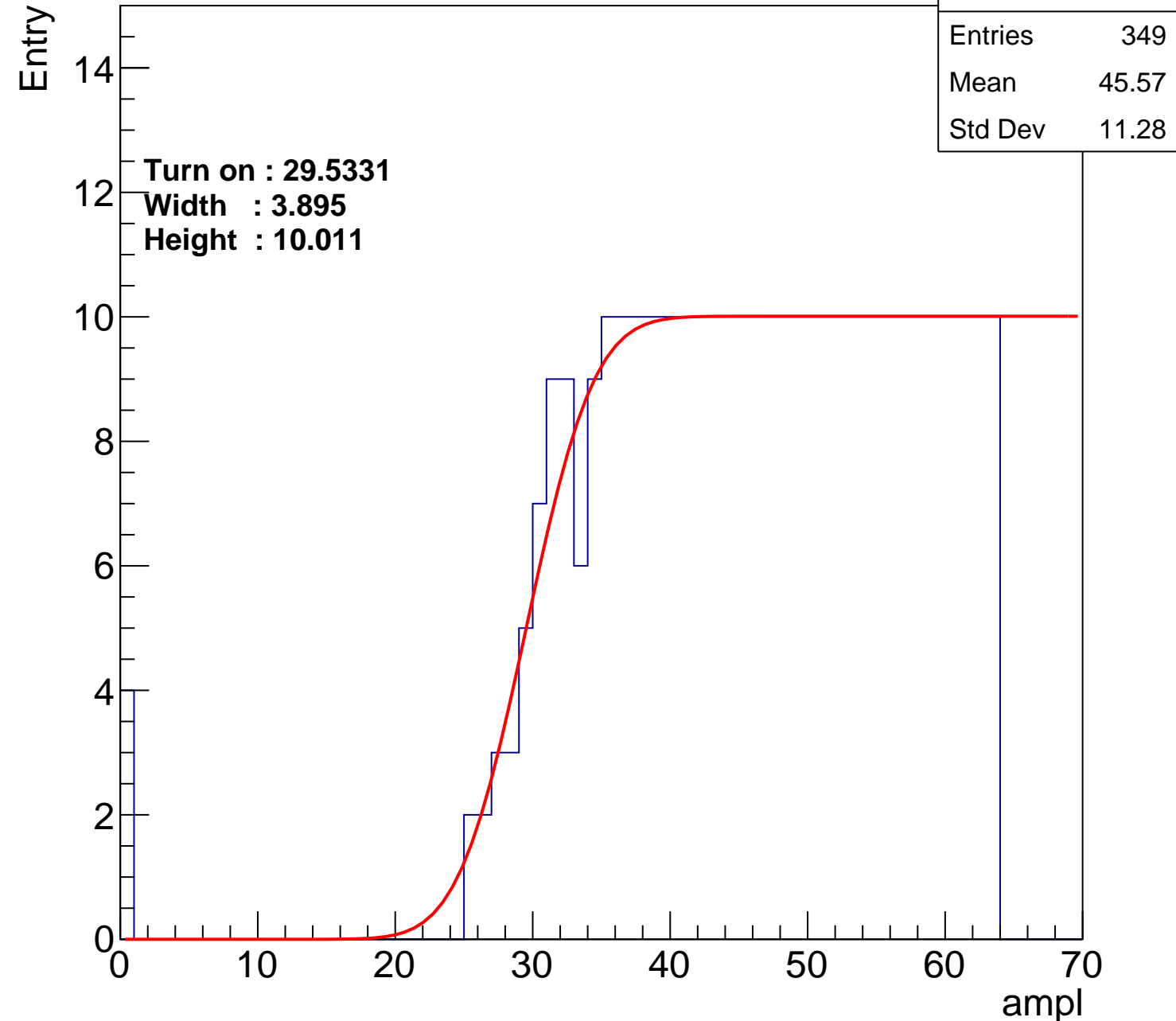
Width : 3.895

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch60

calib_packv5_042523_0143.root, FC#7, port C2

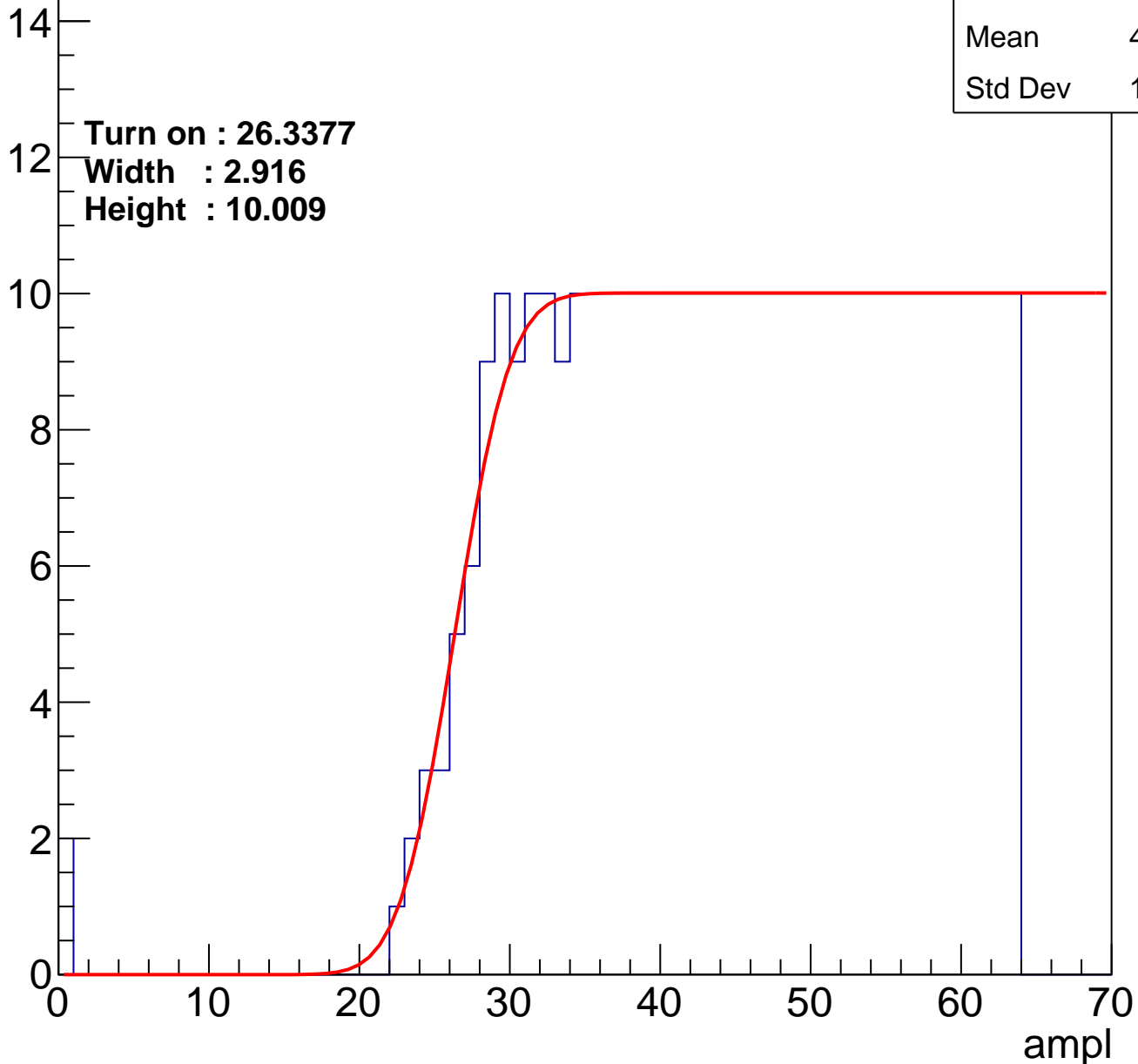
Entries	379
Mean	44.32
Std Dev	11.48

Turn on : 26.3377

Width : 2.916

Height : 10.009

Entry



B1L103S, U17-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.13
Std Dev	12.17

Turn on : 26.9683

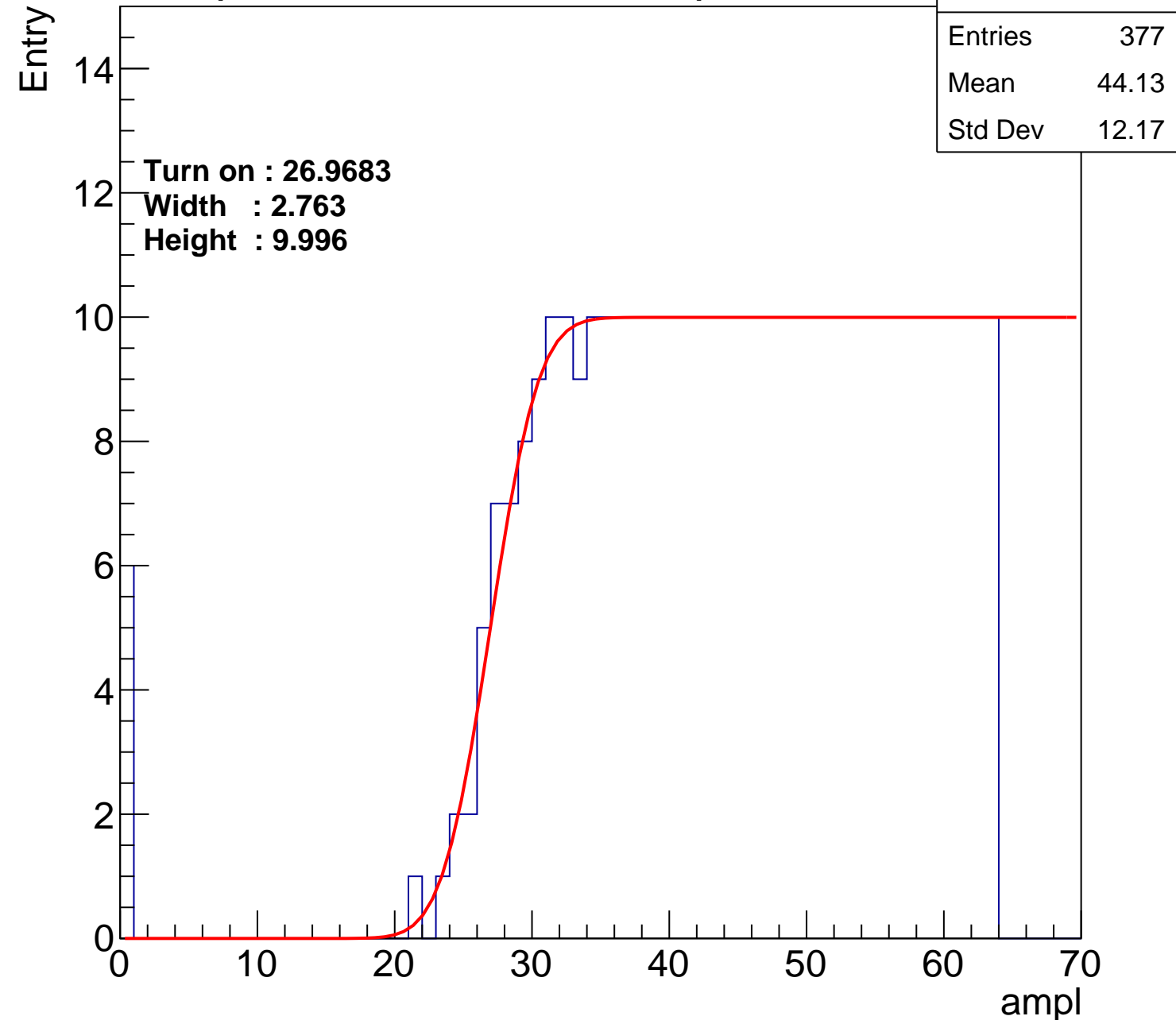
Width : 2.763

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch62

calib_packv5_042523_0143.root, FC#7, port C2

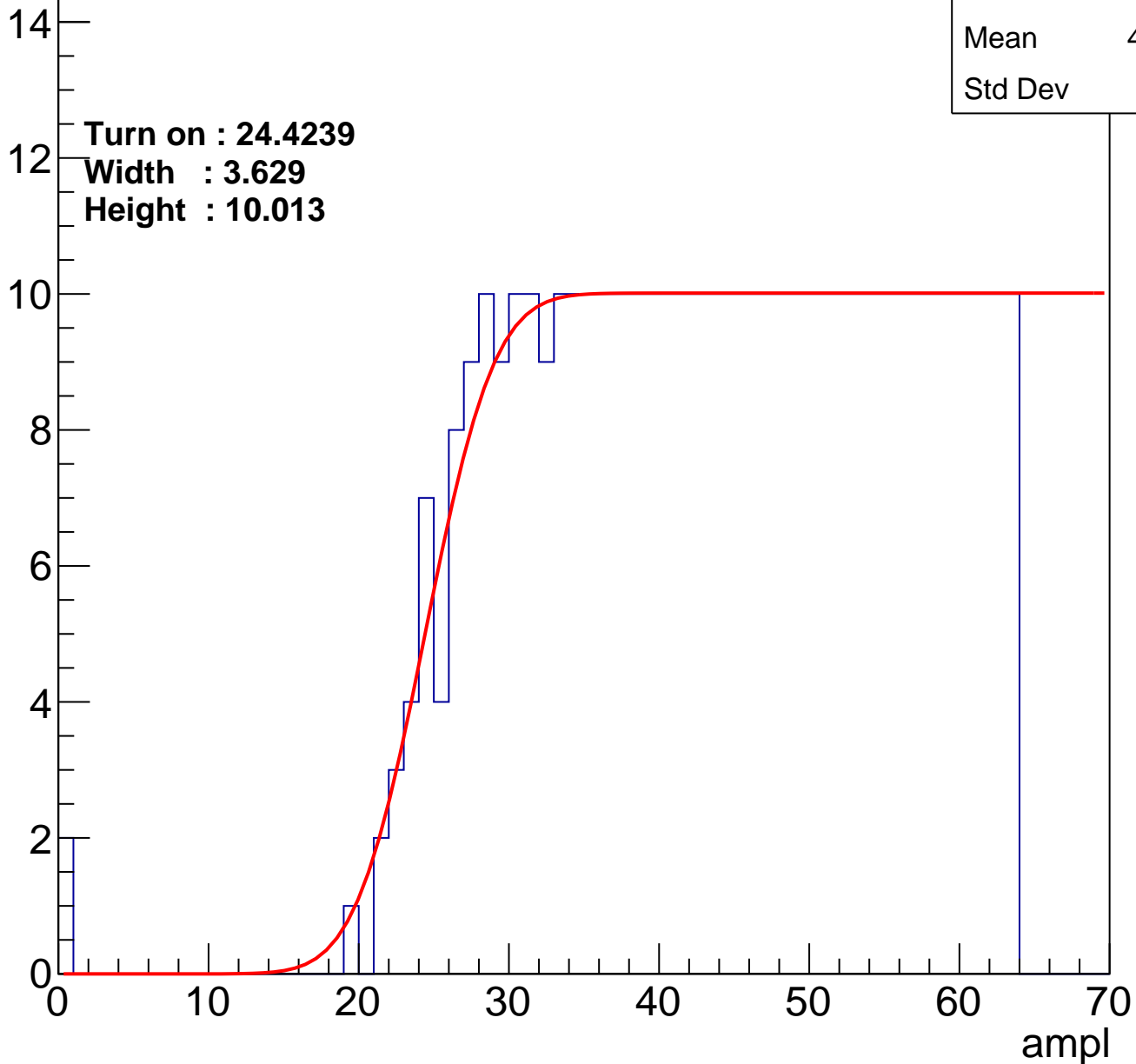
Entries	398
Mean	43.36
Std Dev	12

Turn on : 24.4239

Width : 3.629

Height : 10.013

Entry



B1L103S, U17-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	363
Mean	44.76
Std Dev	11.95

Turn on : 28.3008

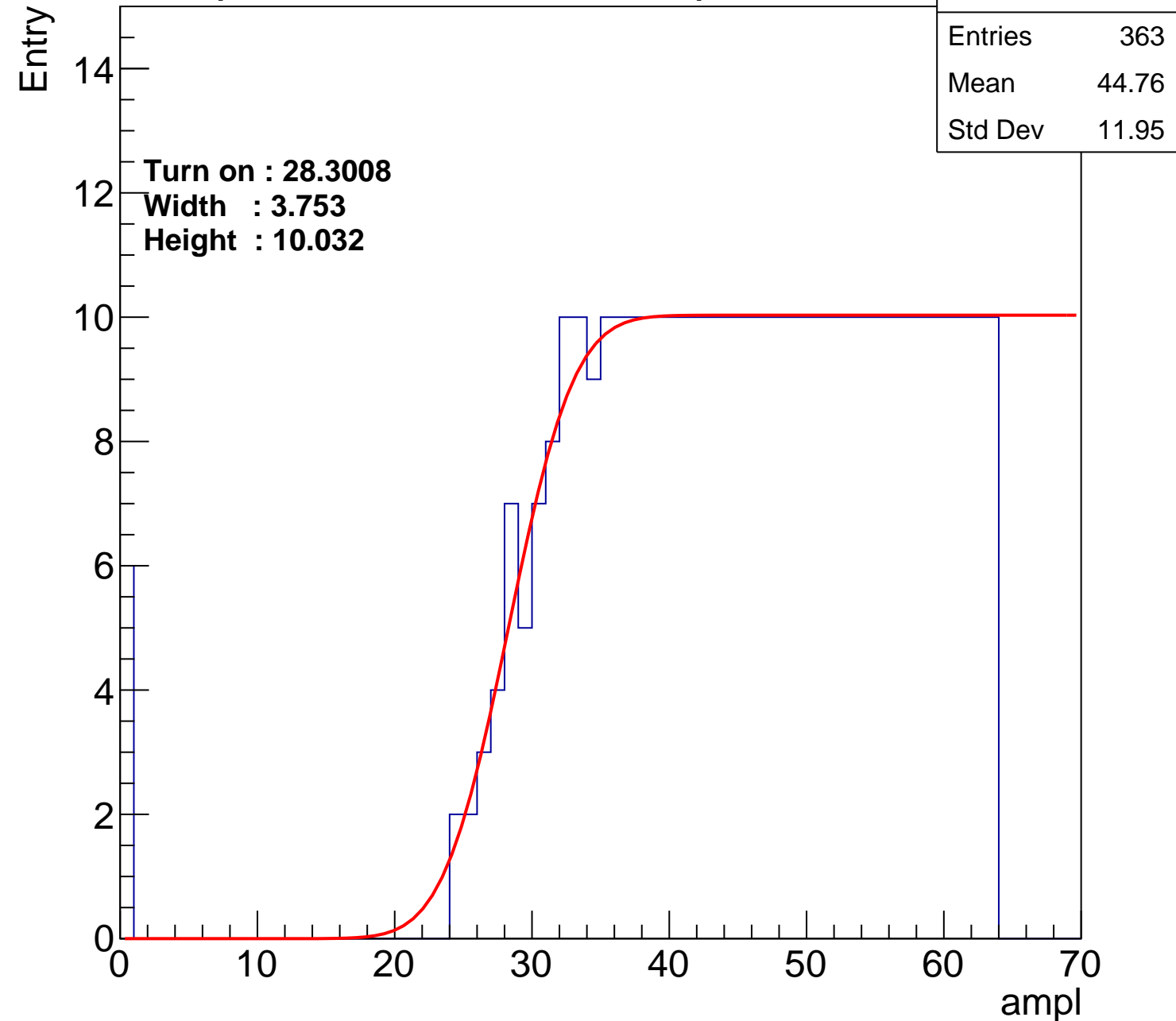
Width : 3.753

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.65
Std Dev	11.34

Turn on : 26.8047

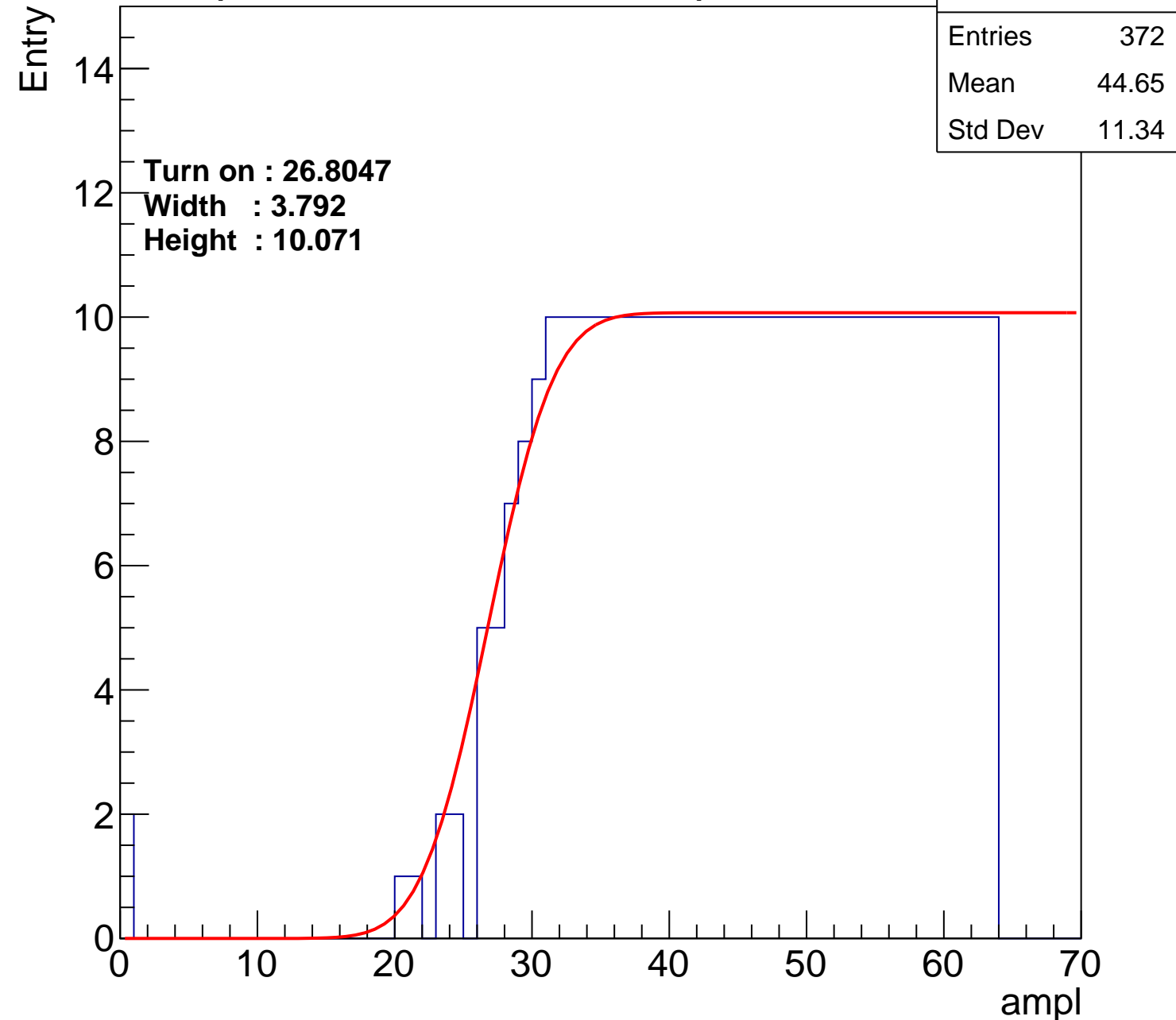
Width : 3.792

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.39
Std Dev	11.78

Turn on : 26.5275

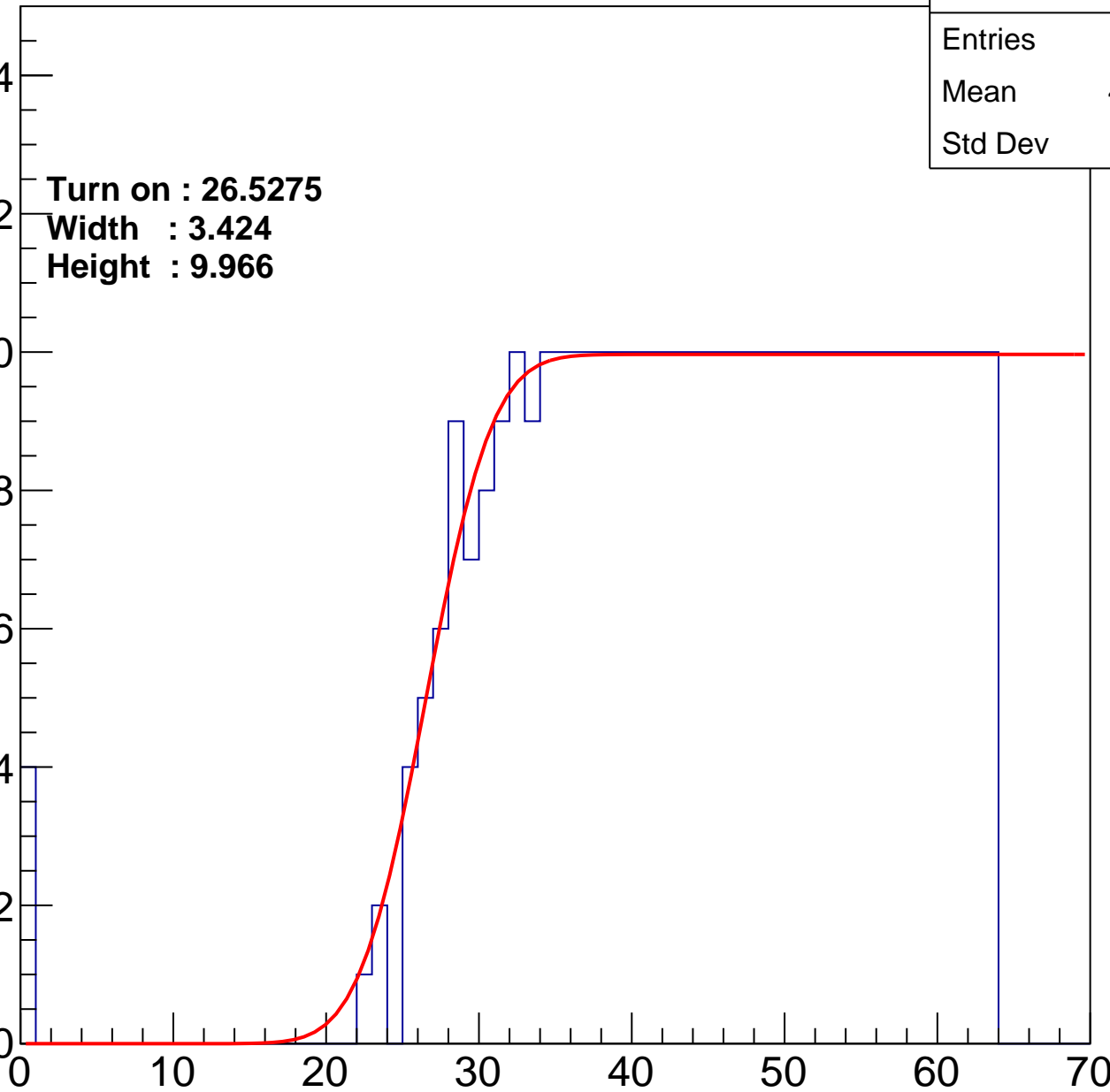
Width : 3.424

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch66

calib_packv5_042523_0143.root, FC#7, port C2

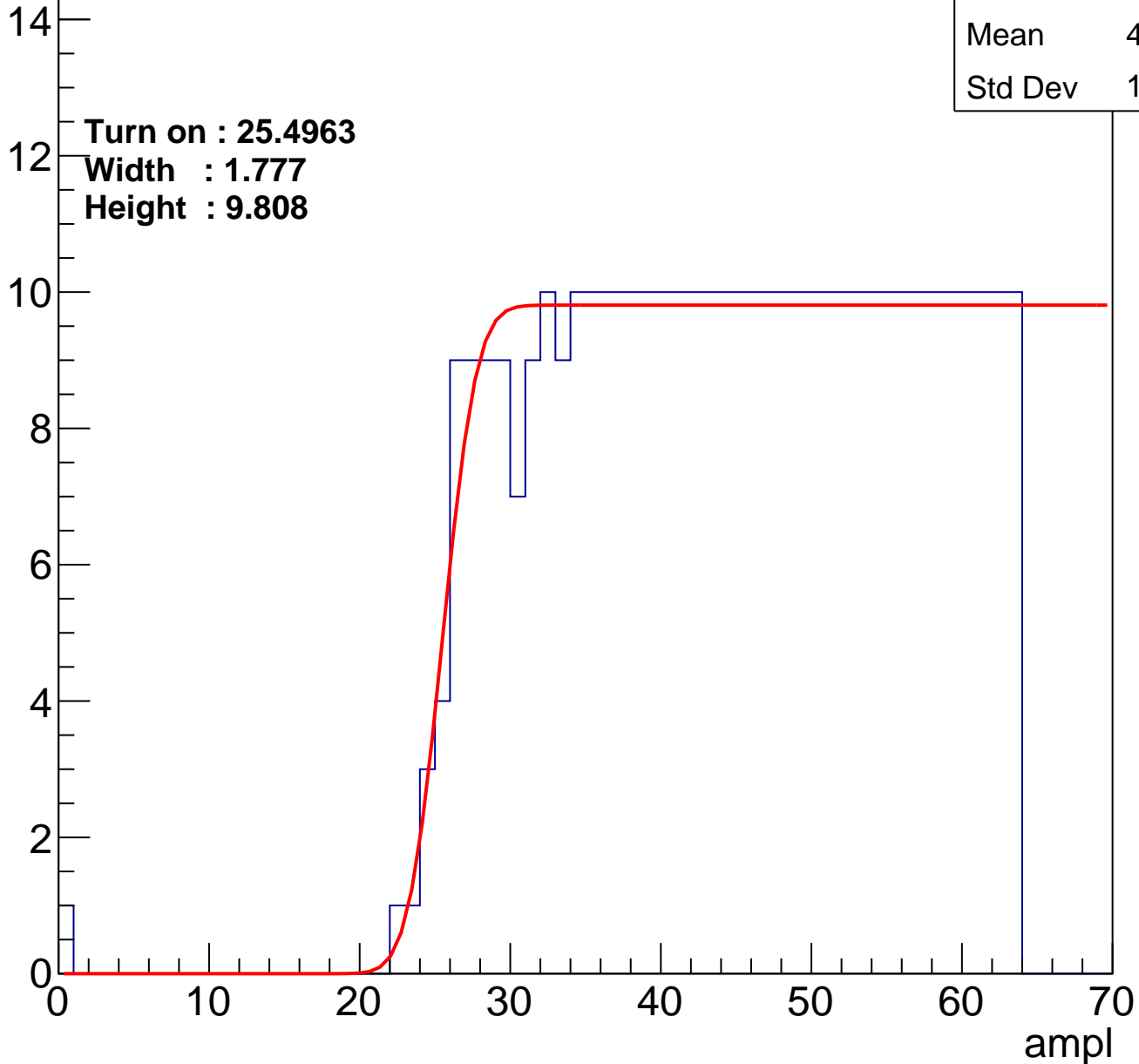
Entries	381
Mean	44.26
Std Dev	11.37

Turn on : 25.4963

Width : 1.777

Height : 9.808

Entry



B1L103S, U17-ch67

calib_packv5_042523_0143.root, FC#7, port C2

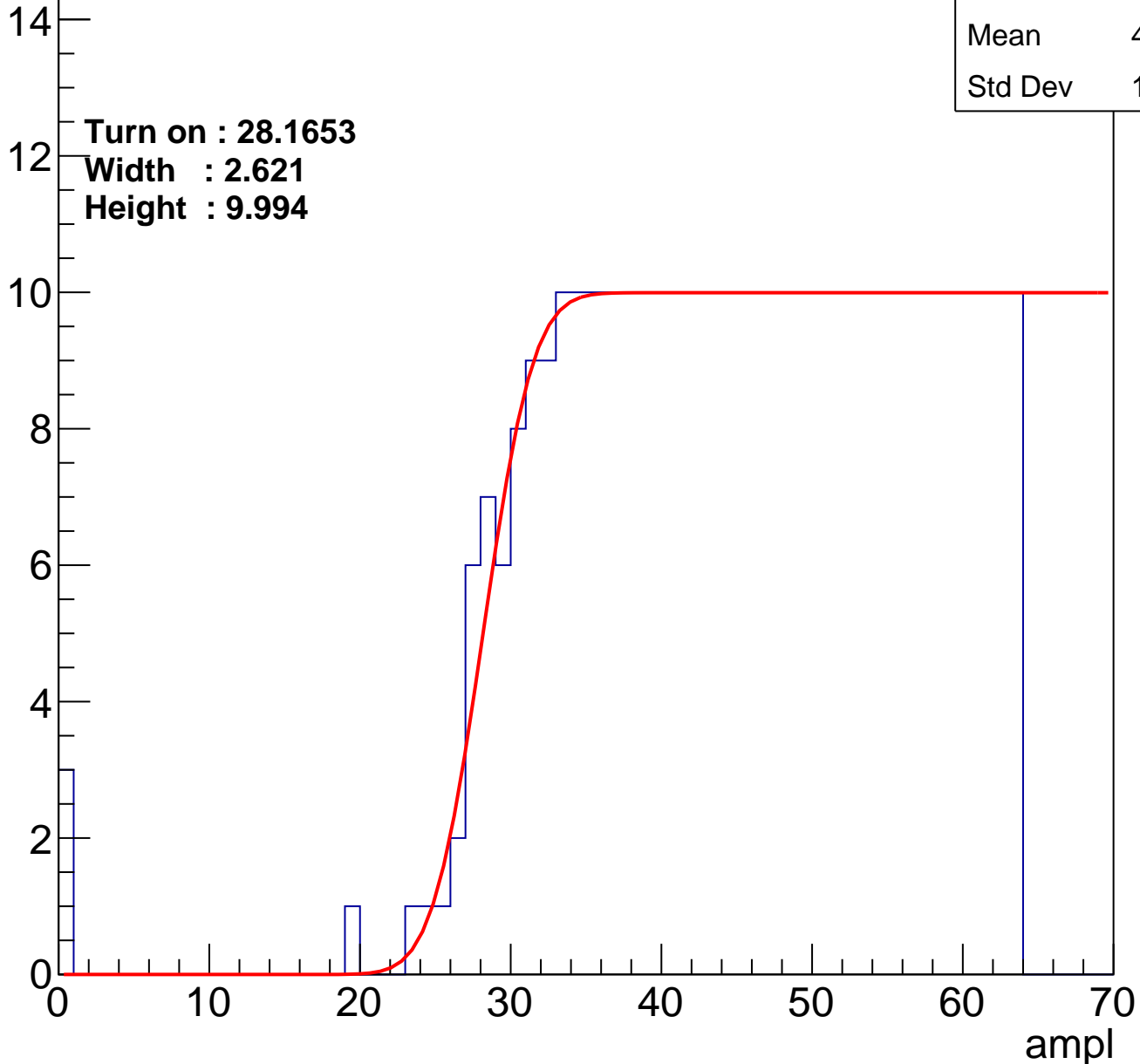
Entries	364
Mean	44.95
Std Dev	11.36

Turn on : 28.1653

Width : 2.621

Height : 9.994

Entry



B1L103S, U17-ch68

calib_packv5_042523_0143.root, FC#7, port C2

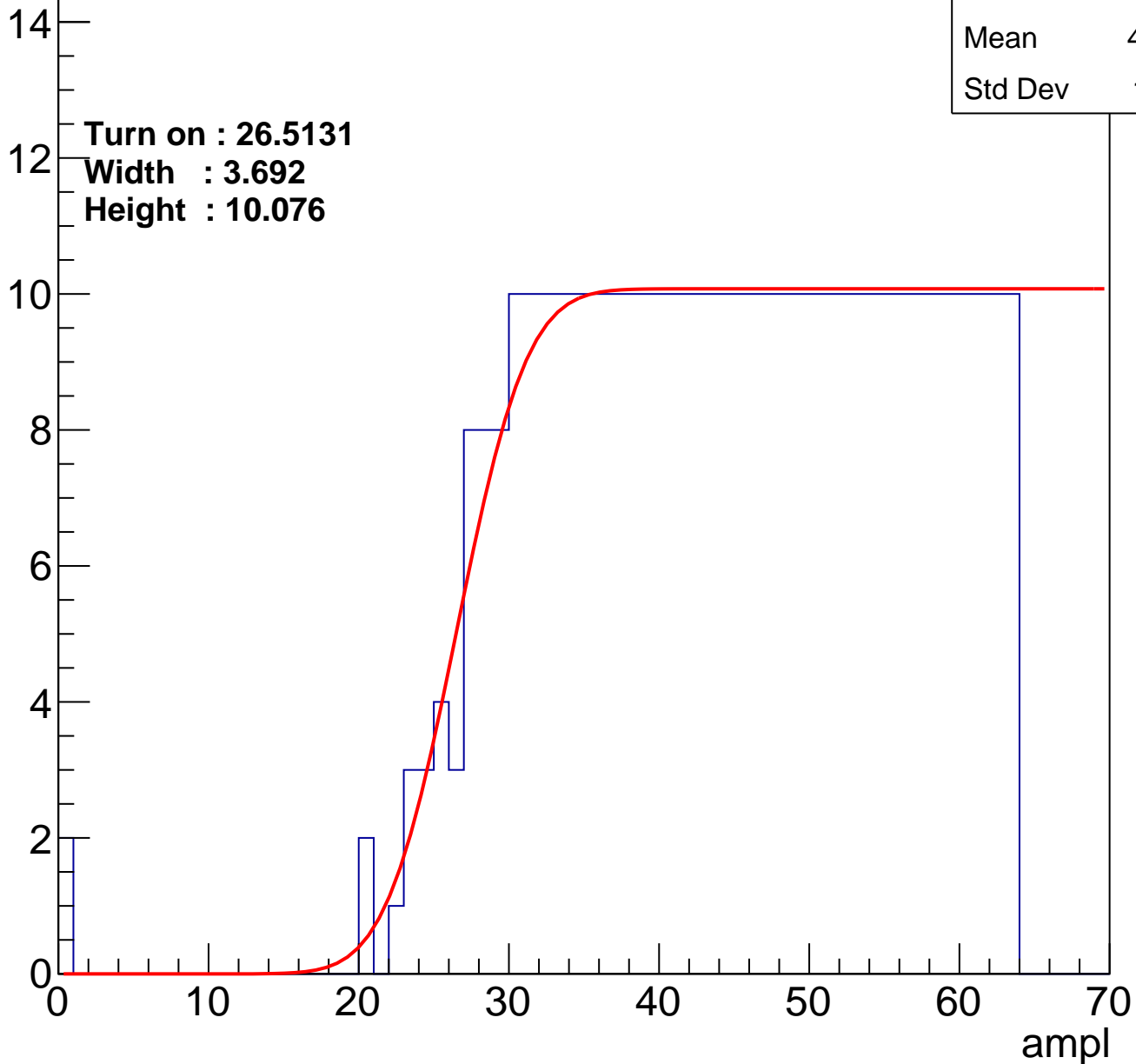
Entries	382
Mean	44.14
Std Dev	11.61

Turn on : 26.5131

Width : 3.692

Height : 10.076

Entry



B1L103S, U17-ch69

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.13
Std Dev	10.9

Turn on : 28.5276

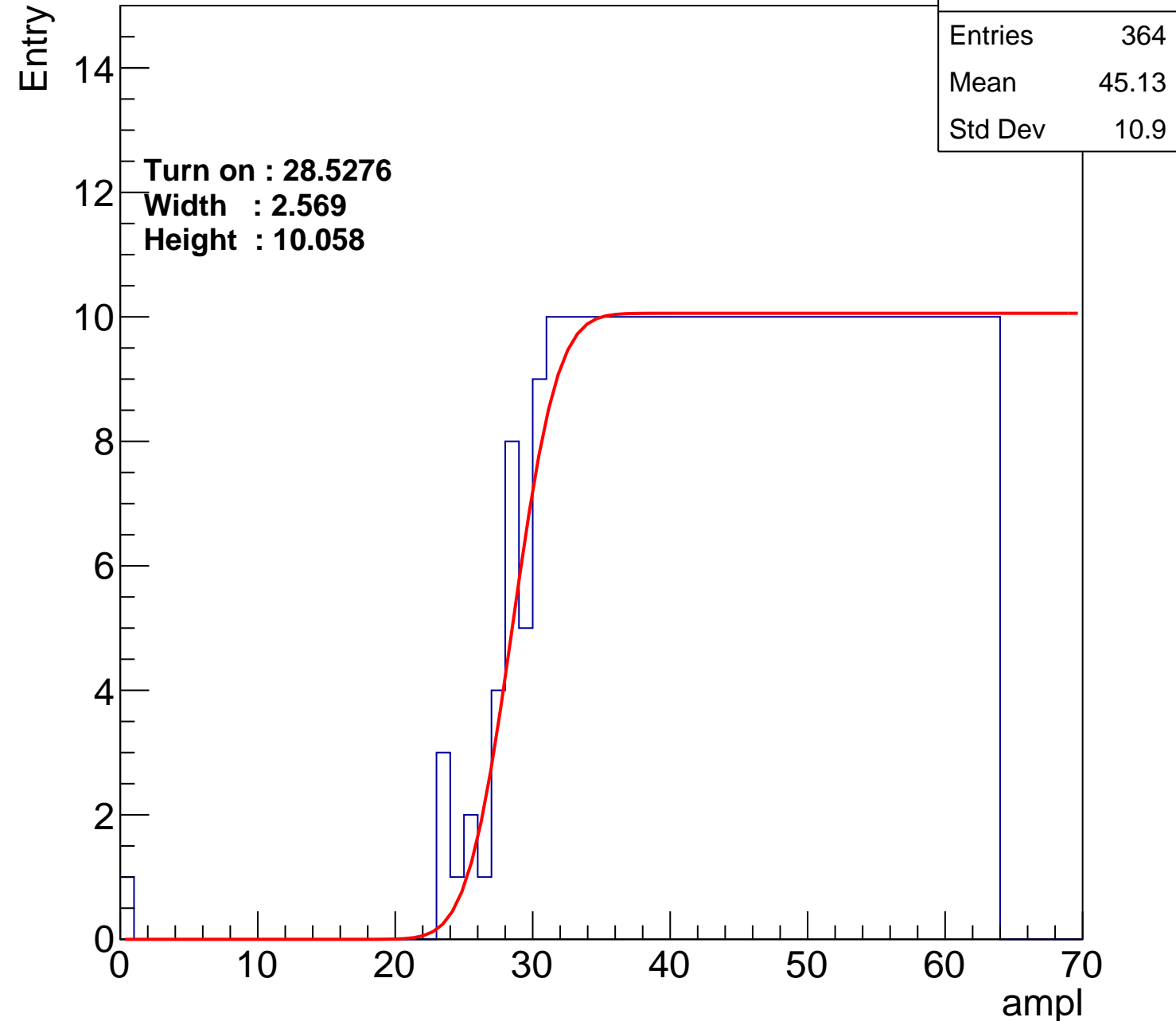
Width : 2.569

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch70

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.36
Std Dev	11.59

Turn on : 26.7282

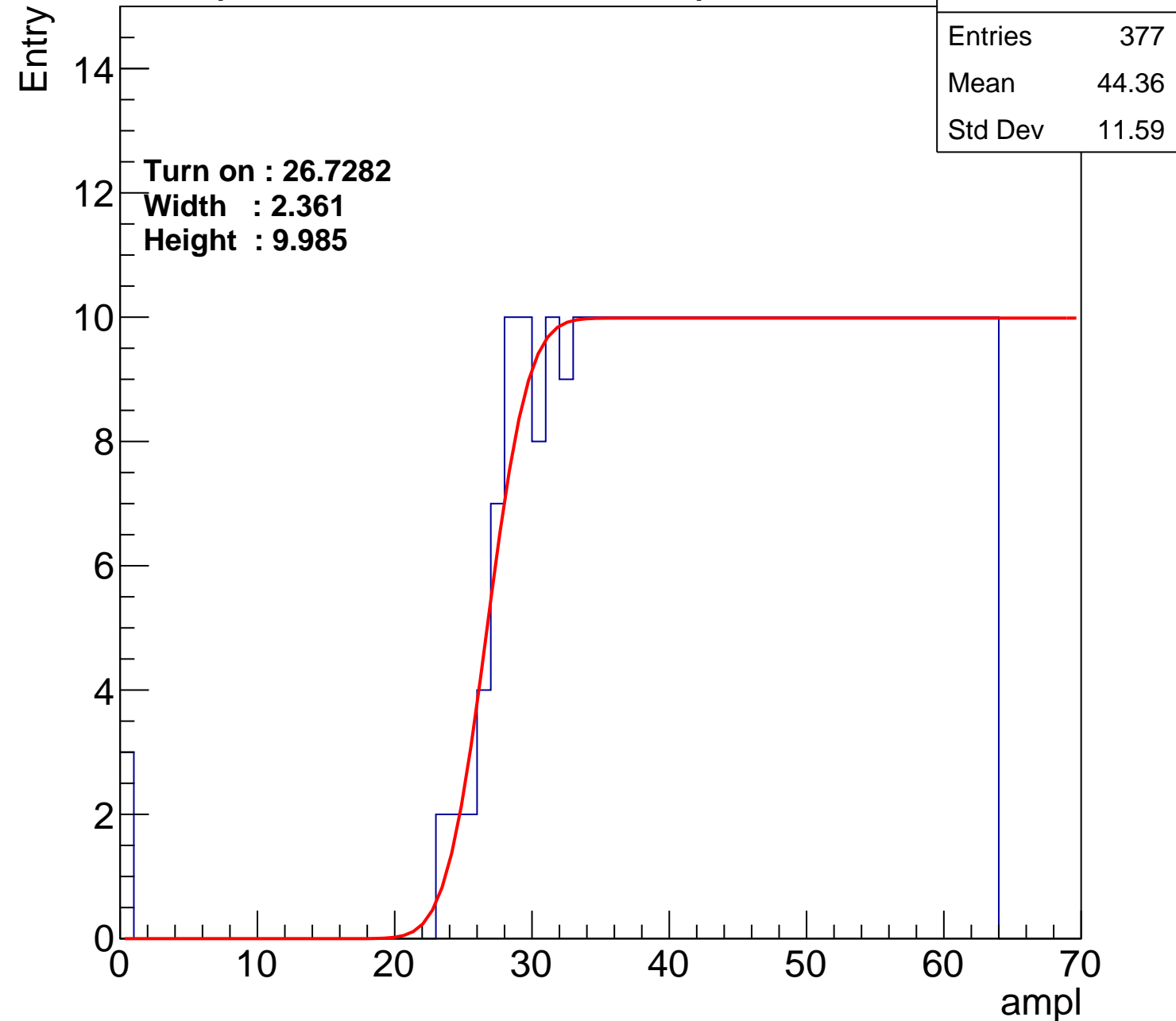
Width : 2.361

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	357
Mean	45.12
Std Dev	11.62

Turn on : 28.7736

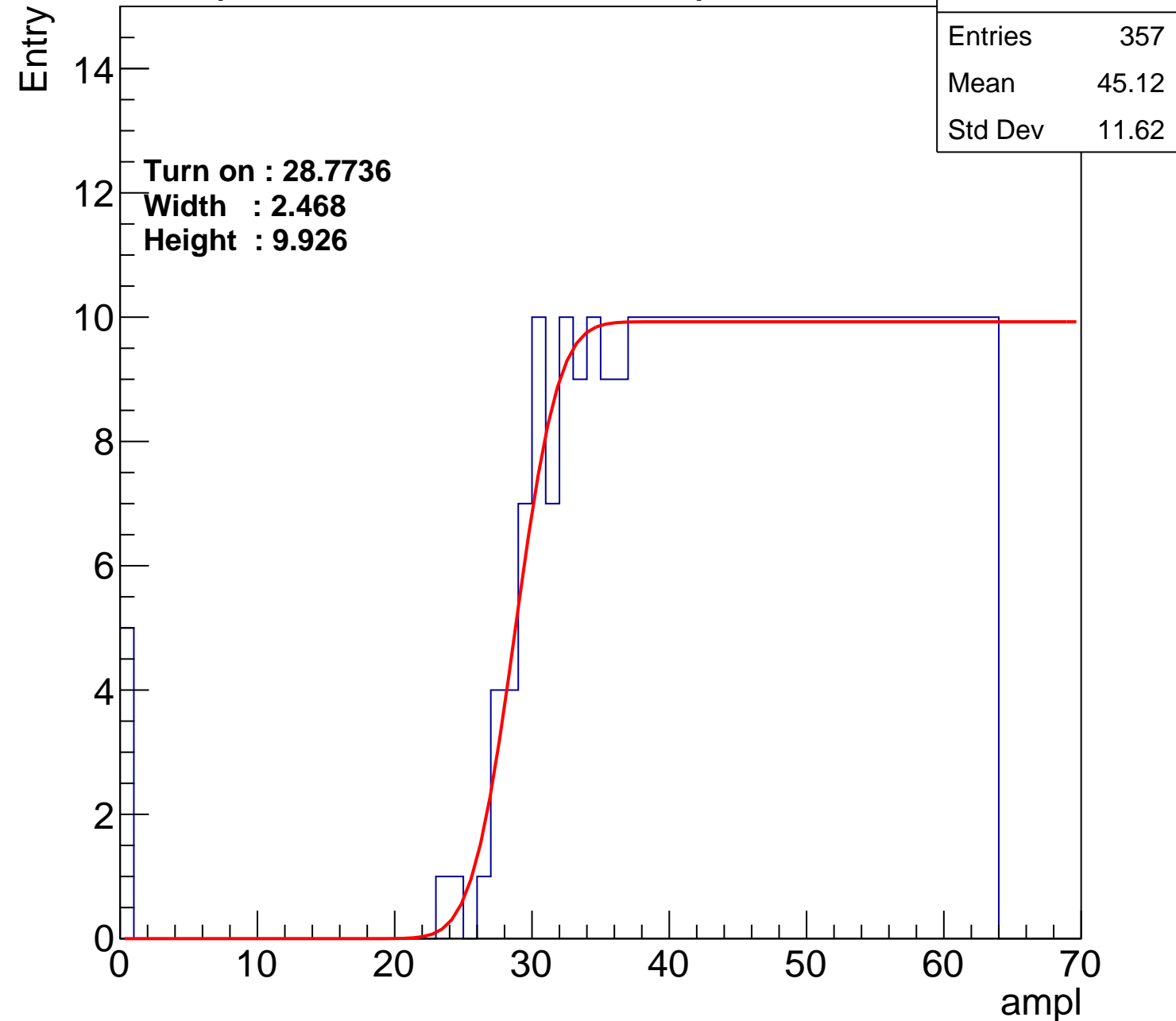
Width : 2.468

Height : 9.926

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch72

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.85
Std Dev	11.54

Turn on : 27.9088

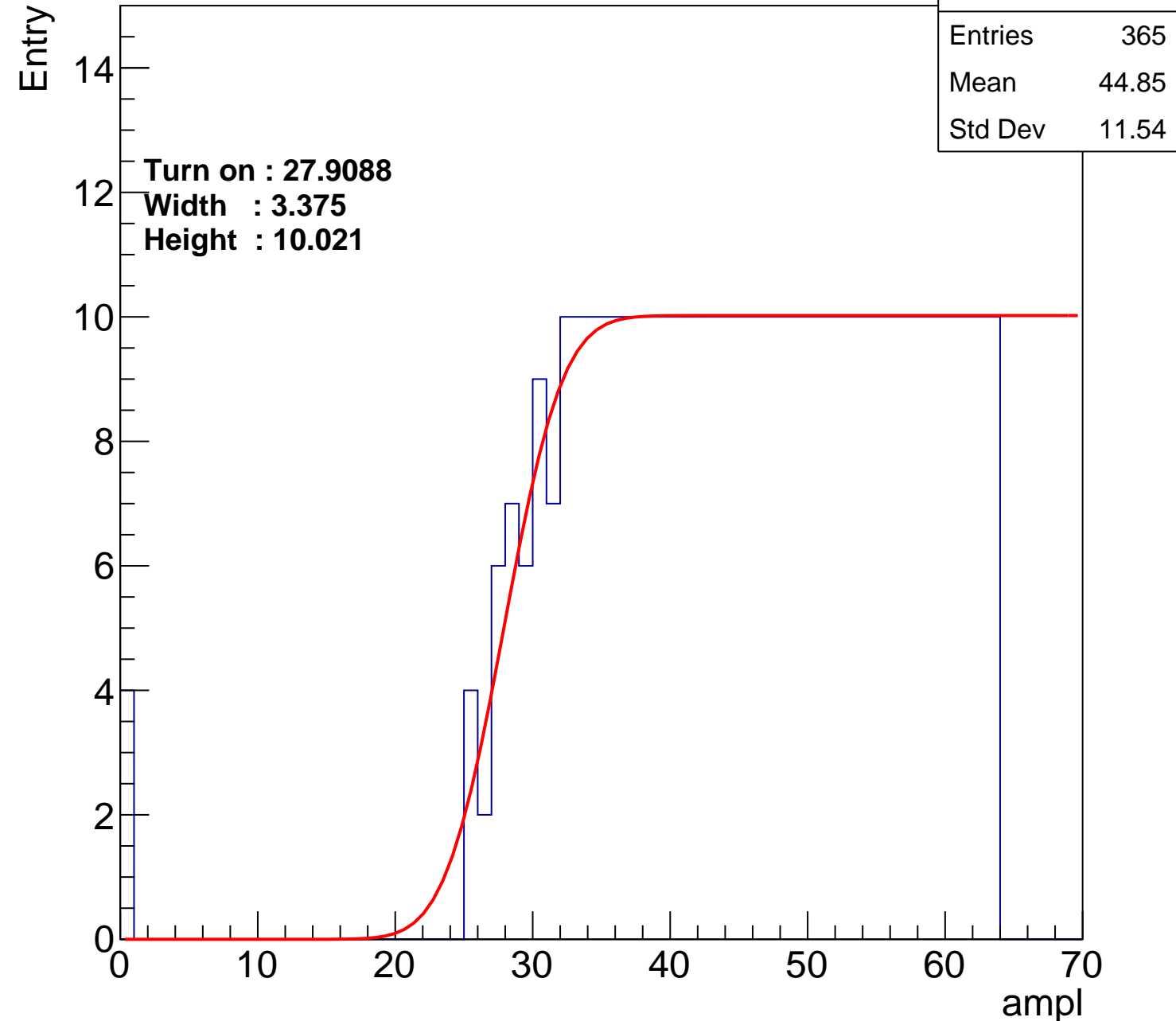
Width : 3.375

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch73

calib_packv5_042523_0143.root, FC#7, port C2

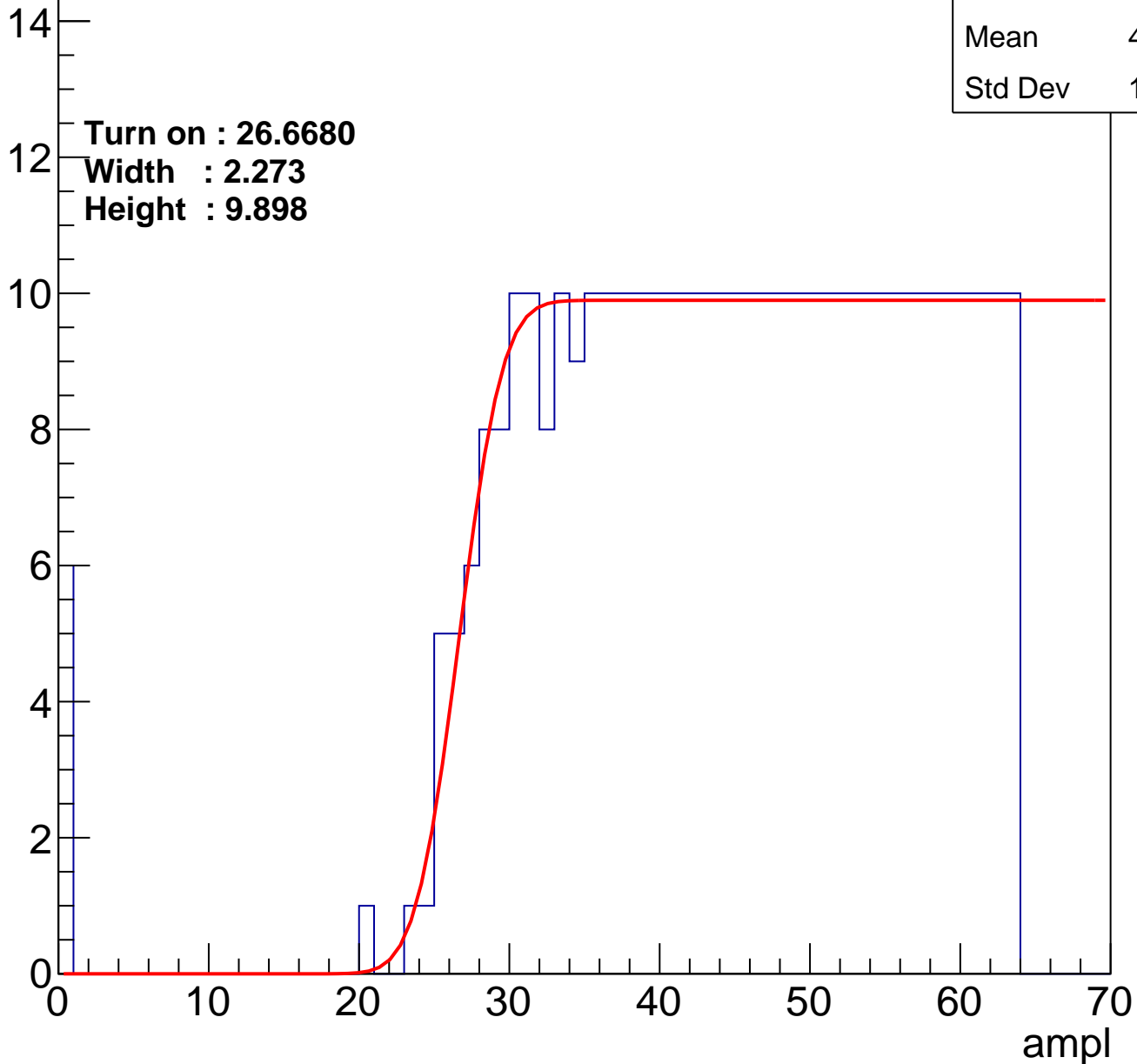
Entries	378
Mean	44.05
Std Dev	12.23

Turn on : 26.6680

Width : 2.273

Height : 9.898

Entry



B1L103S, U17-ch74

calib_packv5_042523_0143.root, FC#7, port C2

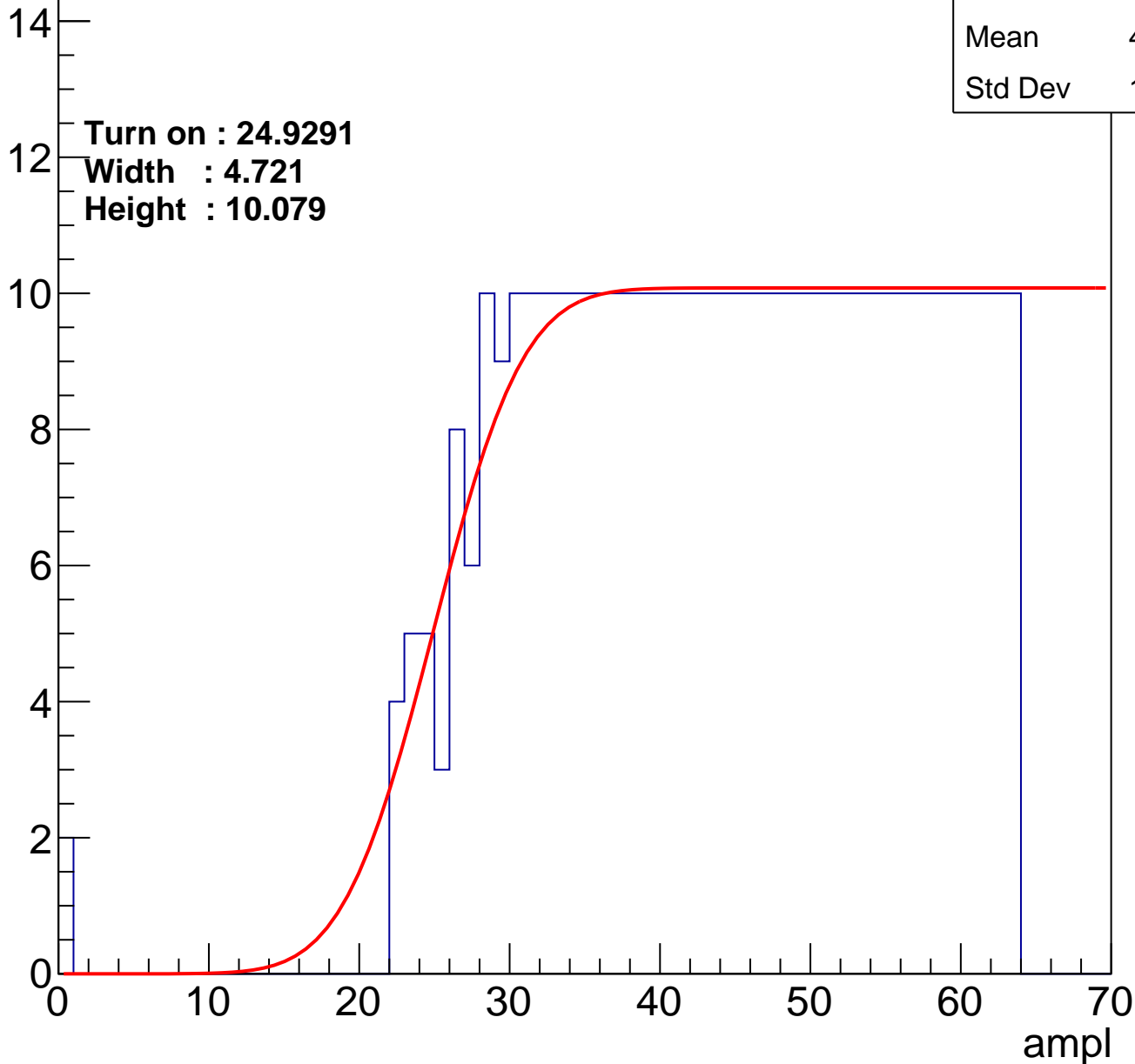
Entries	392
Mean	43.67
Std Dev	11.83

Turn on : 24.9291

Width : 4.721

Height : 10.079

Entry



B1L103S, U17-ch75

calib_packv5_042523_0143.root, FC#7, port C2

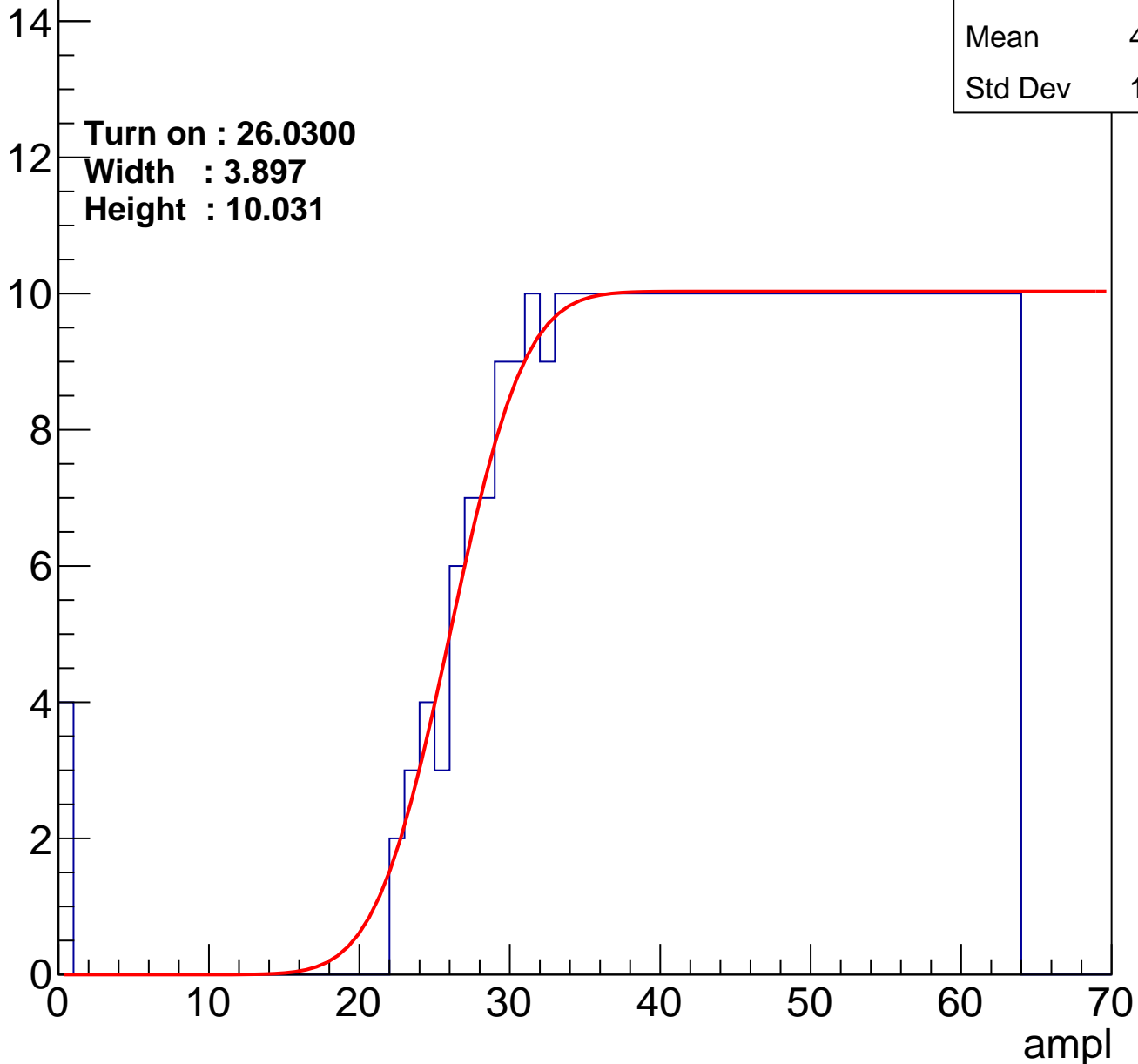
Entries	383
Mean	43.95
Std Dev	11.98

Turn on : 26.0300

Width : 3.897

Height : 10.031

Entry



B1L103S, U17-ch76

calib_packv5_042523_0143.root, FC#7, port C2

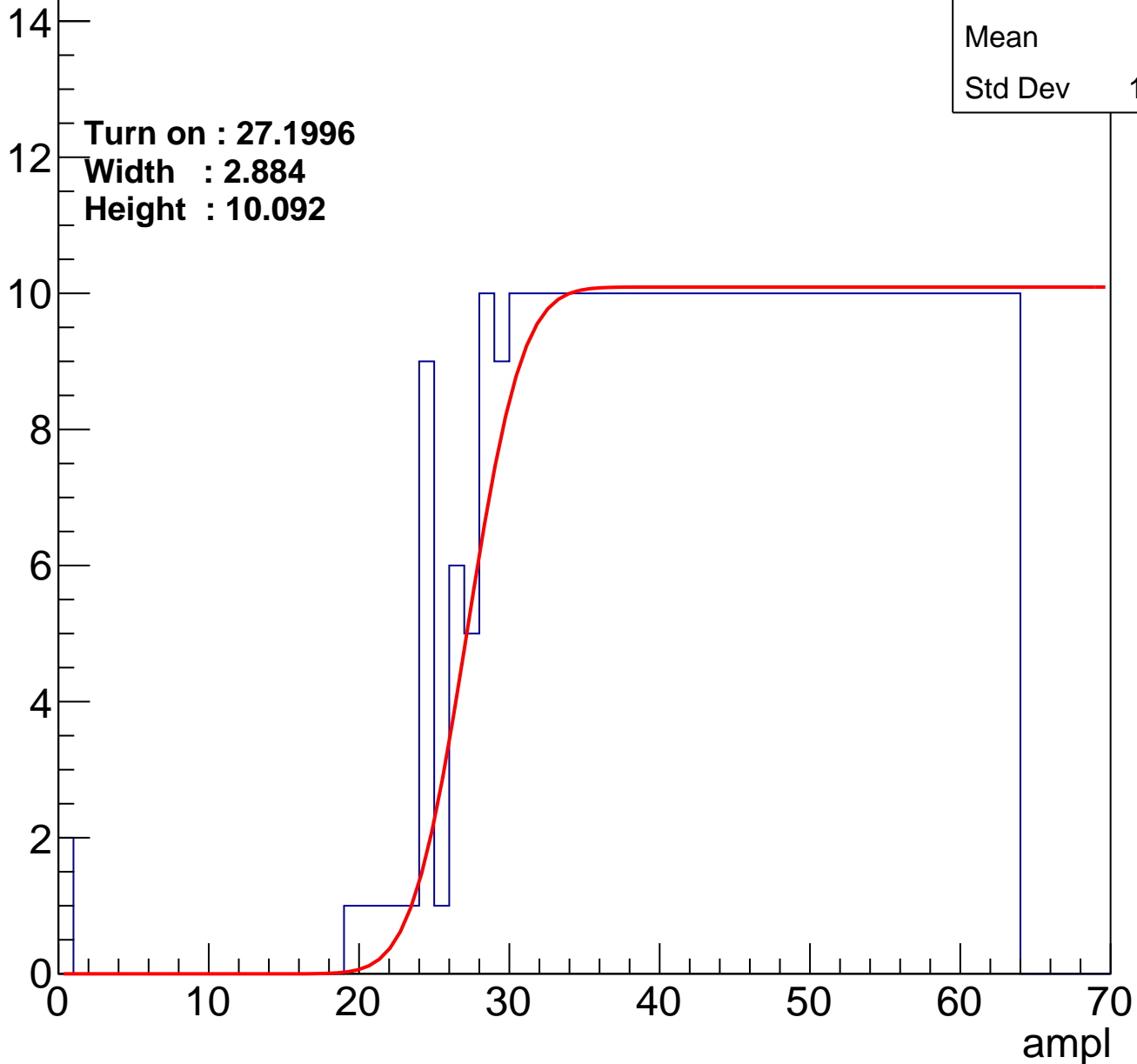
Entries	387
Mean	43.9
Std Dev	11.74

Turn on : 27.1996

Width : 2.884

Height : 10.092

Entry



B1L103S, U17-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.51
Std Dev	11.62

Turn on : 27.8333

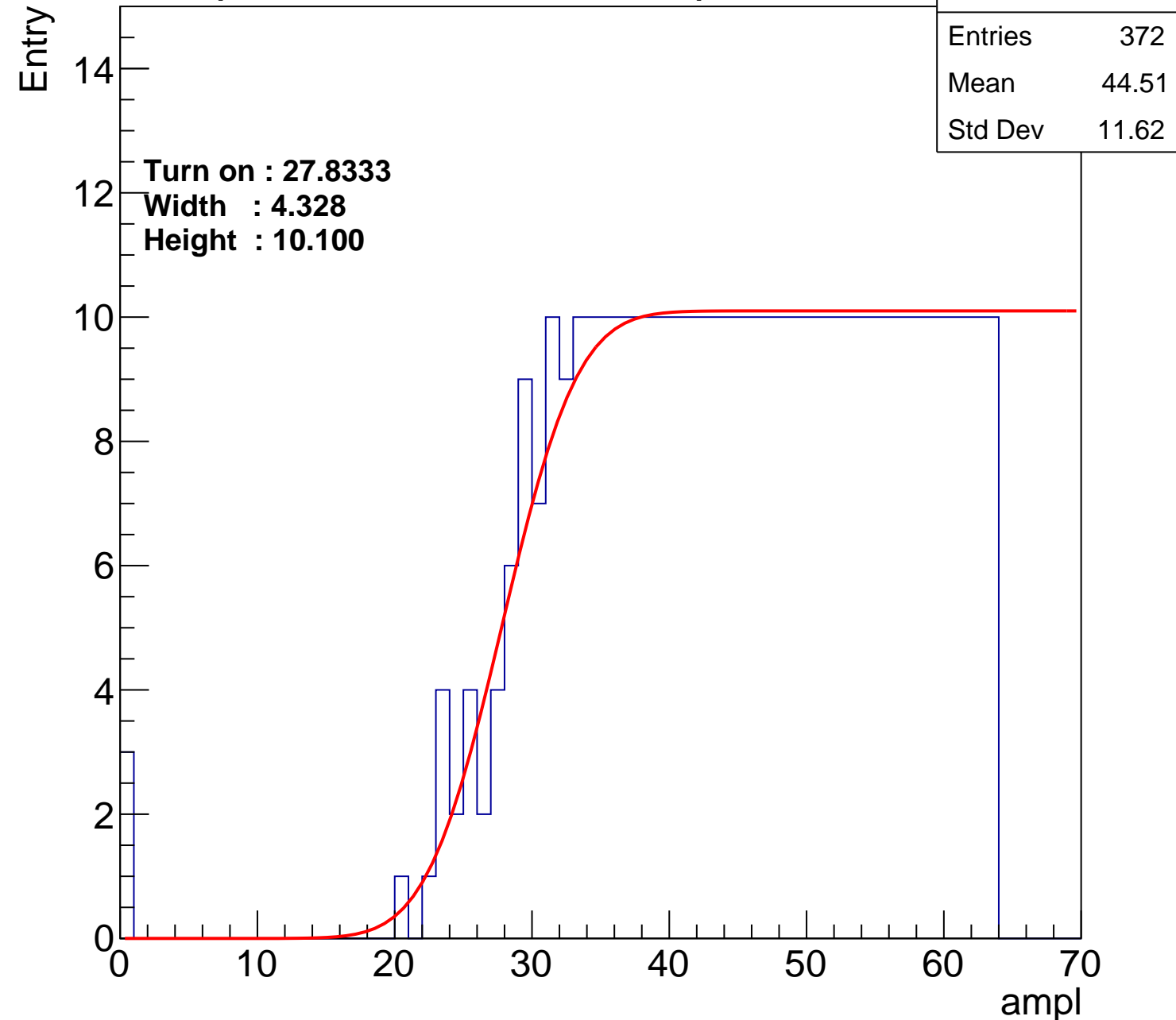
Width : 4.328

Height : 10.100

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch78

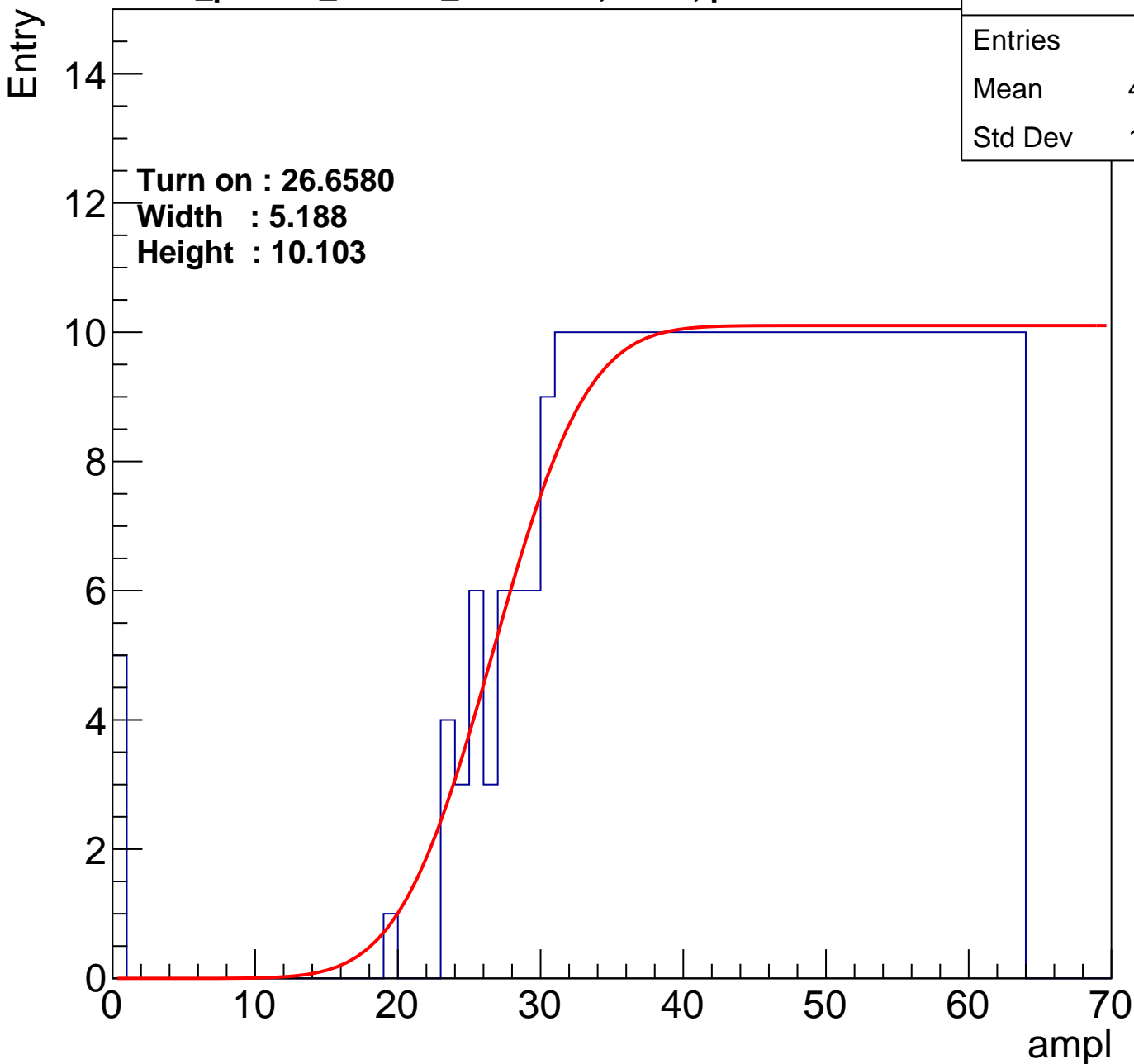
calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.05
Std Dev	12.12

Turn on : 26.6580

Width : 5.188

Height : 10.103



B1L103S, U17-ch79

calib_packv5_042523_0143.root, FC#7, port C2

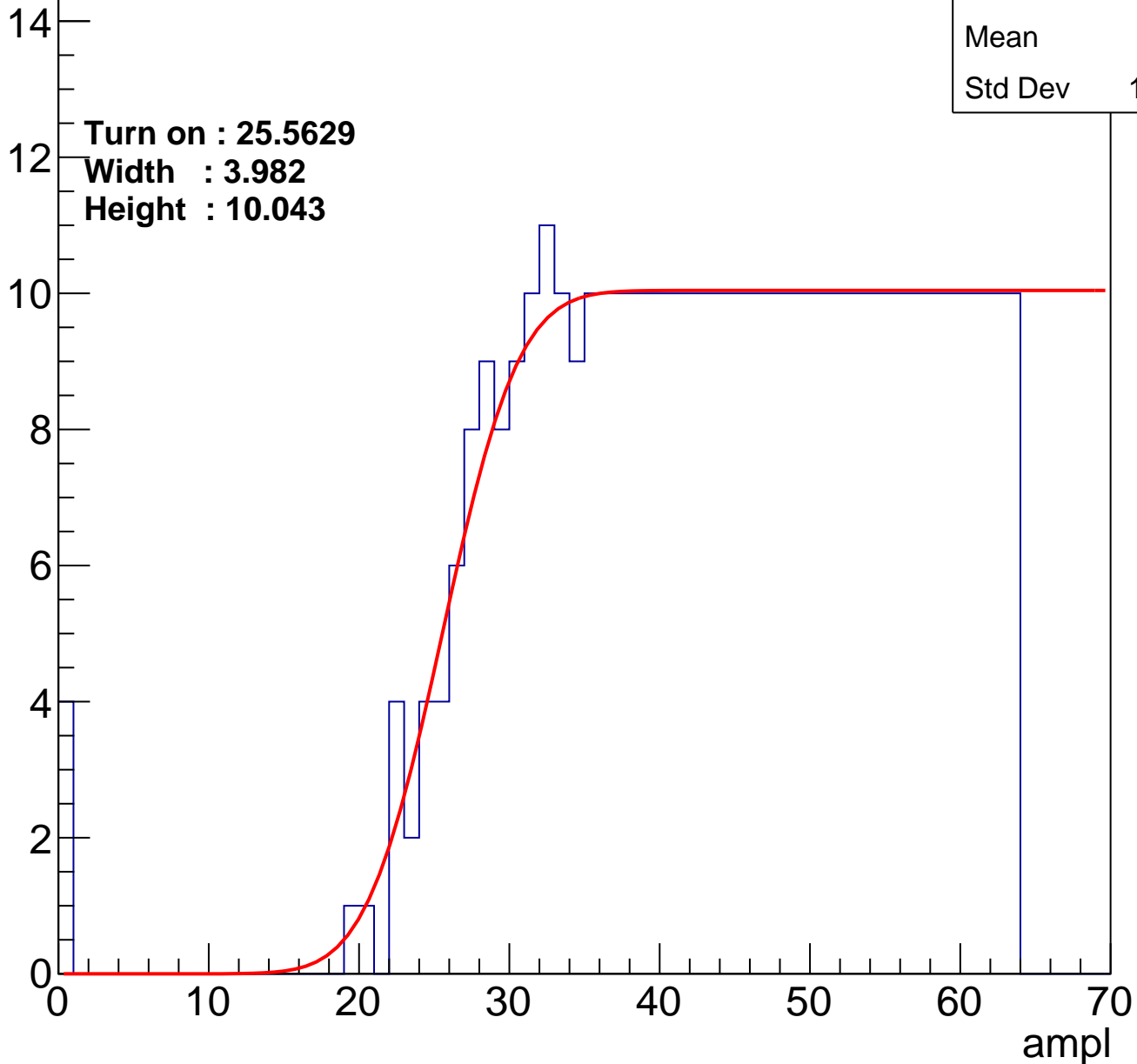
Entries	390
Mean	43.6
Std Dev	12.17

Turn on : 25.5629

Width : 3.982

Height : 10.043

Entry



B1L103S, U17-ch80

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.38
Std Dev	11.86

Turn on : 23.8643

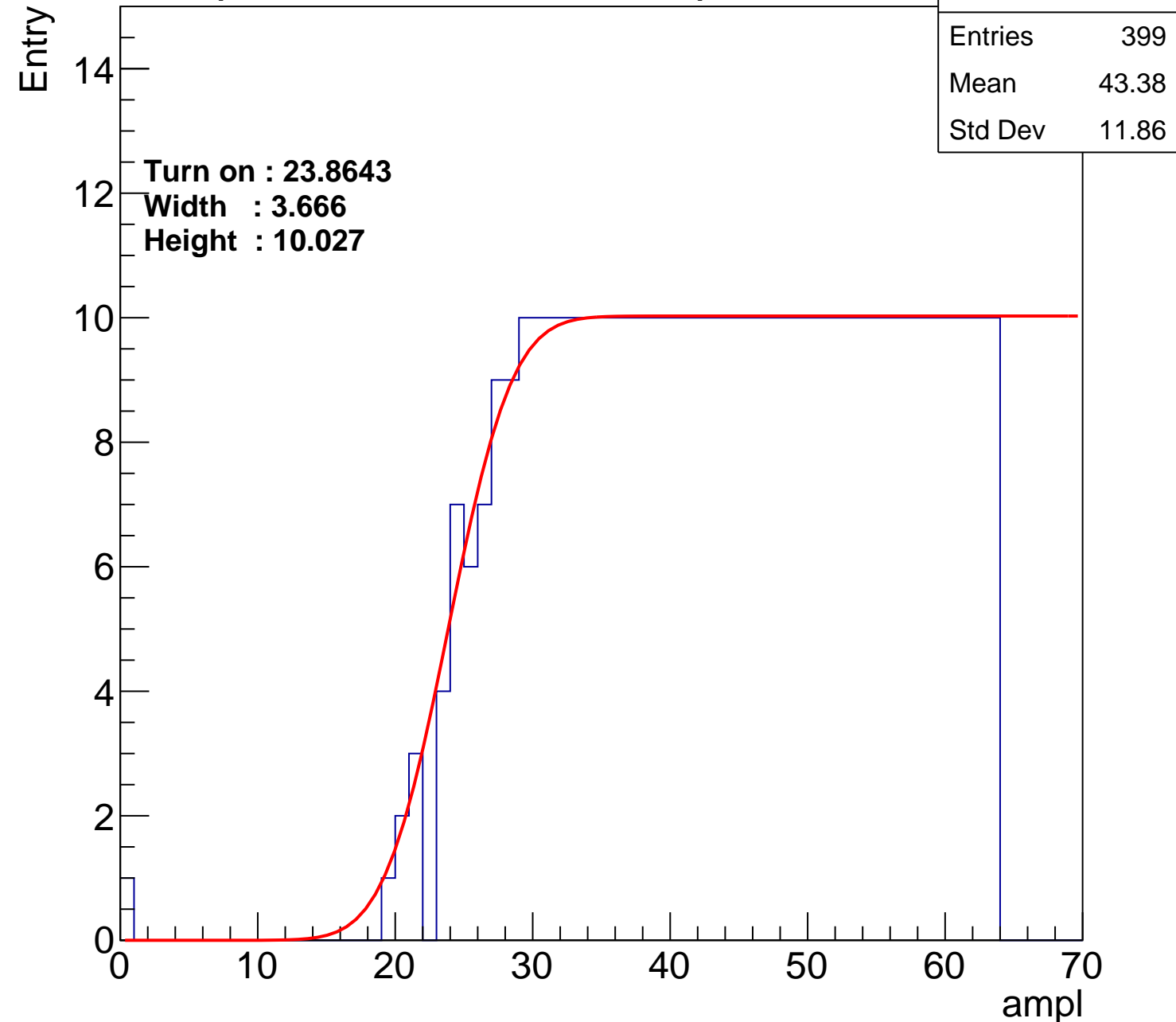
Width : 3.666

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch81

calib_packv5_042523_0143.root, FC#7, port C2

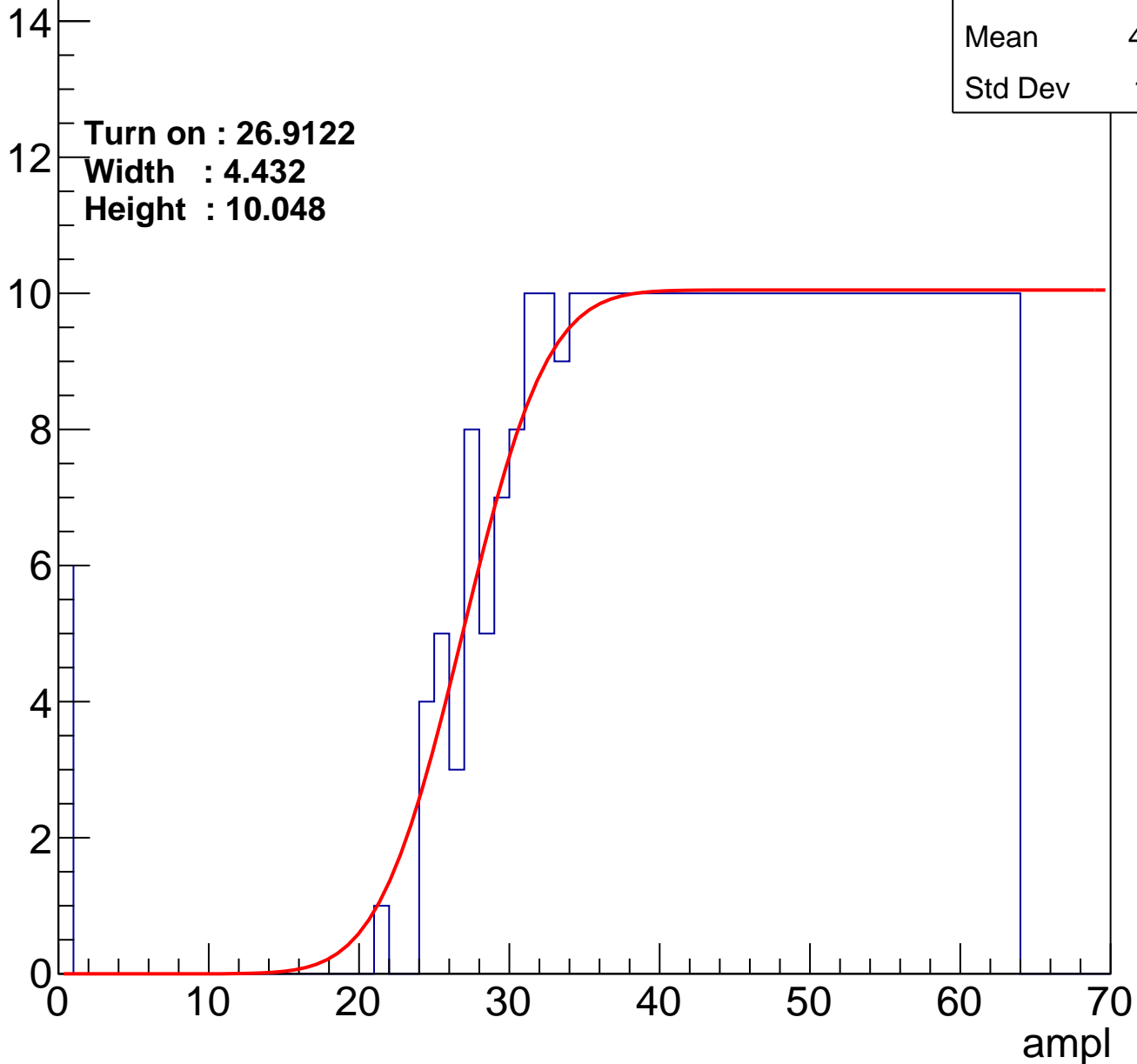
Entries	376
Mean	44.14
Std Dev	12.21

Turn on : 26.9122

Width : 4.432

Height : 10.048

Entry



B1L103S, U17-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.95
Std Dev	11.59

Turn on : 26.2576

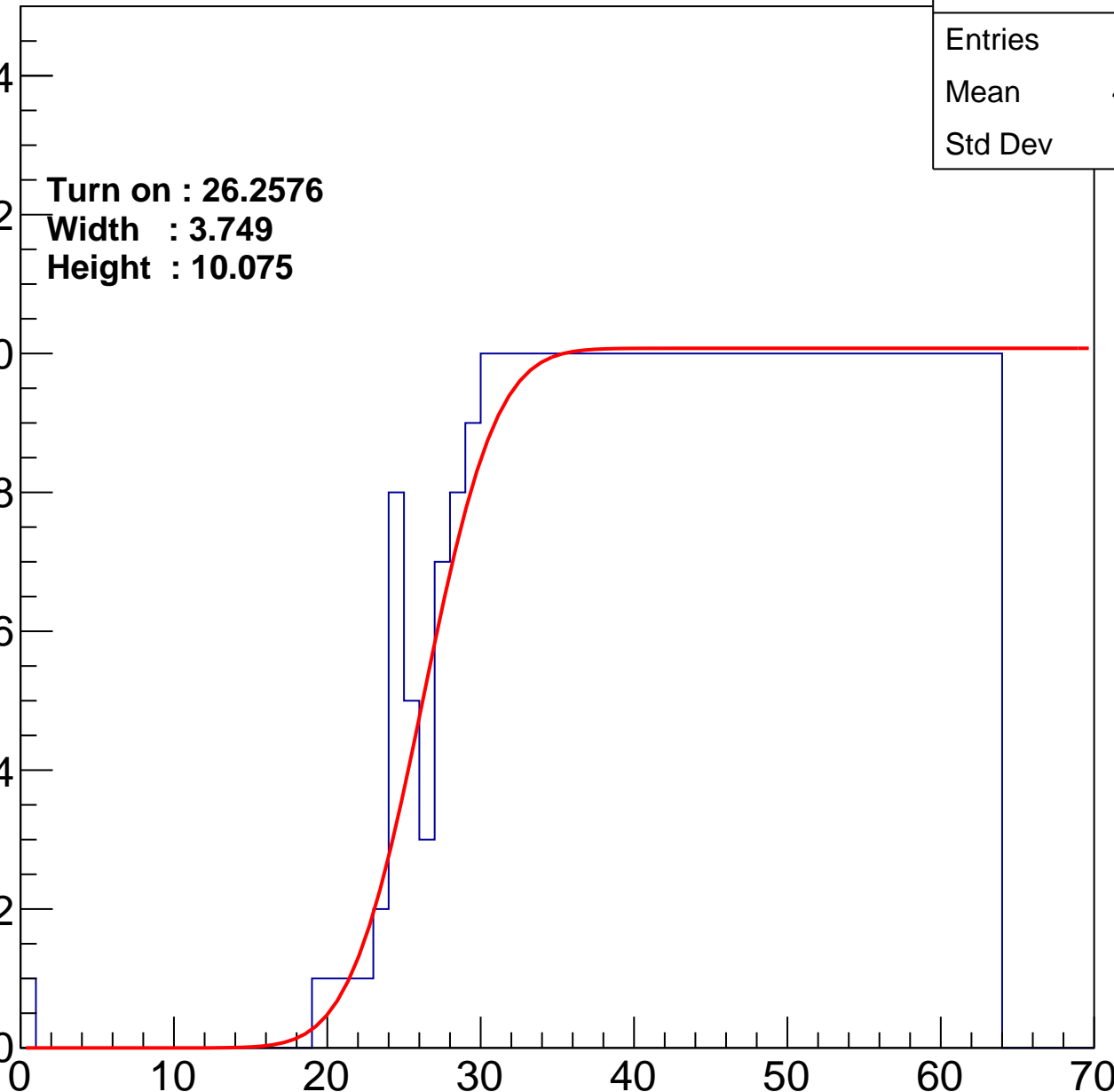
Width : 3.749

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch83

calib_packv5_042523_0143.root, FC#7, port C2

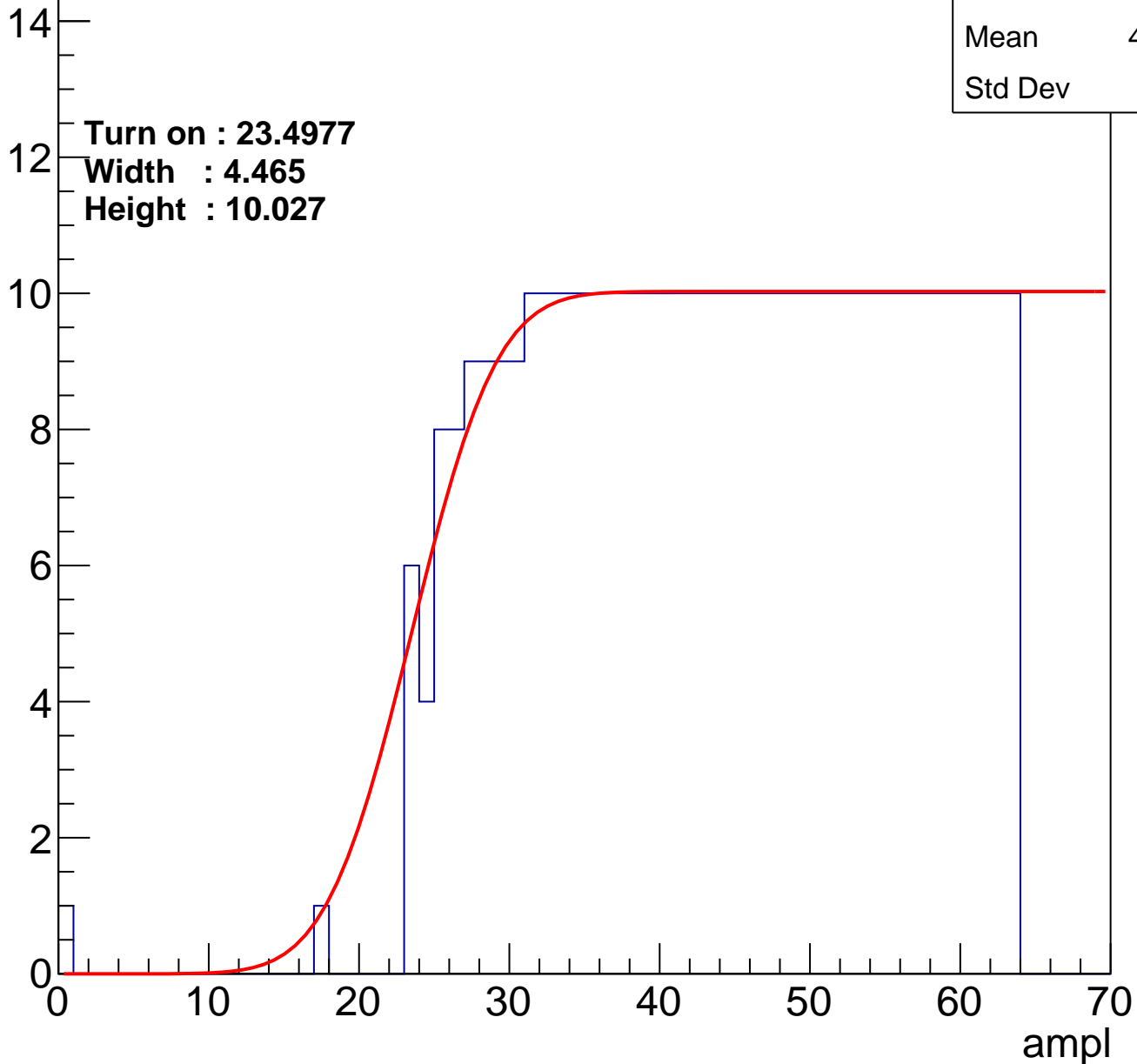
Entries	394
Mean	43.64
Std Dev	11.7

Turn on : 23.4977

Width : 4.465

Height : 10.027

Entry



B1L103S, U17-ch84

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.42
Std Dev	11.64

Turn on : 26.8667

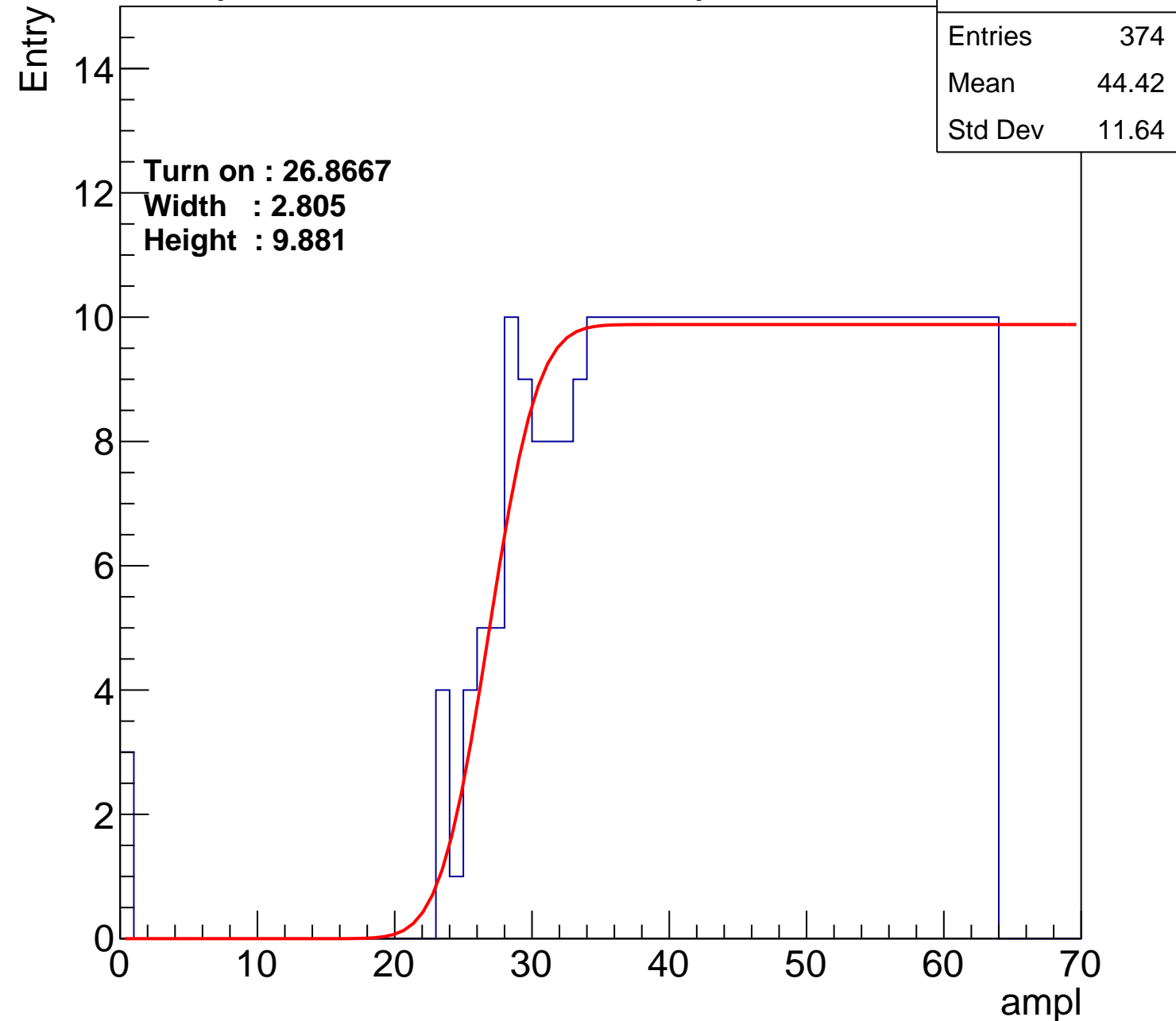
Width : 2.805

Height : 9.881

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch85

calib_packv5_042523_0143.root, FC#7, port C2

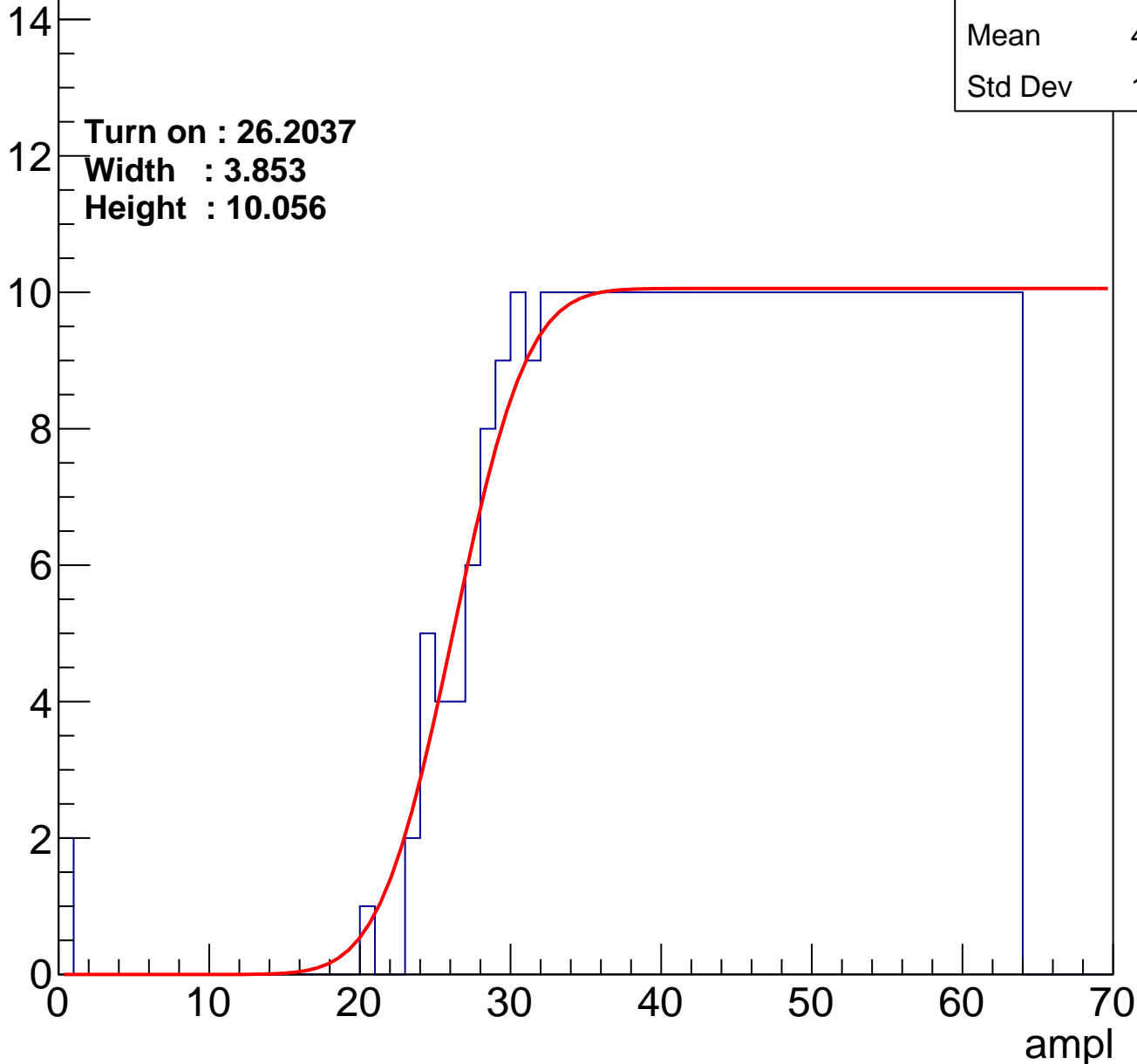
Entries	380
Mean	44.25
Std Dev	11.53

Turn on : 26.2037

Width : 3.853

Height : 10.056

Entry



B1L103S, U17-ch86

calib_packv5_042523_0143.root, FC#7, port C2

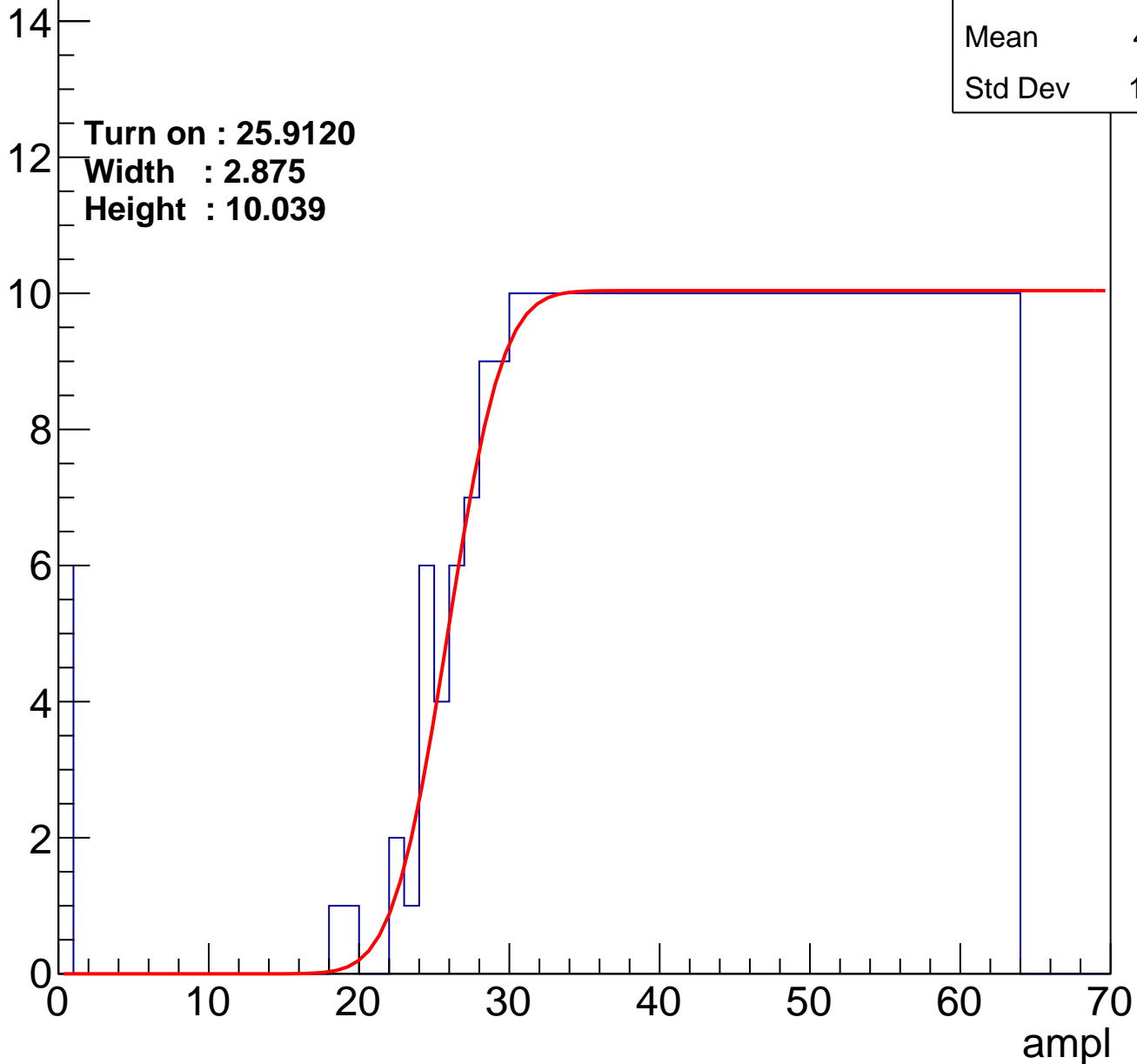
Entries	392
Mean	43.41
Std Dev	12.49

Turn on : 25.9120

Width : 2.875

Height : 10.039

Entry



B1L103S, U17-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.64
Std Dev	11.79

Turn on : 28.3231

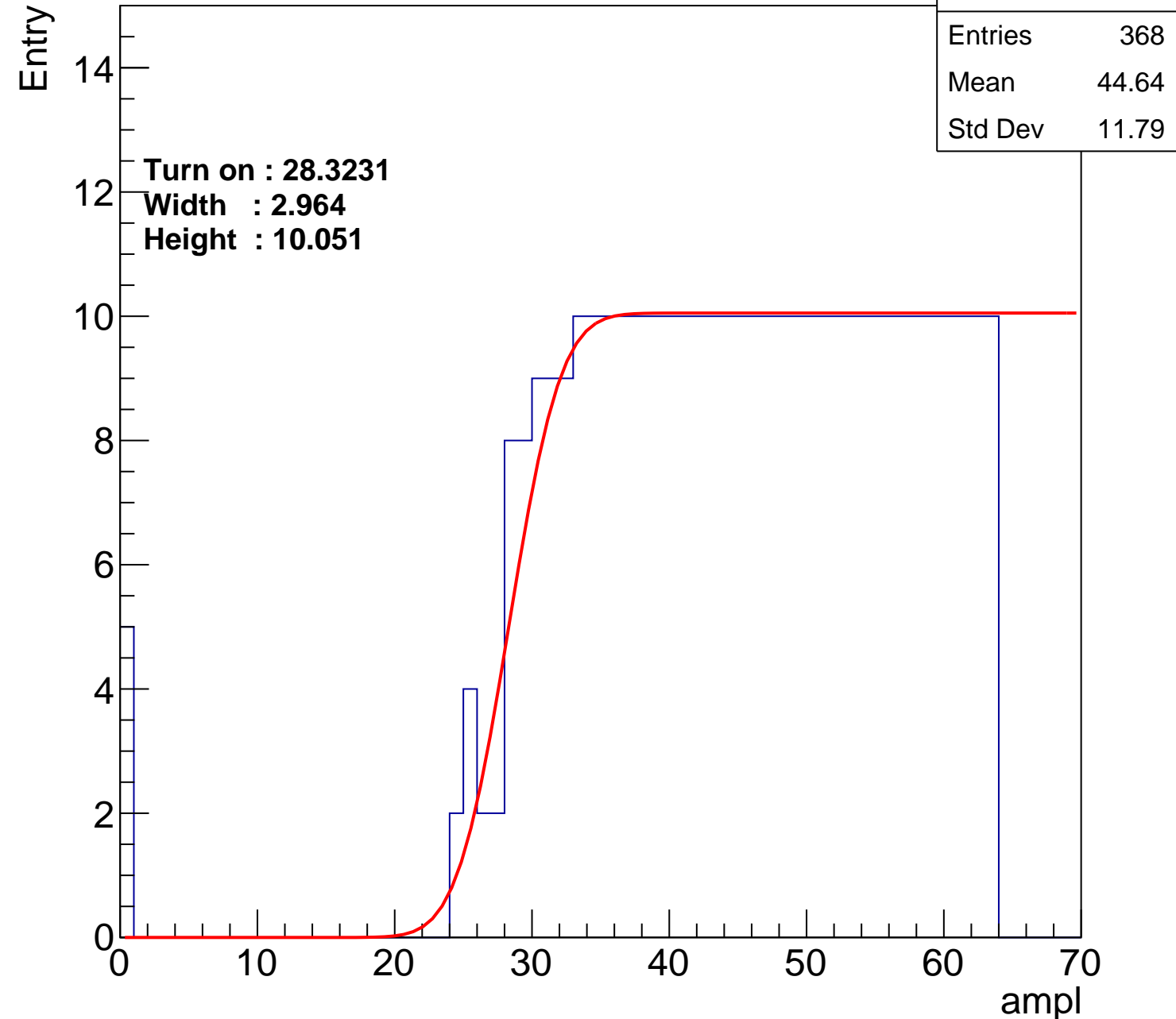
Width : 2.964

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch88

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.3
Std Dev	11.52

Turn on : 26.6689

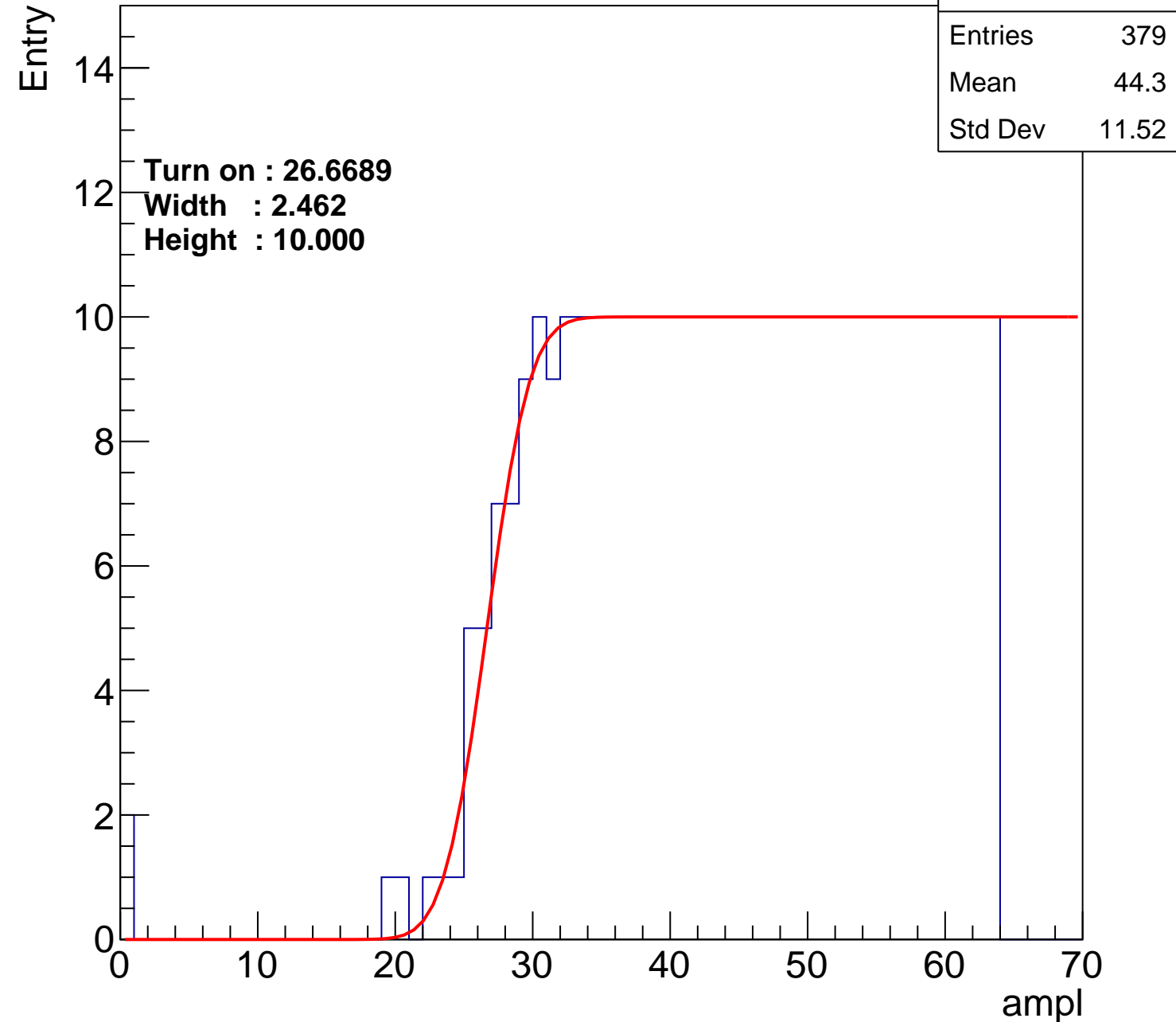
Width : 2.462

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch89

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.85
Std Dev	11.8

Turn on : 25.2503

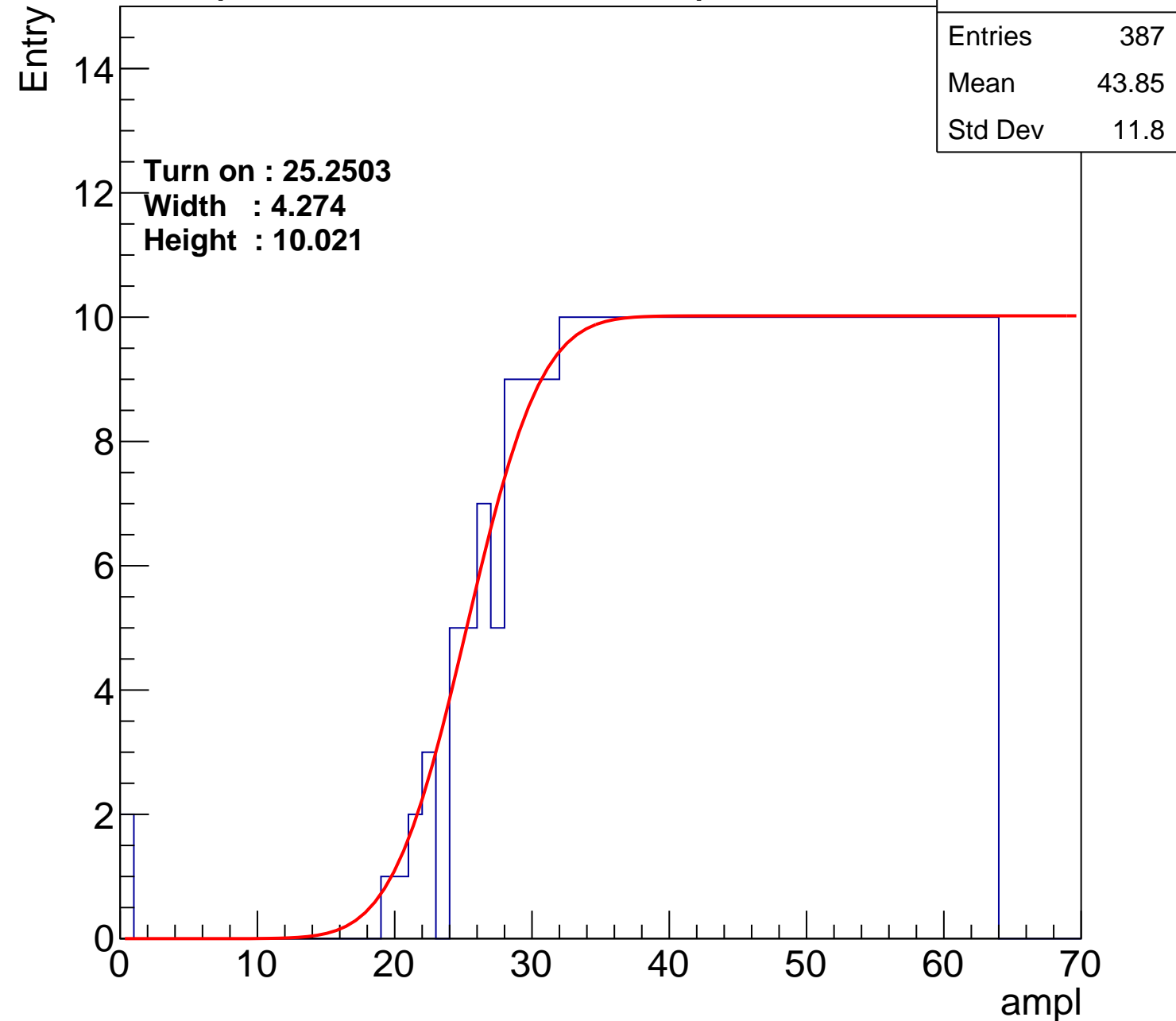
Width : 4.274

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch90

calib_packv5_042523_0143.root, FC#7, port C2

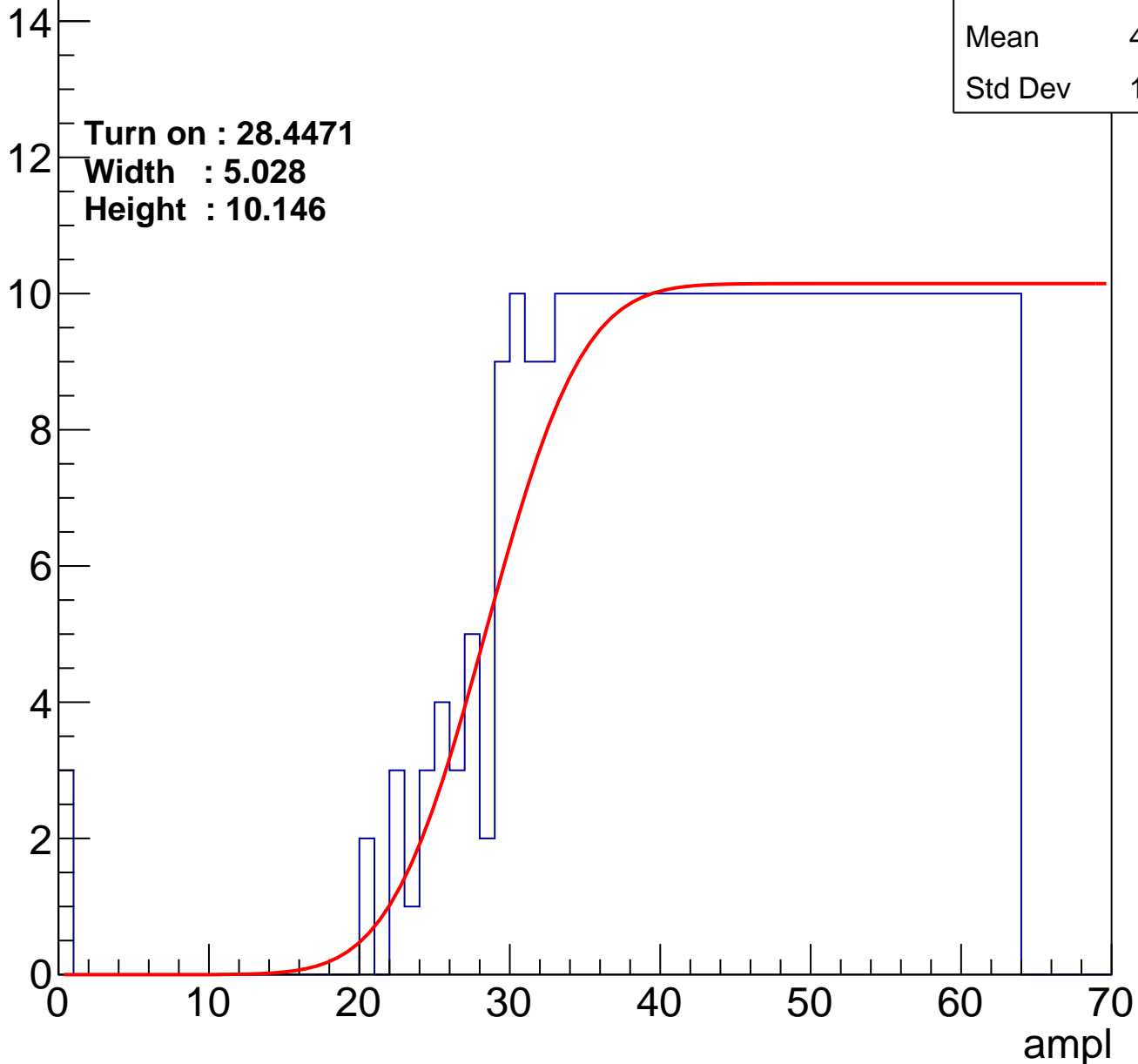
Entries	373
Mean	44.45
Std Dev	11.68

Turn on : 28.4471

Width : 5.028

Height : 10.146

Entry



B1L103S, U17-ch91

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.41
Std Dev	11.88

Turn on : 27.6130

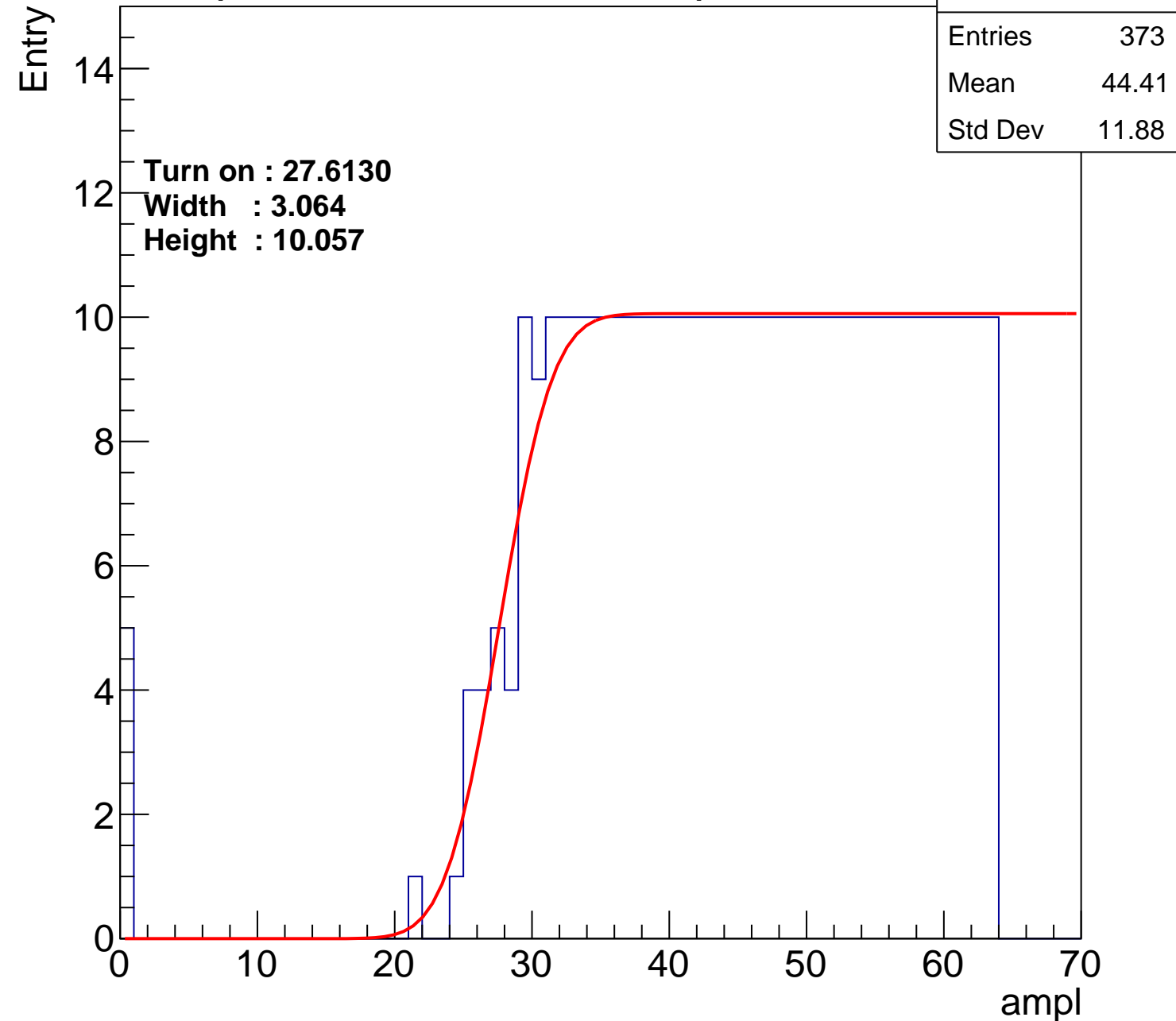
Width : 3.064

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch92

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.81
Std Dev	12.06

Turn on : 26.4090

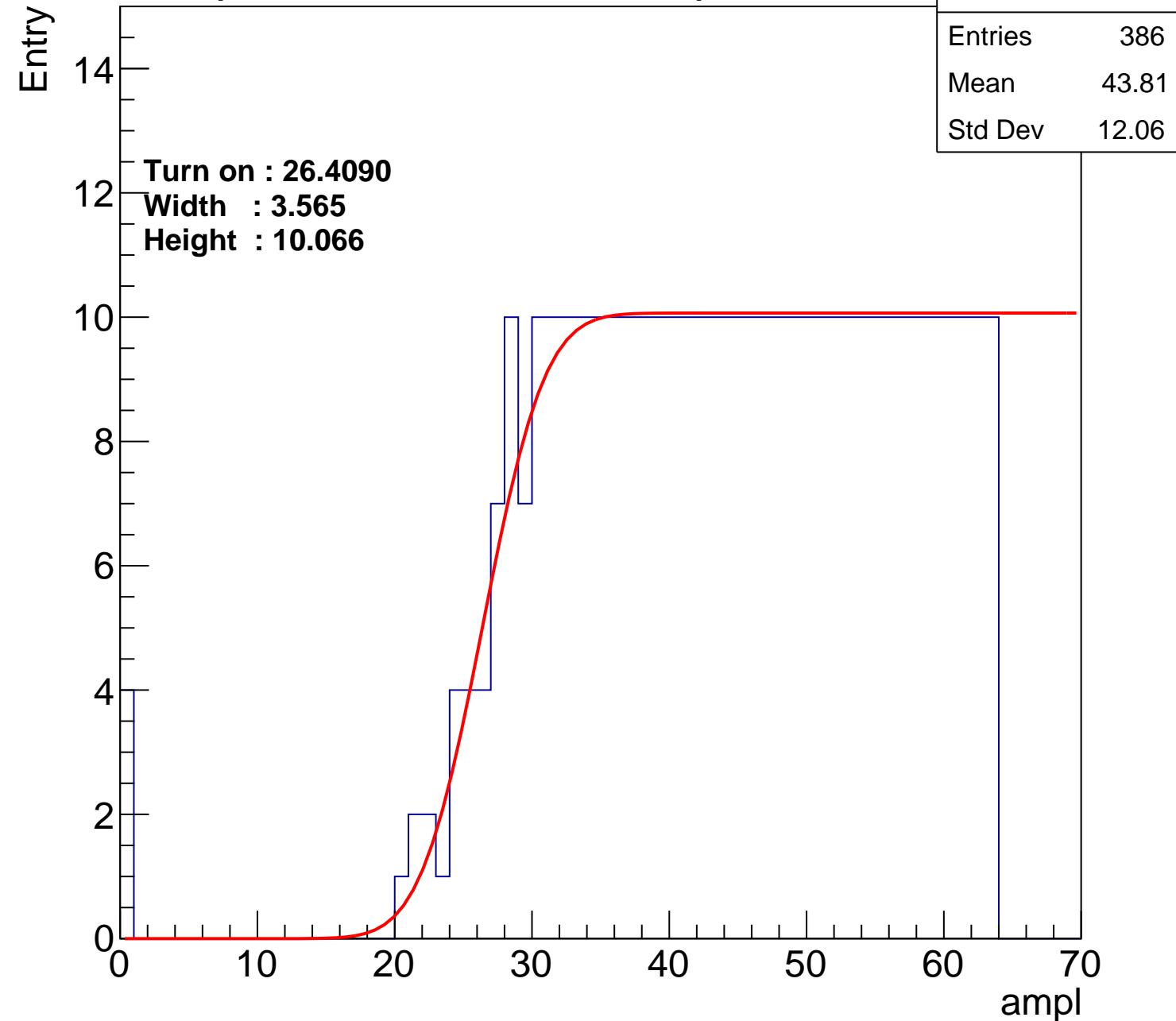
Width : 3.565

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.2
Std Dev	11.75

Turn on : 26.7459

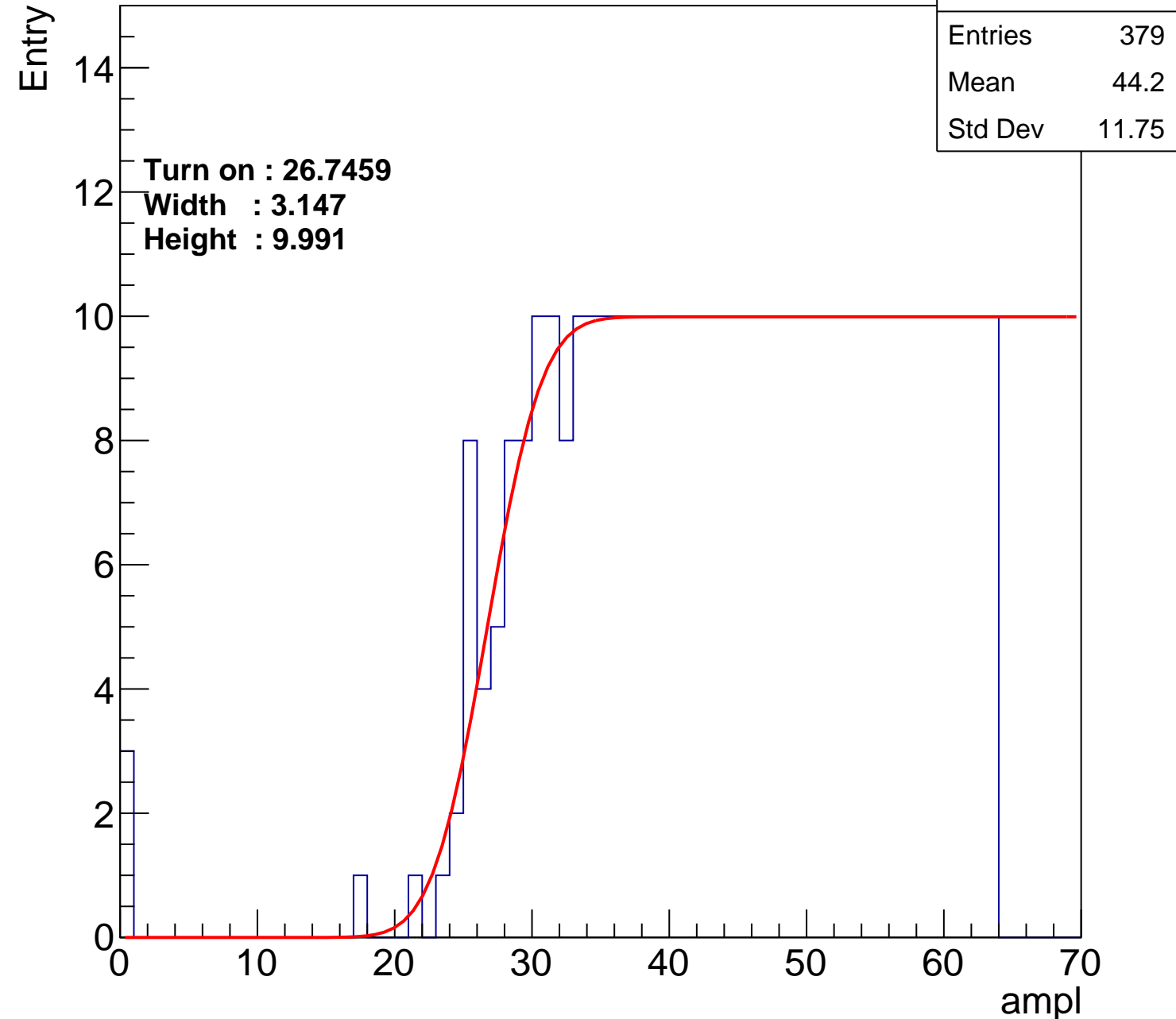
Width : 3.147

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch94

calib_packv5_042523_0143.root, FC#7, port C2

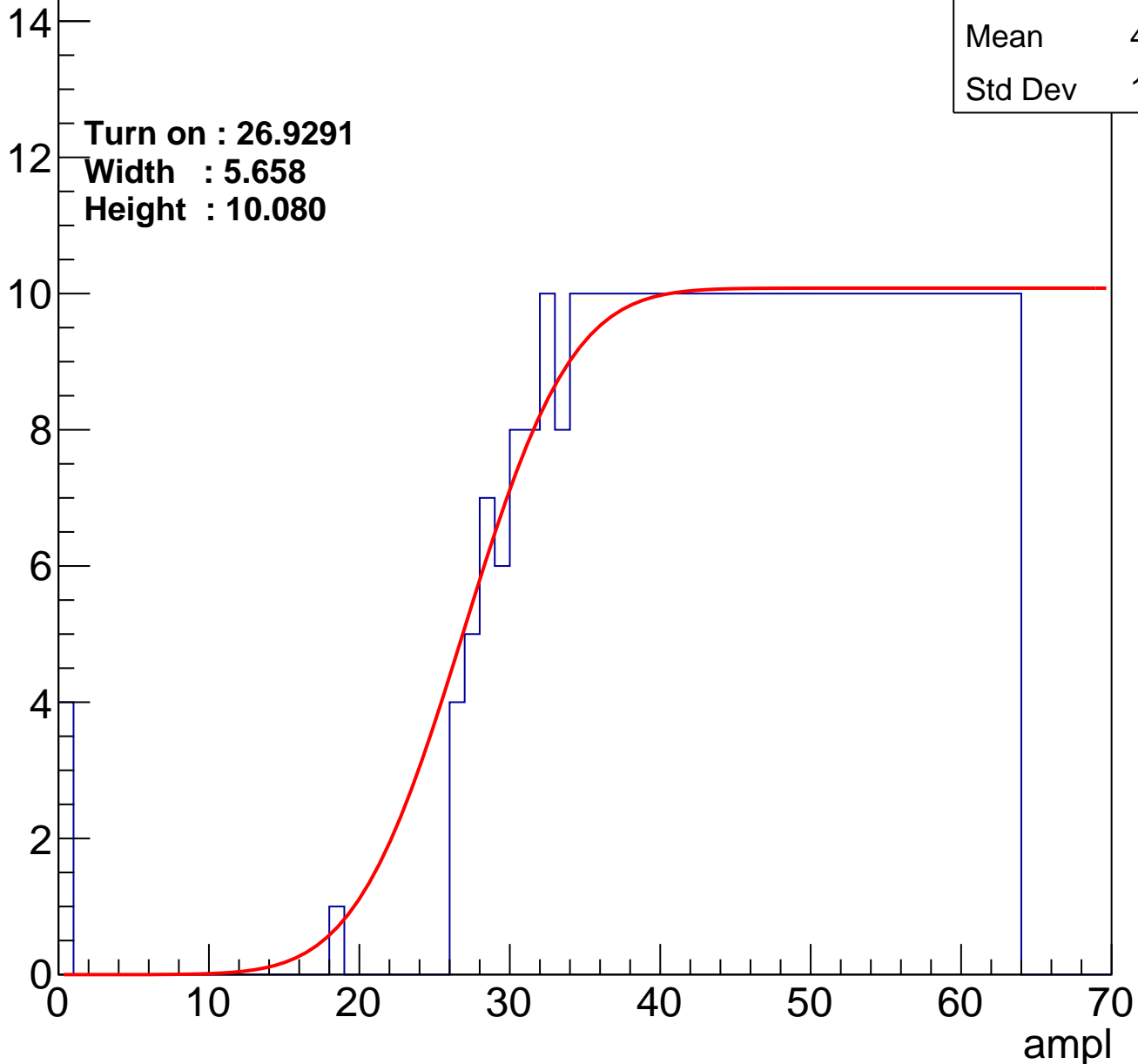
Entries	361
Mean	45.01
Std Dev	11.51

Turn on : 26.9291

Width : 5.658

Height : 10.080

Entry



calib_packv5_042523_0143.root, FC#7, port C2

calib_packv5_042523_0143.root, FC#7, port C2

Turn on : 25.6102
Width : 3.831
Height : 10.013



B1L103S, U17-ch96

calib_packv5_042523_0143.root, FC#7, port C2

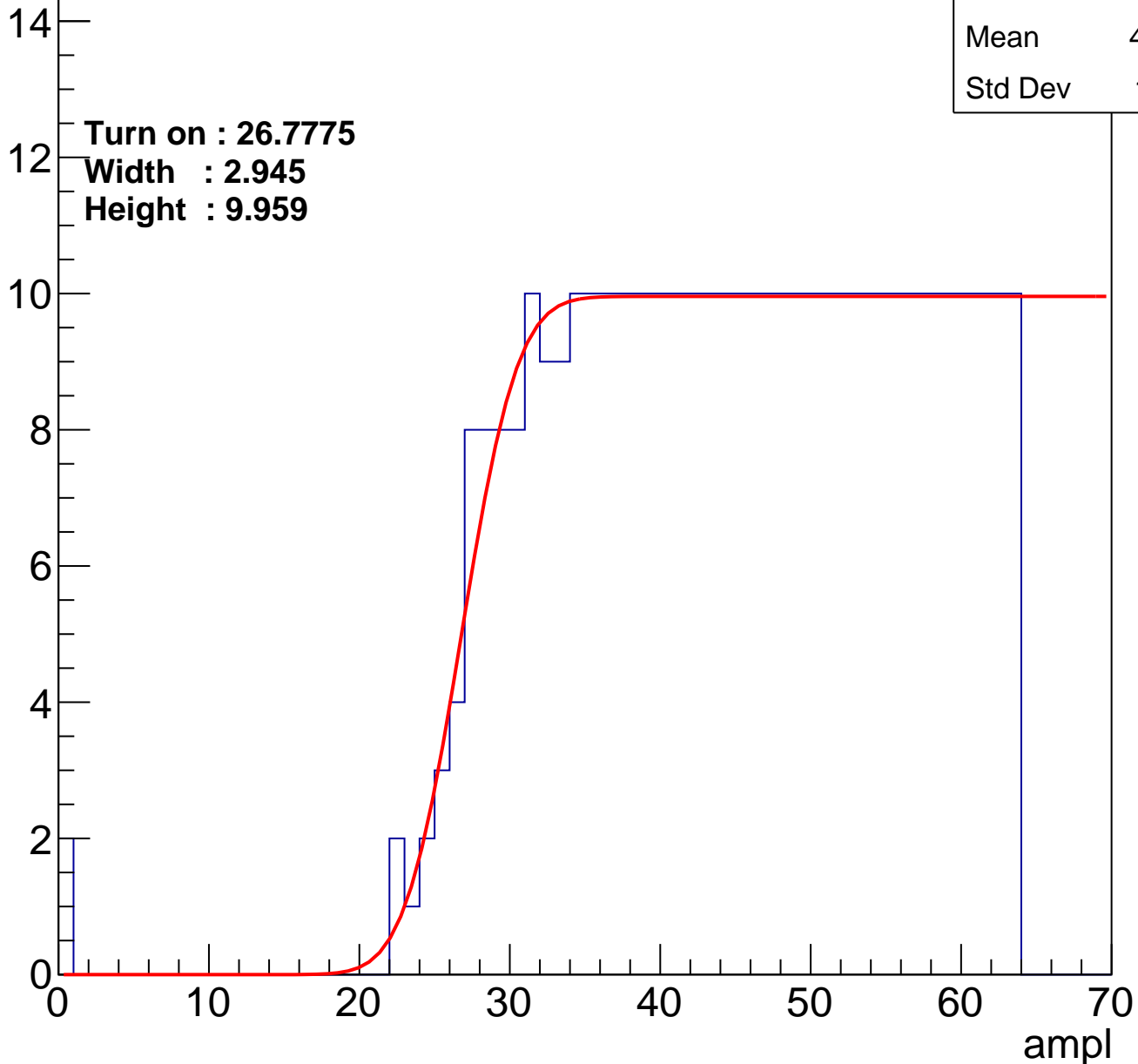
Entries	374
Mean	44.52
Std Dev	11.41

Turn on : 26.7775

Width : 2.945

Height : 9.959

Entry



B1L103S, U17-ch97

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.96
Std Dev	11.84

Turn on : 26.4540

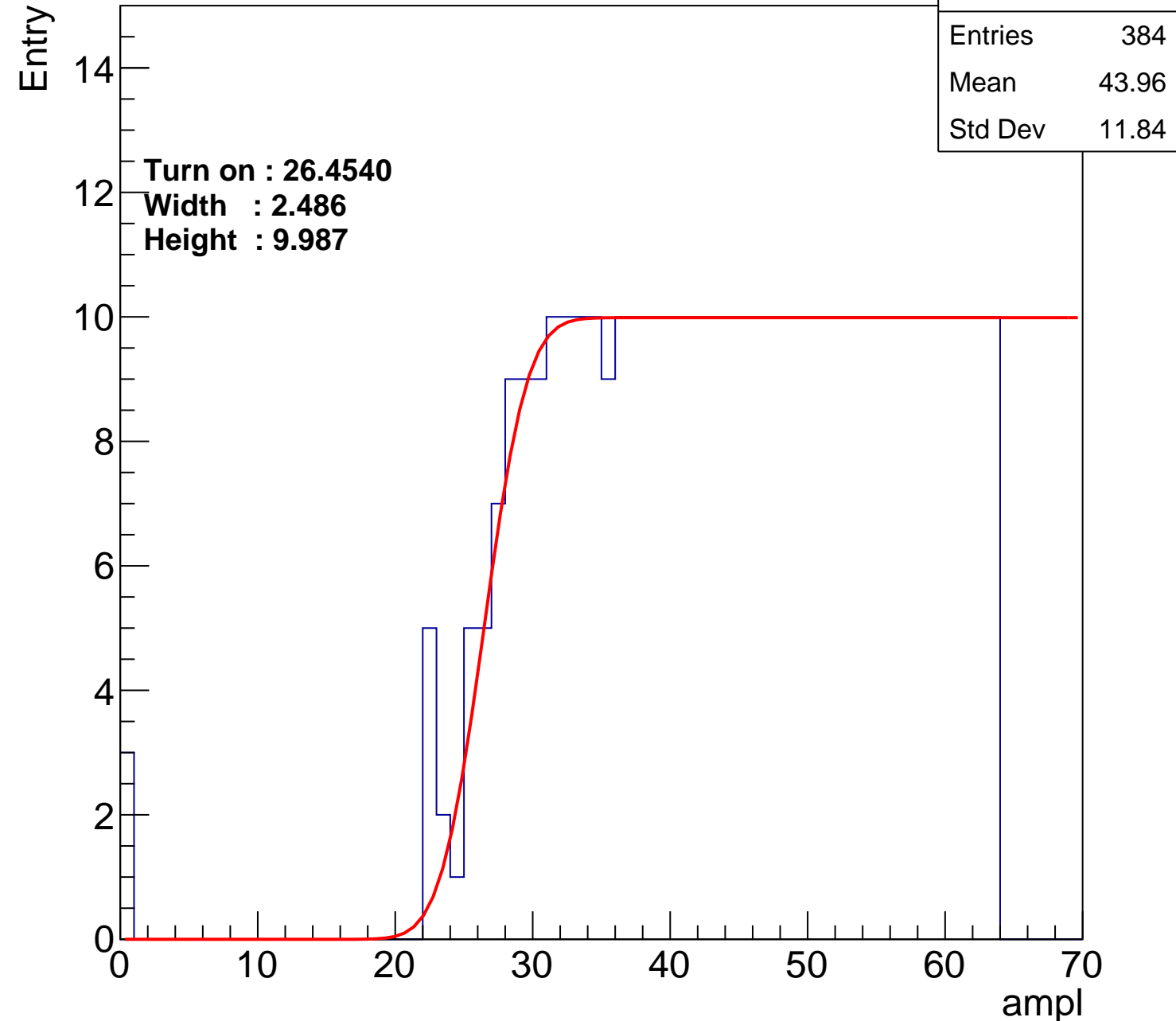
Width : 2.486

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.67
Std Dev	12.11

Turn on : 25.5479

Width : 3.076

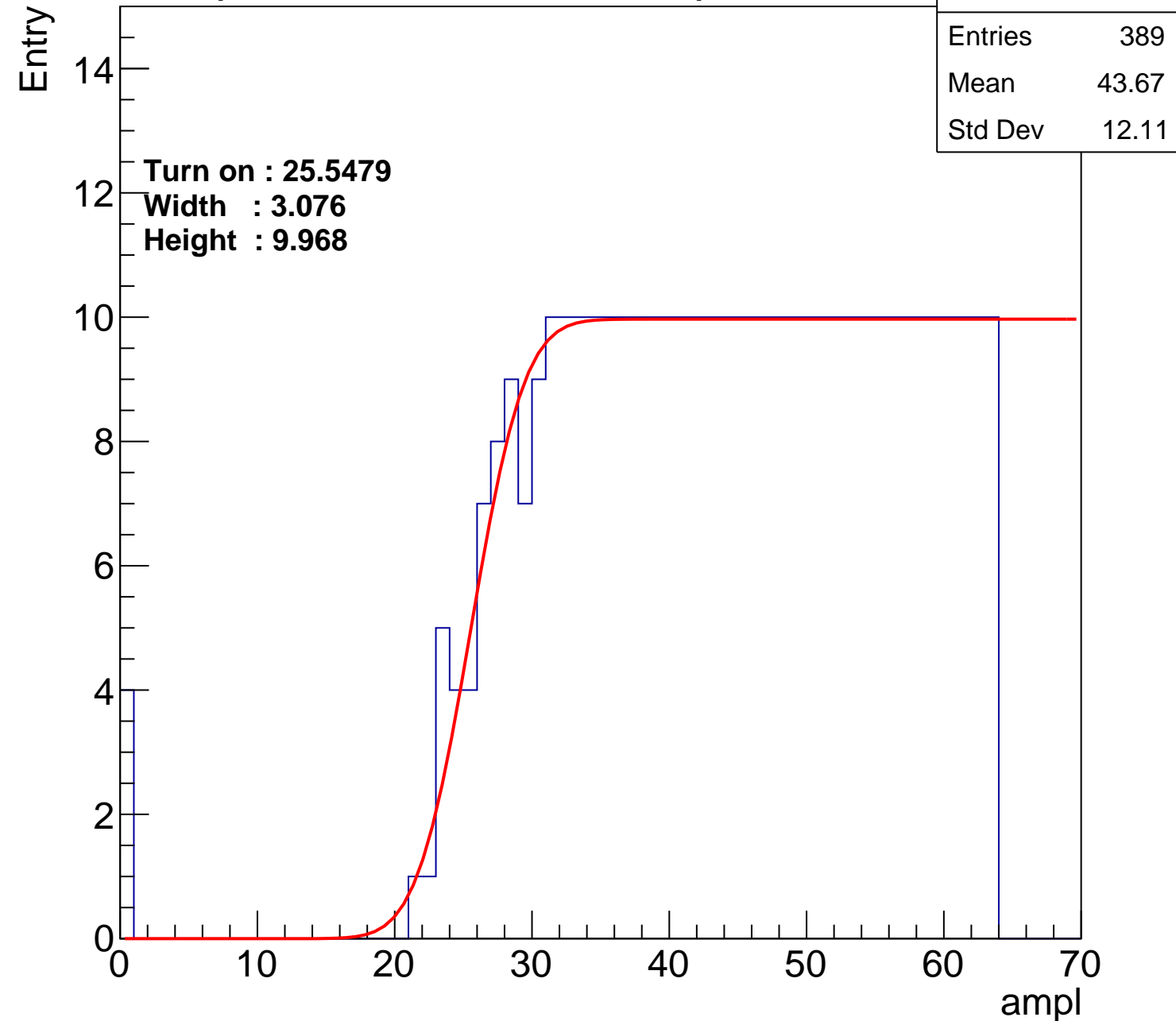
Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.08
Std Dev	11.89

Turn on : 25.4866

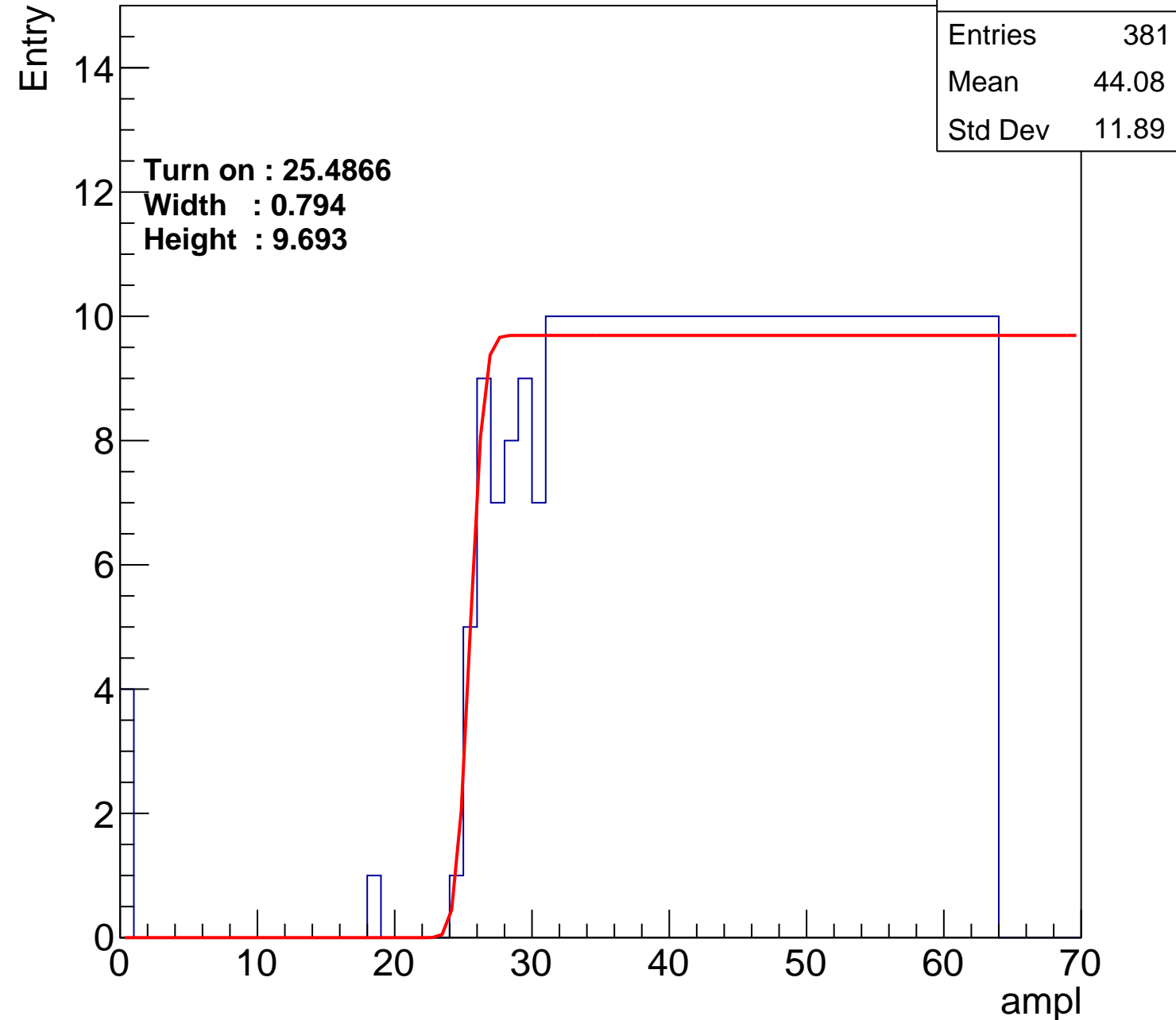
Width : 0.794

Height : 9.693

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch100

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	44.01
Std Dev	11.54

Turn on : 27.0276

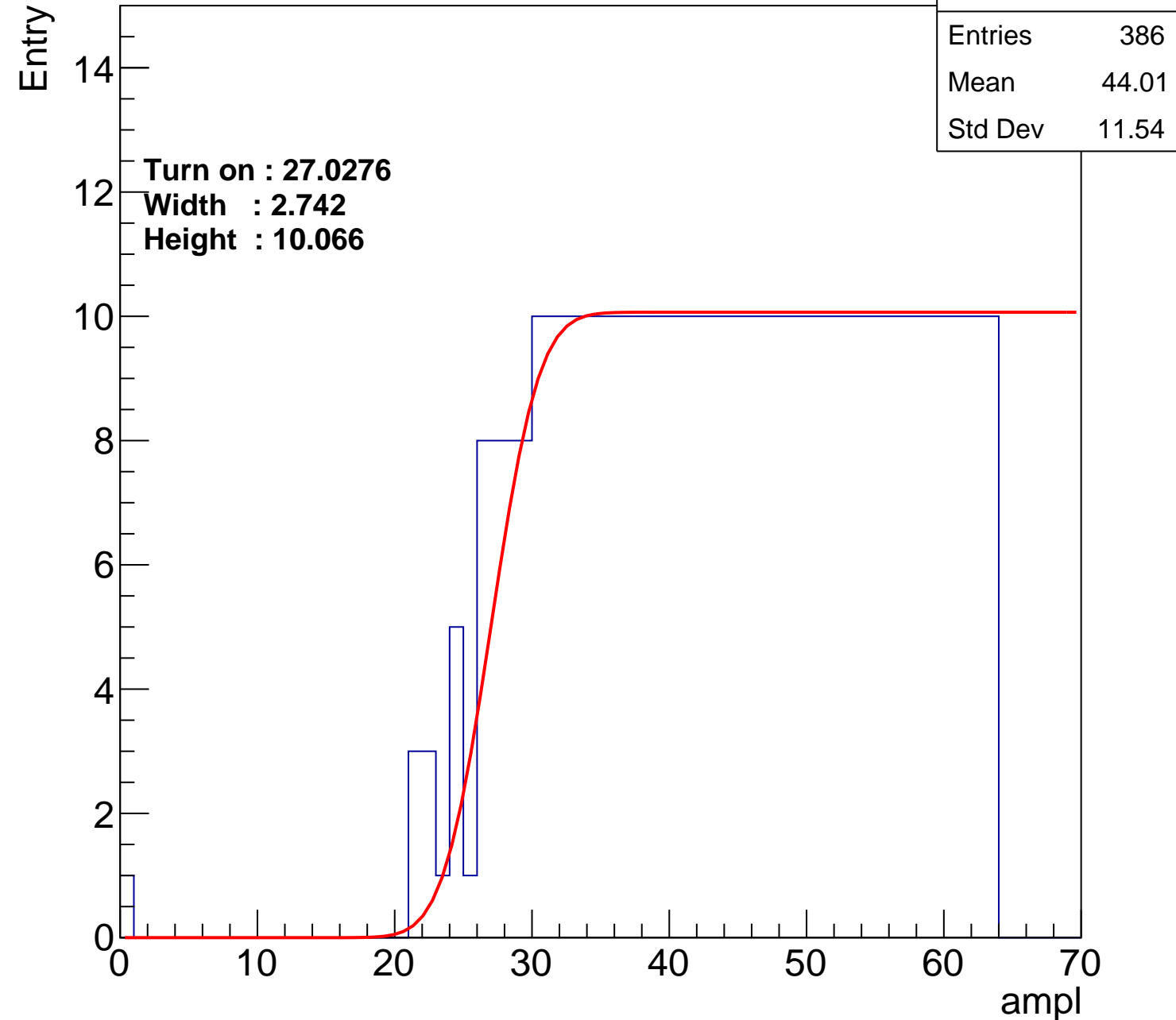
Width : 2.742

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch101

calib_packv5_042523_0143.root, FC#7, port C2

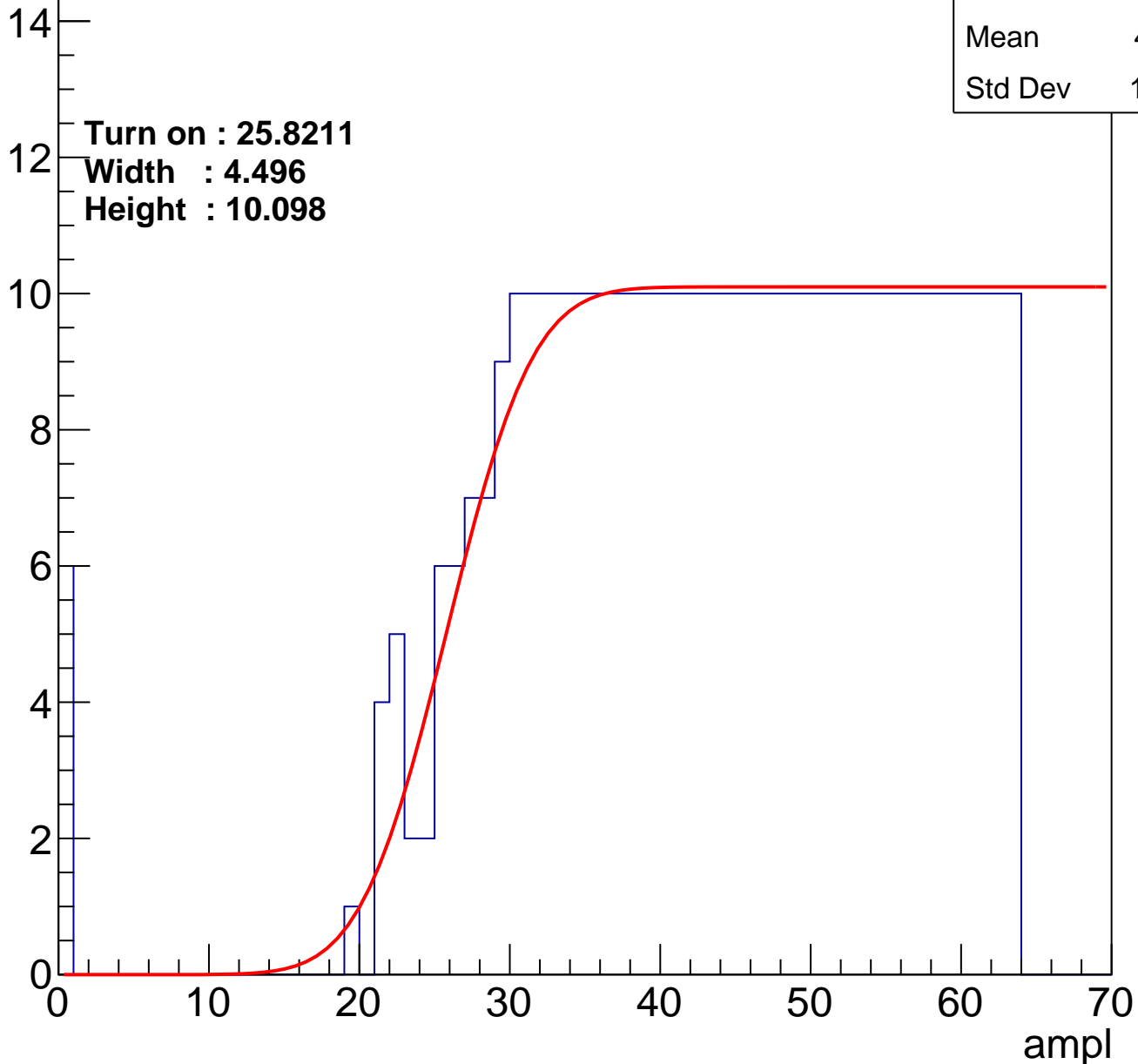
Entries	395
Mean	43.21
Std Dev	12.63

Turn on : 25.8211

Width : 4.496

Height : 10.098

Entry



B1L103S, U17-ch102

calib_packv5_042523_0143.root, FC#7, port C2

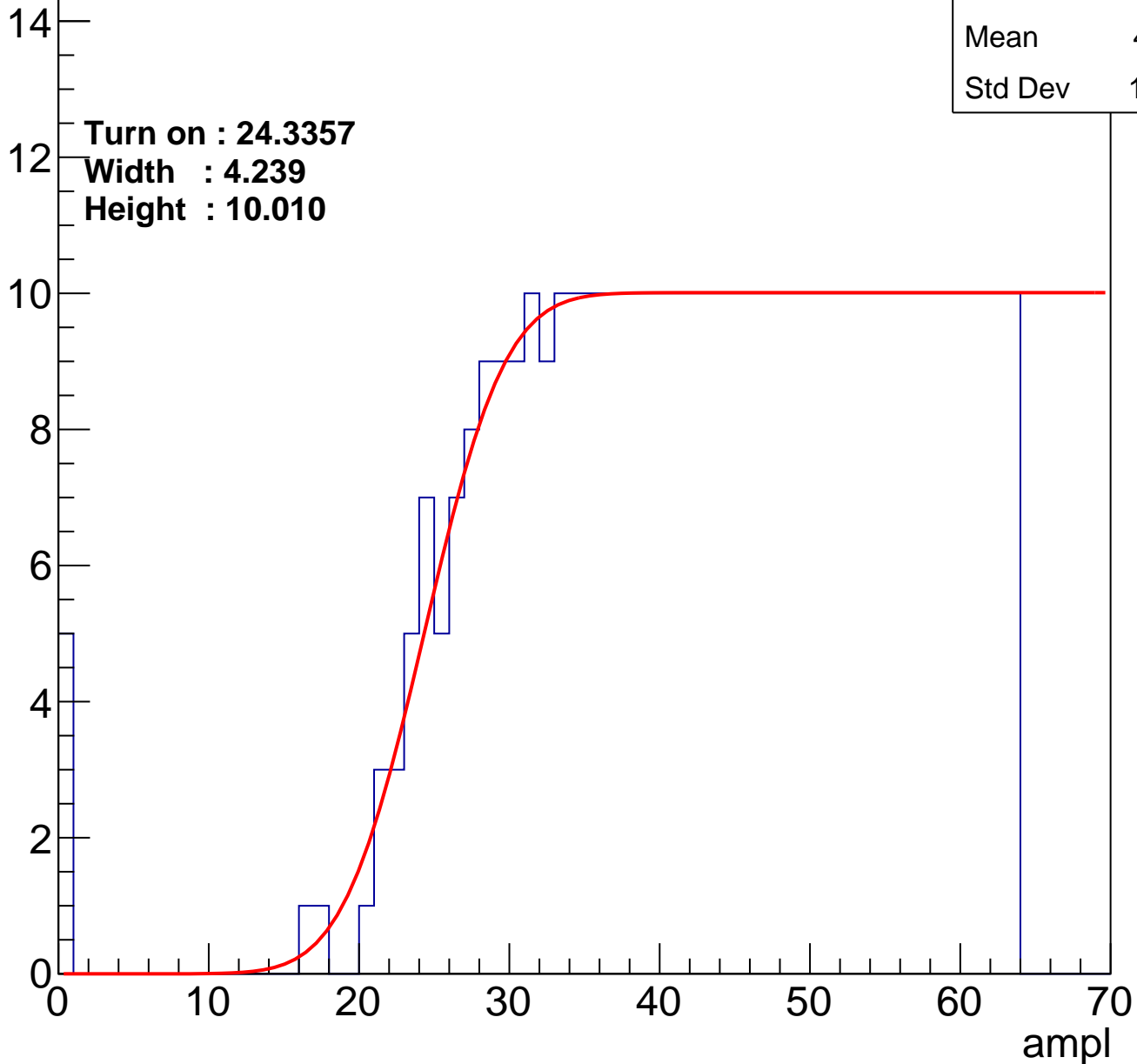
Entries	402
Mean	42.91
Std Dev	12.67

Turn on : 24.3357

Width : 4.239

Height : 10.010

Entry



B1L103S, U17-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.52
Std Dev	12.47

Turn on : 25.9032

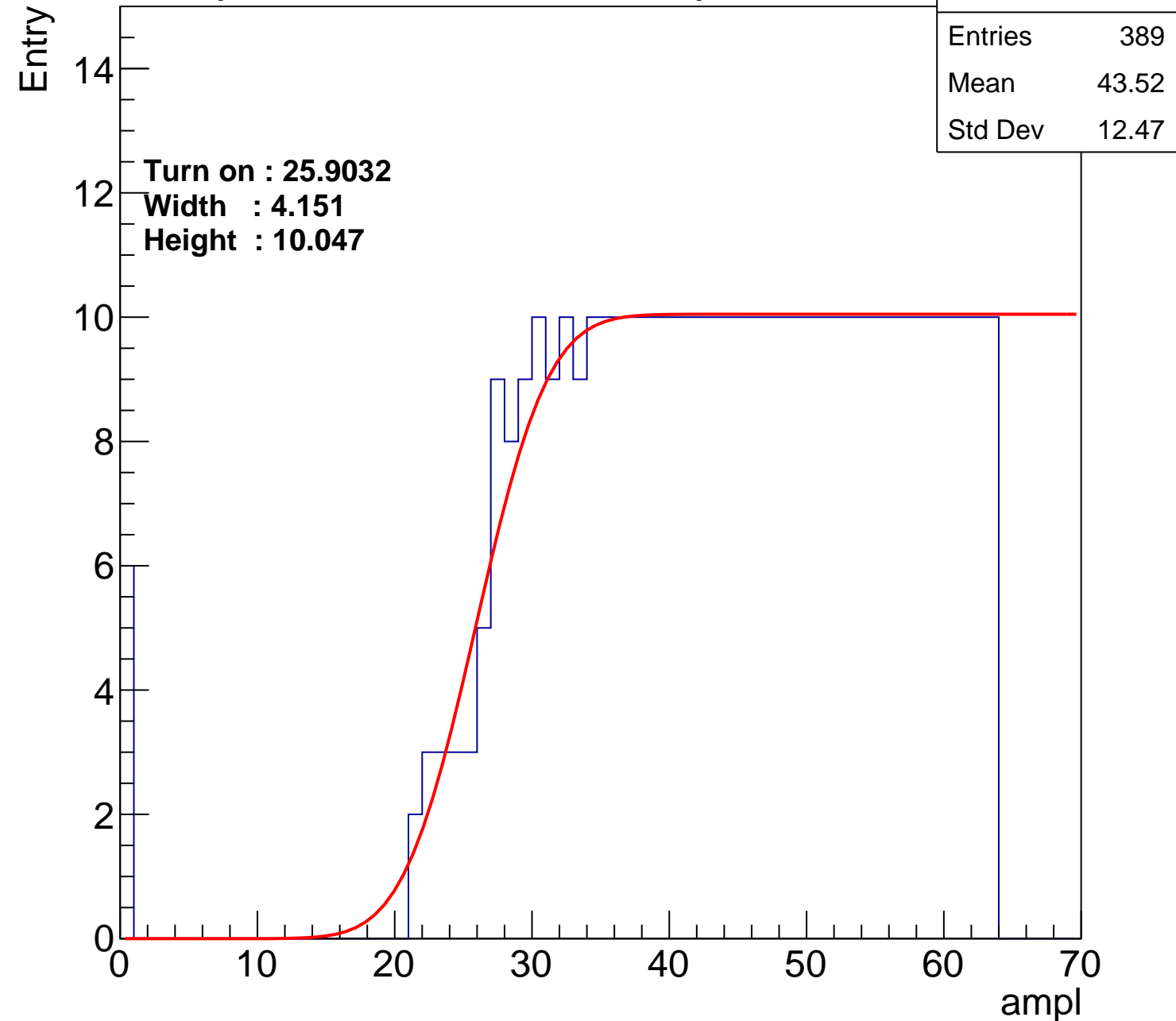
Width : 4.151

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch104

calib_packv5_042523_0143.root, FC#7, port C2

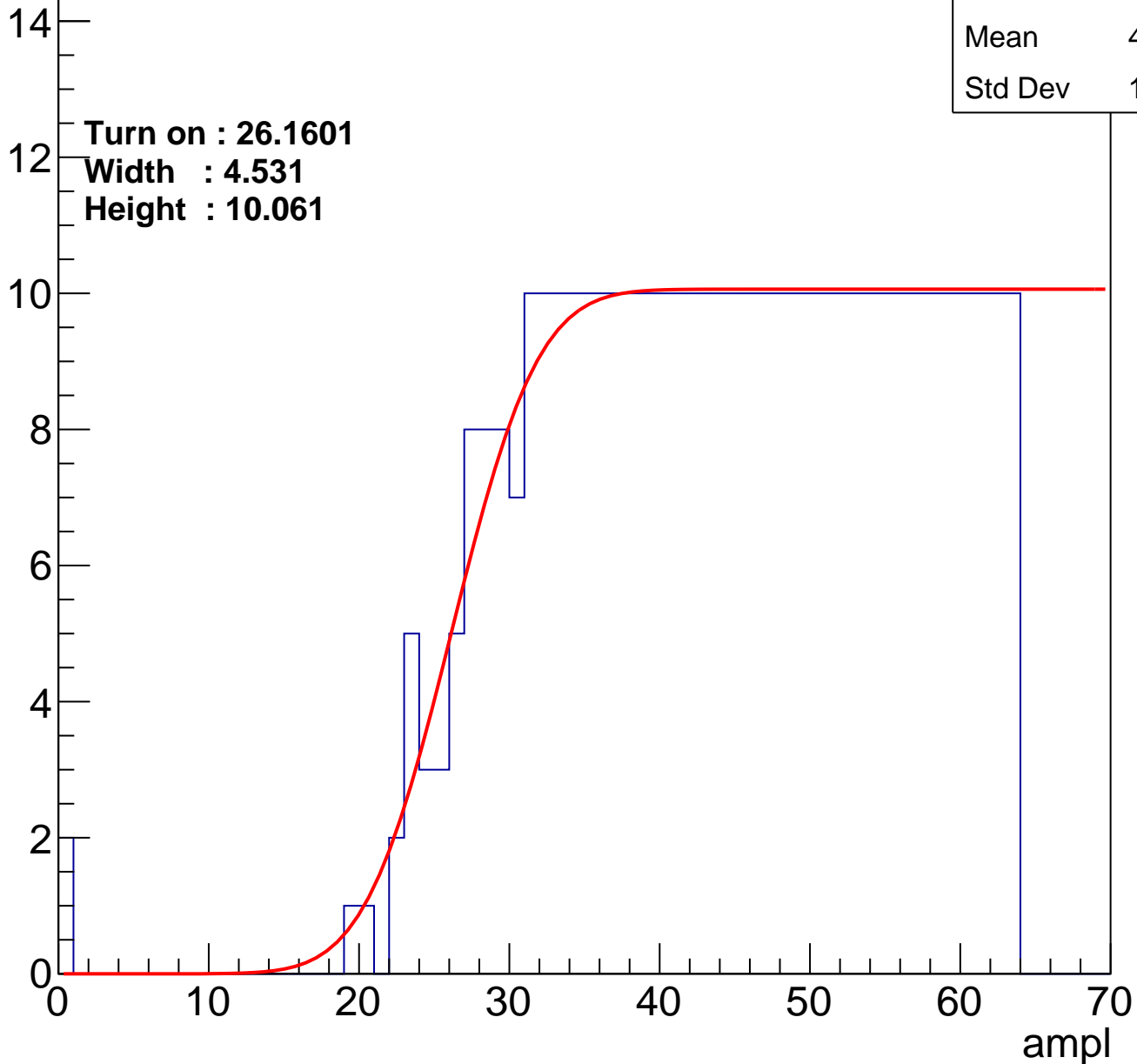
Entries	383
Mean	44.04
Std Dev	11.72

Turn on : 26.1601

Width : 4.531

Height : 10.061

Entry



B1L103S, U17-ch105

calib_packv5_042523_0143.root, FC#7, port C2

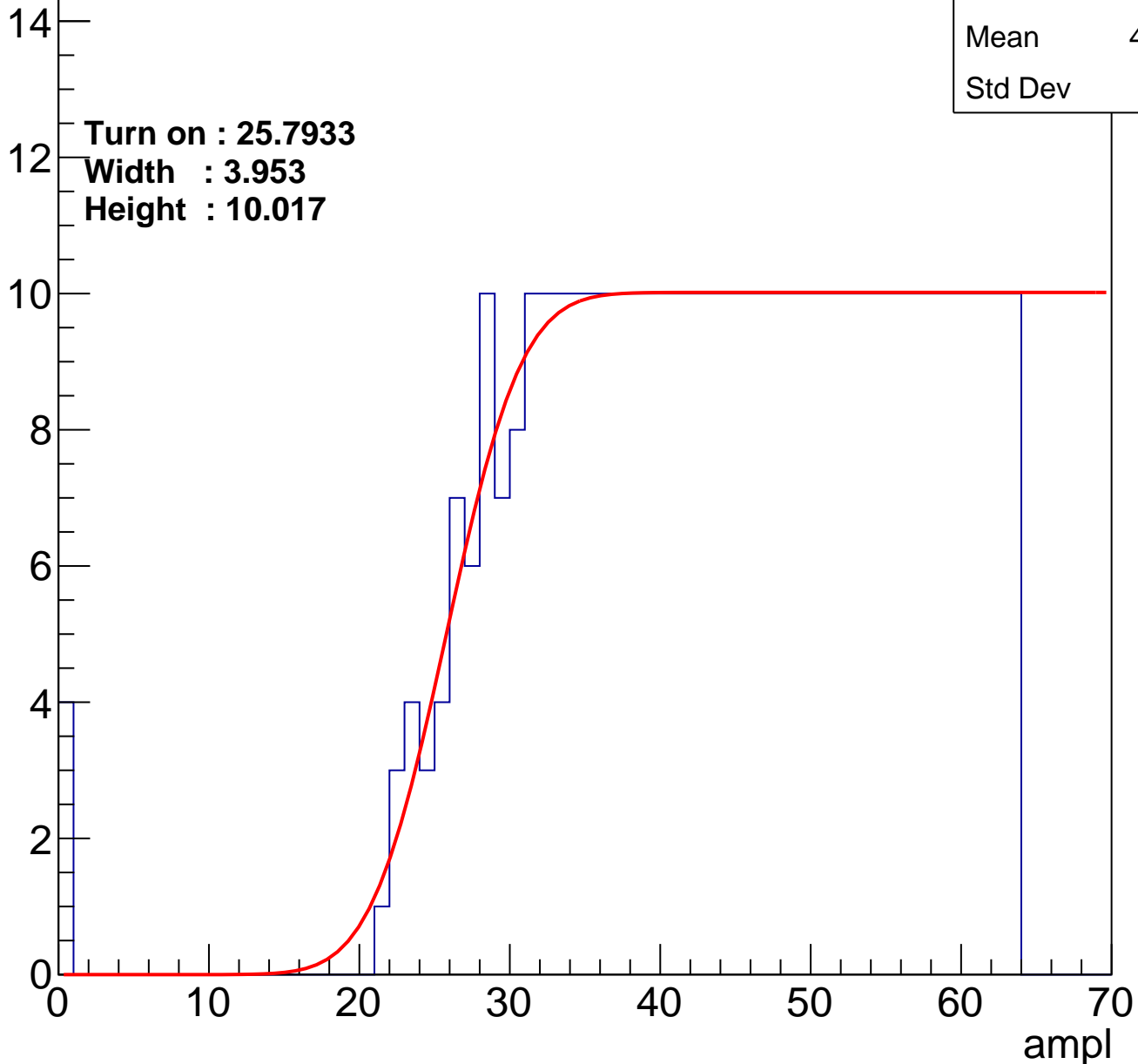
Entries	387
Mean	43.74
Std Dev	12.1

Turn on : 25.7933

Width : 3.953

Height : 10.017

Entry



B1L103S, U17-ch106

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	43.93
Std Dev	12.26

Turn on : 27.0537

Width : 3.148

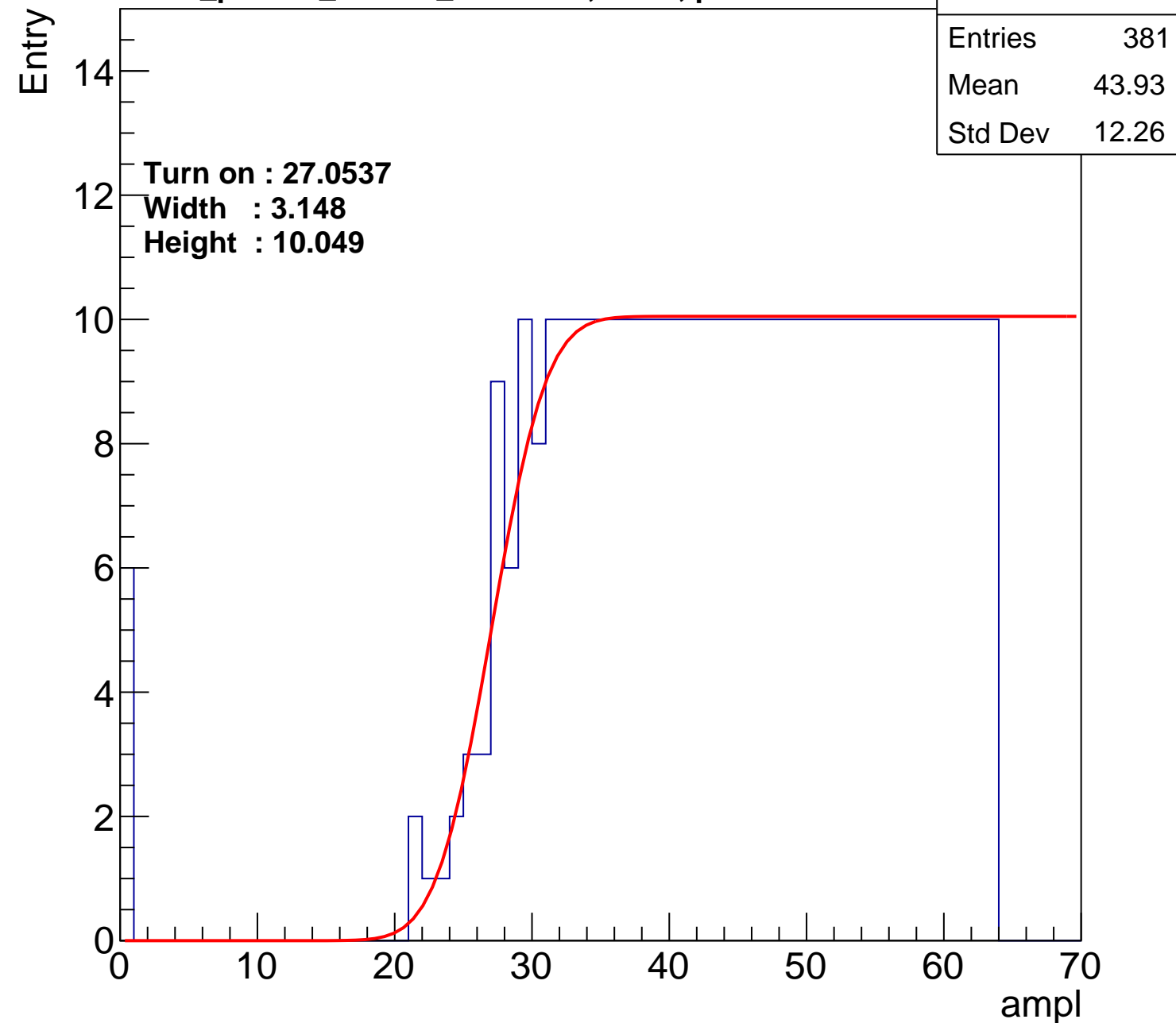
Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.86
Std Dev	11.39

Turn on : 27.9958

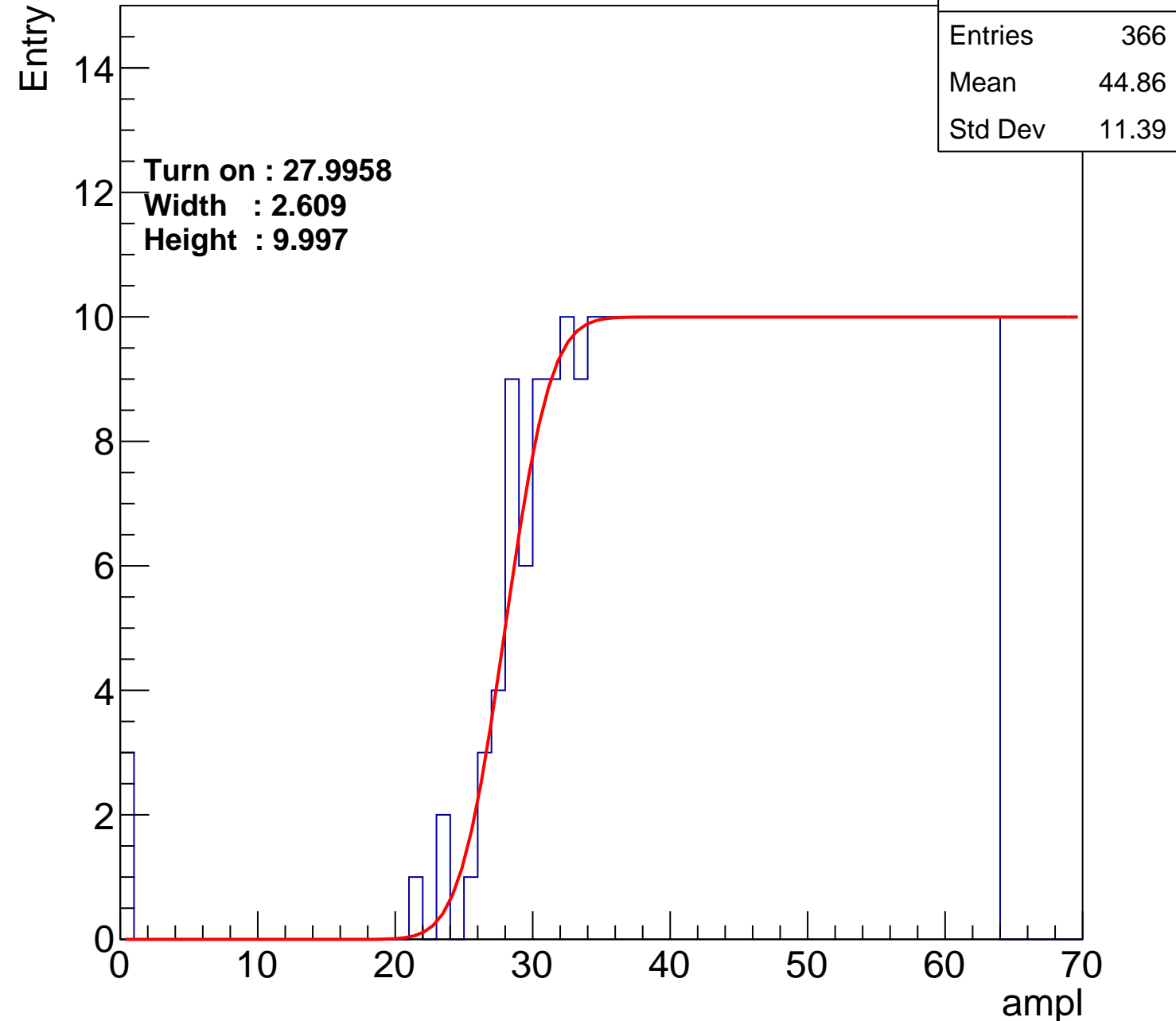
Width : 2.609

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.2
Std Dev	12.04

Turn on : 27.0931

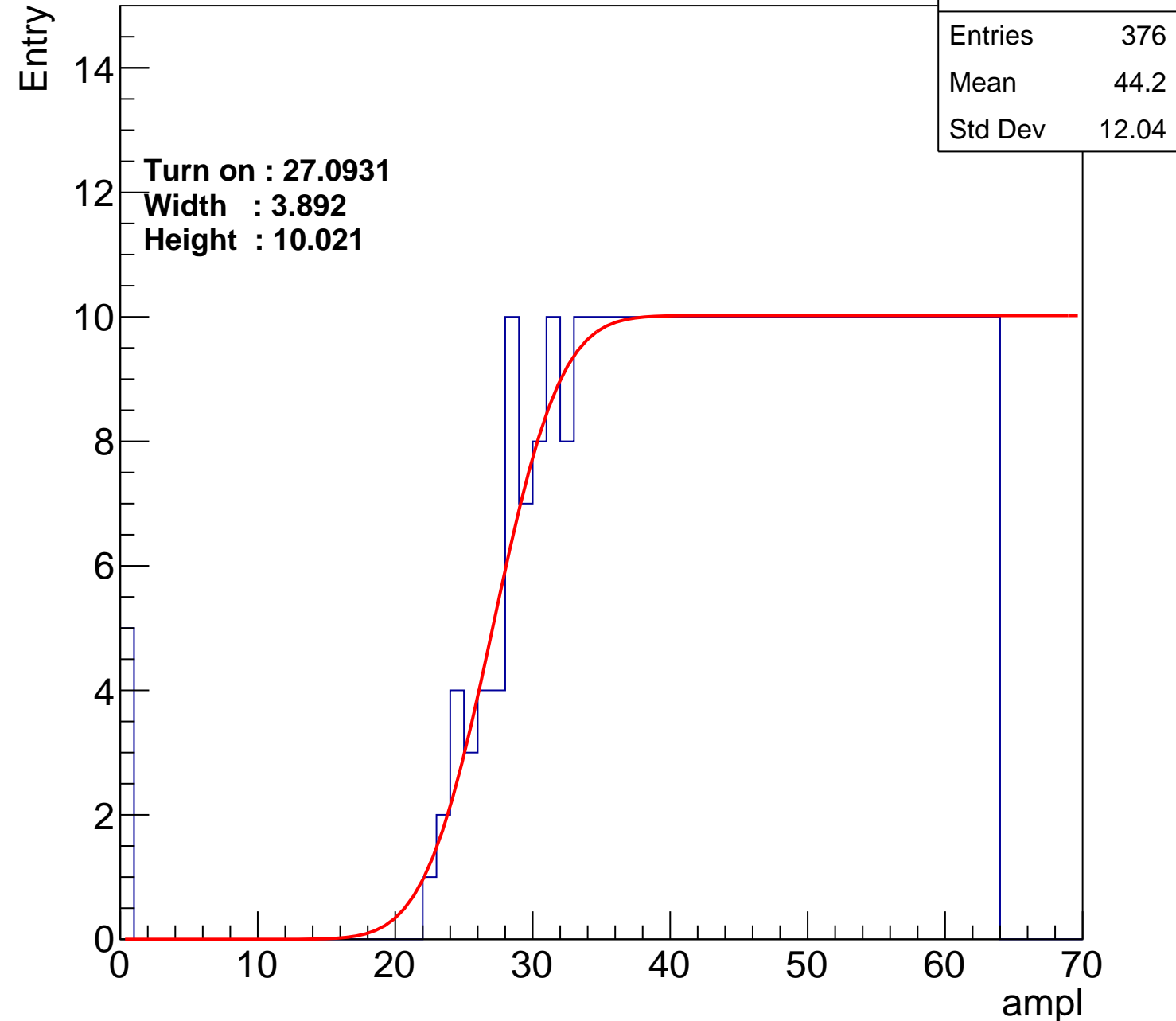
Width : 3.892

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch109

calib_packv5_042523_0143.root, FC#7, port C2

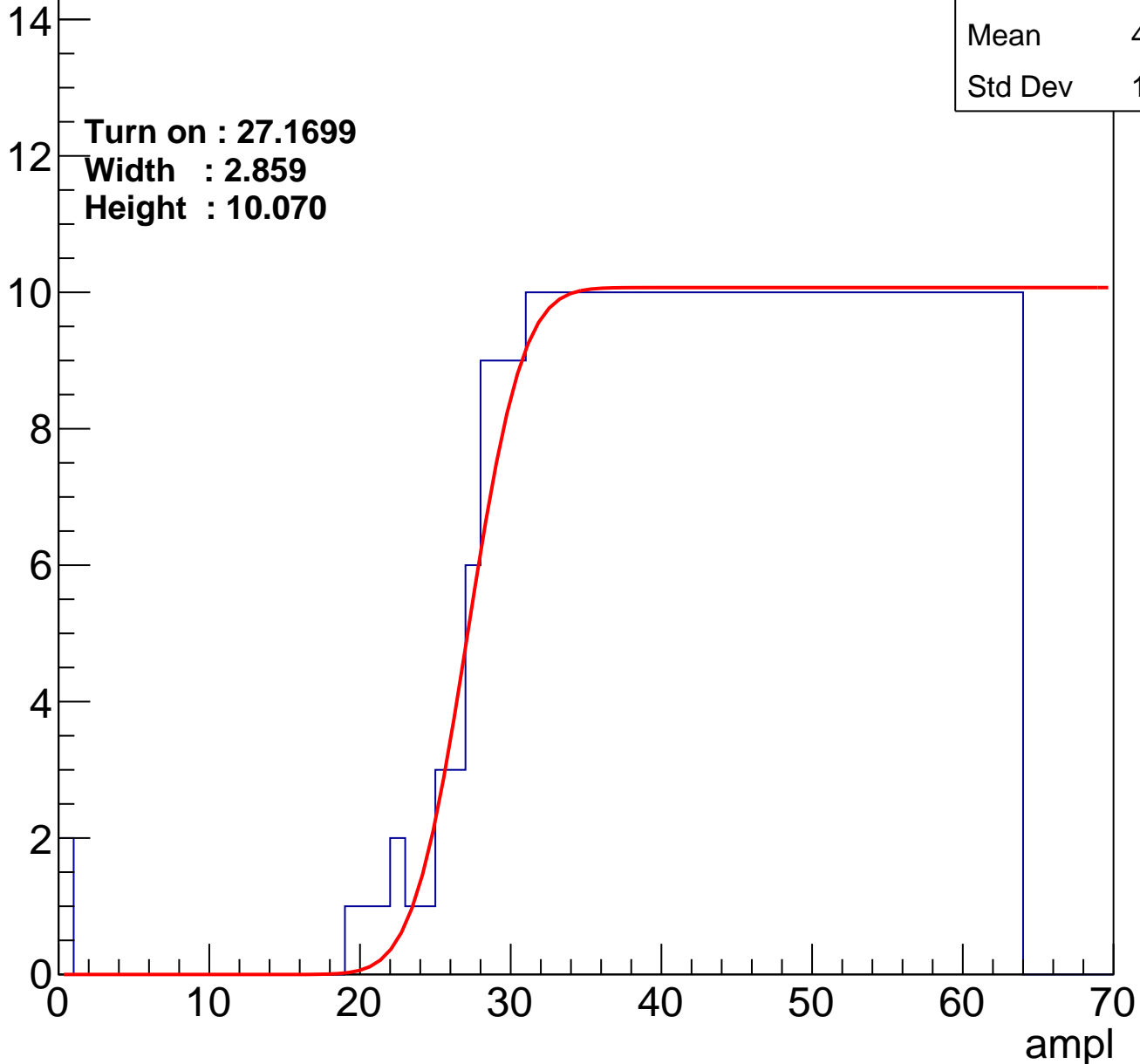
Entries	378
Mean	44.34
Std Dev	11.52

Turn on : 27.1699

Width : 2.859

Height : 10.070

Entry



B1L103S, U17-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.81
Std Dev	11.98

Turn on : 26.2984

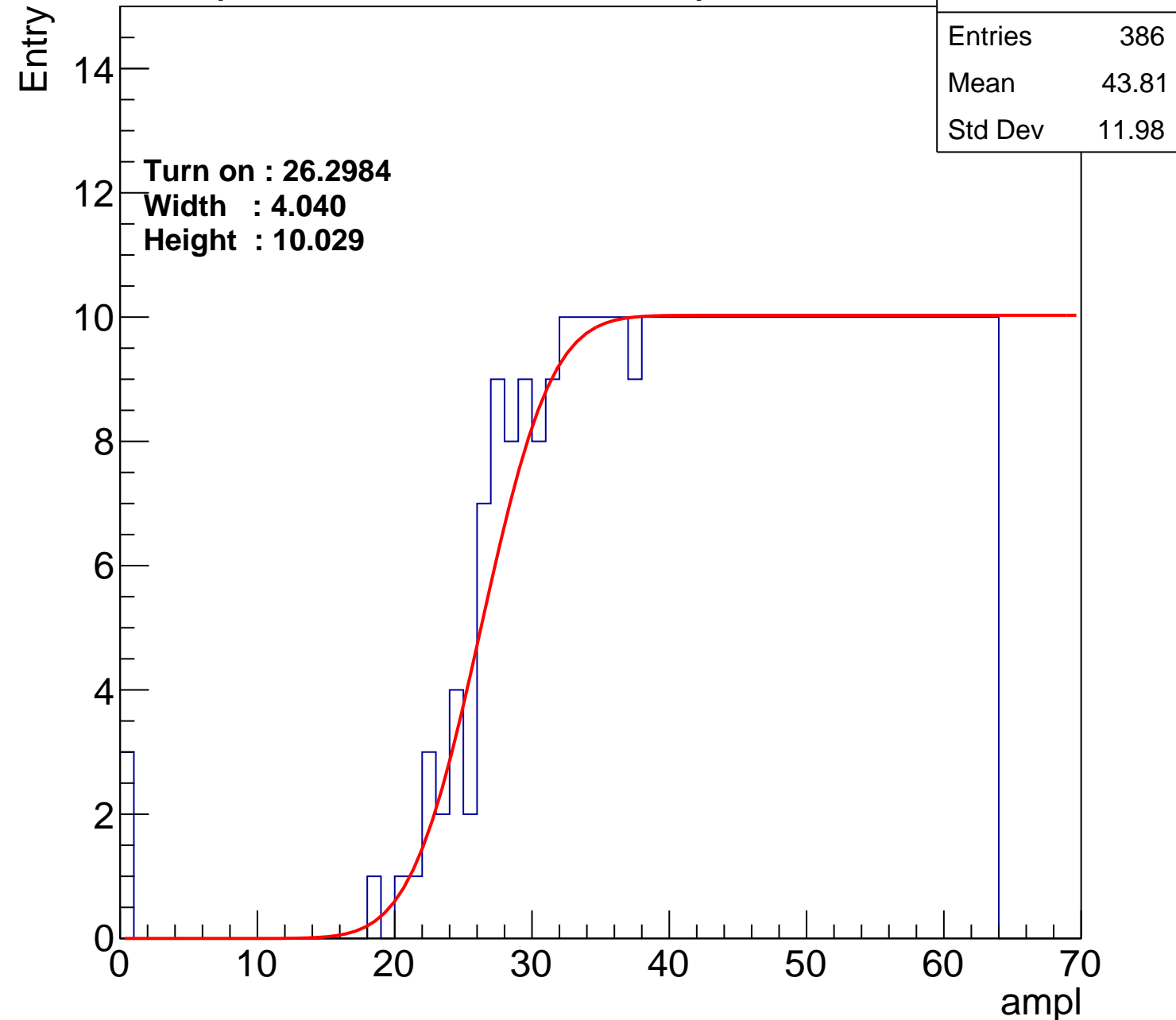
Width : 4.040

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.95
Std Dev	11.57

Turn on : 25.3516

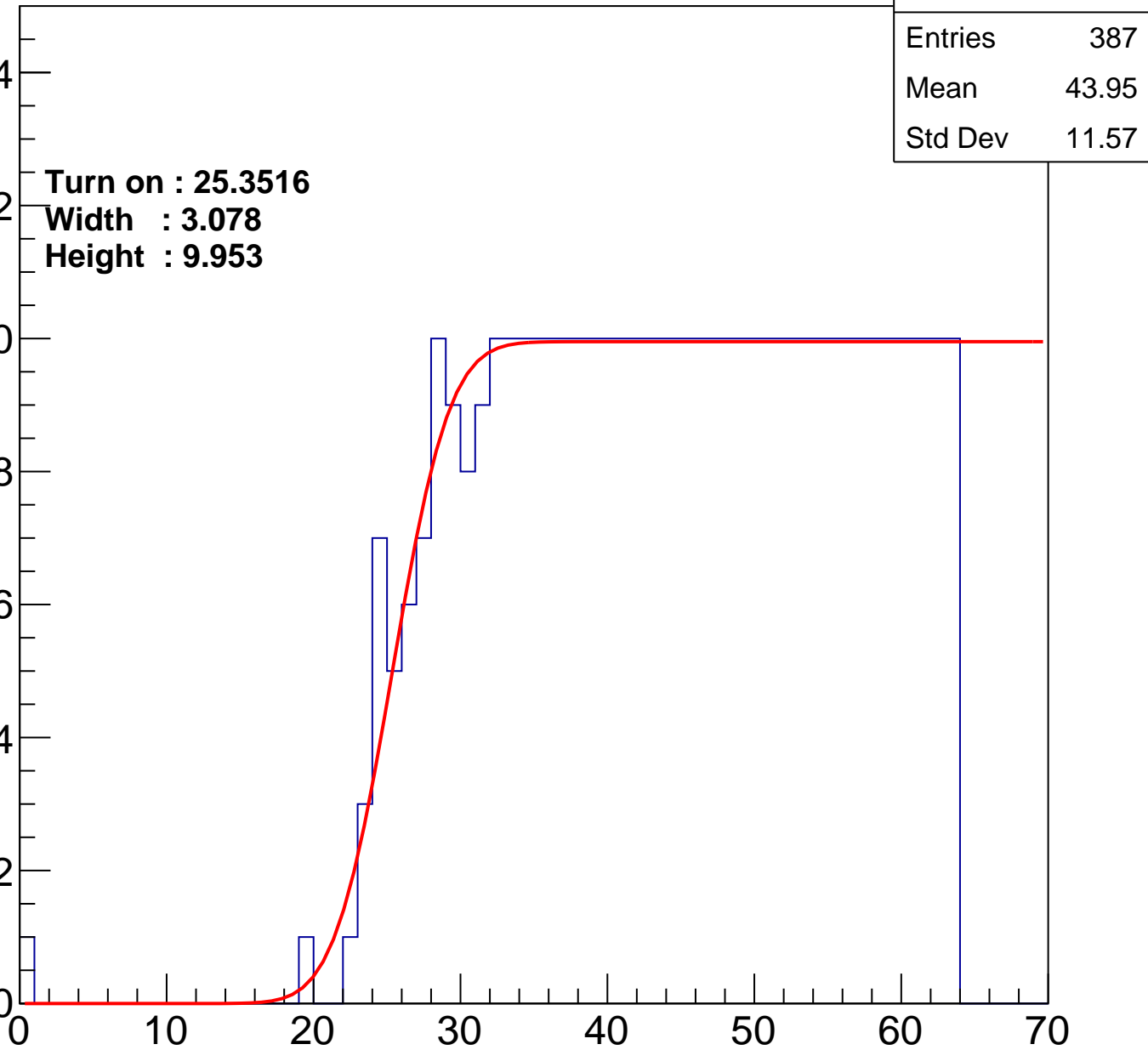
Width : 3.078

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.54
Std Dev	12.15

Turn on : 28.2144

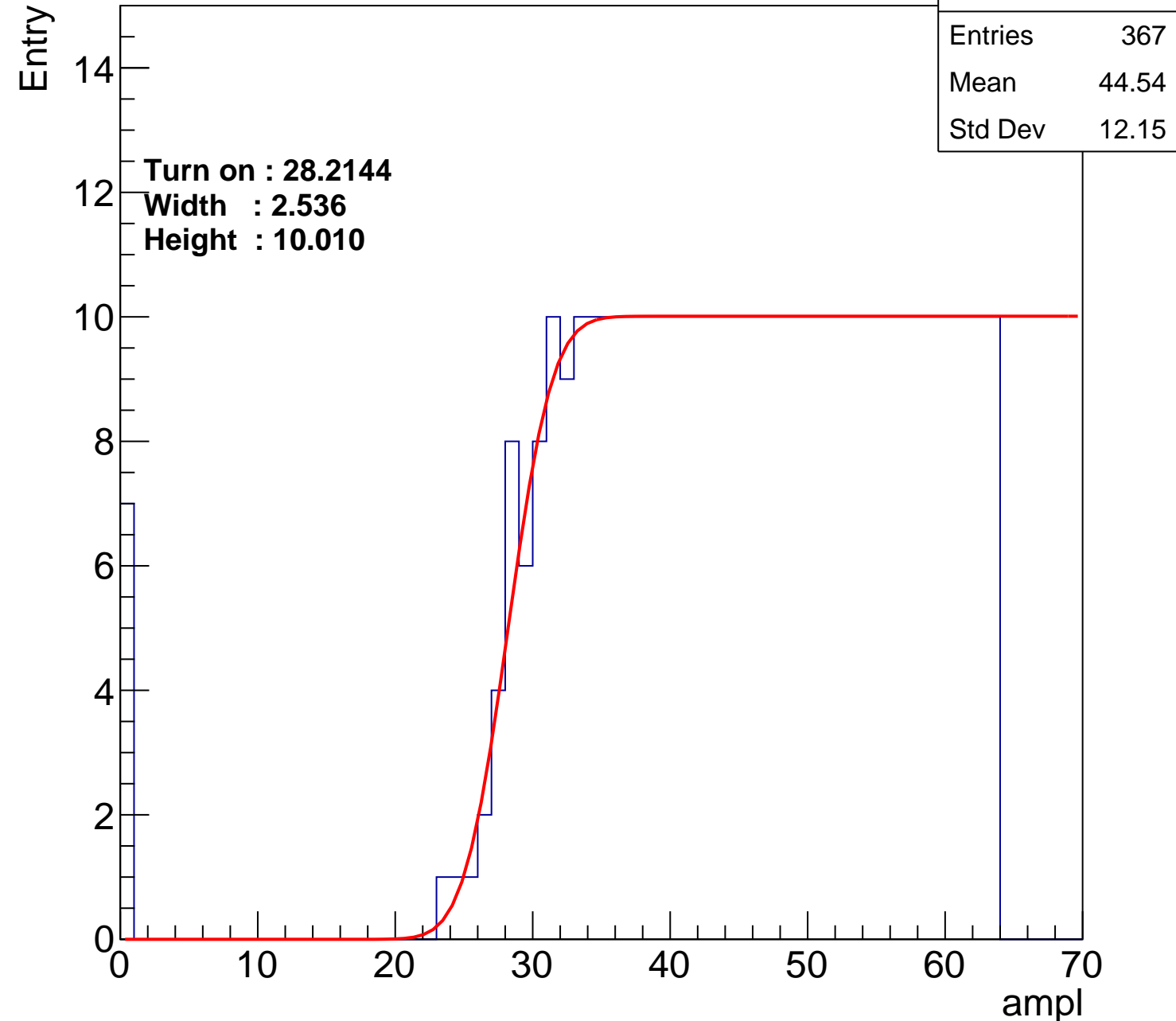
Width : 2.536

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.24
Std Dev	12.17

Turn on : 27.2801

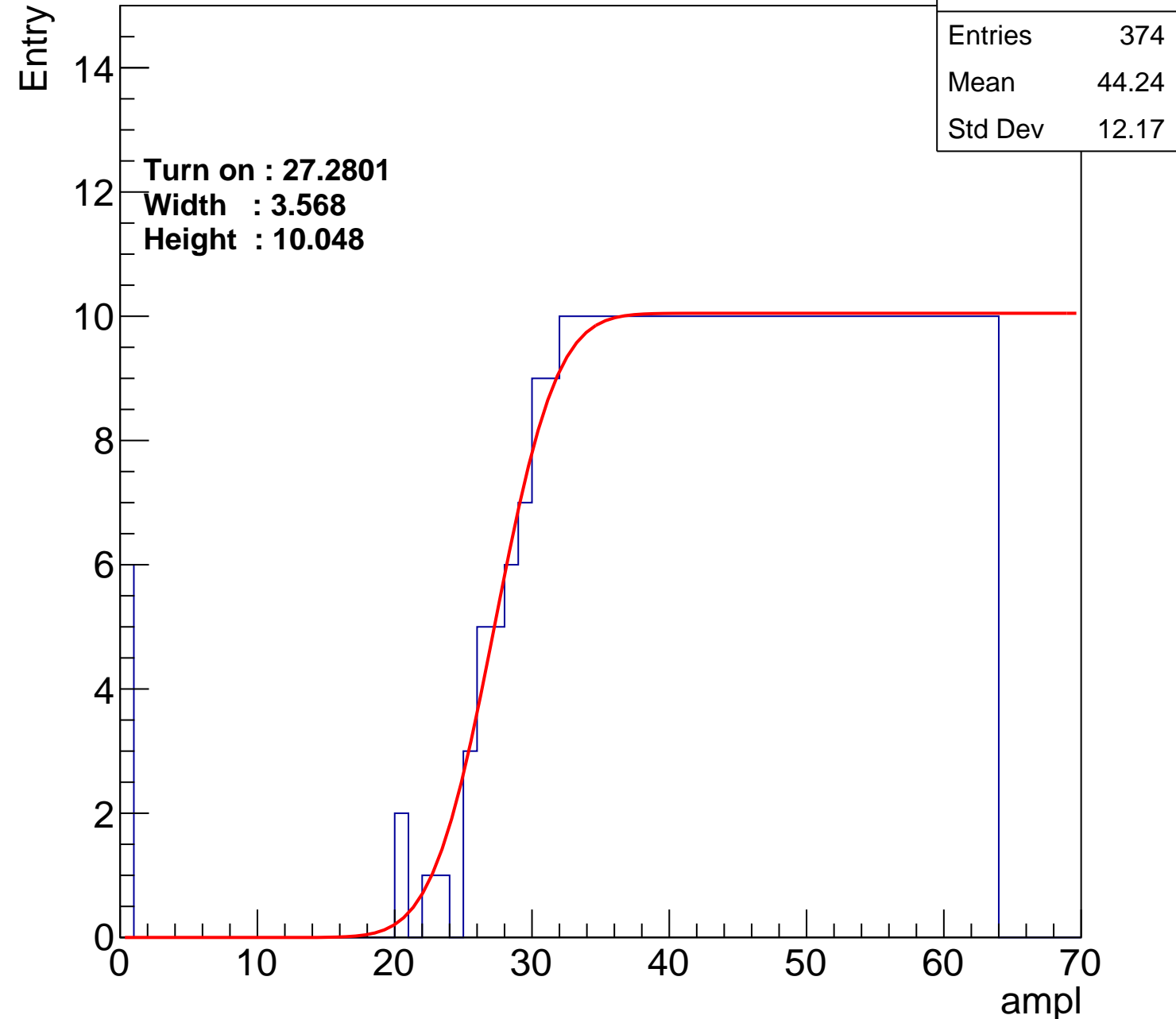
Width : 3.568

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.31
Std Dev	11.91

Turn on : 24.8238

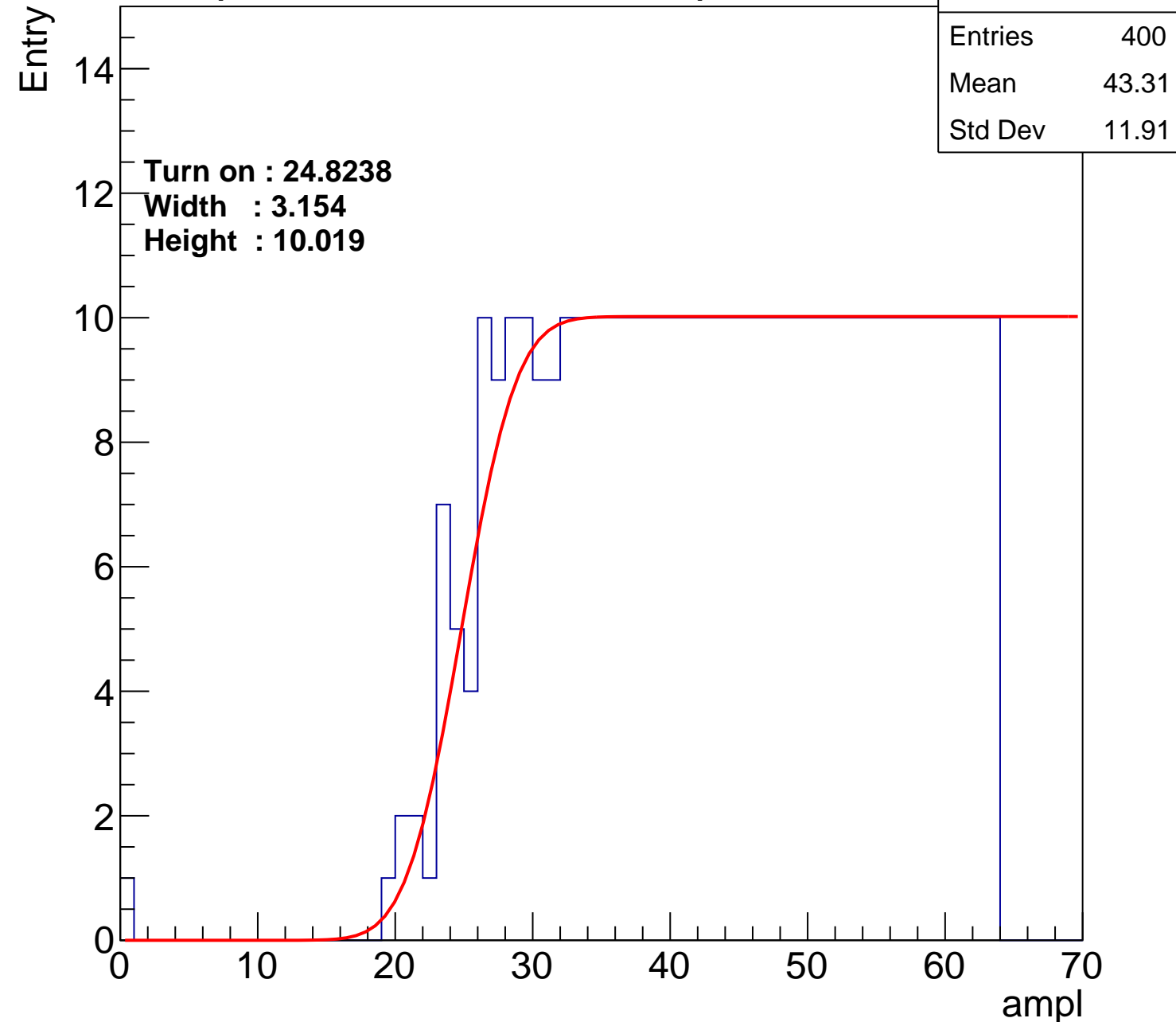
Width : 3.154

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.24
Std Dev	12.79

Turn on : 25.8566

Width : 4.739

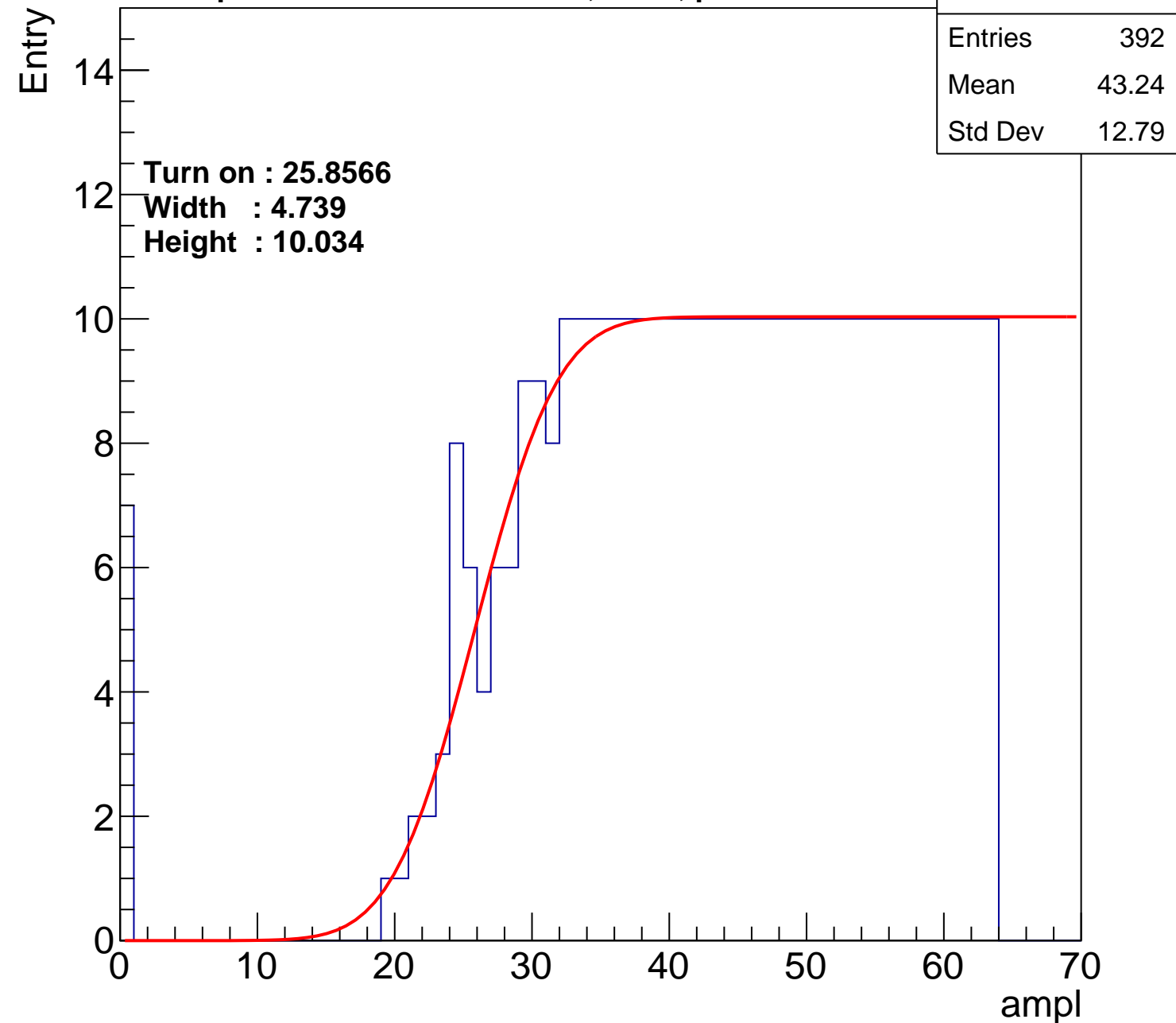
Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.75
Std Dev	11.46

Turn on : 27.5498

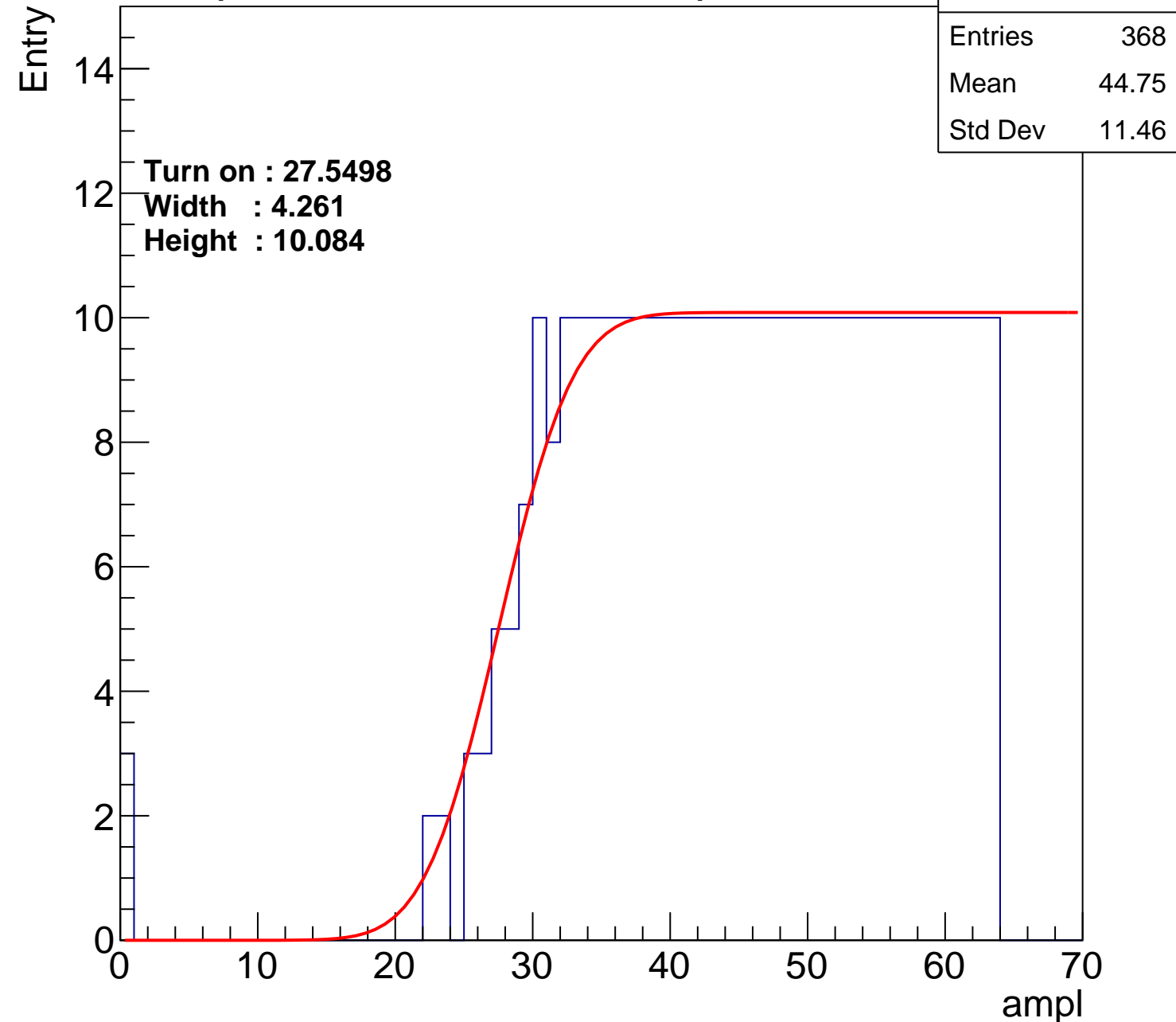
Width : 4.261

Height : 10.084

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	377
Mean	44.4
Std Dev	11.46

Turn on : 27.4301

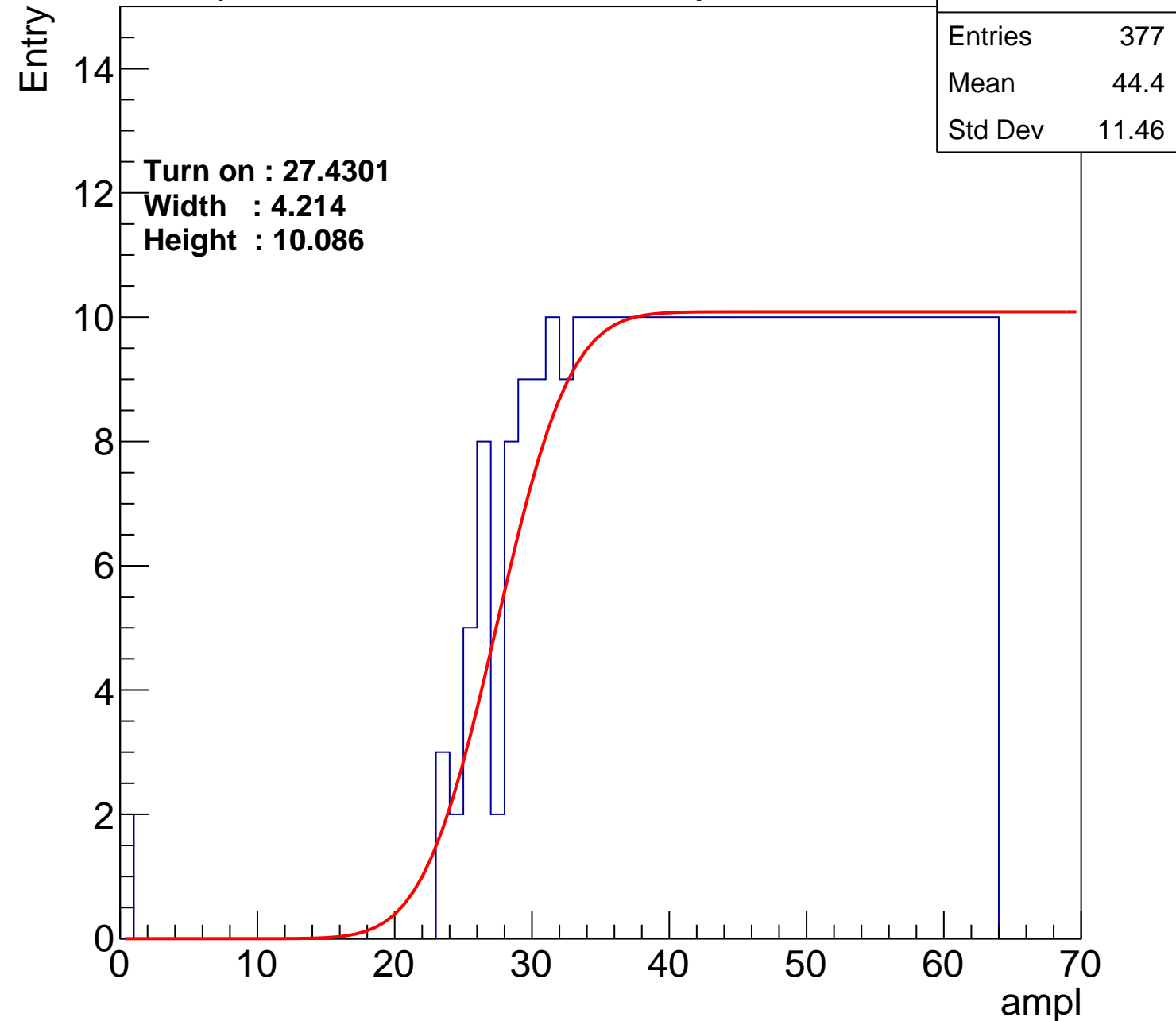
Width : 4.214

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	411
Mean	42.26
Std Dev	13.44

Turn on : 24.0606

Width : 3.014

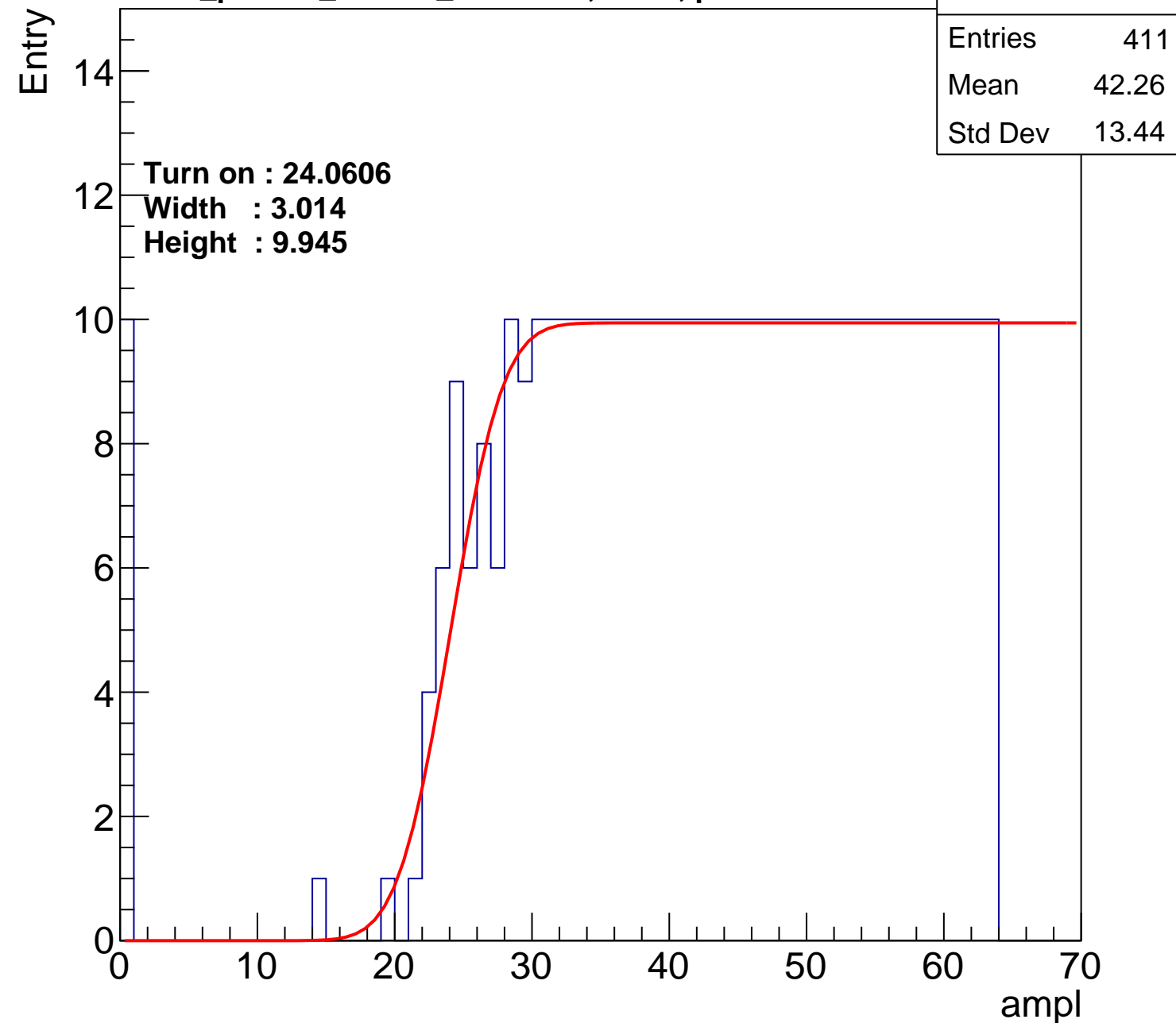
Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U17-ch119

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	43.13
Std Dev	12.43

Turn on : 24.3731

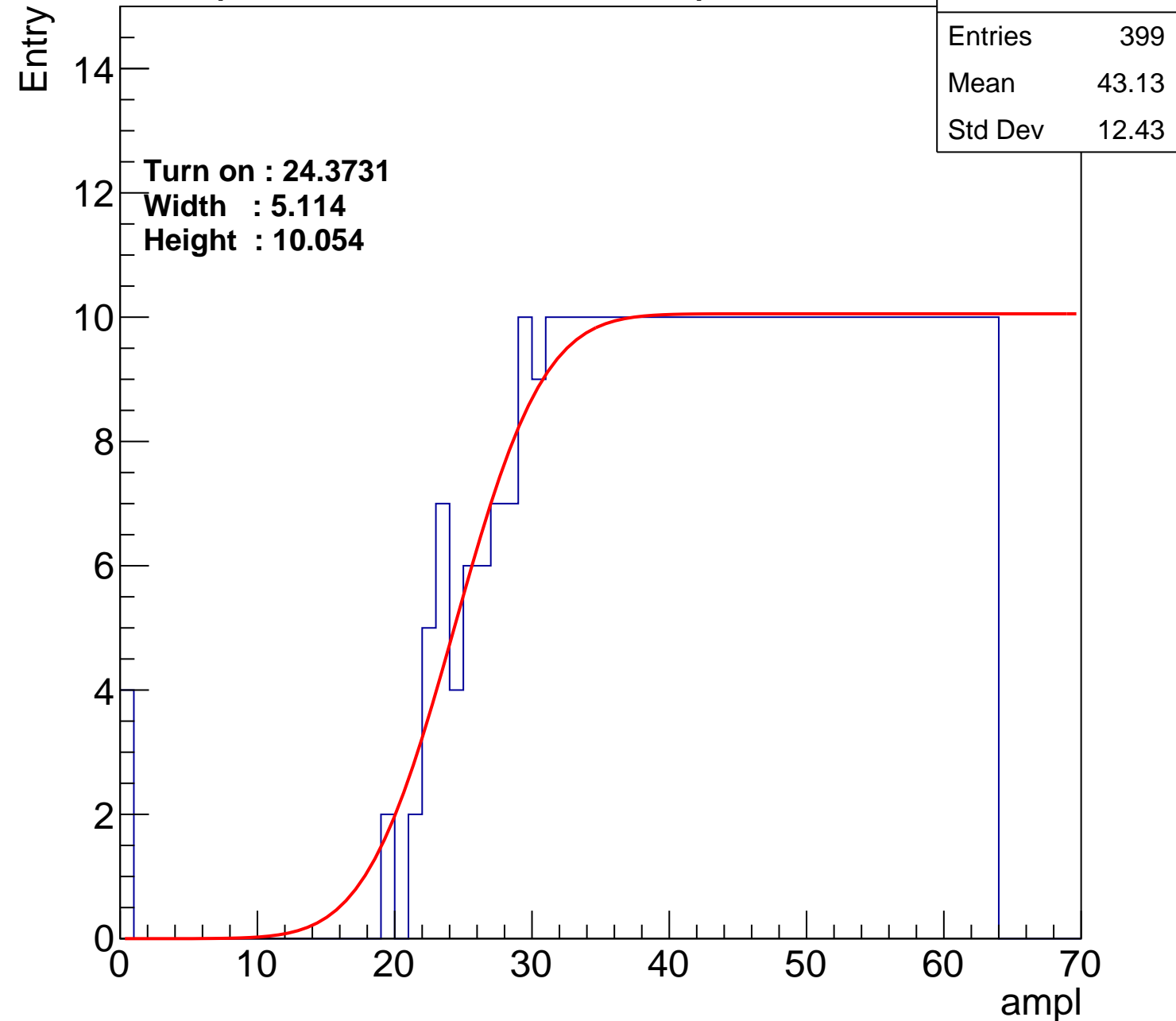
Width : 5.114

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch120

calib_packv5_042523_0143.root, FC#7, port C2

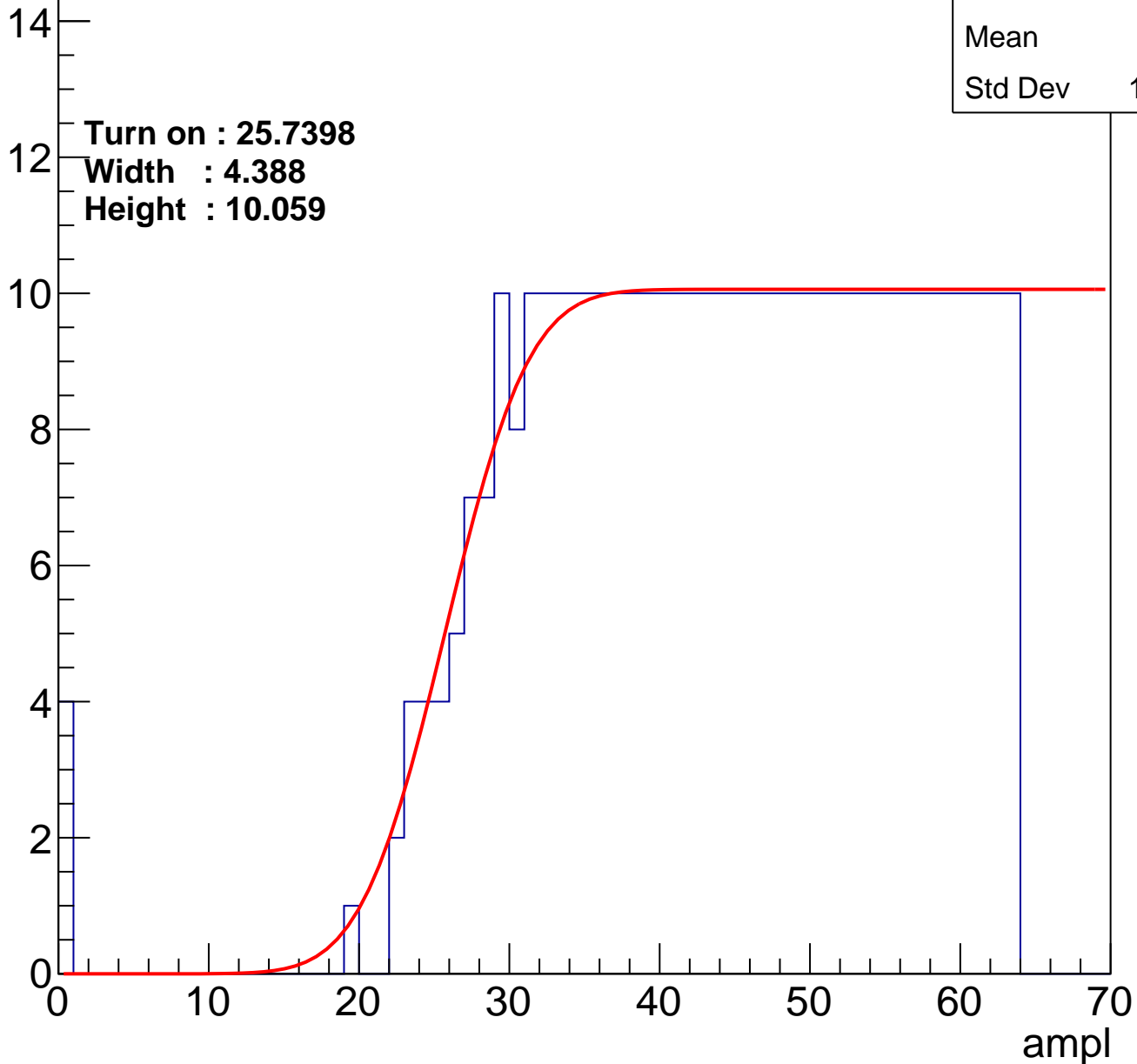
Entries	386
Mean	43.8
Std Dev	12.07

Turn on : 25.7398

Width : 4.388

Height : 10.059

Entry



B1L103S, U17-ch121

calib_packv5_042523_0143.root, FC#7, port C2

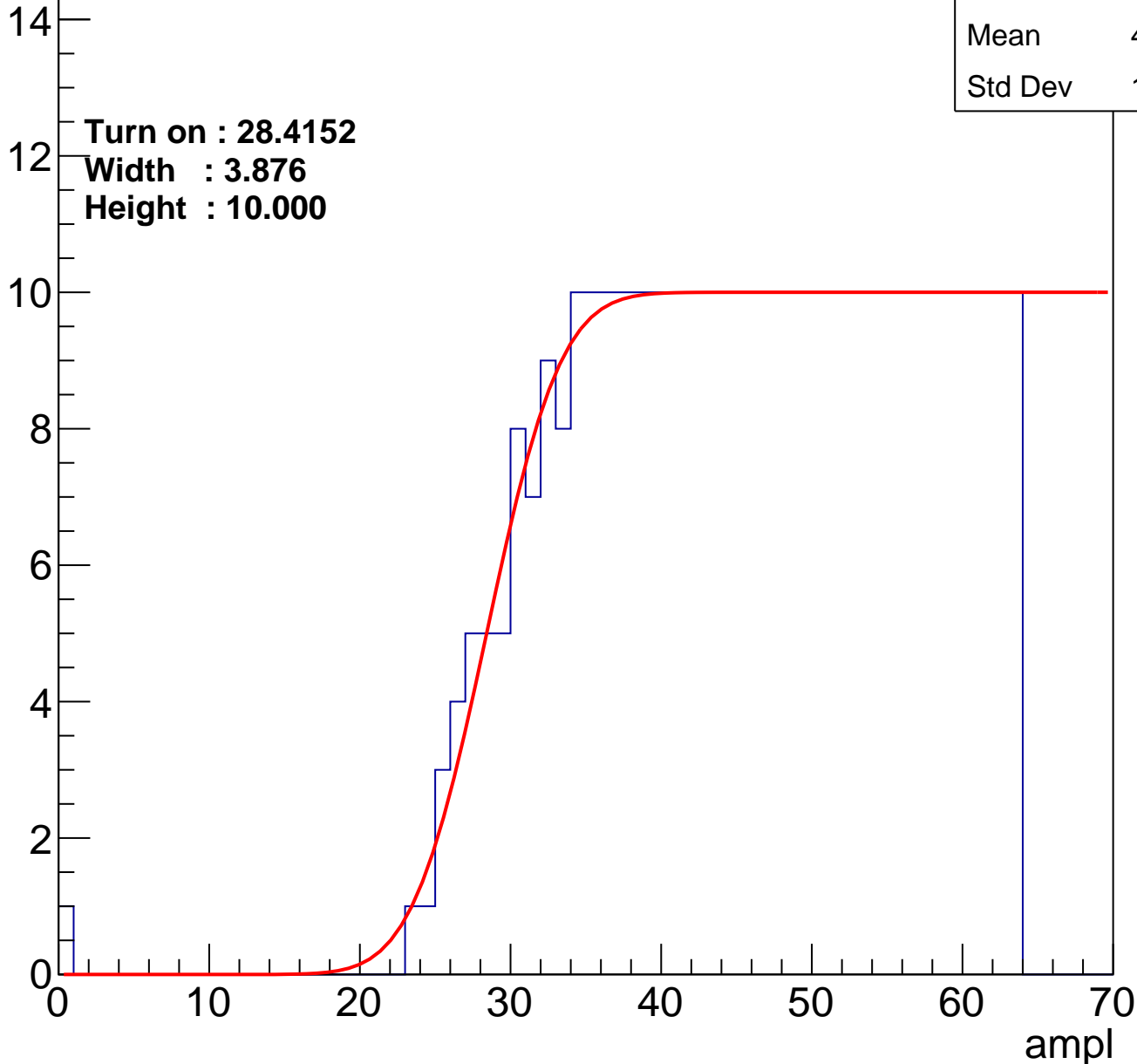
Entries	357
Mean	45.39
Std Dev	10.83

Turn on : 28.4152

Width : 3.876

Height : 10.000

Entry



B1L103S, U17-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	42.98
Std Dev	12.47

Turn on : 24.5560

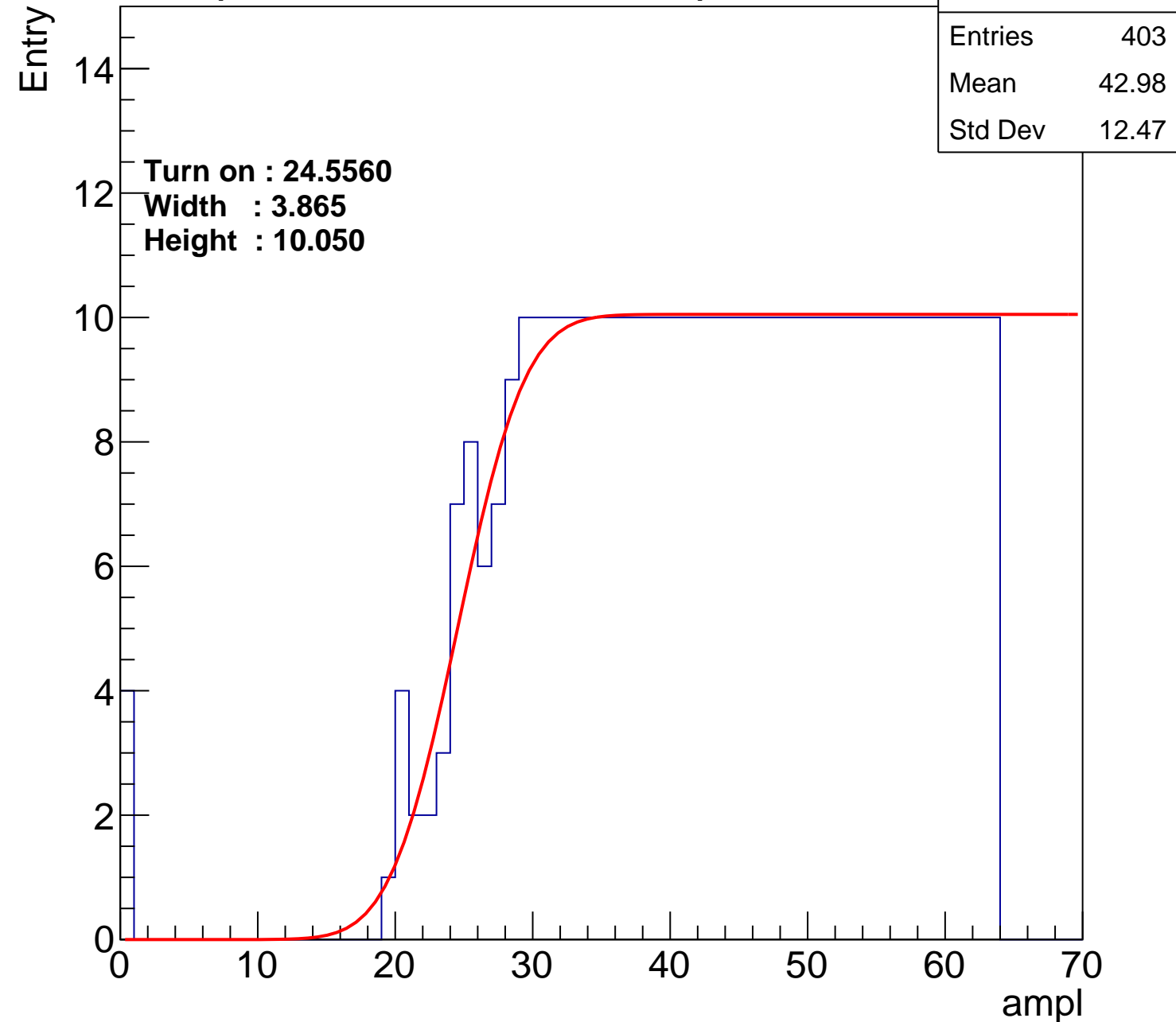
Width : 3.865

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch123

calib_packv5_042523_0143.root, FC#7, port C2

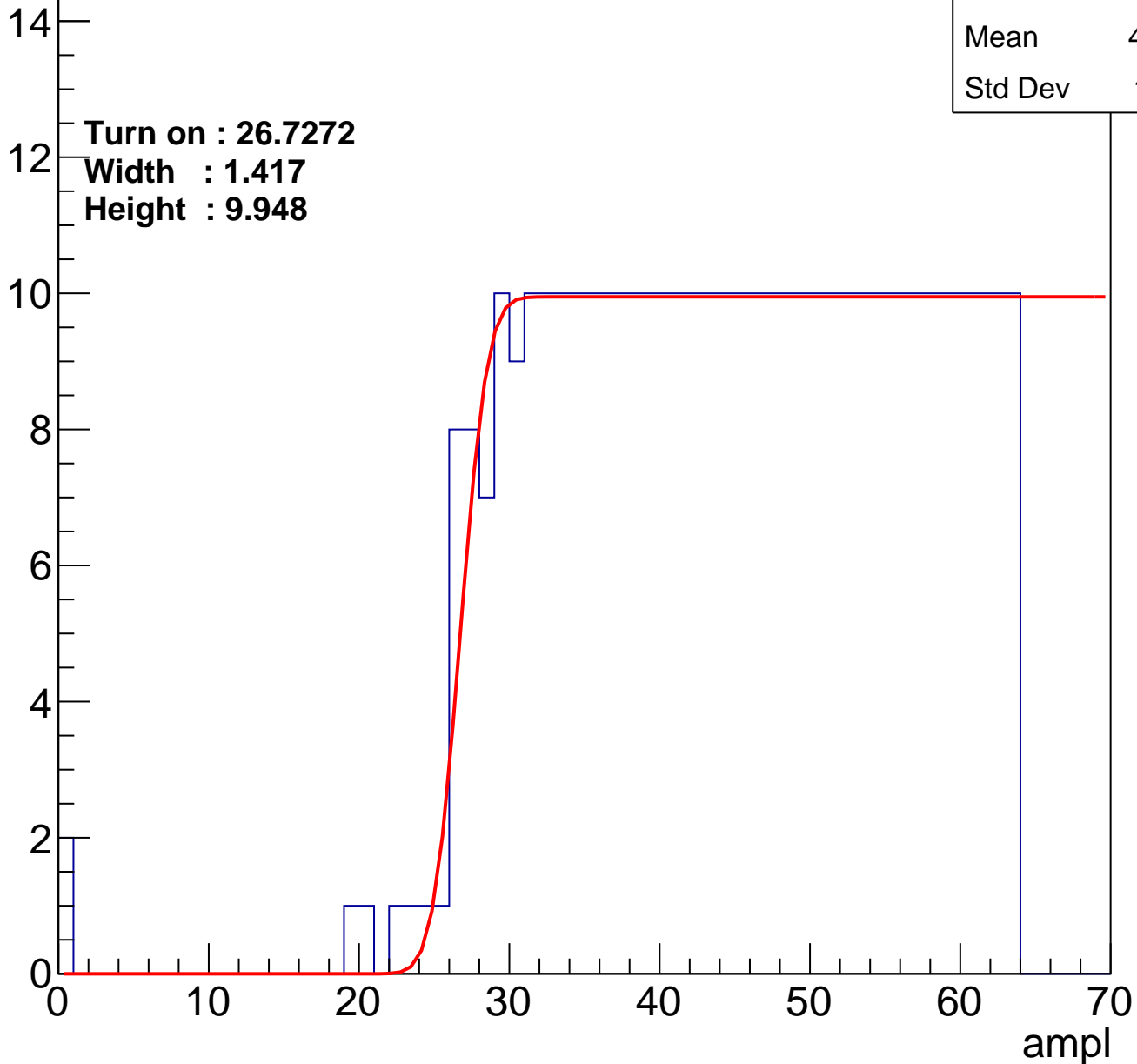
Entries	380
Mean	44.27
Std Dev	11.51

Turn on : 26.7272

Width : 1.417

Height : 9.948

Entry



B1L103S, U17-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.49
Std Dev	12.47

Turn on : 25.9952

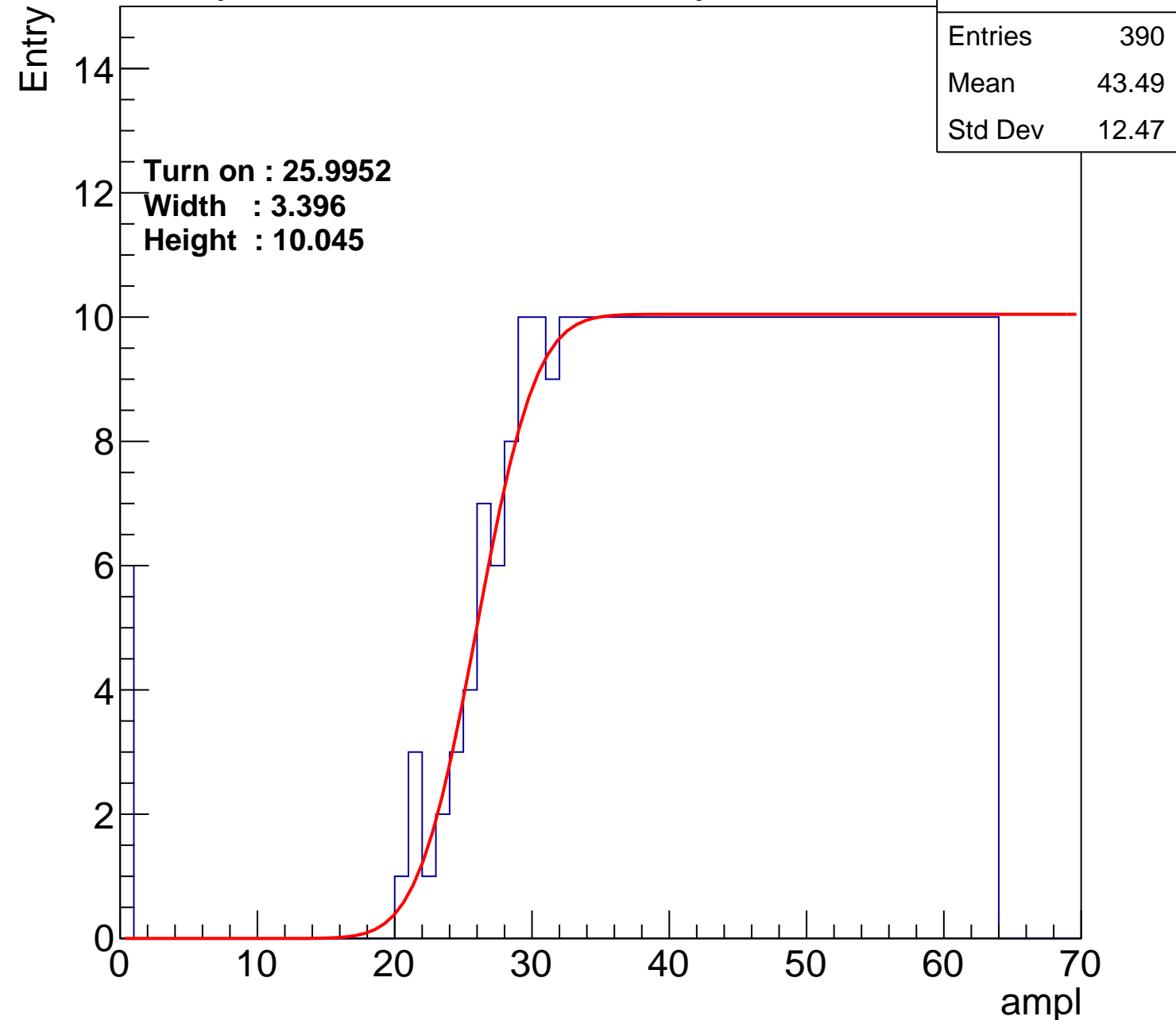
Width : 3.396

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U17-ch125

calib_packv5_042523_0143.root, FC#7, port C2

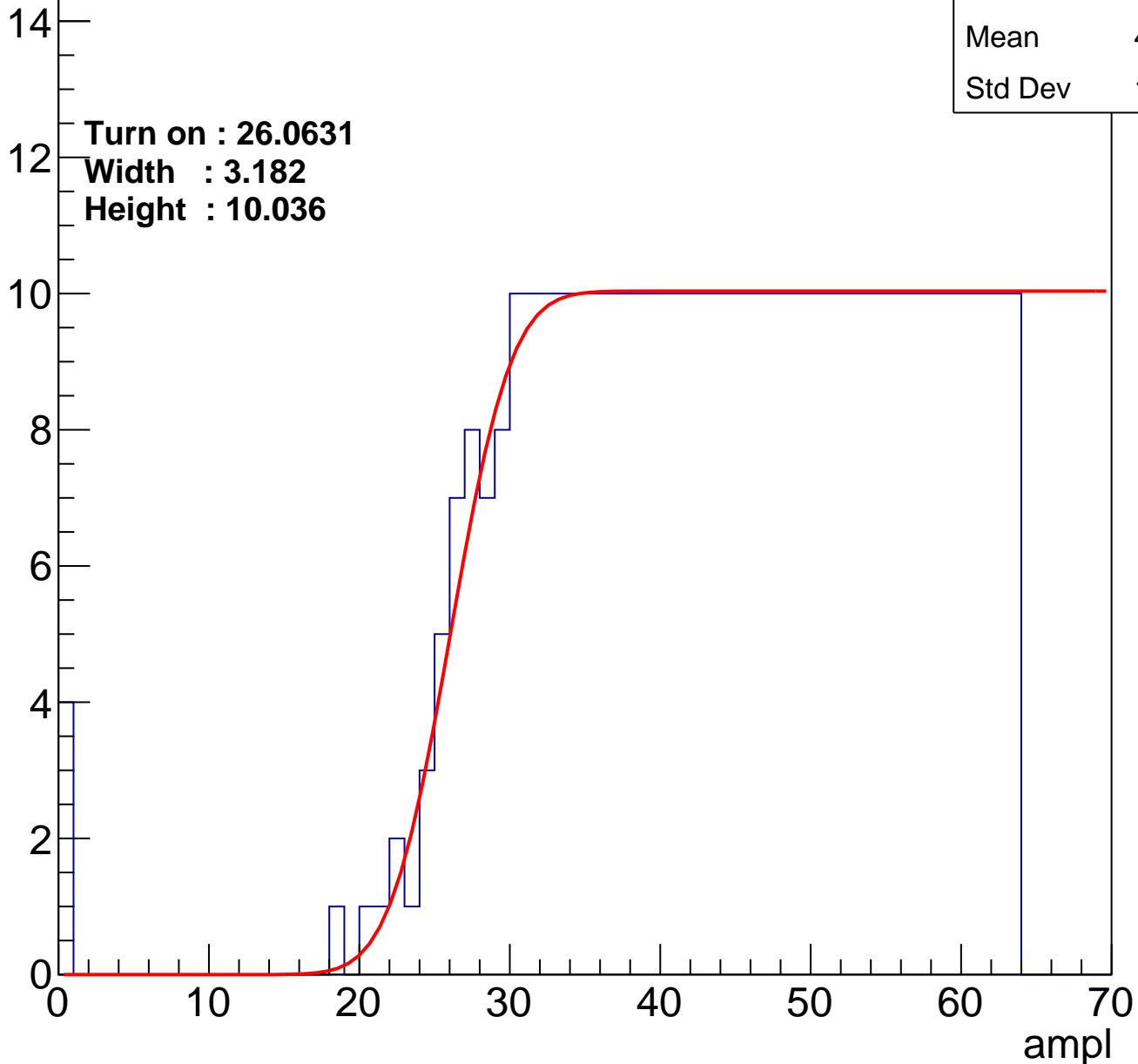
Entries	388
Mean	43.71
Std Dev	12.11

Turn on : 26.0631

Width : 3.182

Height : 10.036

Entry



B1L103S, U17-ch126

calib_packv5_042523_0143.root, FC#7, port C2

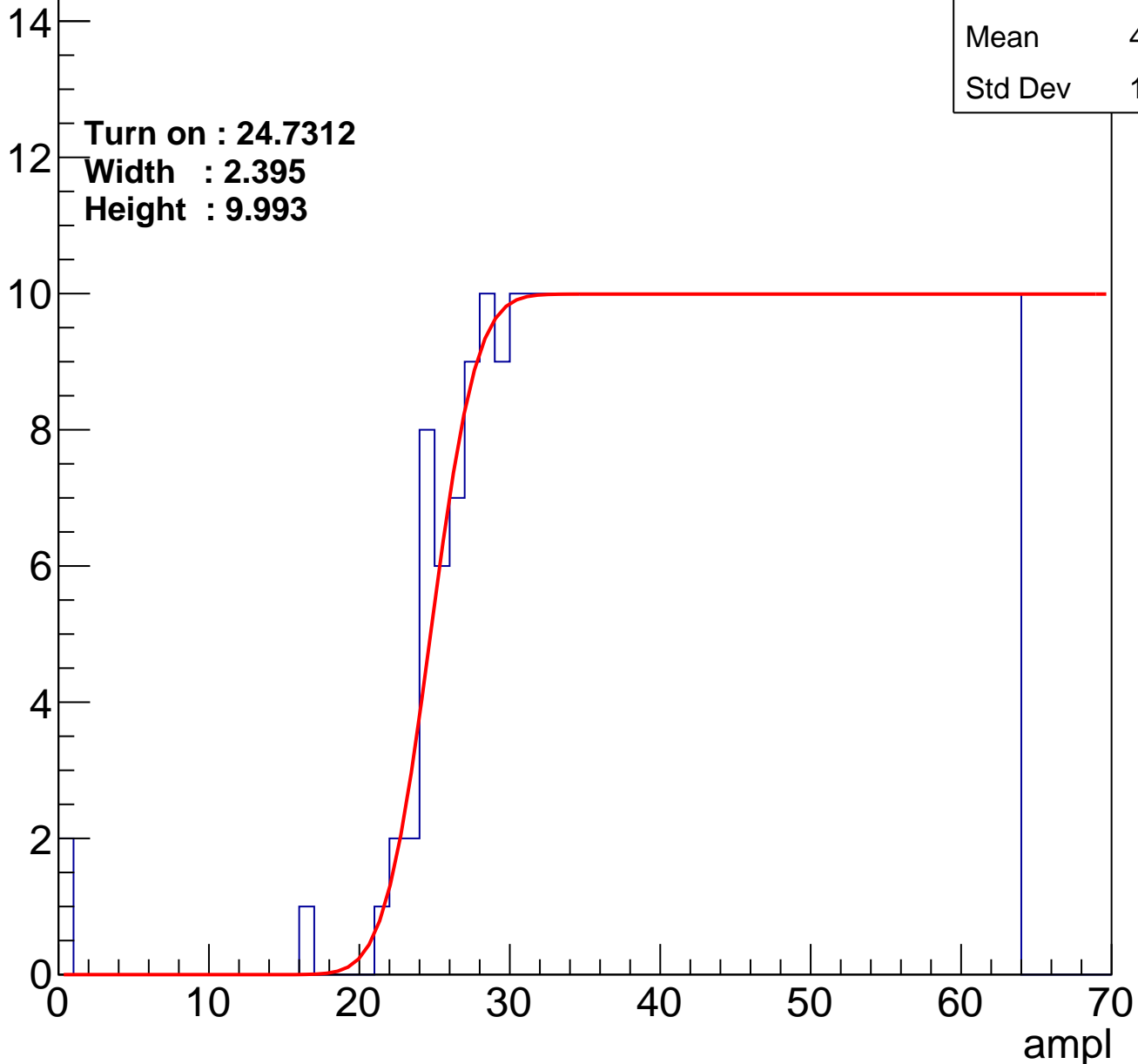
Entries	397
Mean	43.44
Std Dev	11.94

Turn on : 24.7312

Width : 2.395

Height : 9.993

Entry



B1L103S, U17-ch127

calib_packv5_042523_0143.root, FC#7, port C2

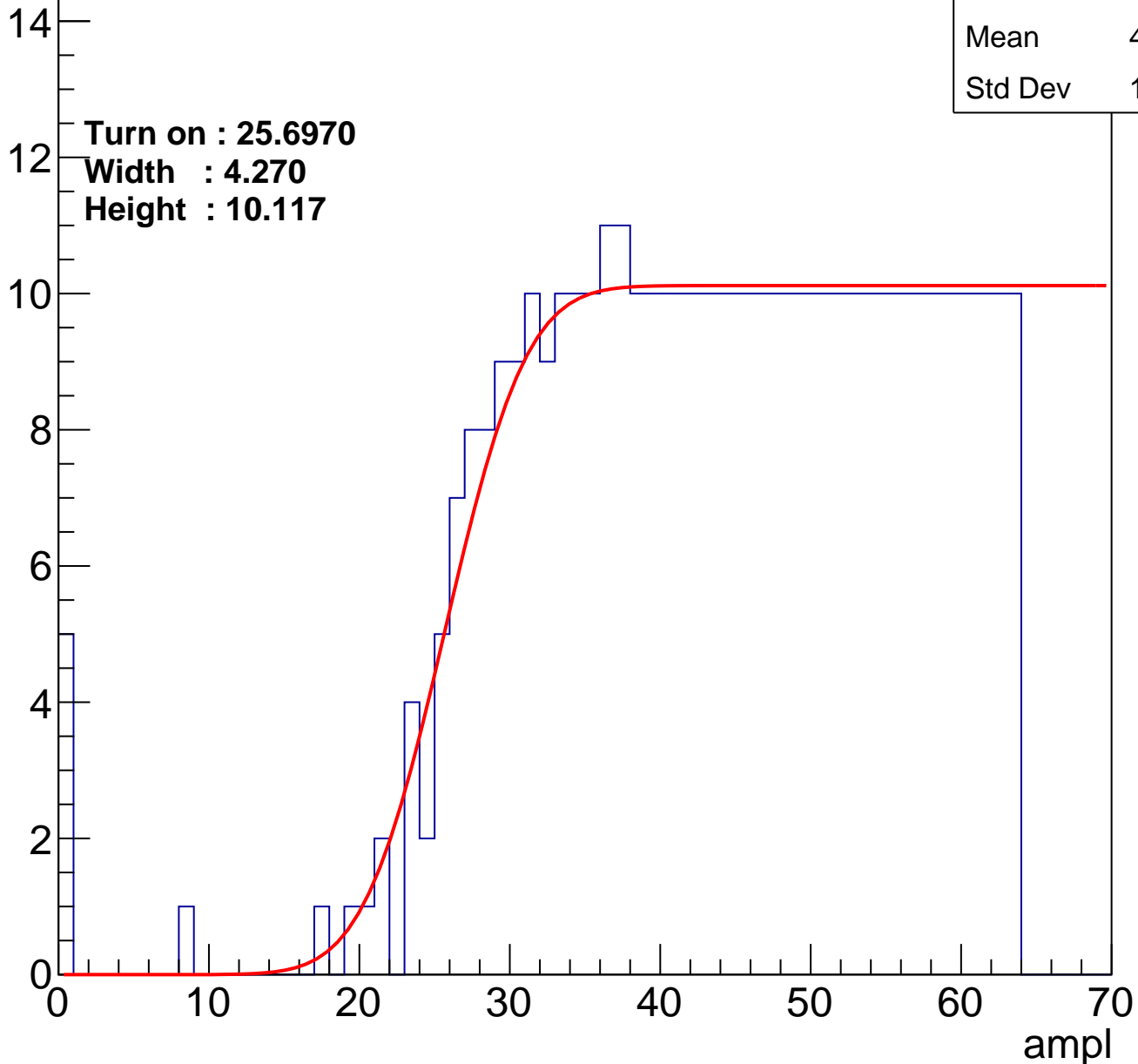
Entries	394
Mean	43.34
Std Dev	12.49

Turn on : 25.6970

Width : 4.270

Height : 10.117

Entry



B1L103S, U17-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.34
Std Dev	12.49

Turn on : 25.6970

Width : 4.270

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl

