

B1L003S, U21-ch0, adc0

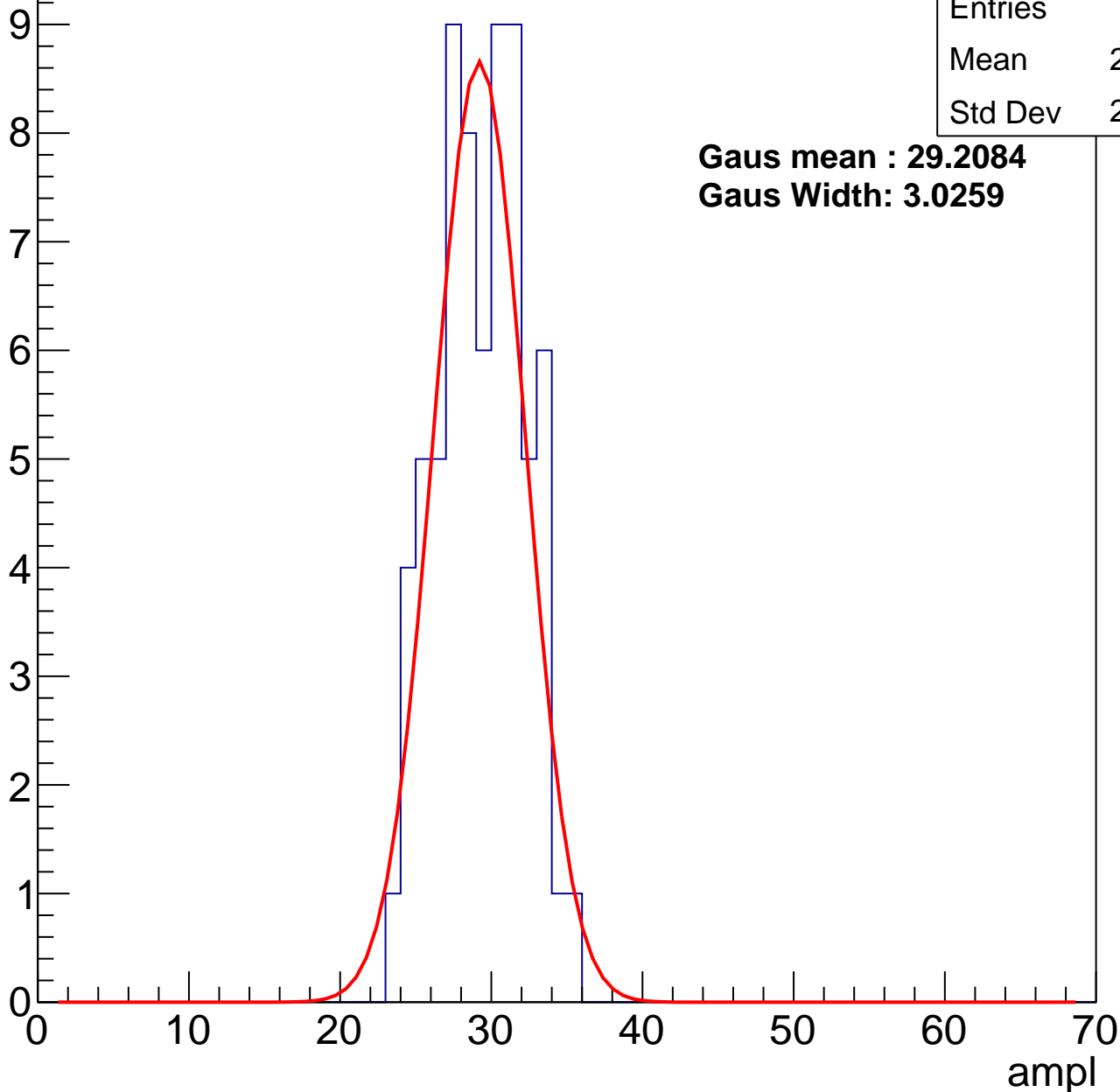
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	28.86
Std Dev	2.825

Gaus mean : 29.2084

Gaus Width: 3.0259



B1L003S, U21-ch0, adc1

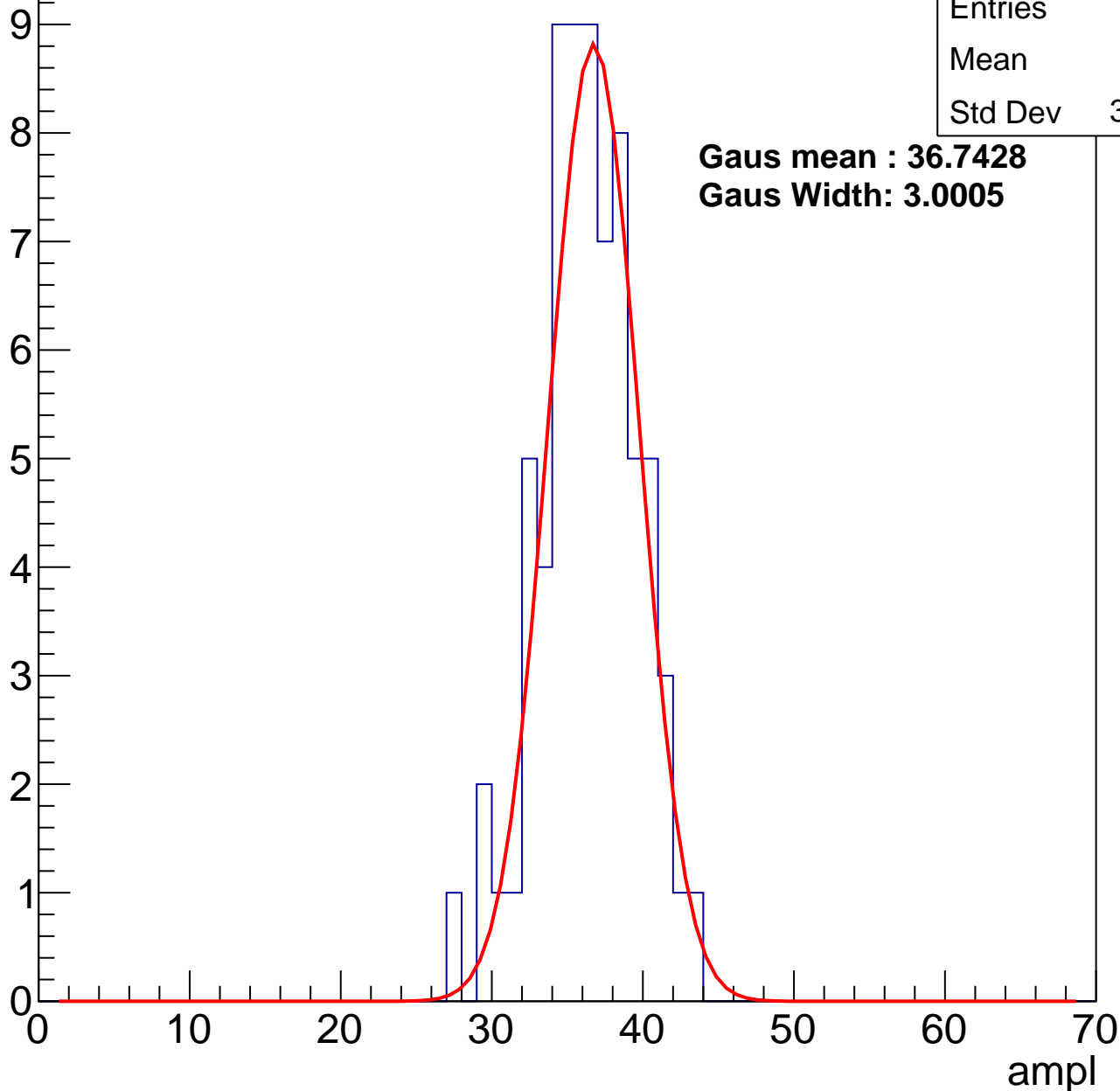
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.9
Std Dev	3.194

Gaus mean : 36.7428

Gaus Width: 3.0005



B1L003S, U21-ch0, adc2

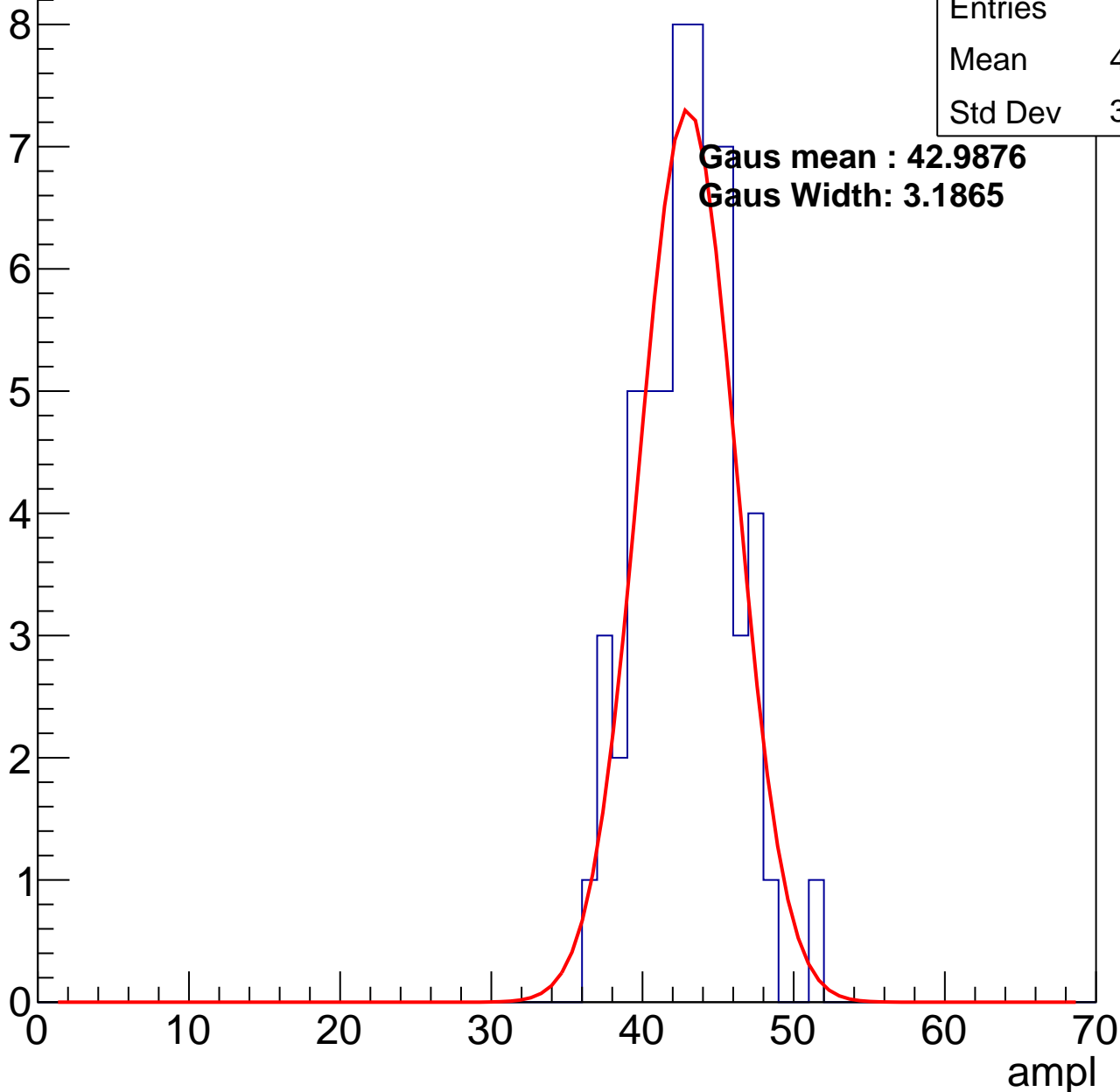
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.52
Std Dev	3.058

Gaus mean : 42.9876

Gaus Width: 3.1865

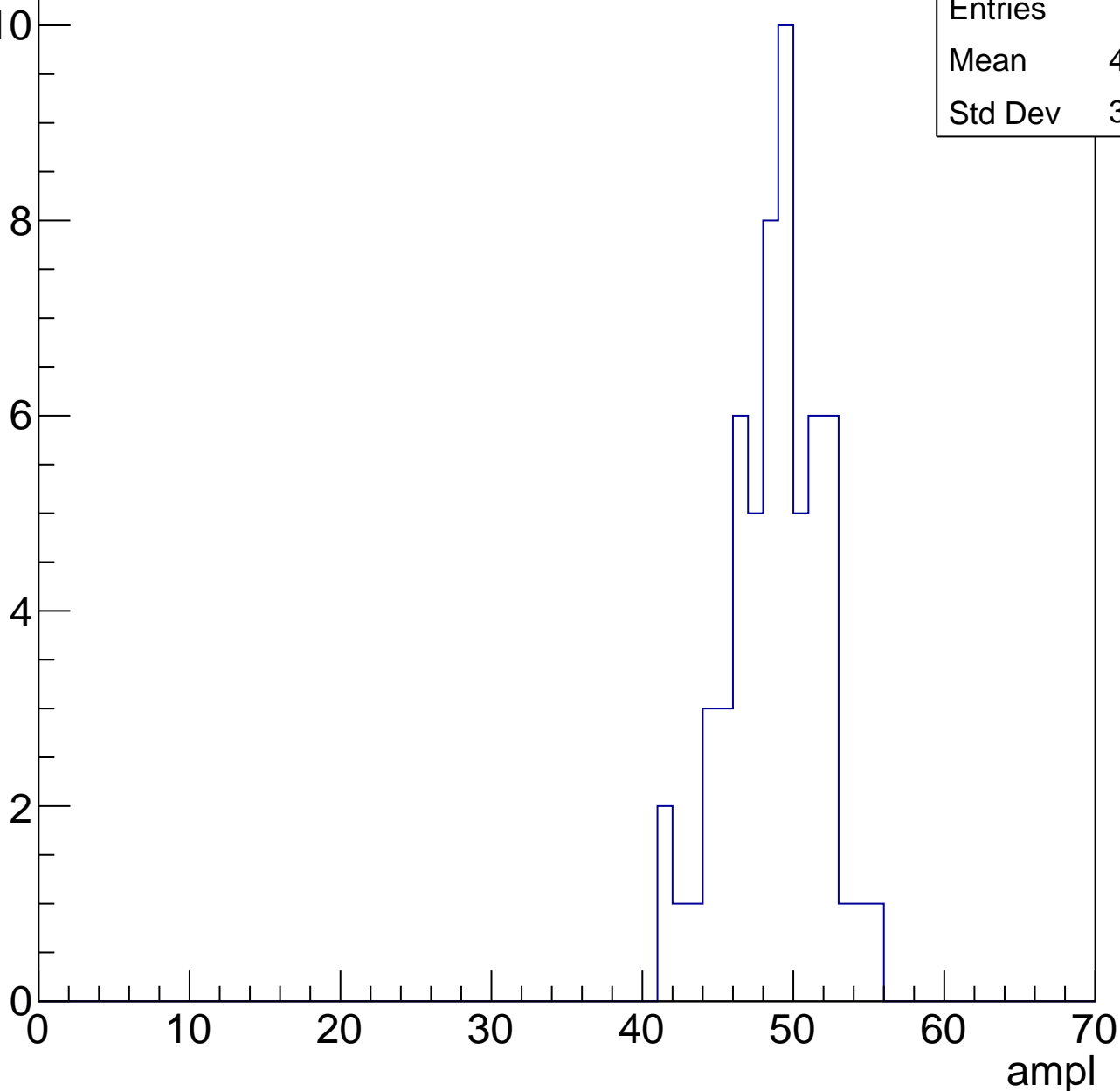


B1L003S, U21-ch0, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	48.29
Std Dev	3.048

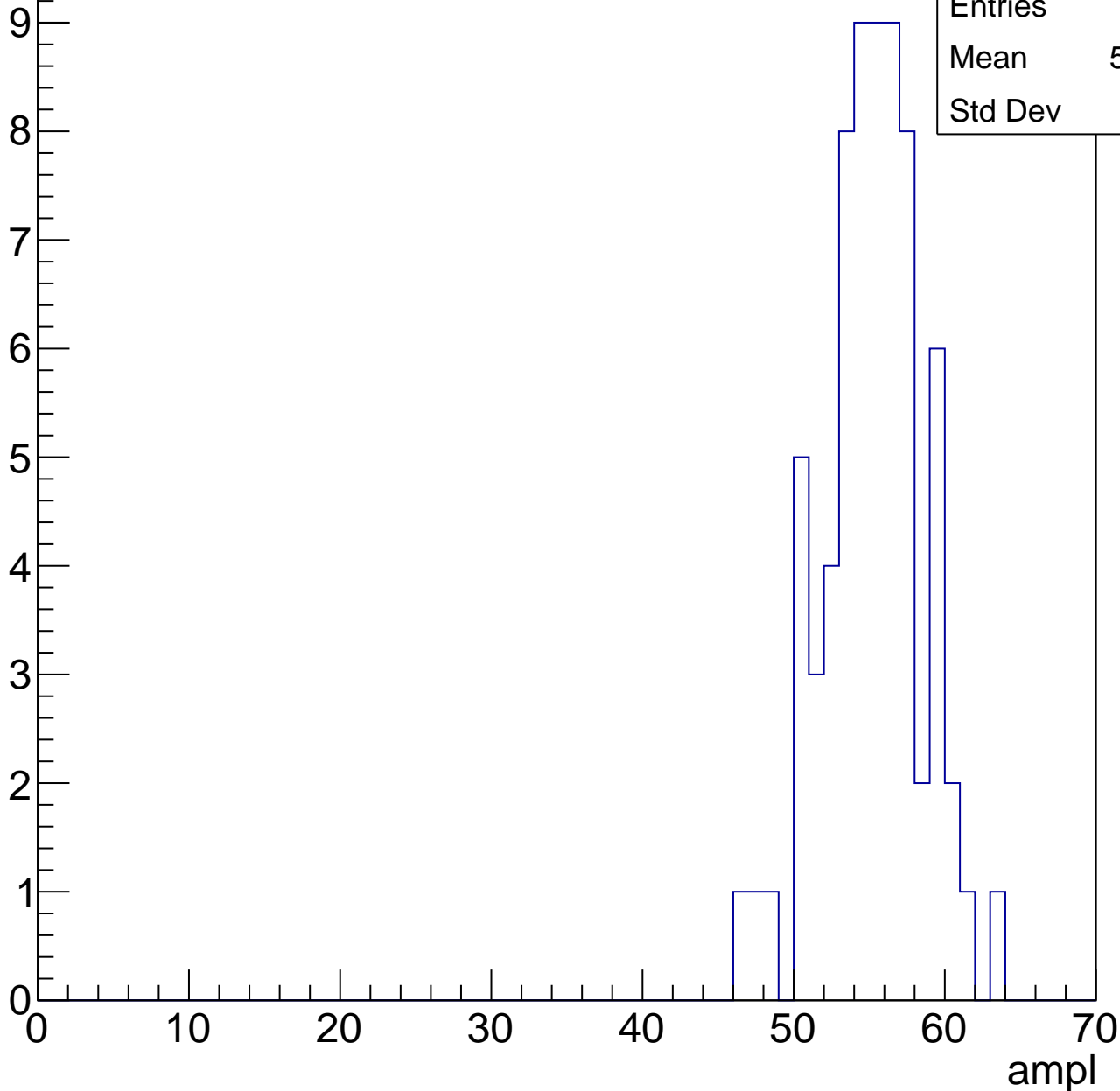


B1L003S, U21-ch0, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	54.73
Std Dev	3.26

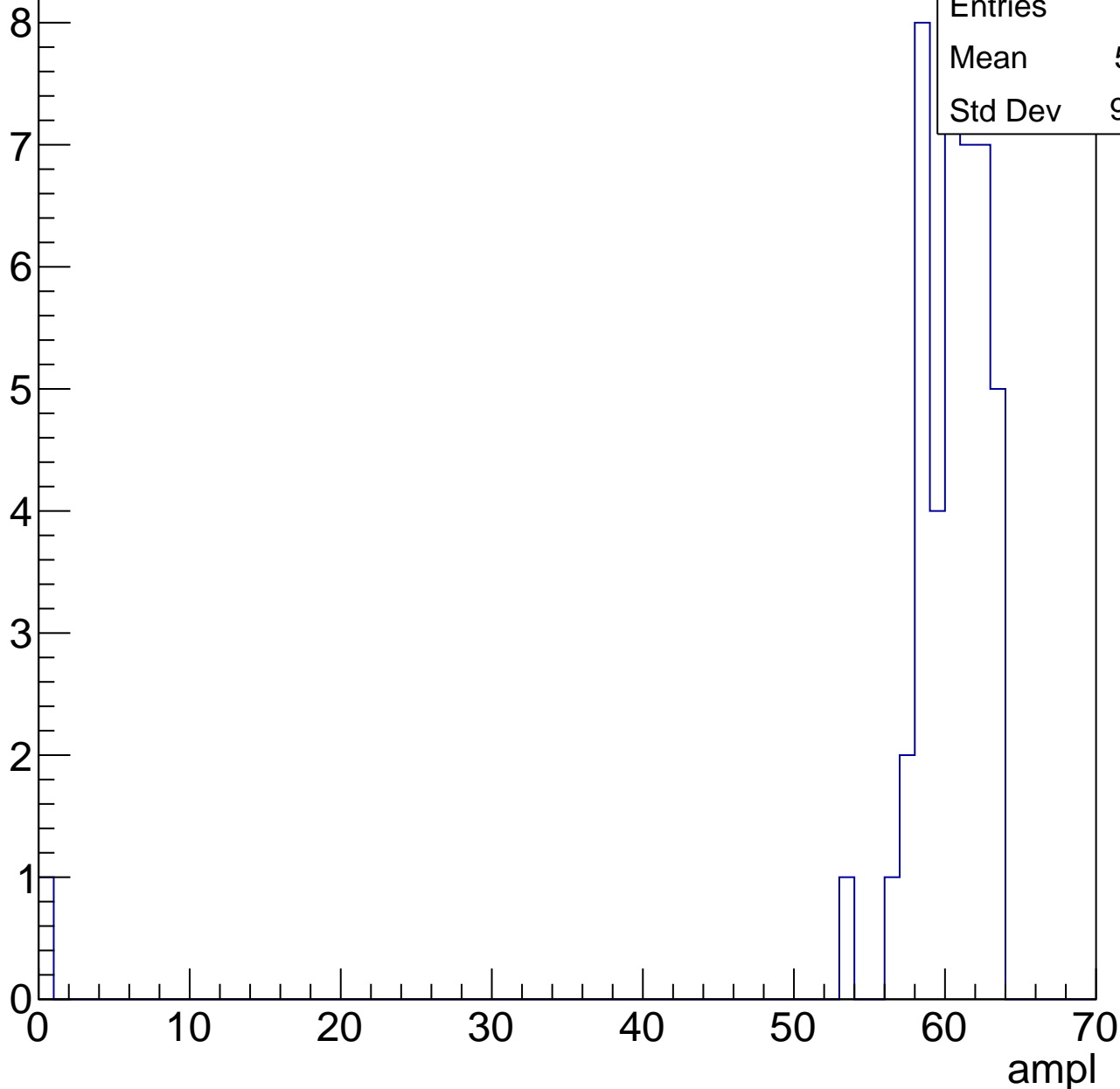


B1L003S, U21-ch0, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

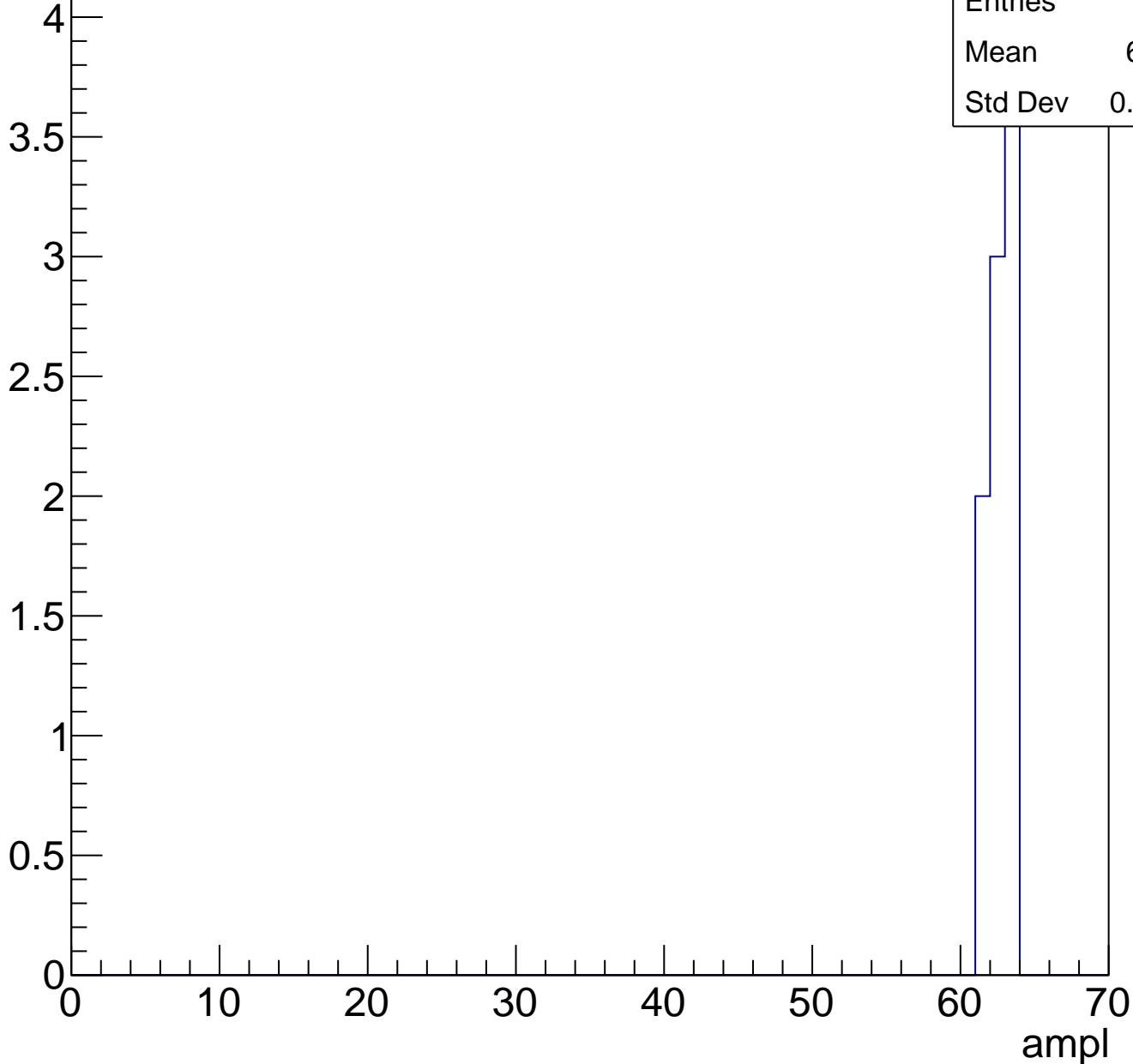
Entries	44
Mean	58.61
Std Dev	9.188



B1L003S, U21-ch0, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch0, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch1, adc0

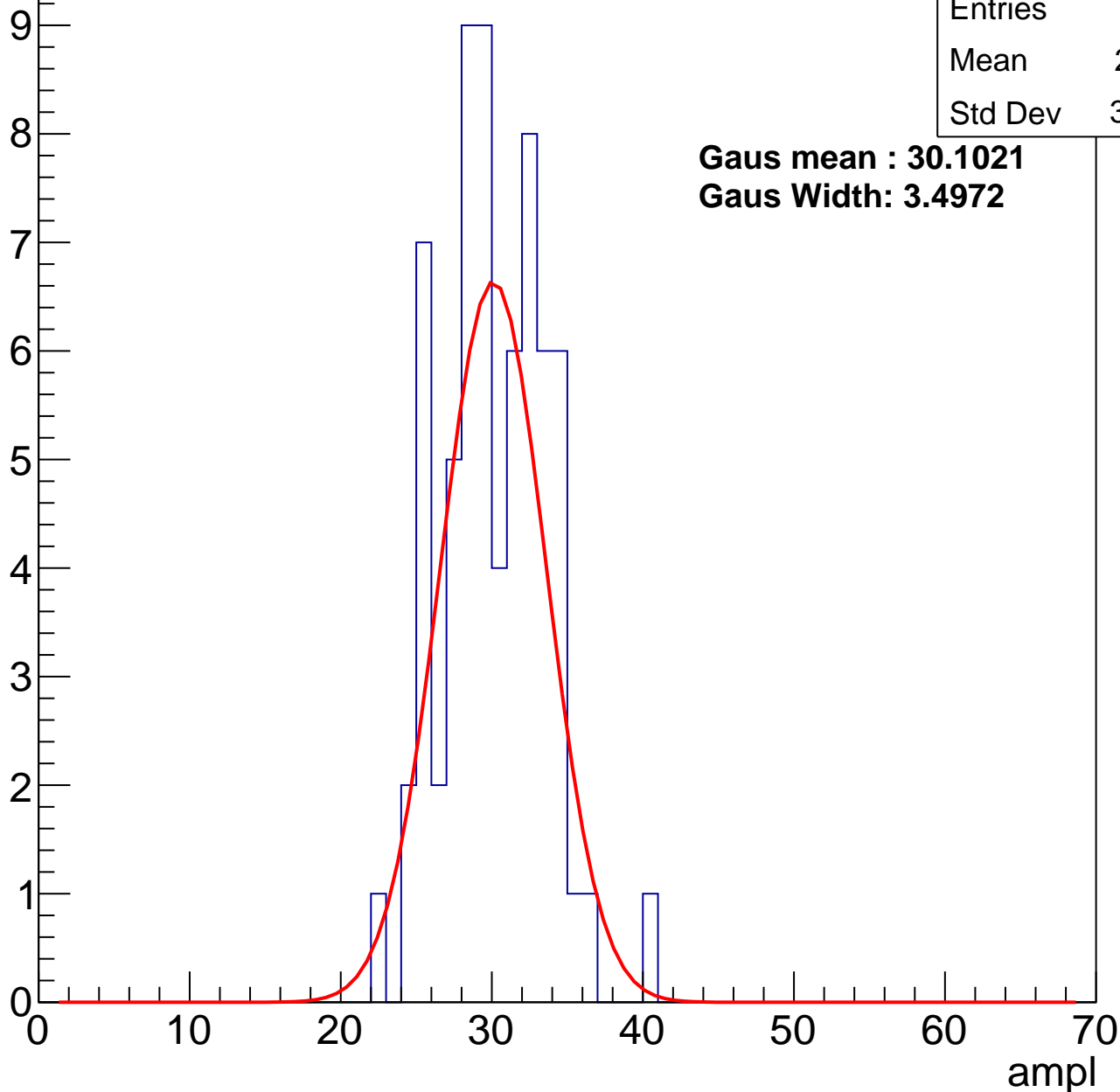
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.71
Std Dev	3.378

Gaus mean : 30.1021

Gaus Width: 3.4972



B1L003S, U21-ch1, adc1

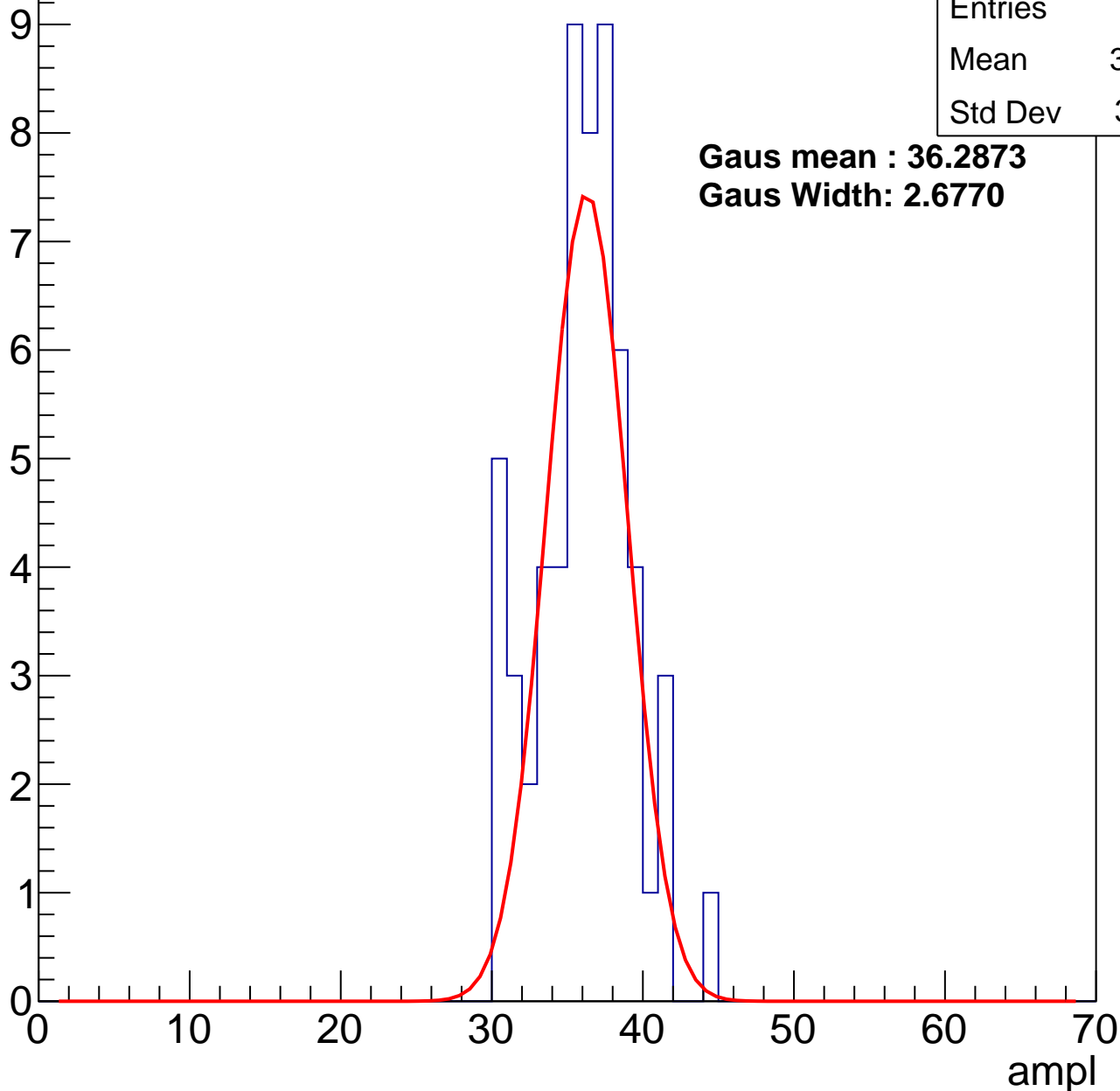
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	35.63
Std Dev	3.091

Gaus mean : 36.2873

Gaus Width: 2.6770



B1L003S, U21-ch1, adc2

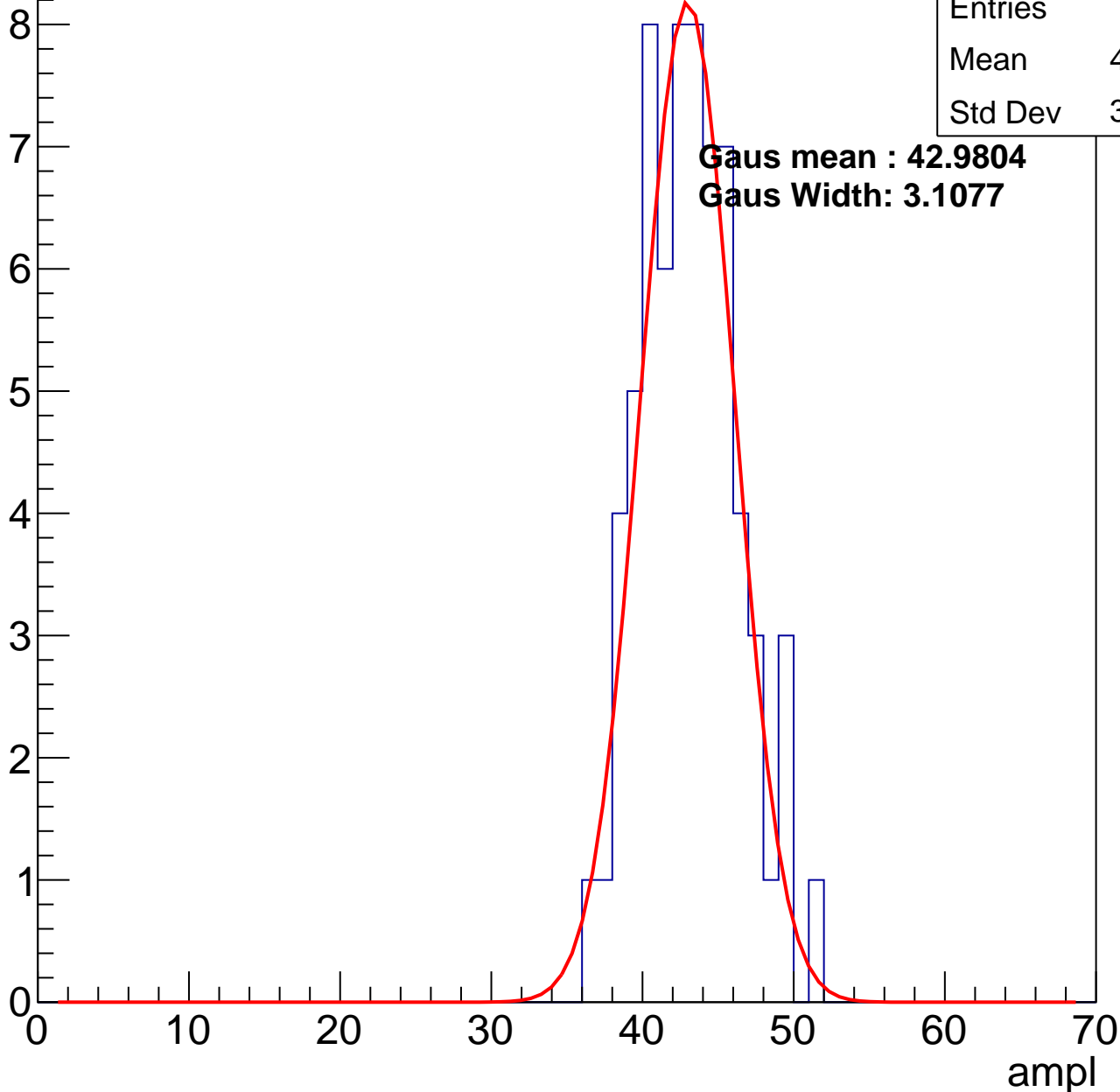
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.69
Std Dev	3.182

Gaus mean : 42.9804

Gaus Width: 3.1077

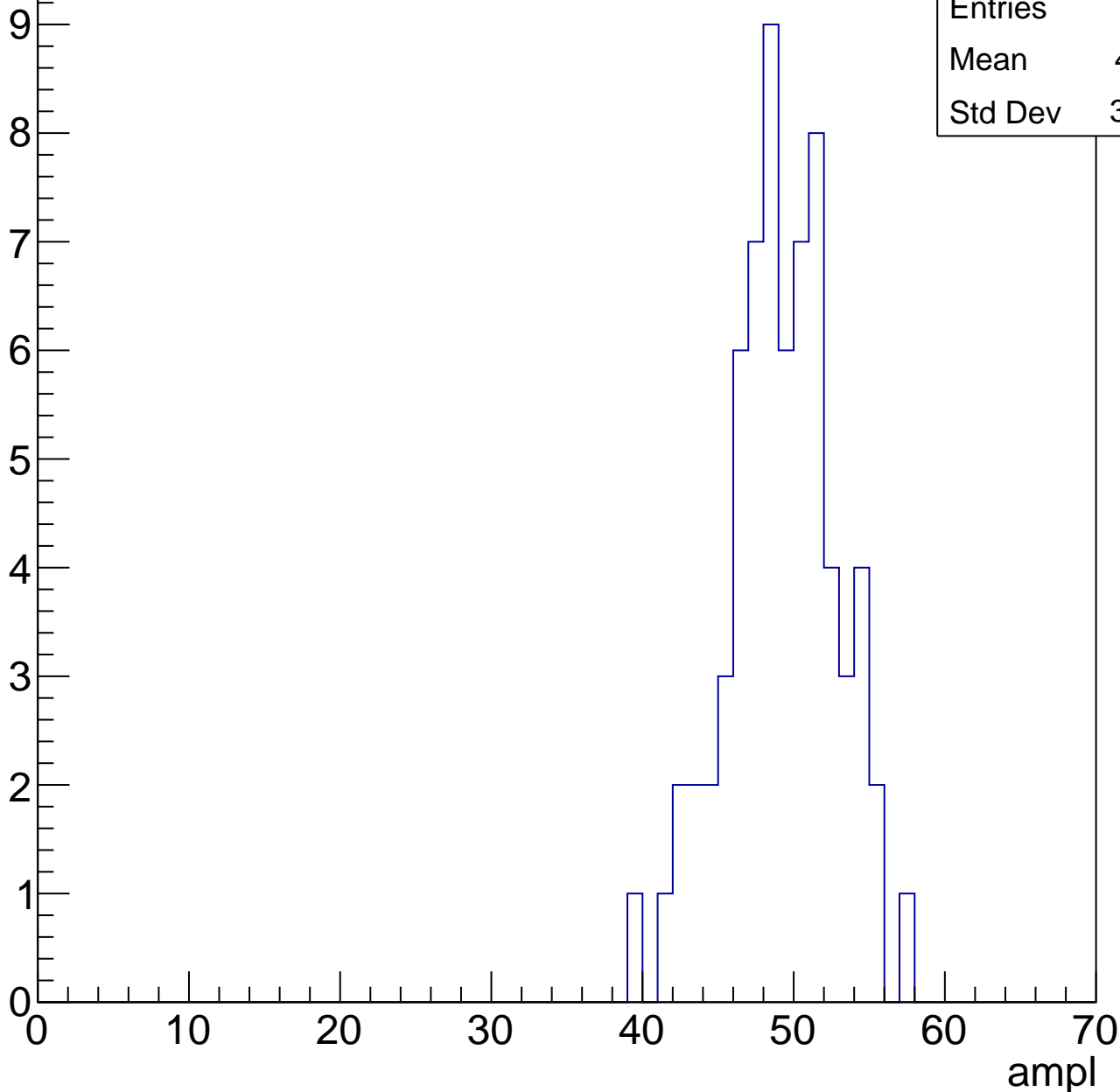


B1L003S, U21-ch1, adc3

calib_packv5_042523_0143.root, FC#13, port D2

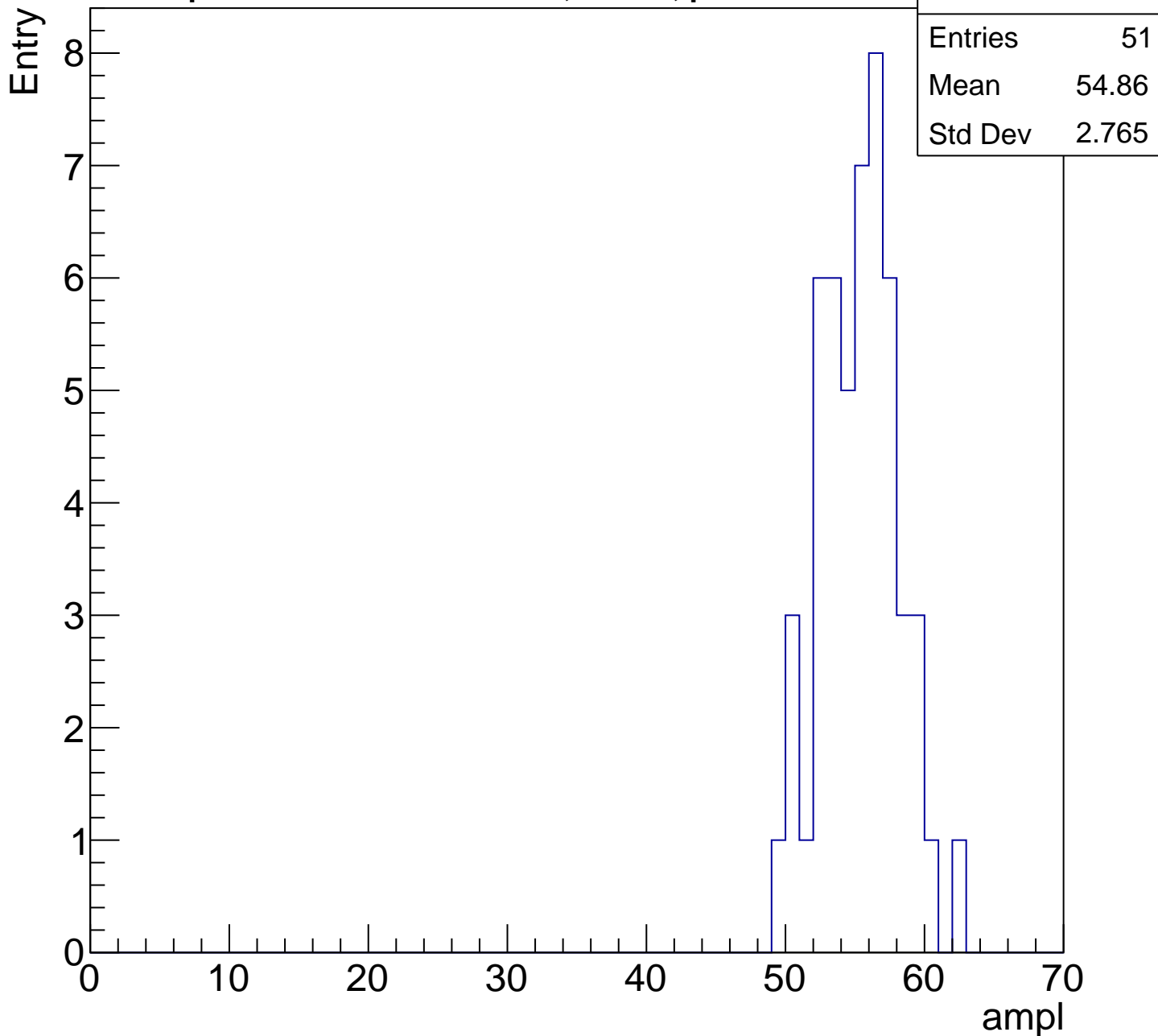
Entry

Entries	68
Mean	48.71
Std Dev	3.589



B1L003S, U21-ch1, adc4

calib_packv5_042523_0143.root, FC#13, port D2

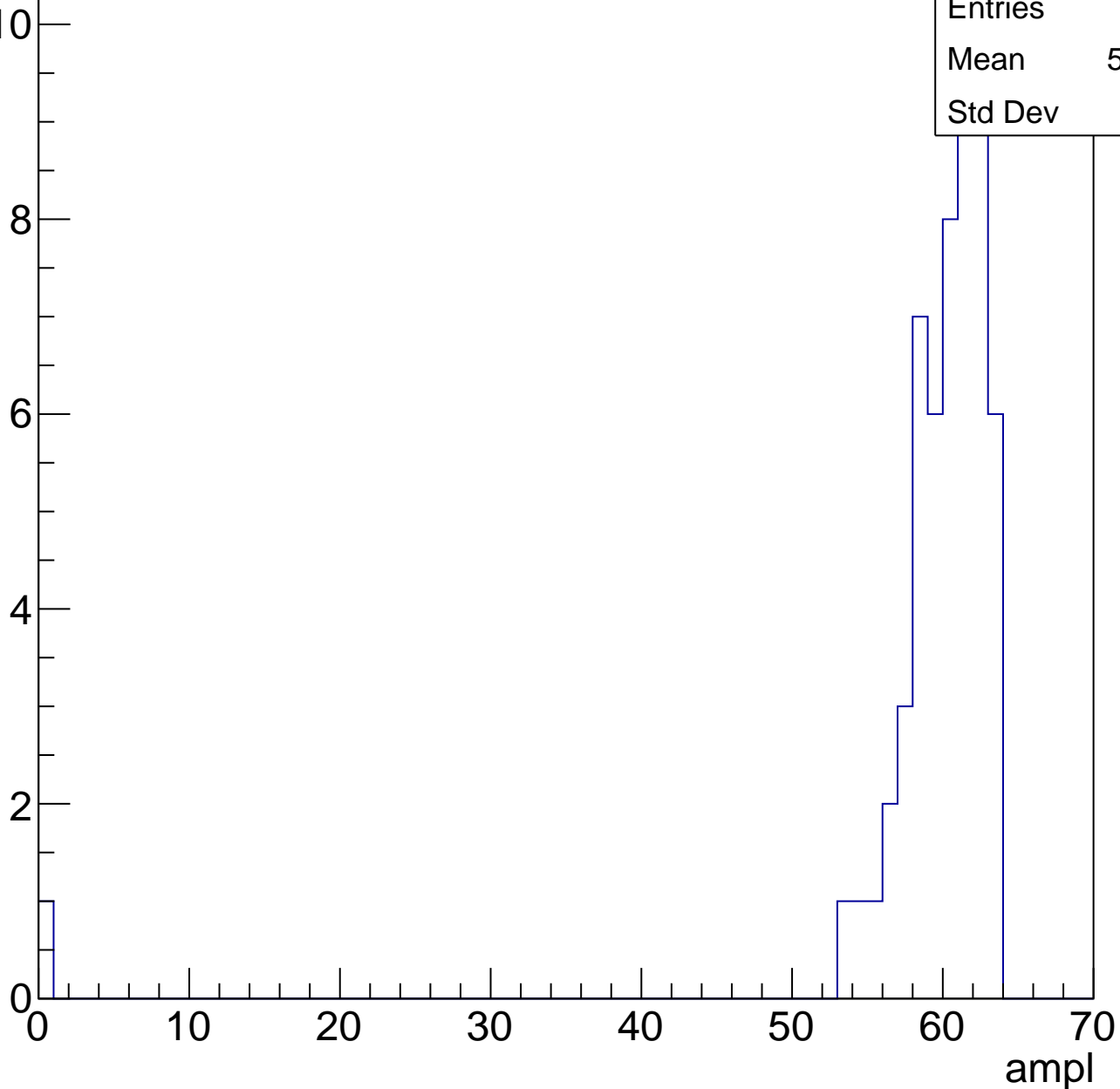


B1L003S, U21-ch1, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

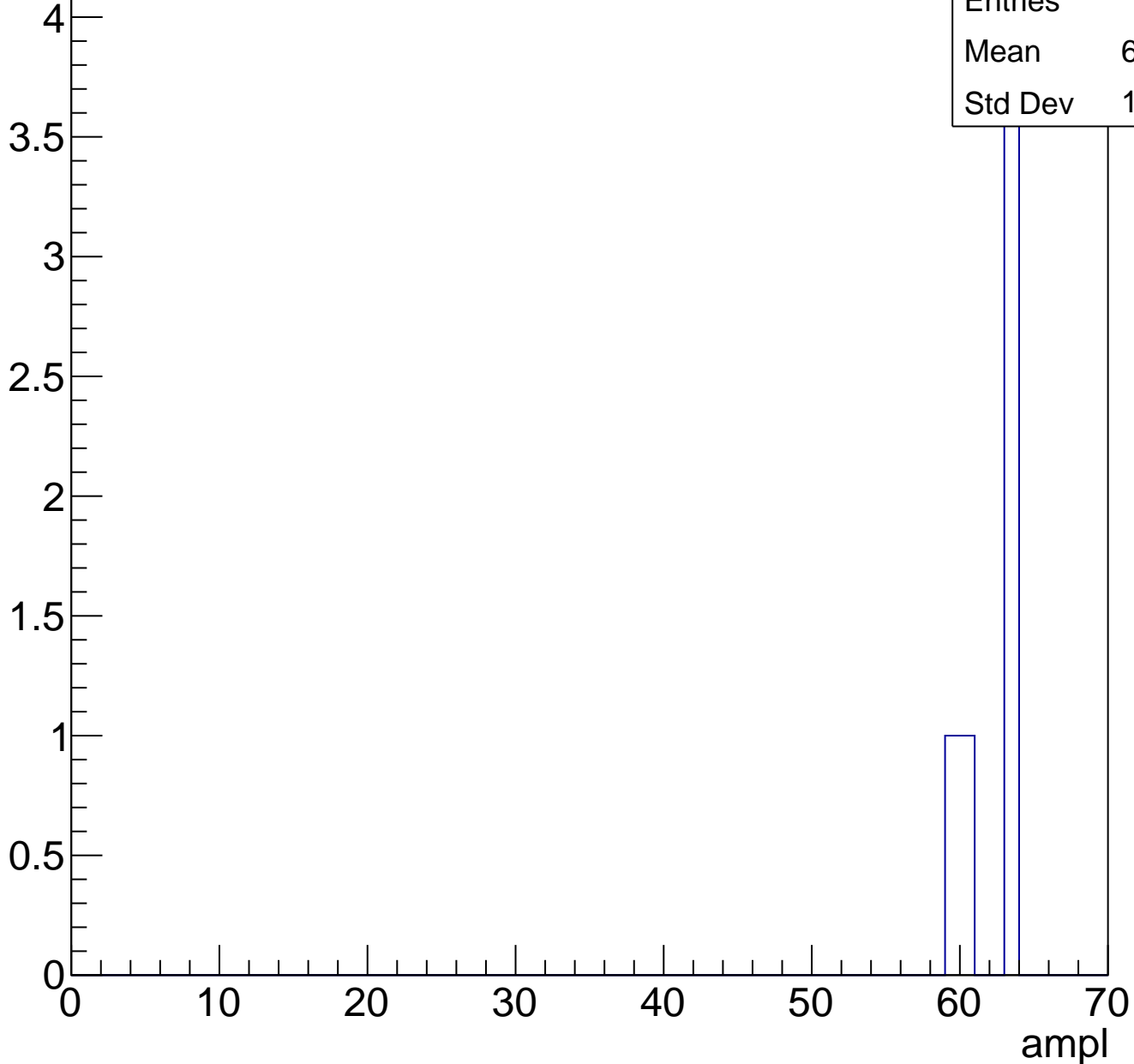
Entries	55
Mean	58.75
Std Dev	8.33



B1L003S, U21-ch1, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch1, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch2, adc0

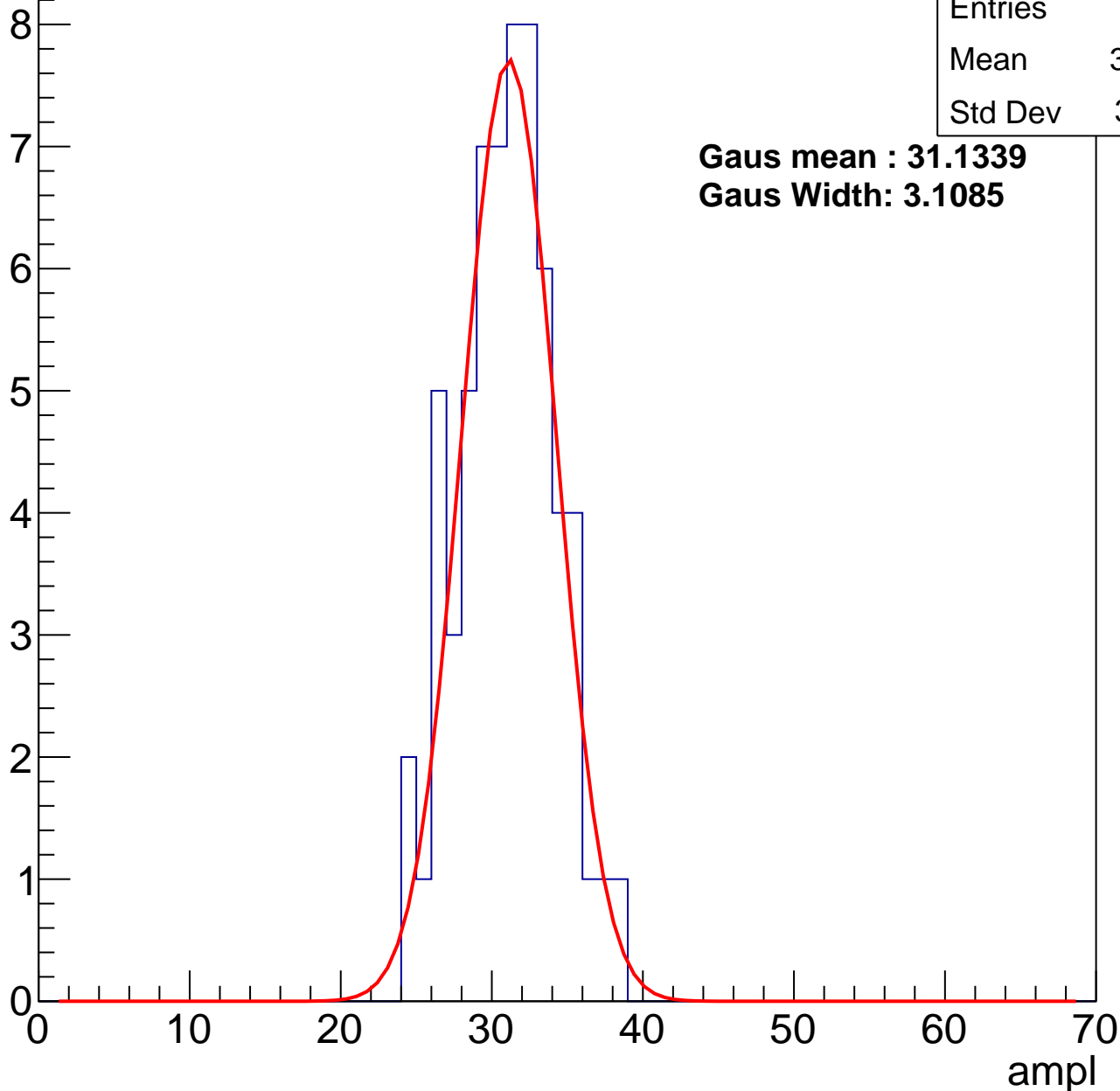
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.57
Std Dev	3.131

Gaus mean : 31.1339

Gaus Width: 3.1085



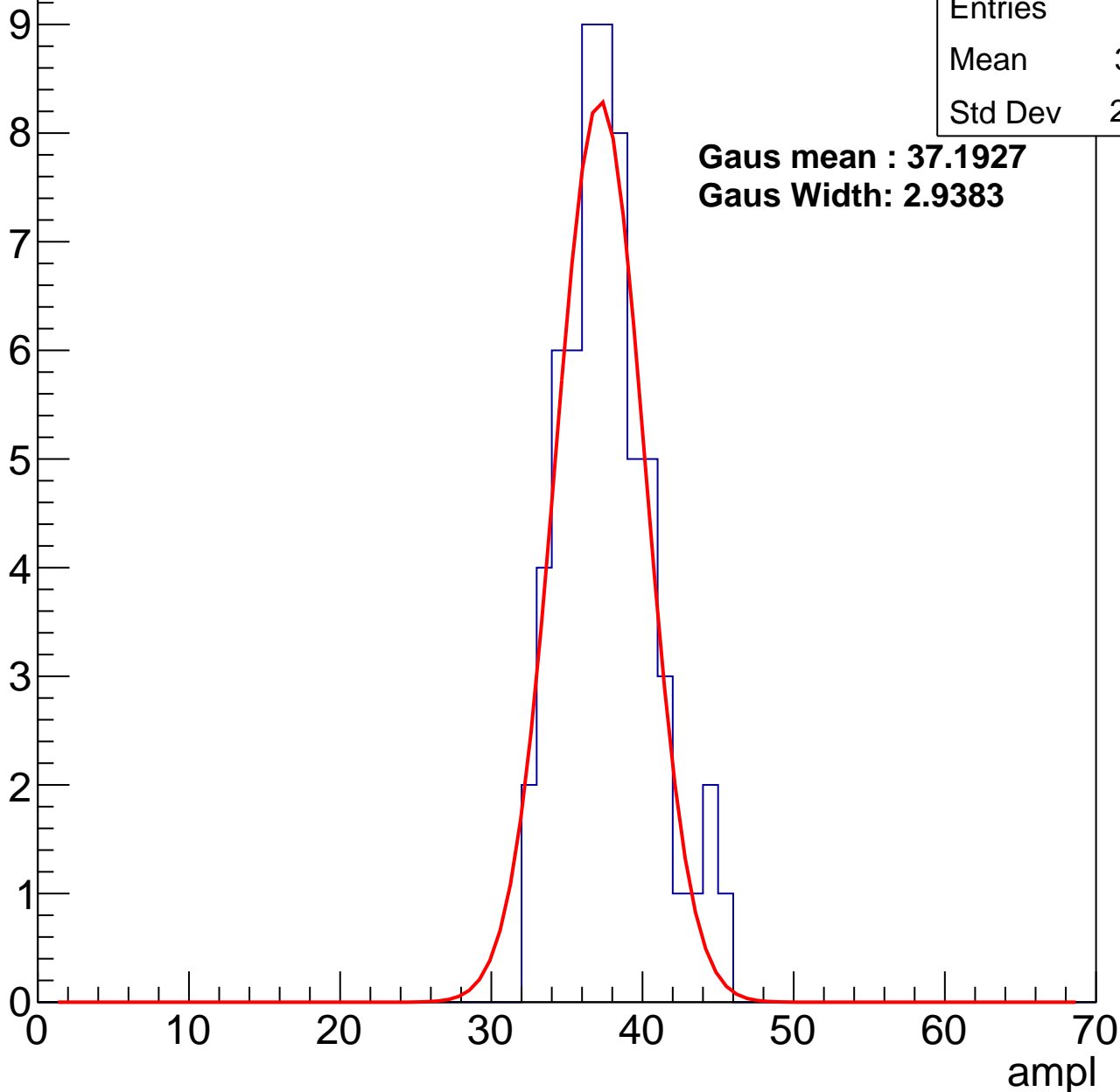
B1L003S, U21-ch2, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.21
Std Dev	2.957

Gaus mean : 37.1927
Gaus Width: 2.9383



B1L003S, U21-ch2, adc2

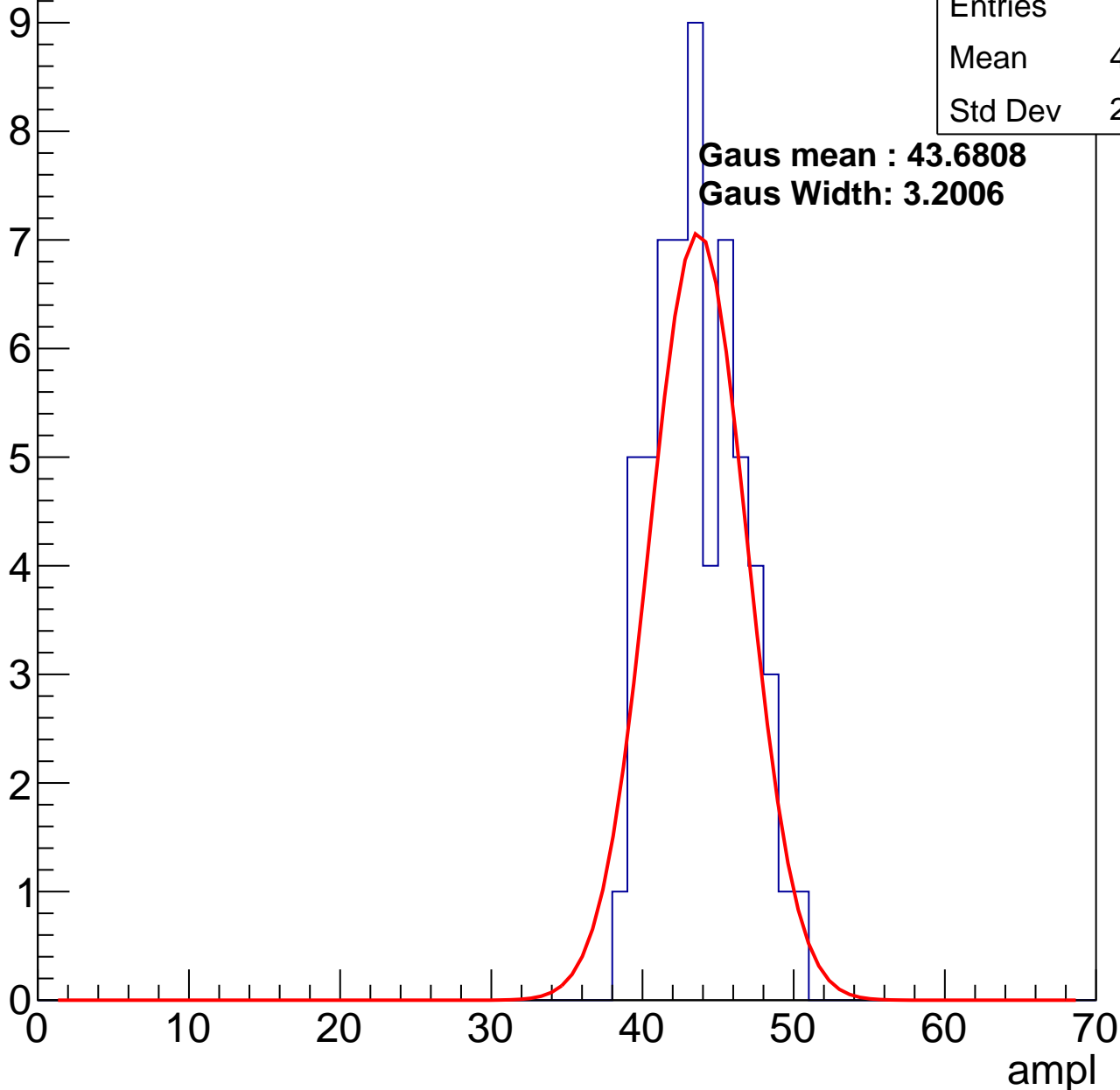
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.27
Std Dev	2.857

Gaus mean : 43.6808

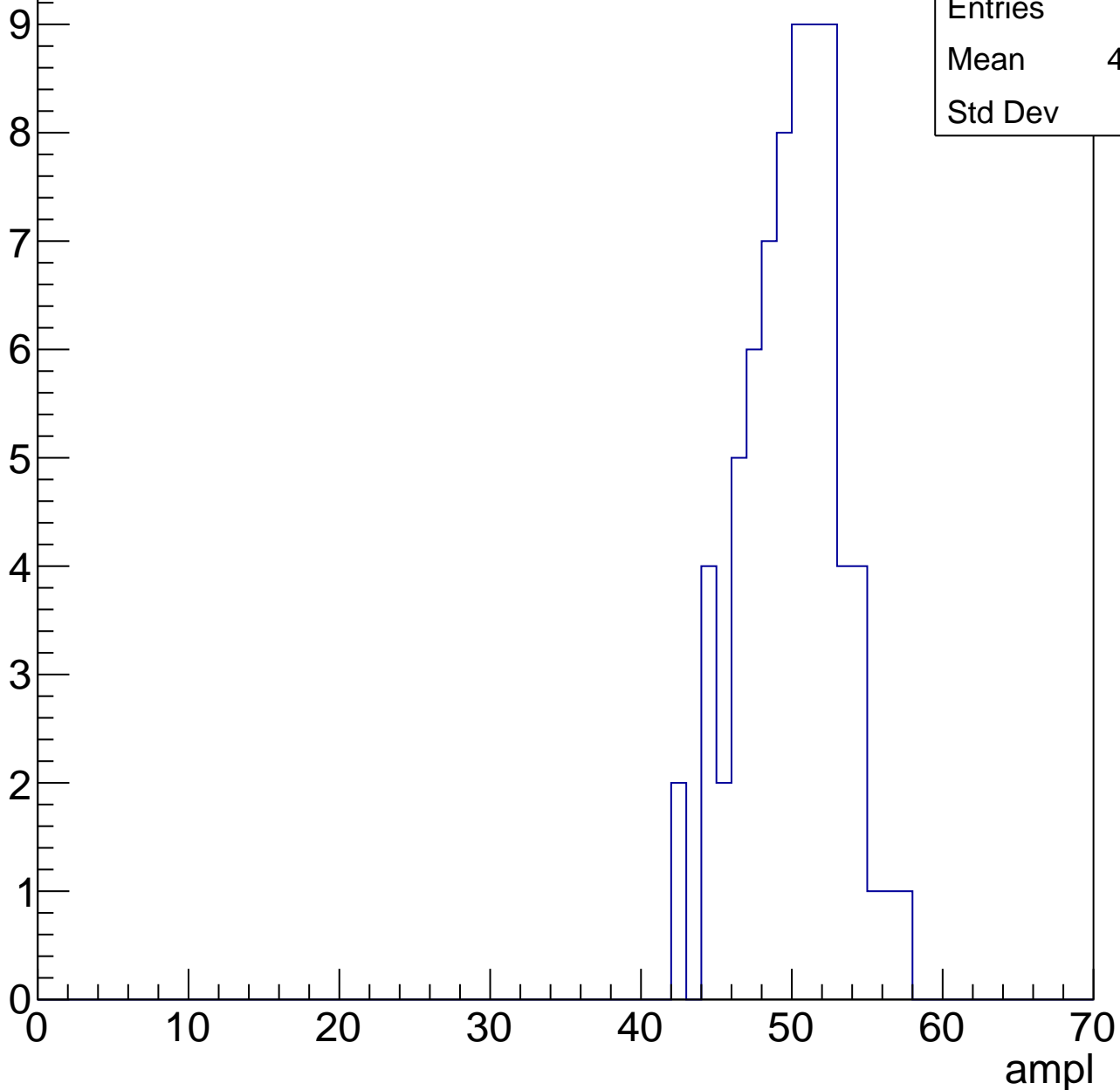
Gaus Width: 3.2006



B1L003S, U21-ch2, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



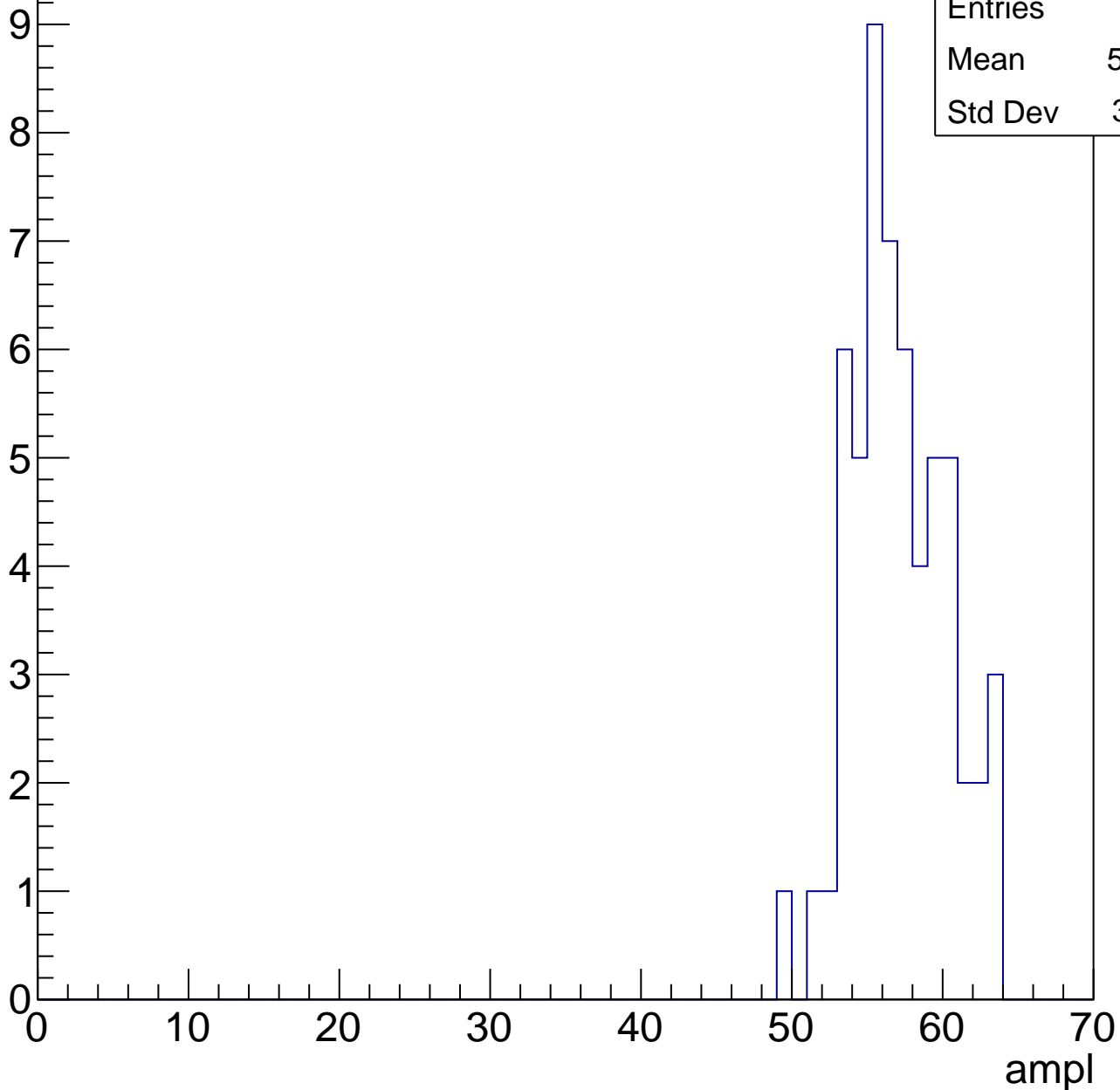
Entries	72
Mean	49.49
Std Dev	3.18

B1L003S, U21-ch2, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.68
Std Dev	3.141

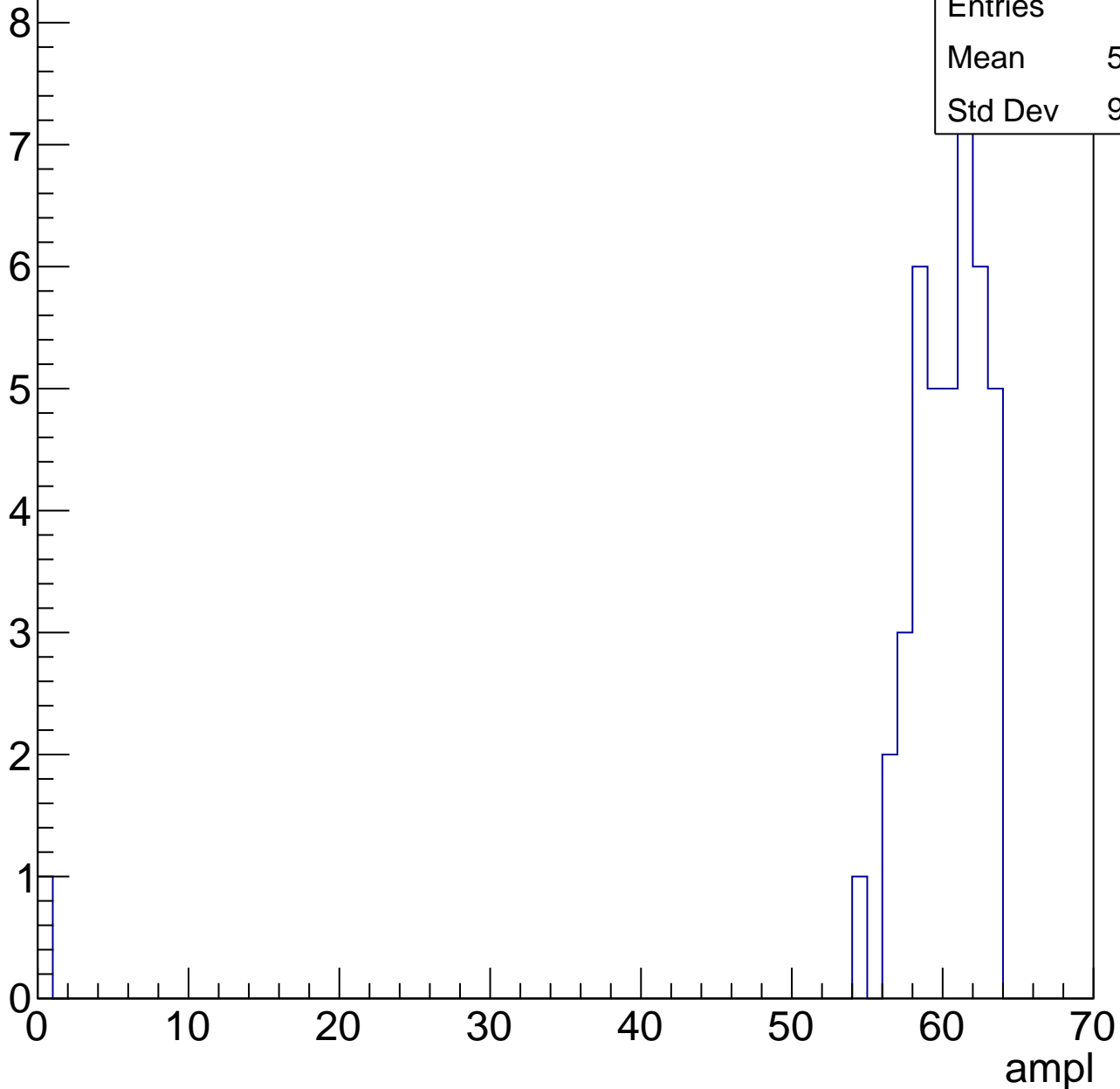


B1L003S, U21-ch2, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.45
Std Dev	9.386



B1L003S, U21-ch2, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch2, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L003S, U21-ch3, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	81
Mean	30.65
Std Dev	4.592

Gaus mean : 31.3256

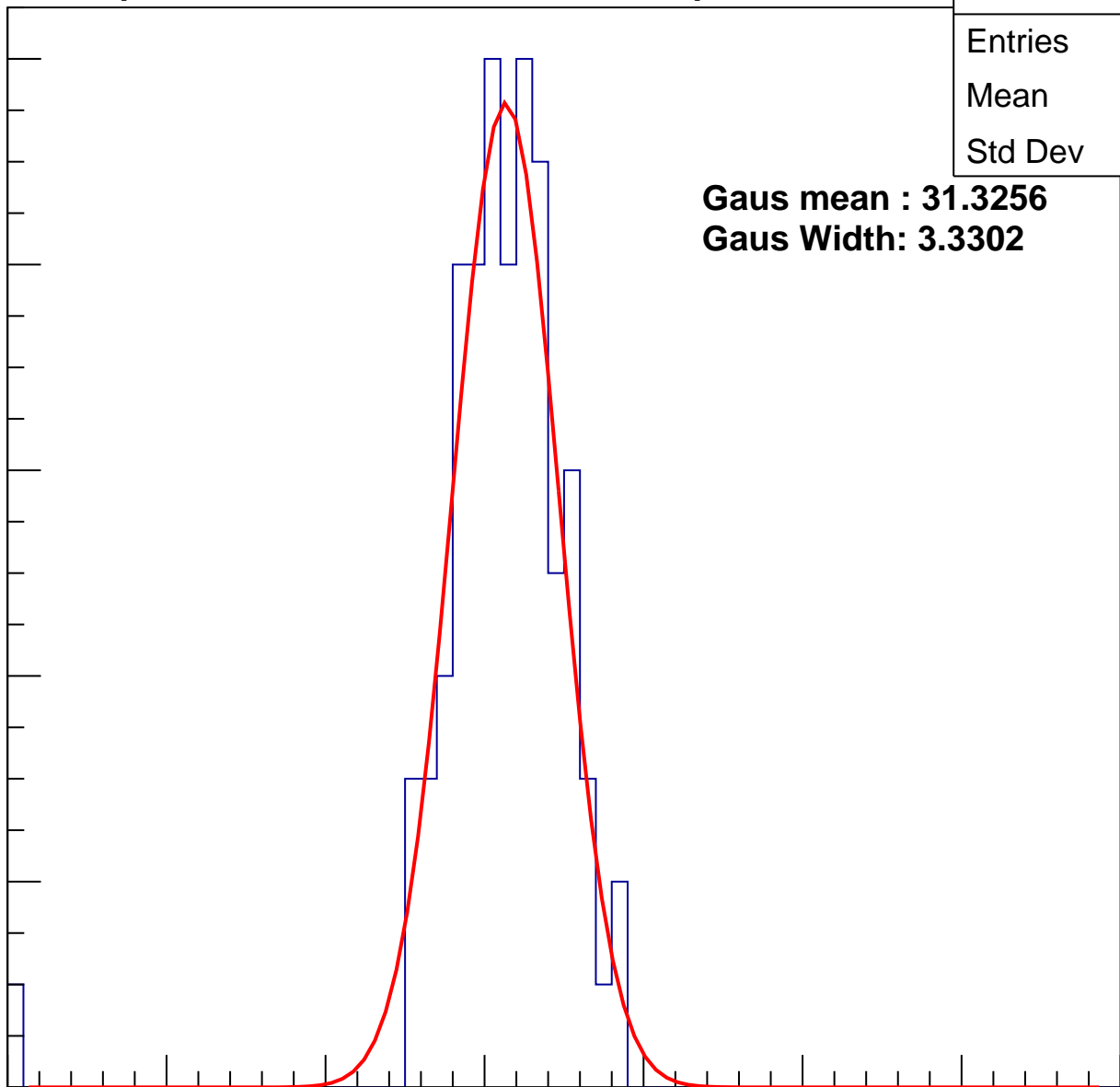
Gaus Width: 3.3302

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch3, adc1

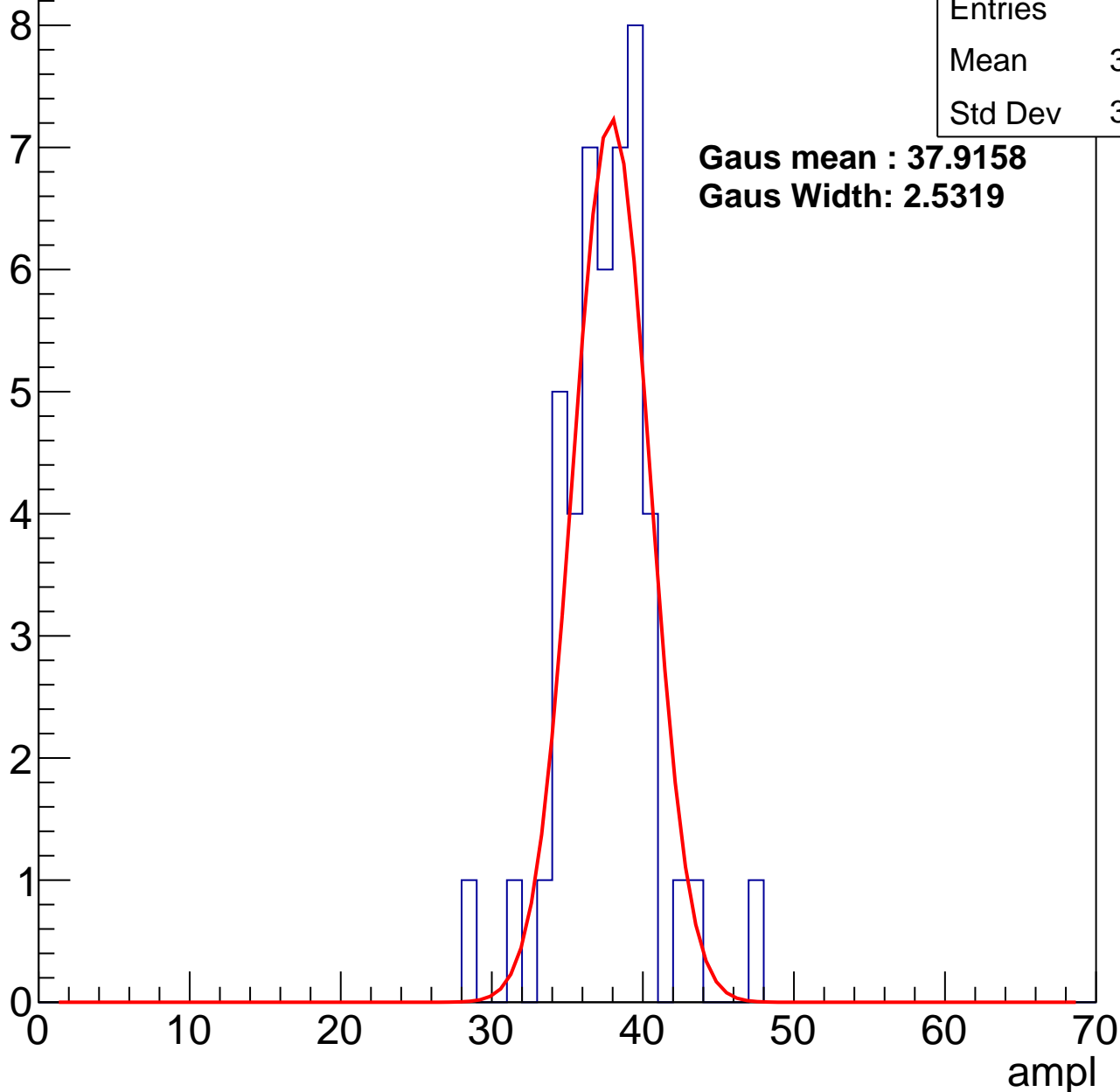
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	37.15
Std Dev	3.046

Gaus mean : 37.9158

Gaus Width: 2.5319



B1L003S, U21-ch3, adc2

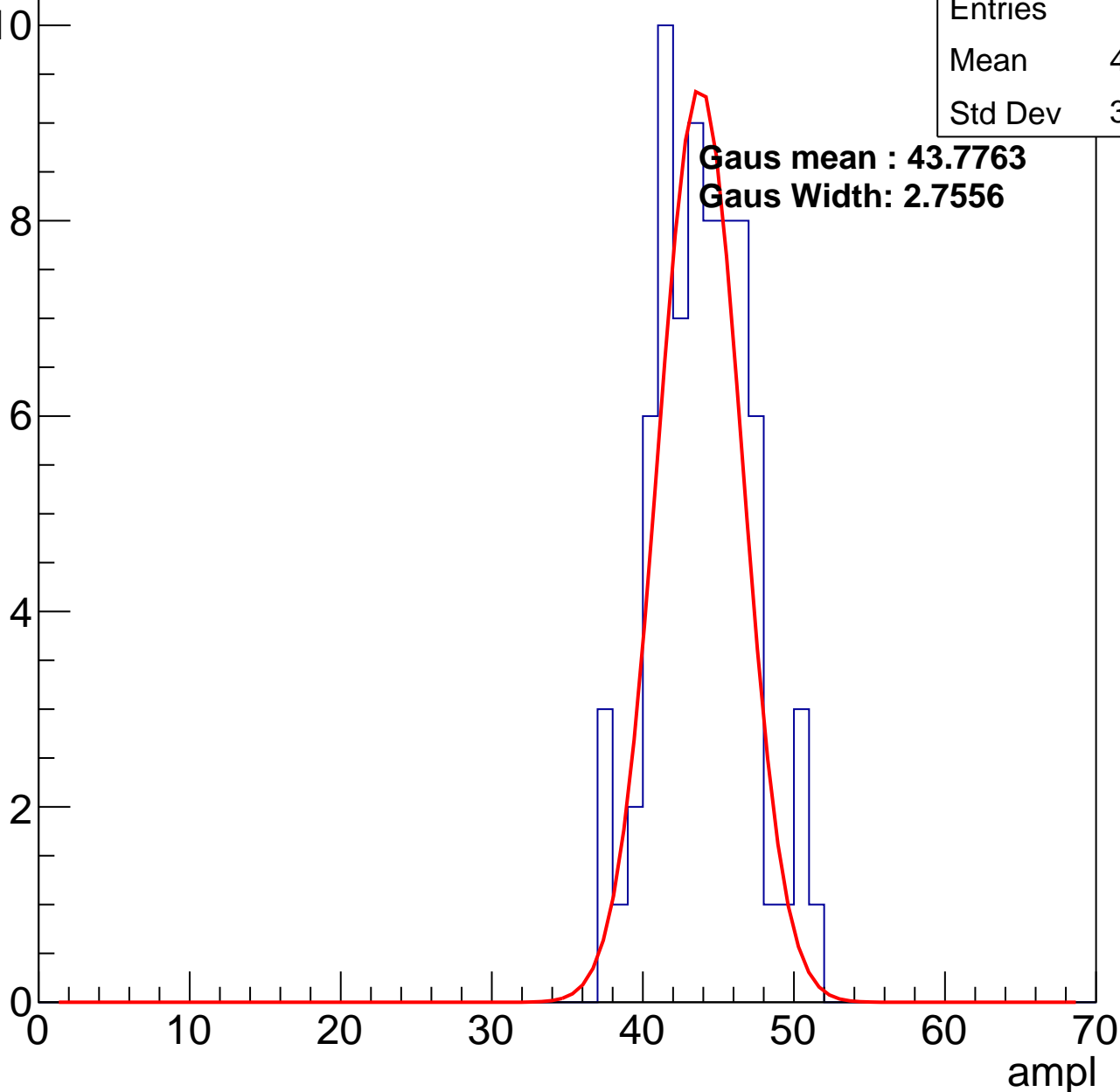
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	43.49
Std Dev	3.138

Gaus mean : 43.7763

Gaus Width: 2.7556

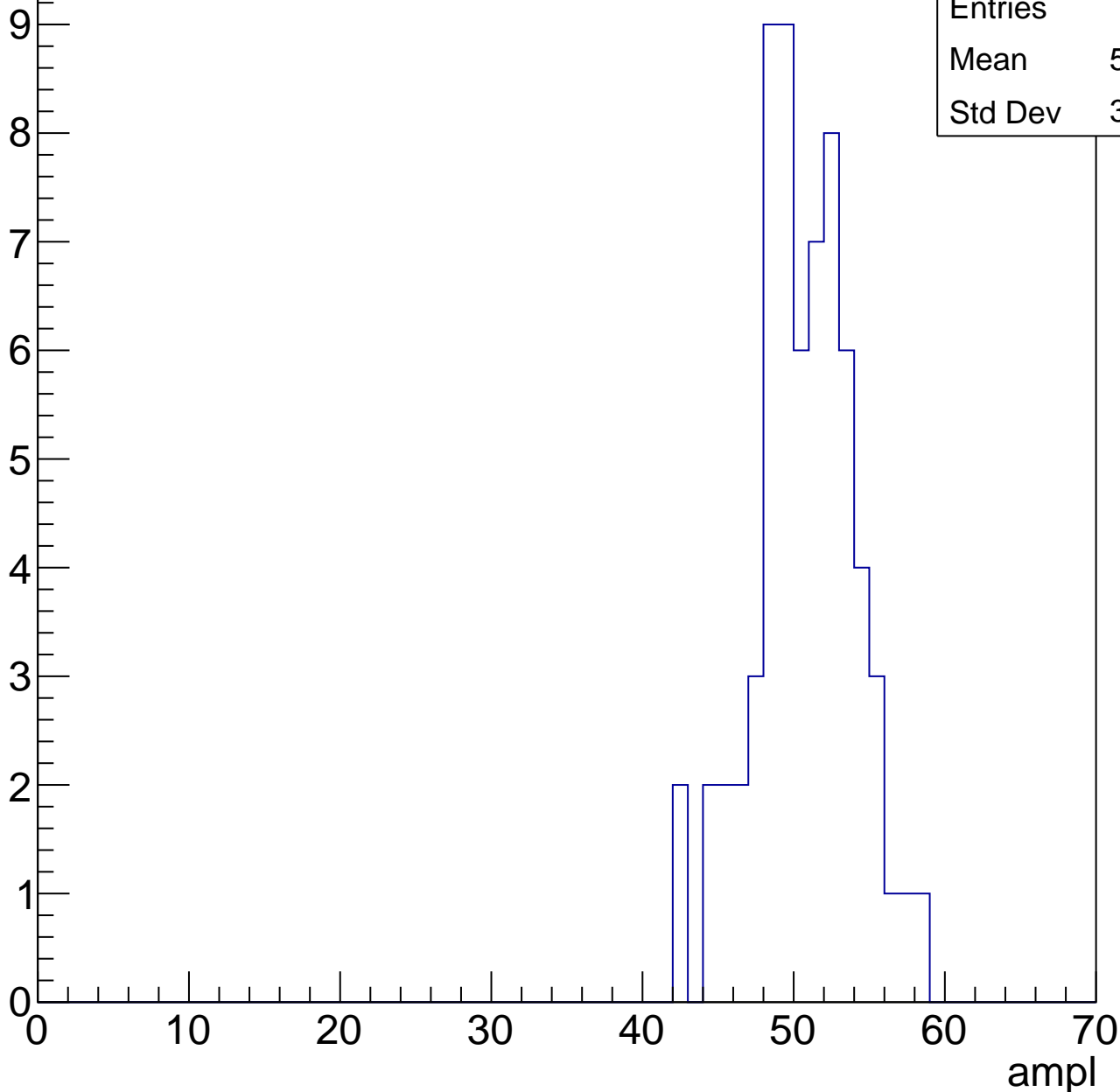


B1L003S, U21-ch3, adc3

calib_packv5_042523_0143.root, FC#13, port D2

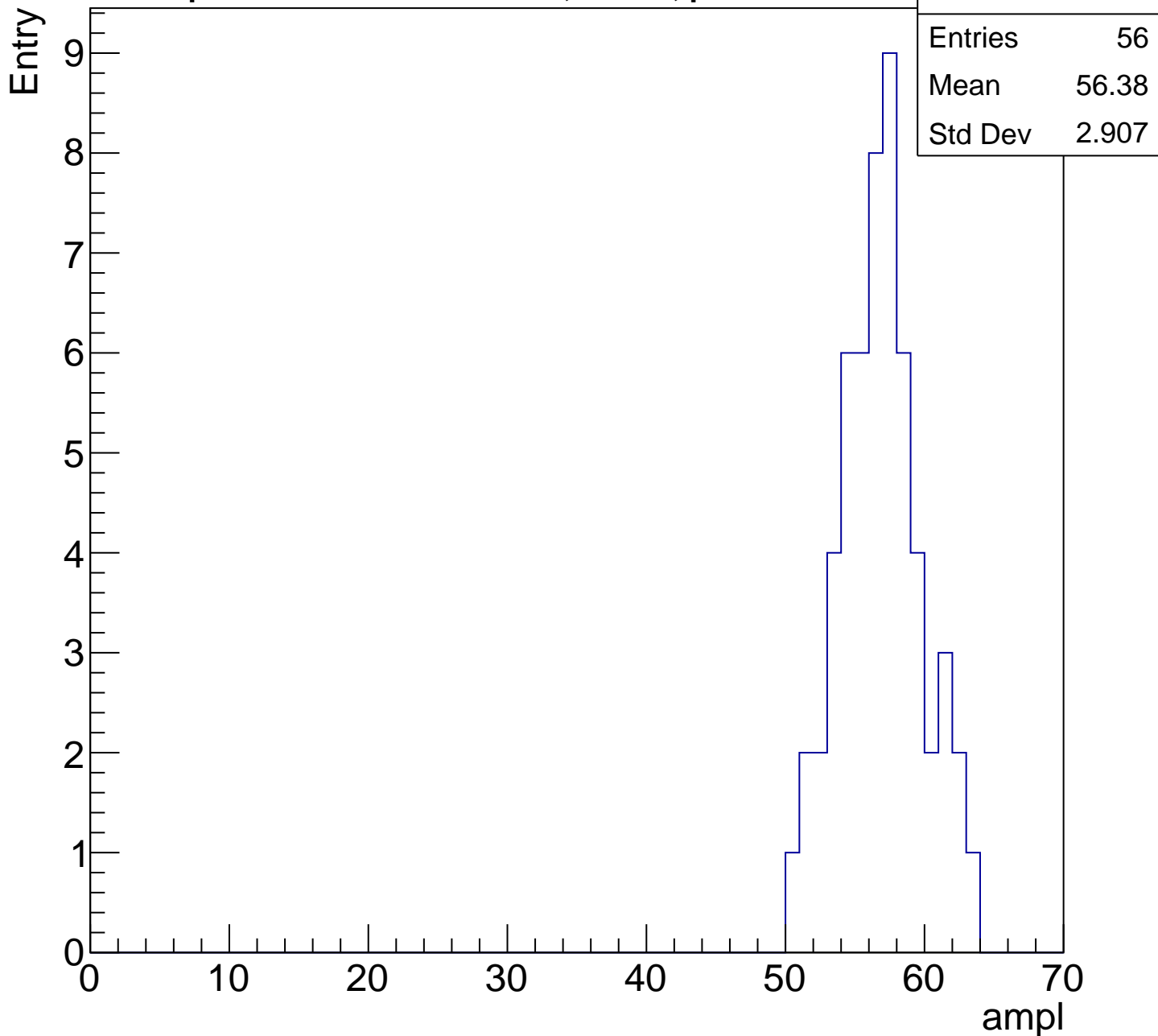
Entry

Entries	66
Mean	50.17
Std Dev	3.333



B1L003S, U21-ch3, adc4

calib_packv5_042523_0143.root, FC#13, port D2

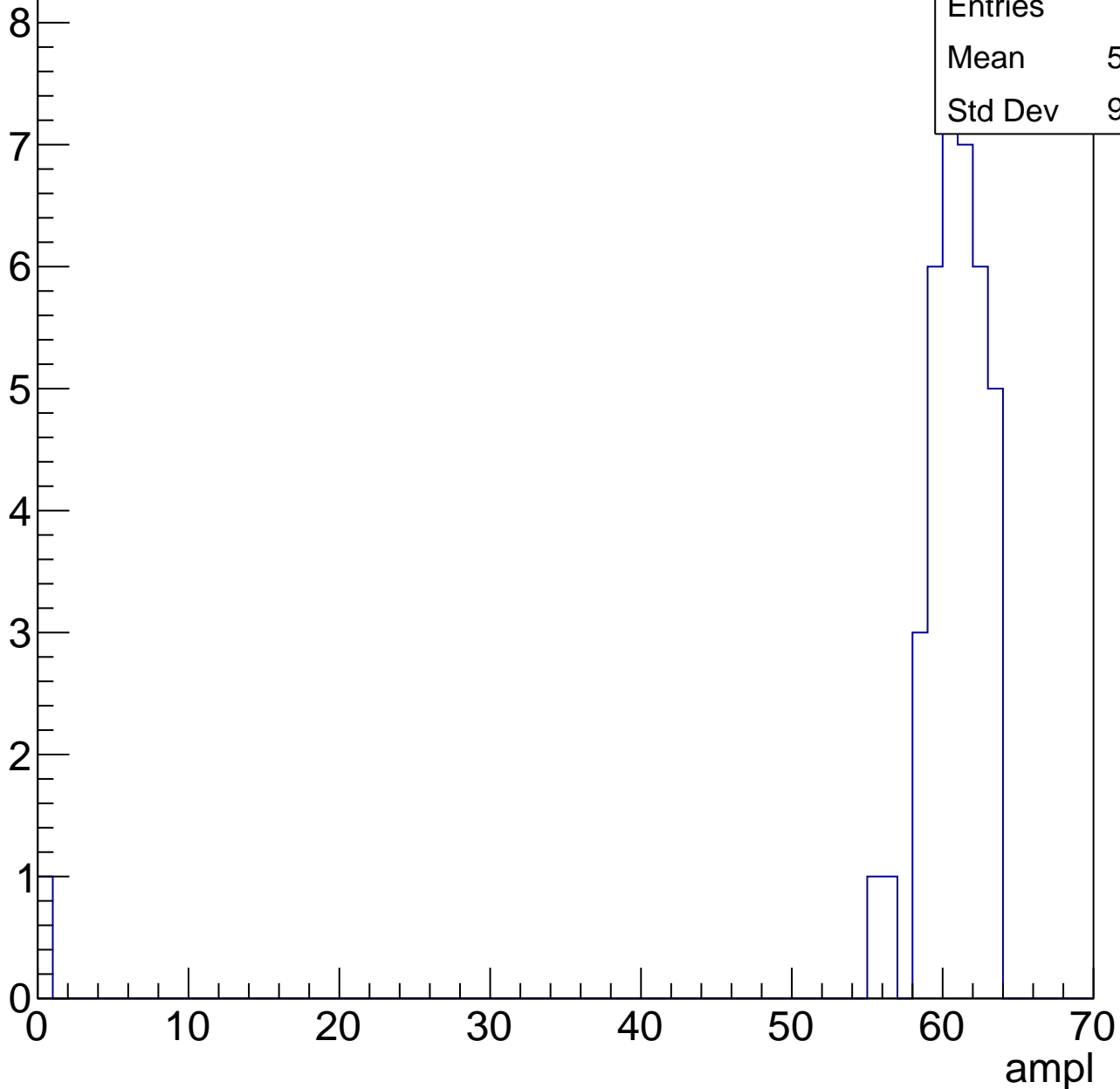


B1L003S, U21-ch3, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

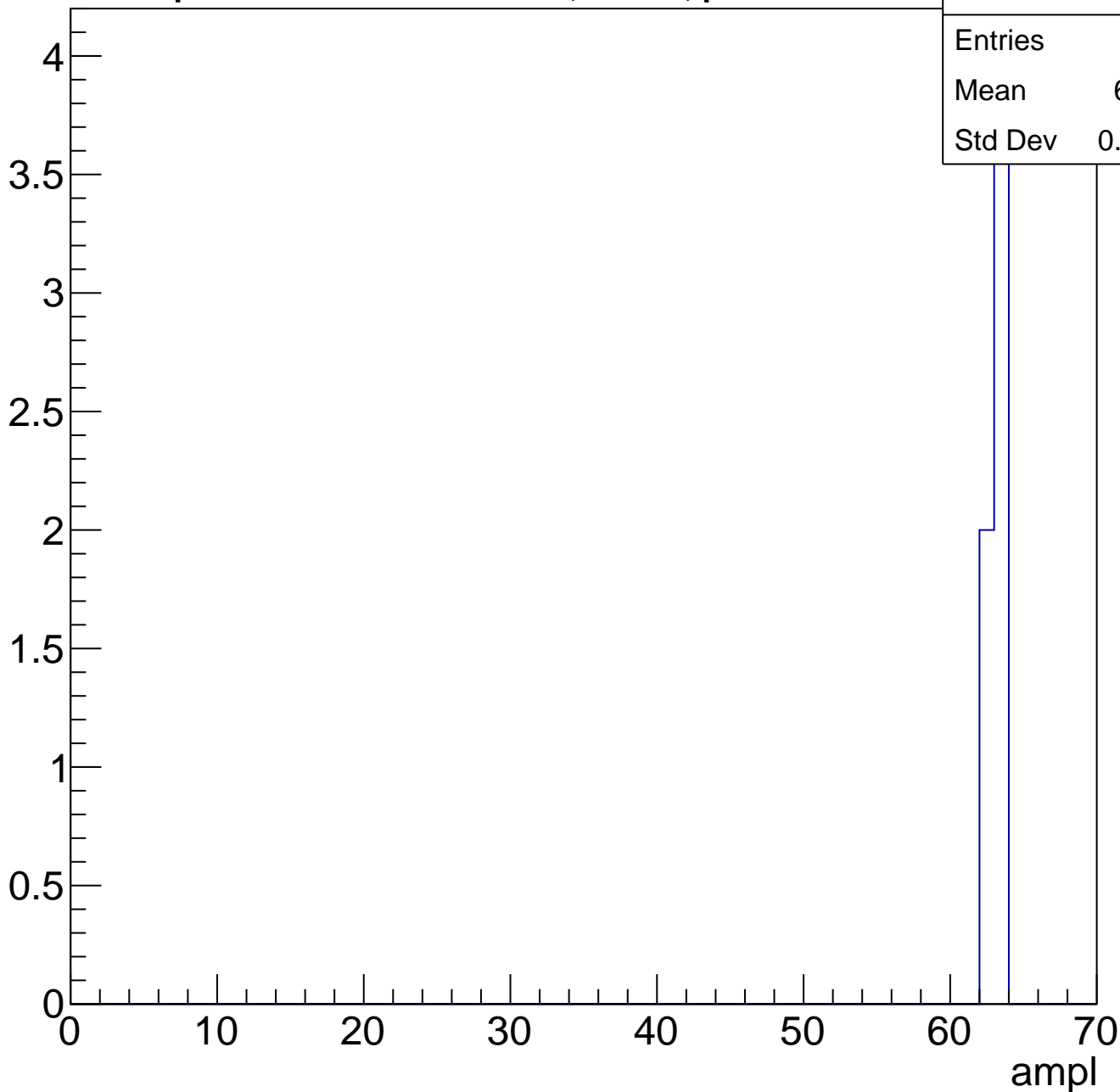
Entries	38
Mean	58.76
Std Dev	9.837



B1L003S, U21-ch3, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch3, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch4, adc0

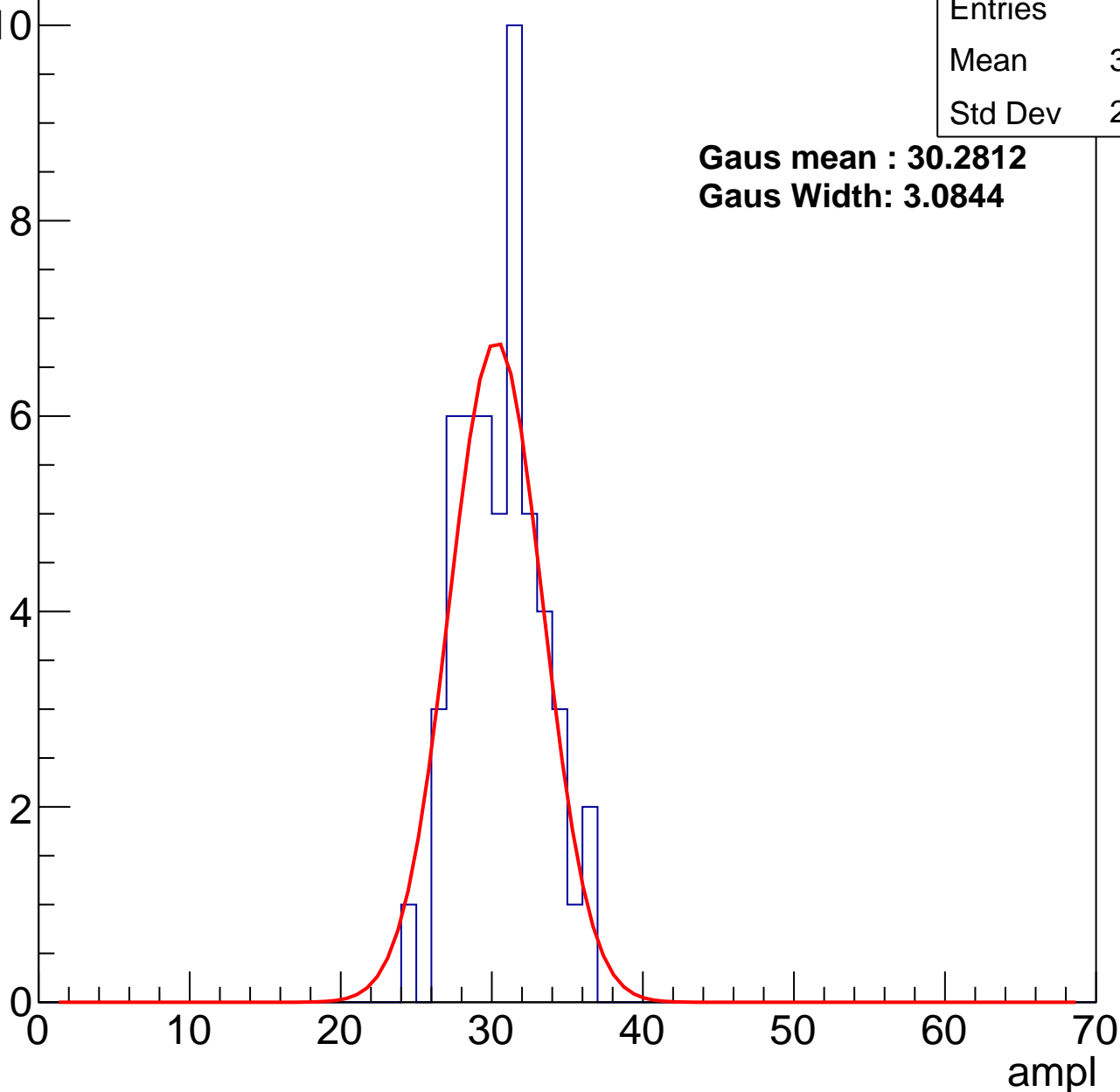
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	30.13
Std Dev	2.696

Gaus mean : 30.2812

Gaus Width: 3.0844



B1L003S, U21-ch4, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	36.81
Std Dev	3.334

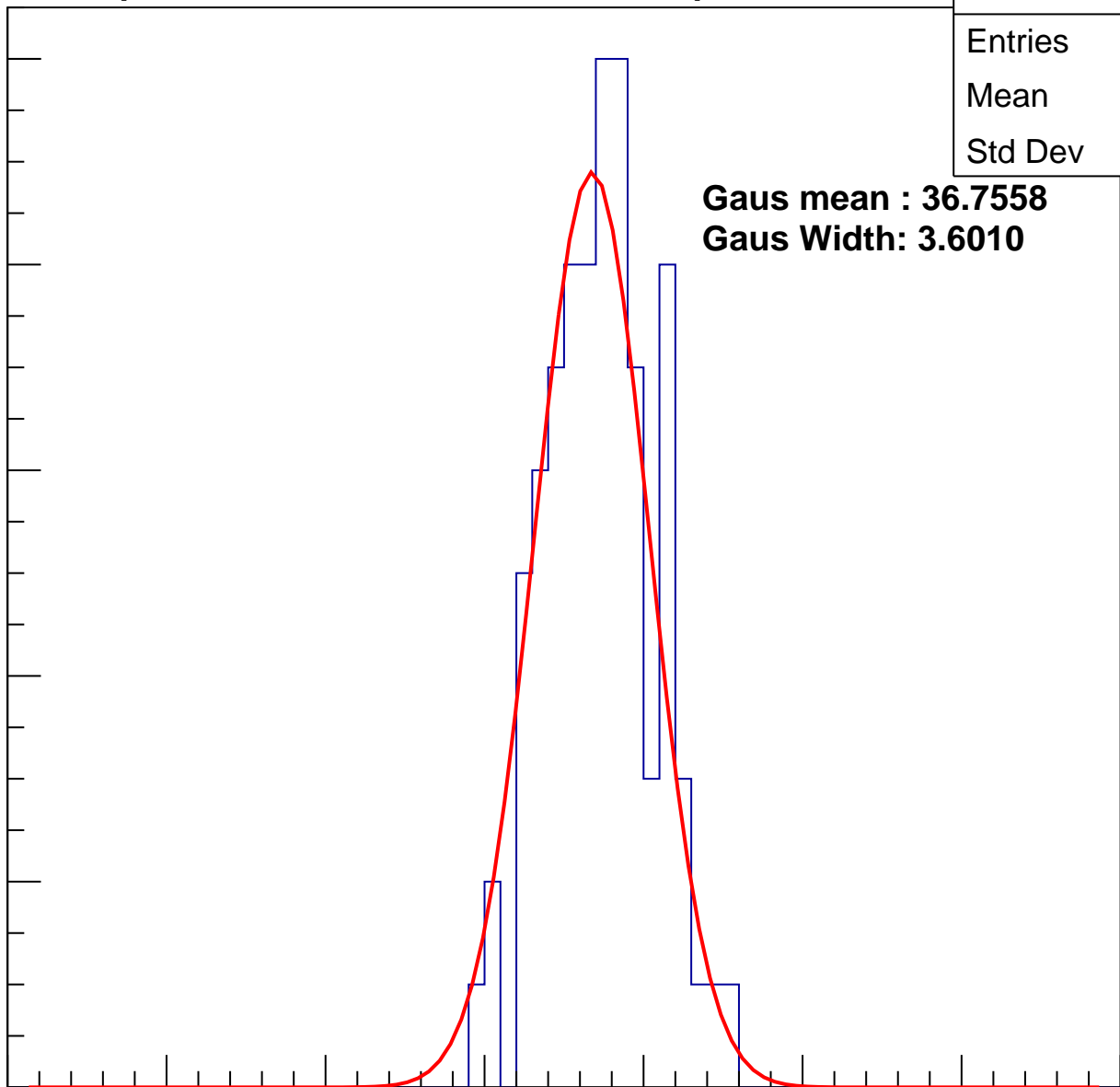
Gaus mean : 36.7558

Gaus Width: 3.6010

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch4, adc2

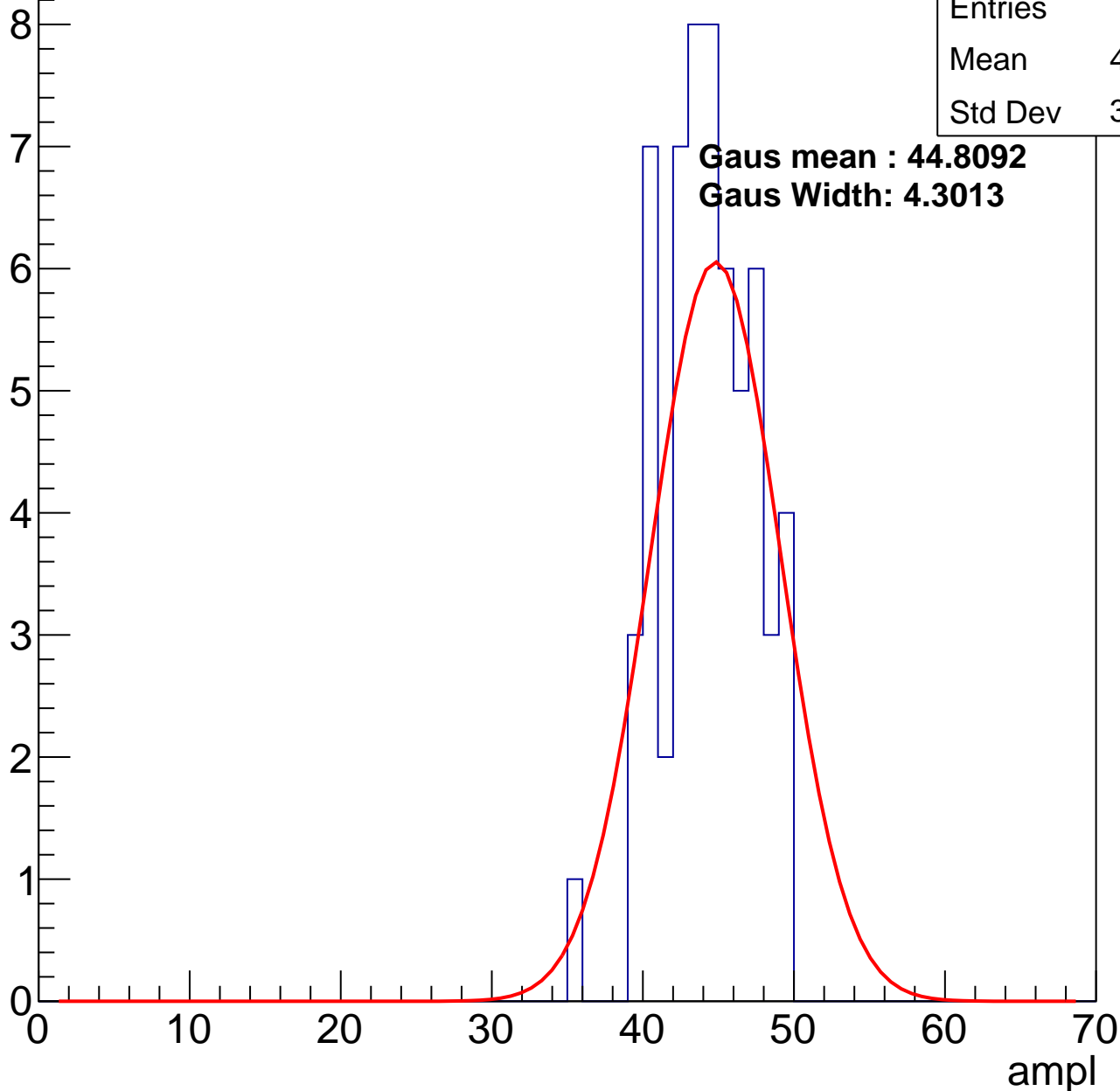
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.77
Std Dev	3.019

Gaus mean : 44.8092

Gaus Width: 4.3013

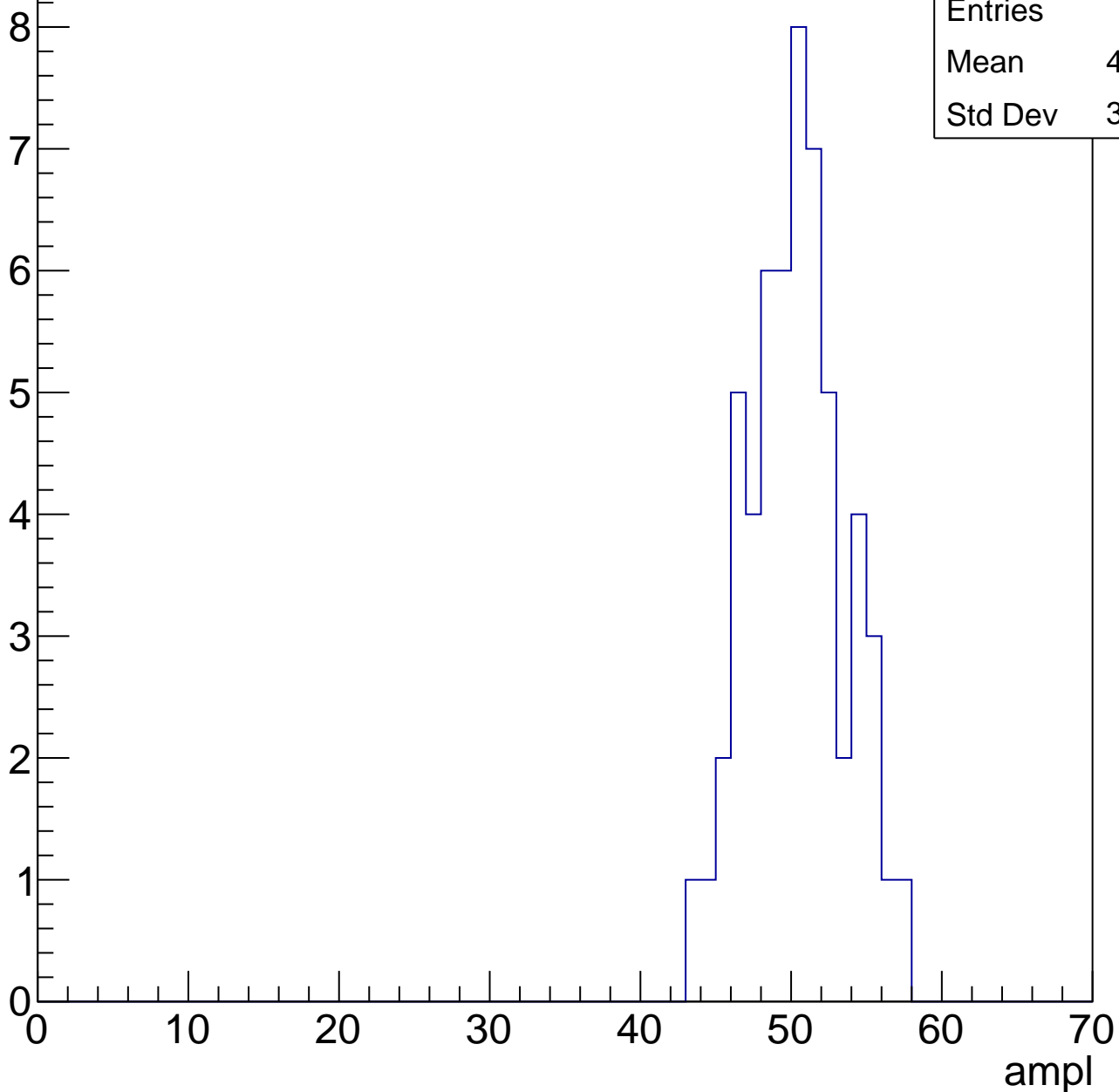


B1L003S, U21-ch4, adc3

calib_packv5_042523_0143.root, FC#13, port D2

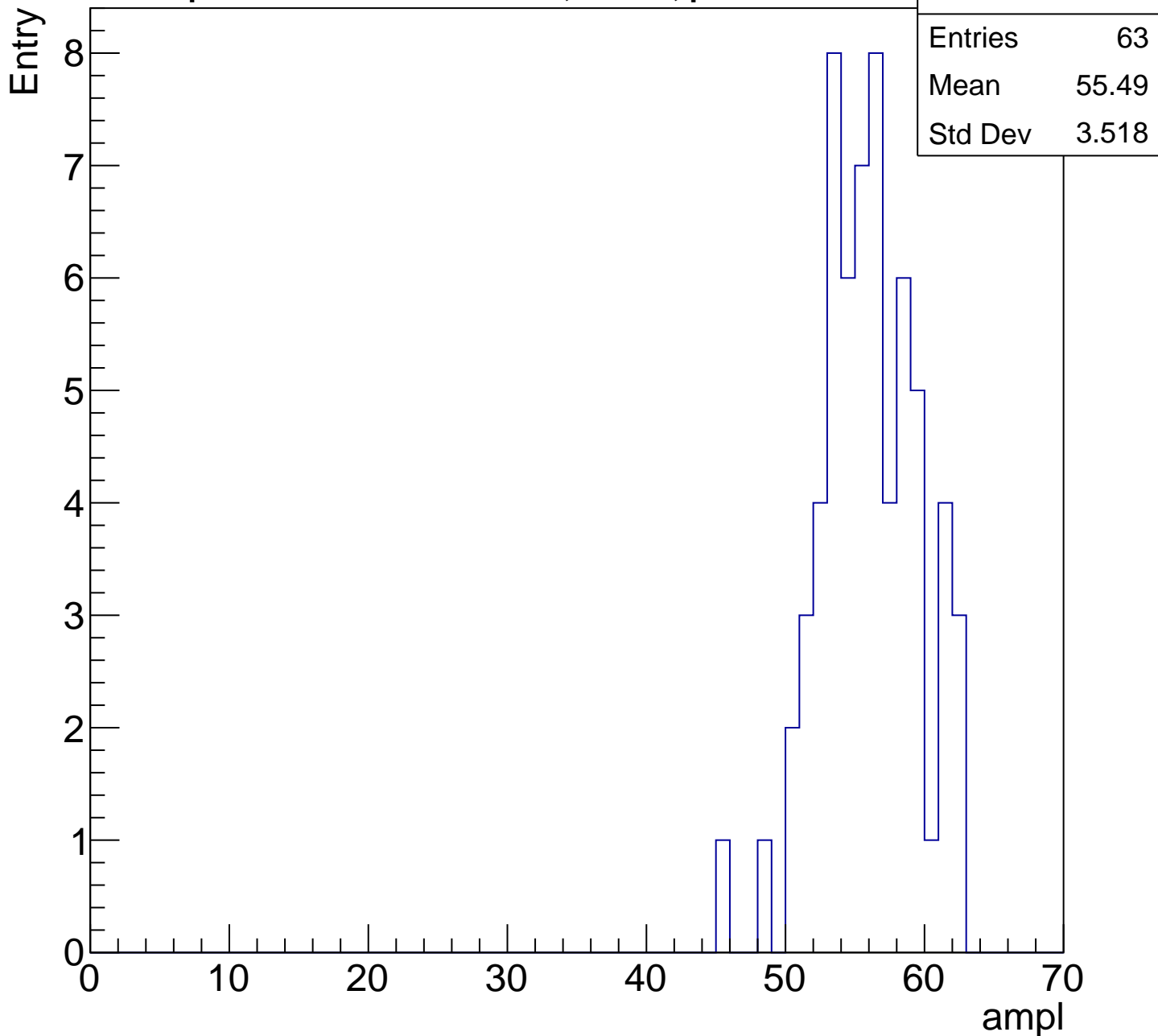
Entry

Entries	56
Mean	49.89
Std Dev	3.132



B1L003S, U21-ch4, adc4

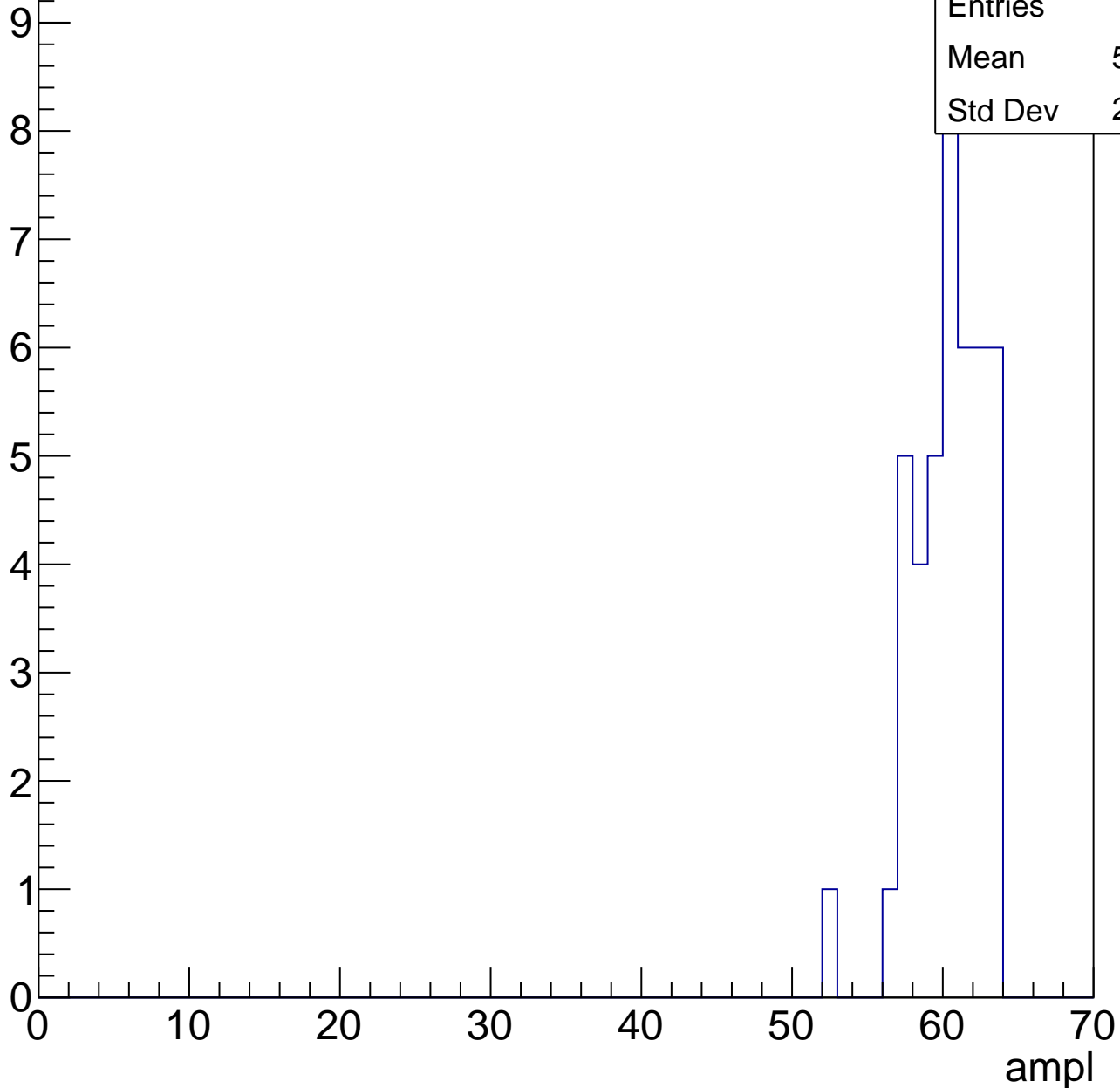
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U21-ch4, adc5

calib_packv5_042523_0143.root, FC#13, port D2

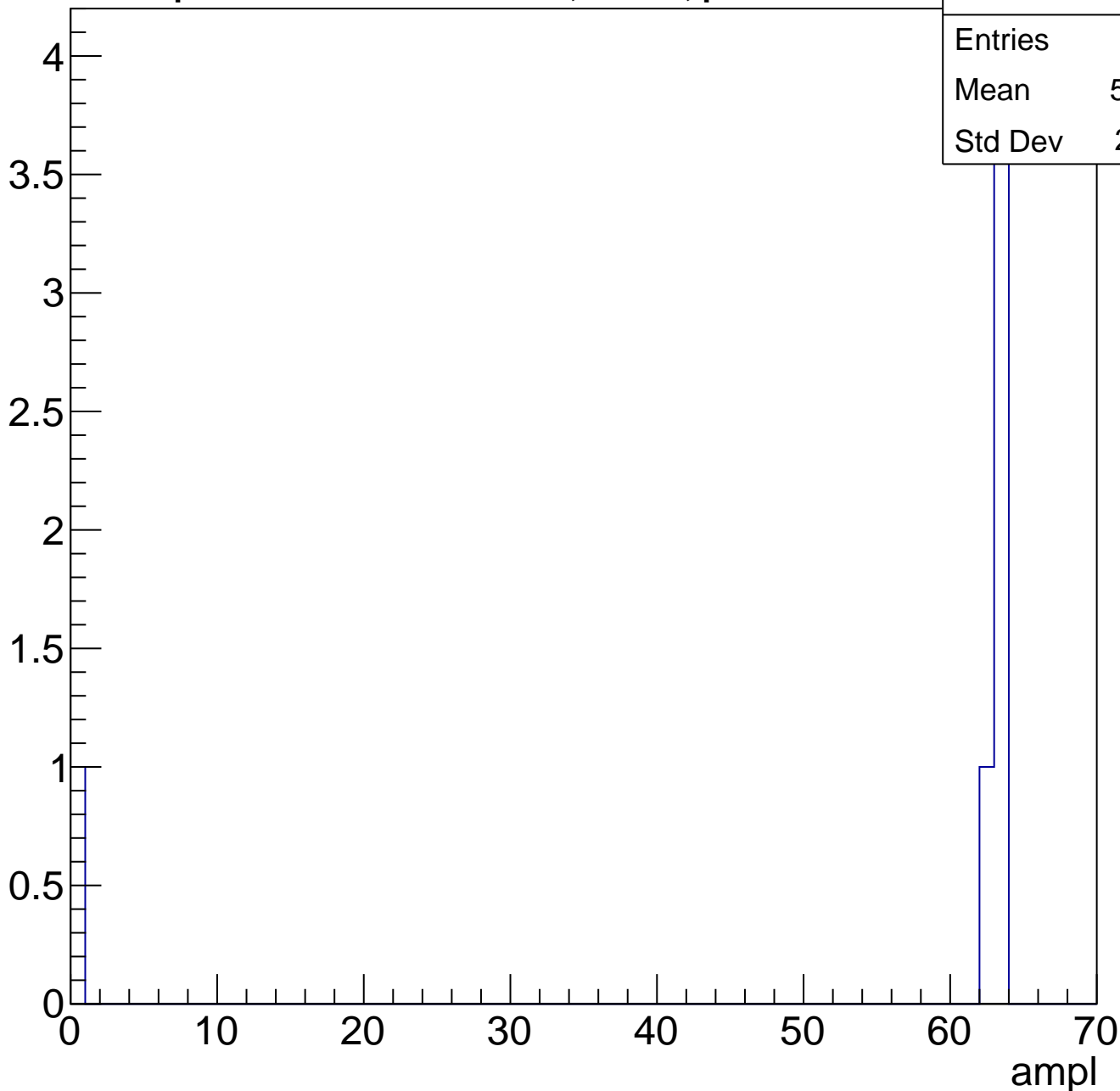
Entry



B1L003S, U21-ch4, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch4, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch5, adc0

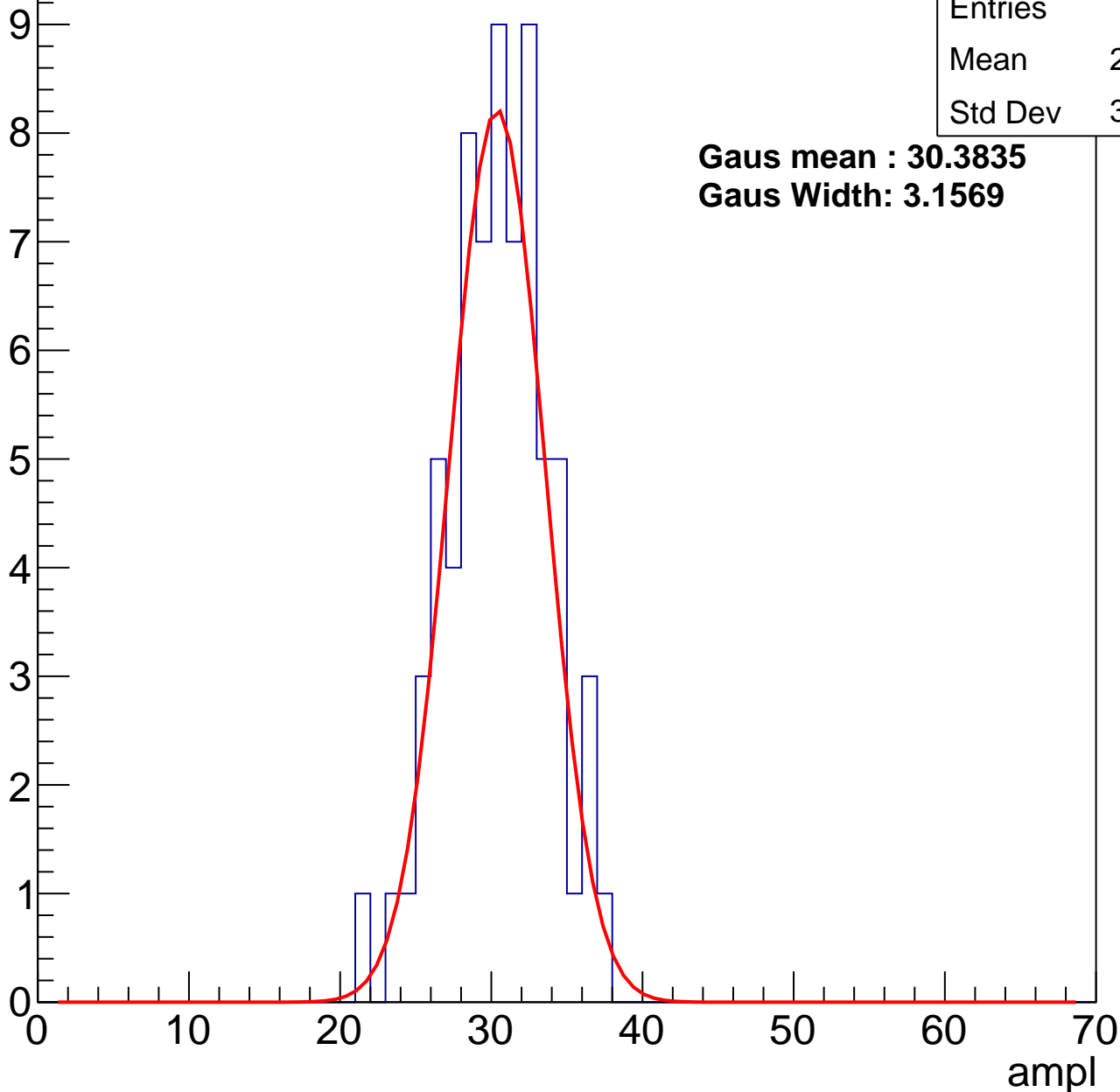
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.97
Std Dev	3.264

Gaus mean : 30.3835

Gaus Width: 3.1569



B1L003S, U21-ch5, adc1

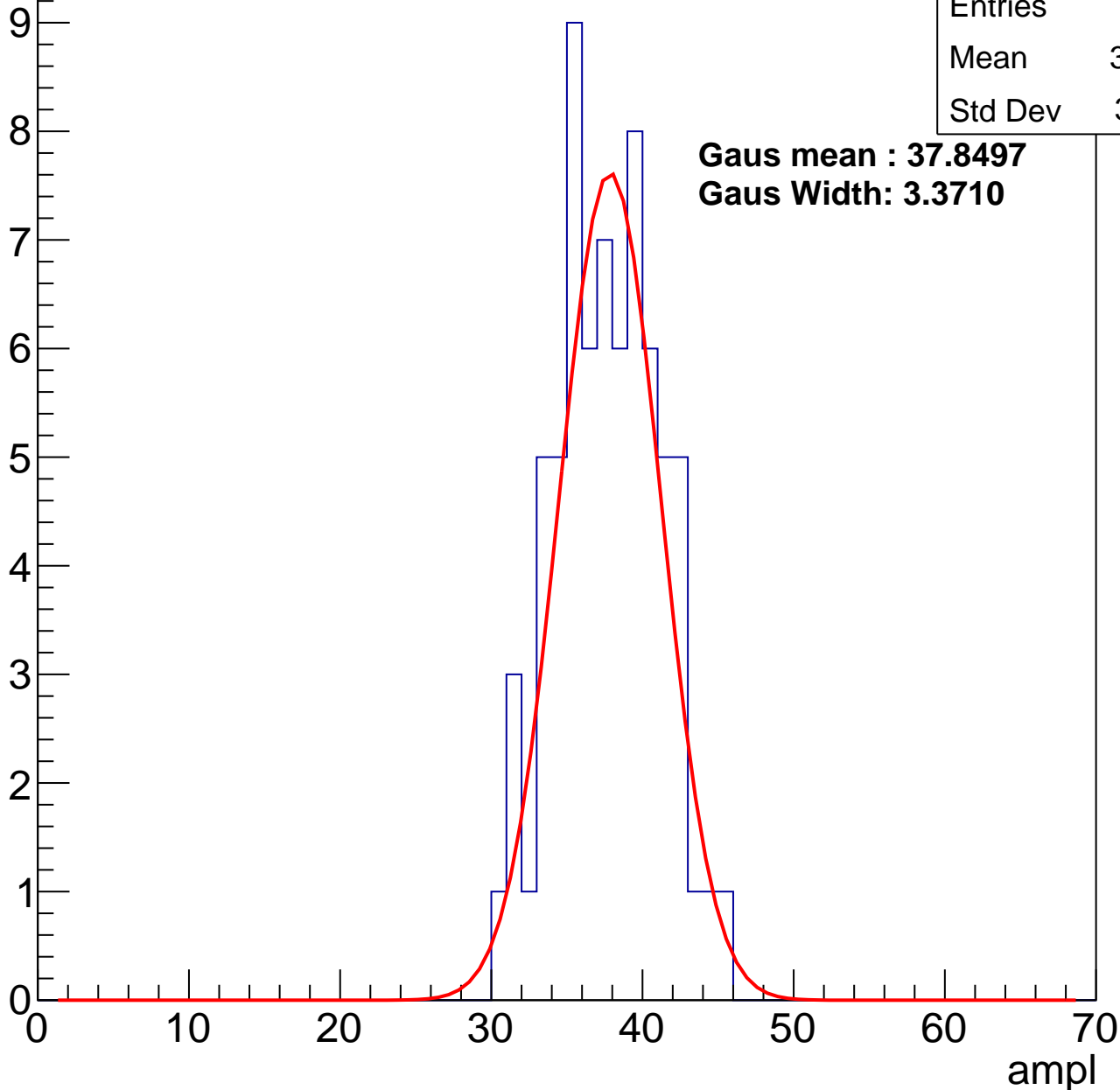
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	37.24
Std Dev	3.361

Gaus mean : 37.8497

Gaus Width: 3.3710



B1L003S, U21-ch5, adc2

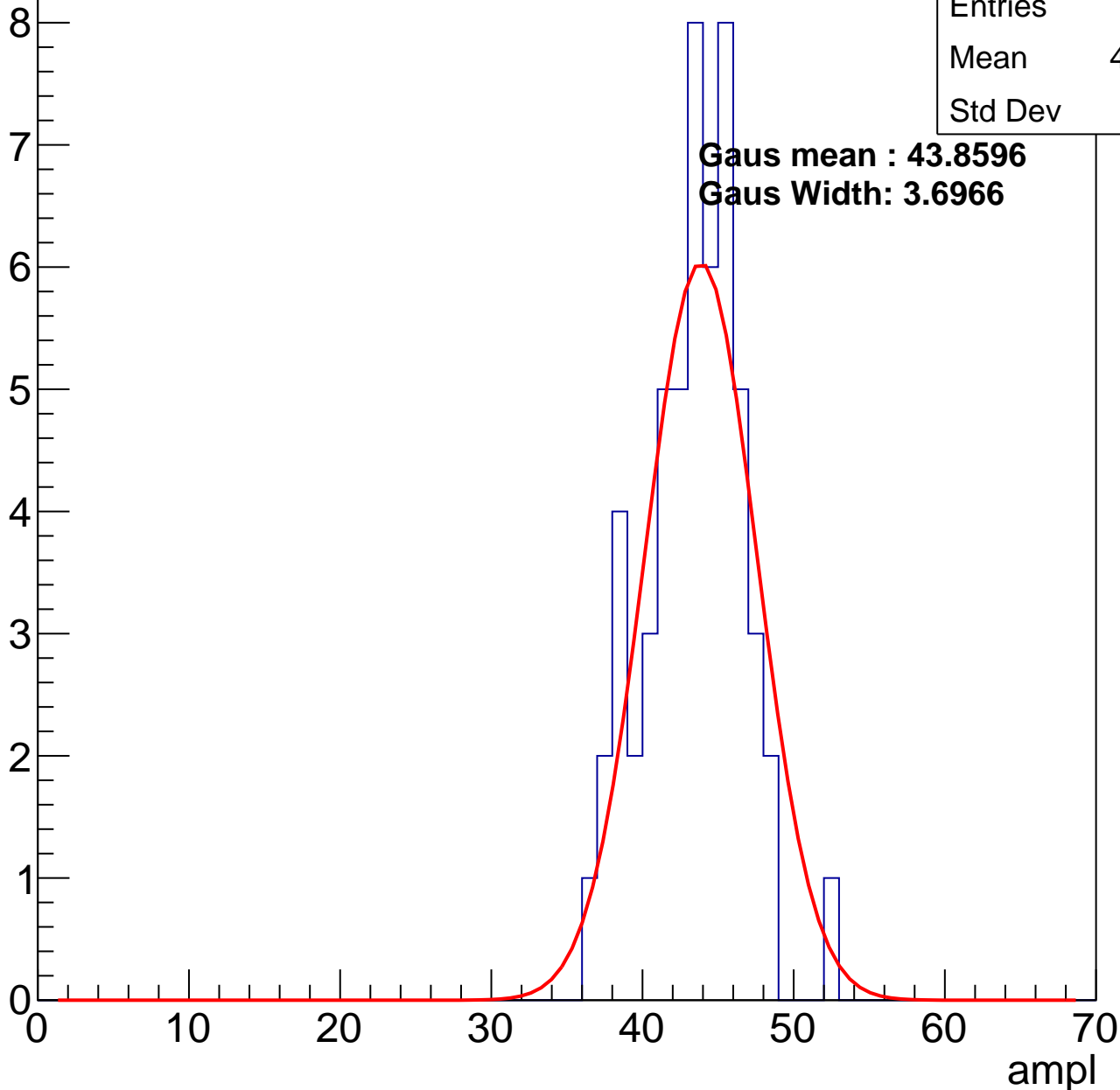
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.95
Std Dev	3.21

Gaus mean : 43.8596

Gaus Width: 3.6966

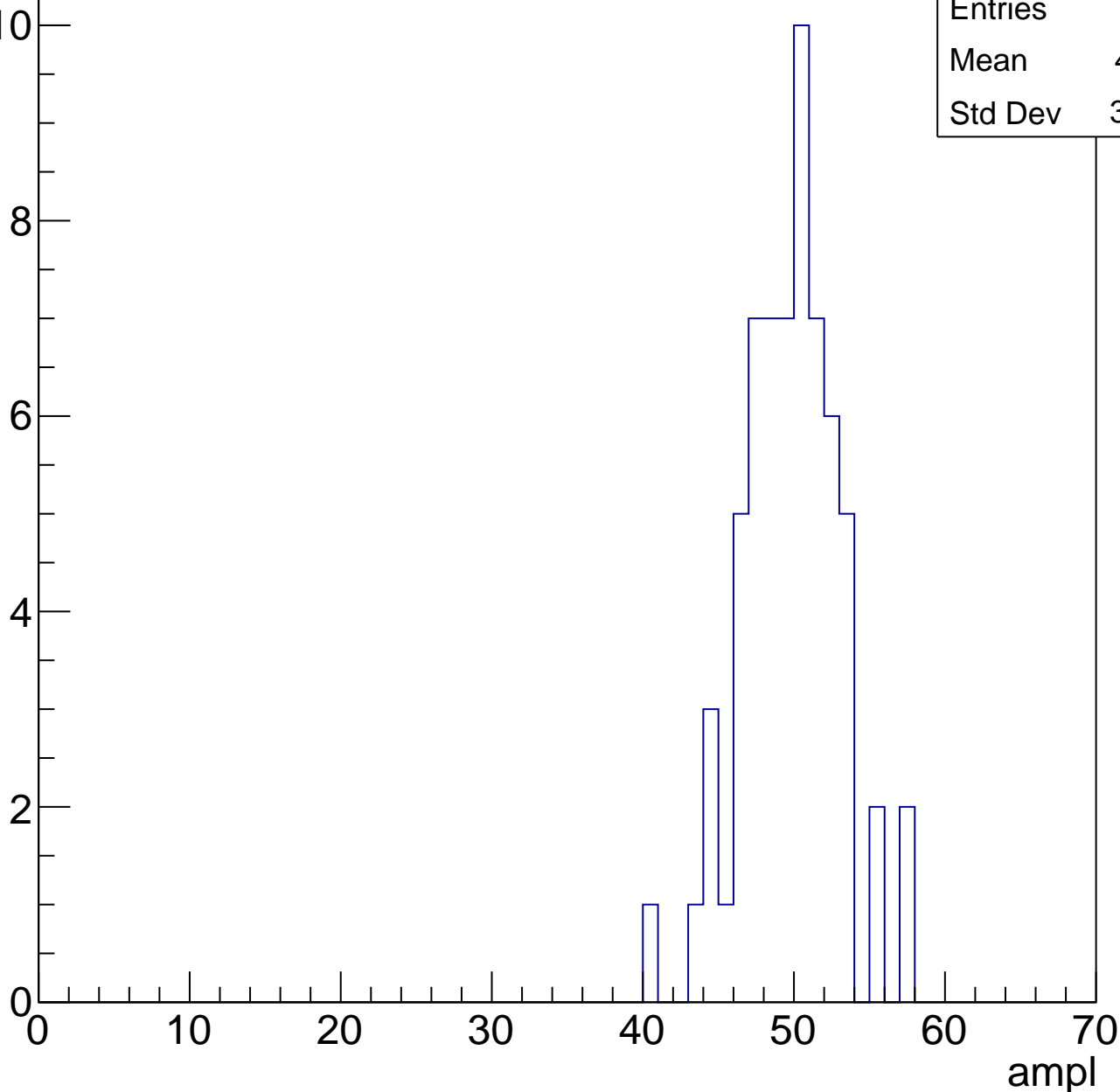


B1L003S, U21-ch5, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	49.31
Std Dev	3.196

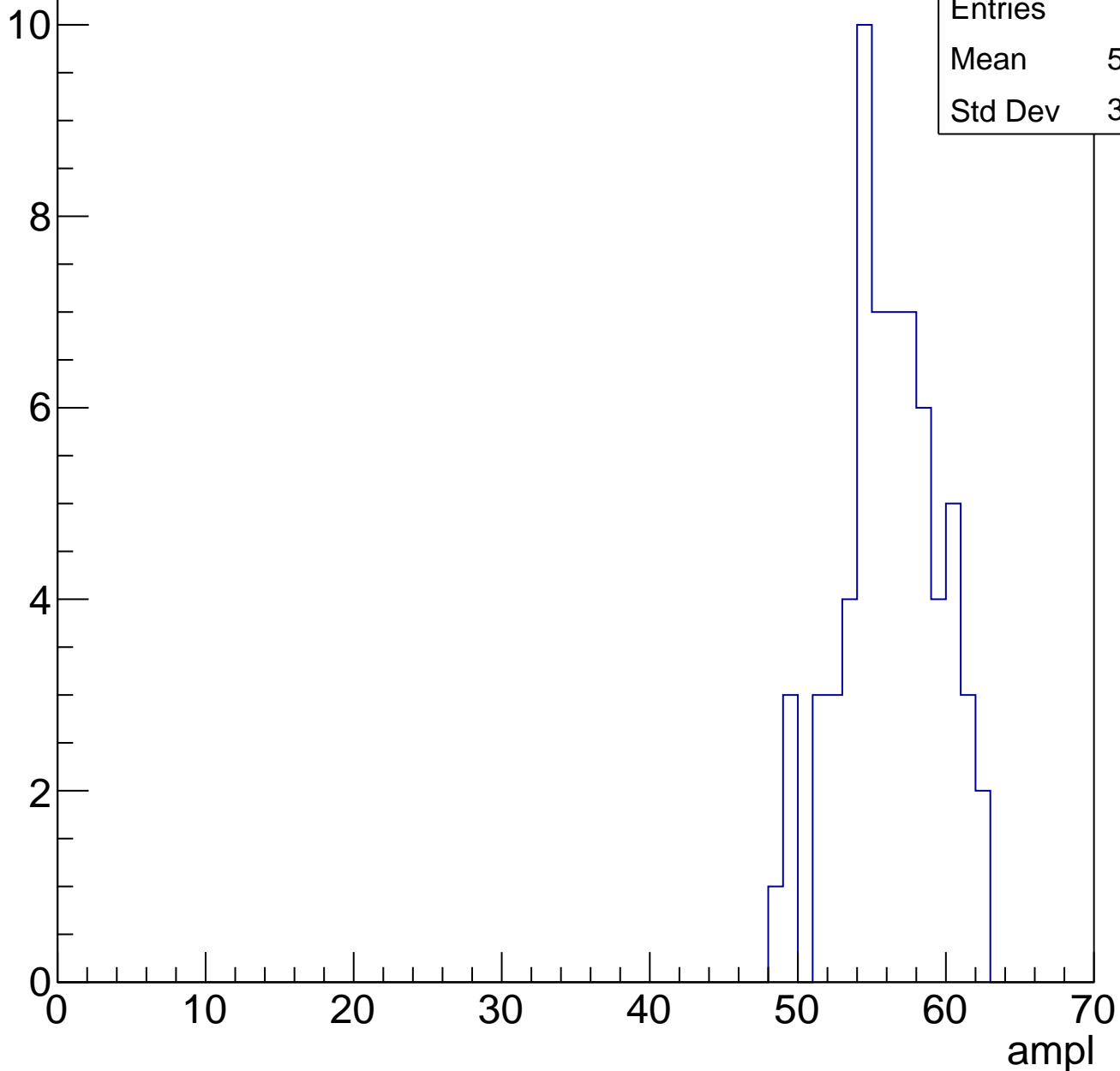


B1L003S, U21-ch5, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	55.74
Std Dev	3.297

Entry

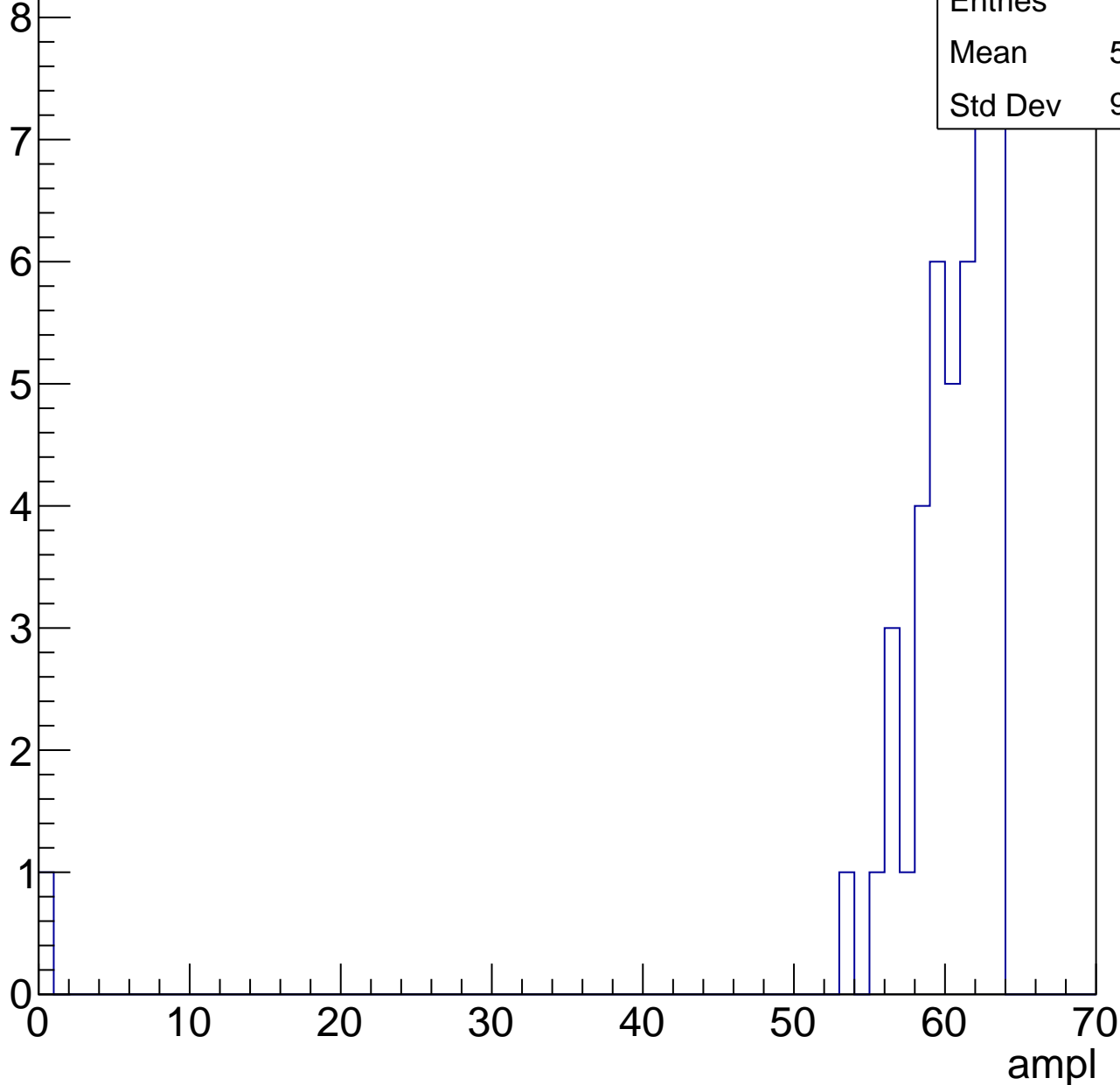


B1L003S, U21-ch5, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	58.75
Std Dev	9.286



B1L003S, U21-ch5, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch5, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch6, adc0

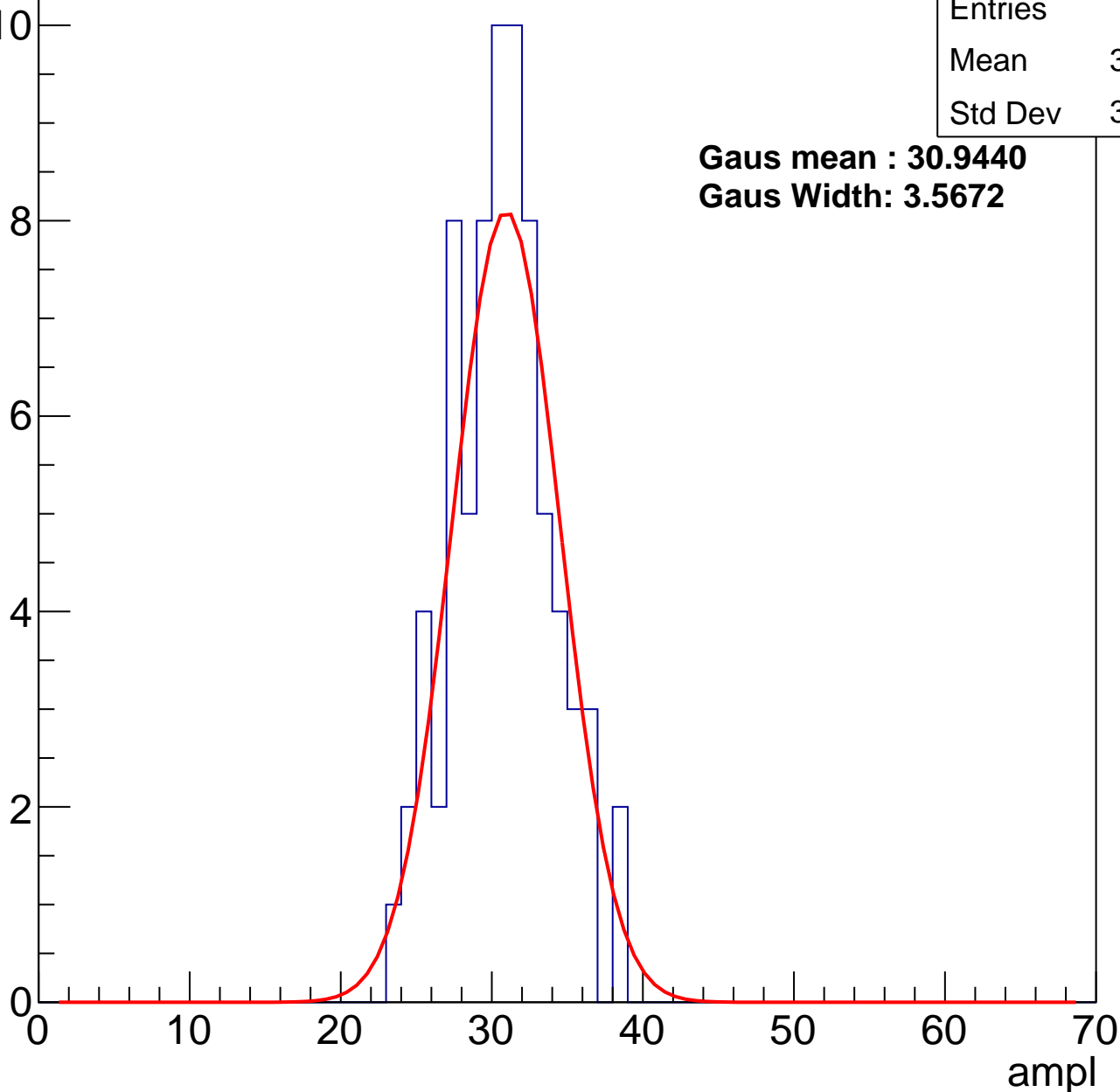
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	30.23
Std Dev	3.289

Gaus mean : 30.9440

Gaus Width: 3.5672



B1L003S, U21-ch6, adc1

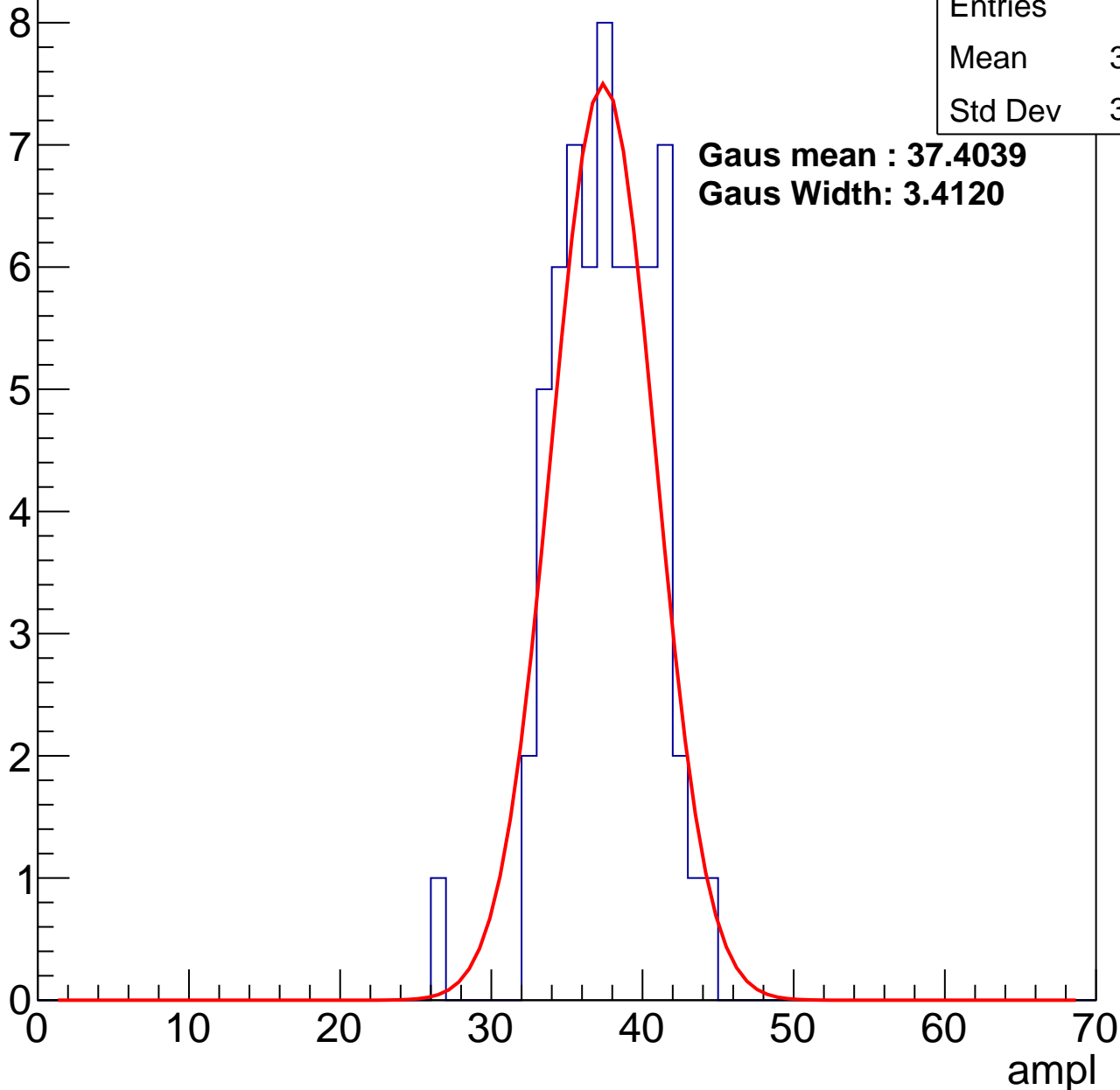
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.12
Std Dev	3.233

Gaus mean : 37.4039

Gaus Width: 3.4120



B1L003S, U21-ch6, adc2

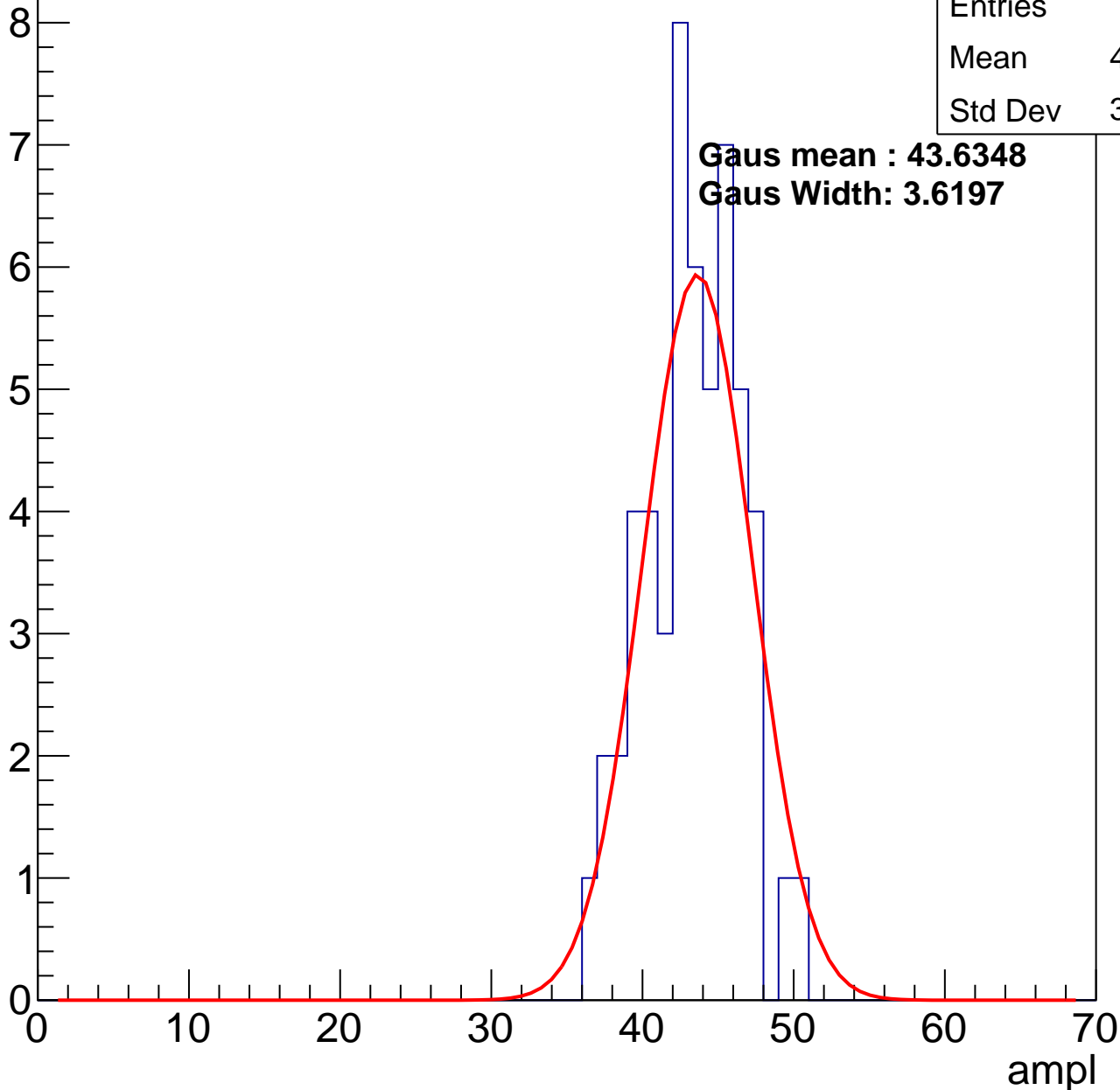
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	42.85
Std Dev	3.123

Gaus mean : 43.6348

Gaus Width: 3.6197

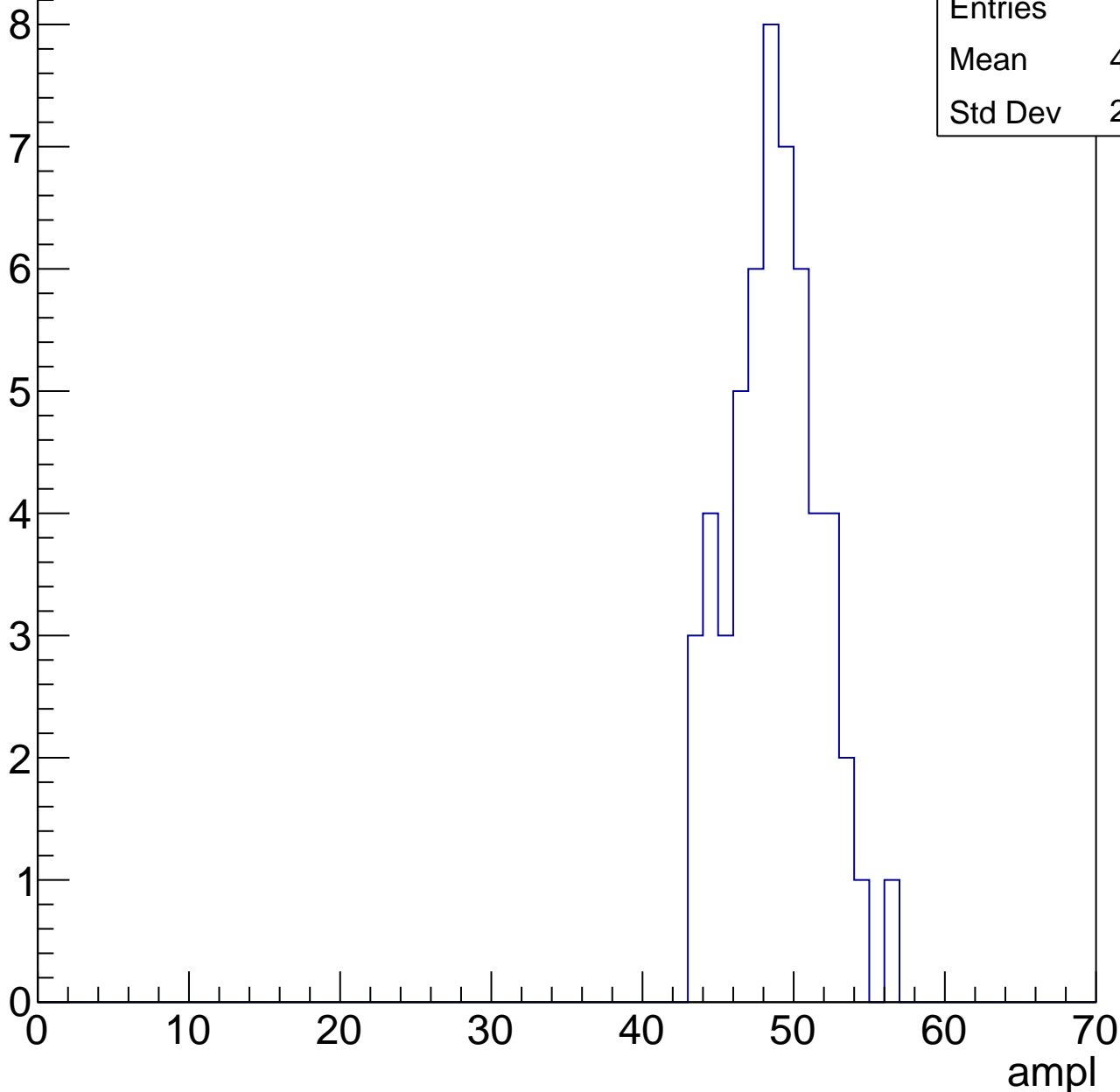


B1L003S, U21-ch6, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	48.28
Std Dev	2.947

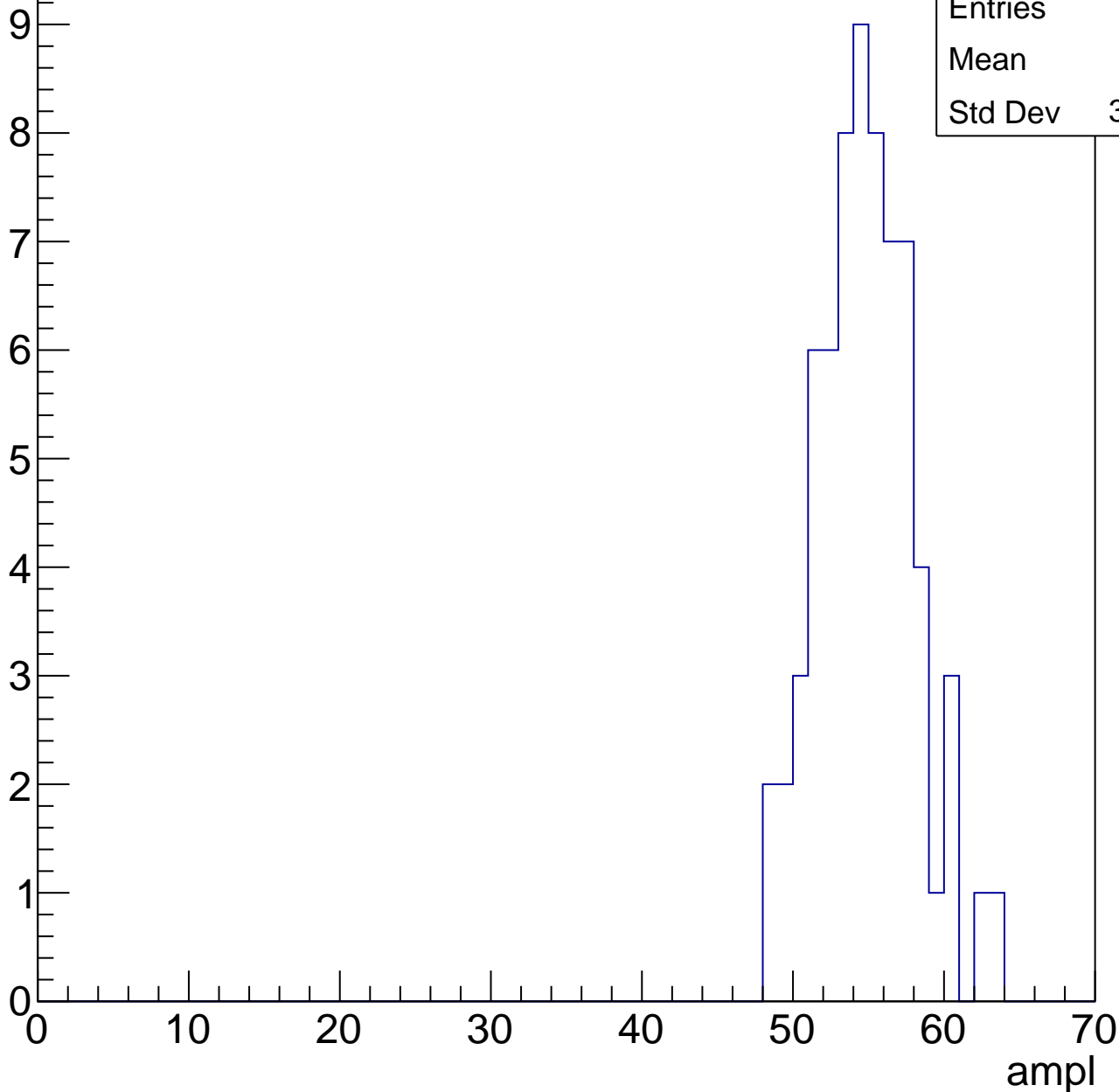


B1L003S, U21-ch6, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	54.4
Std Dev	3.177



B1L003S, U21-ch6, adc5

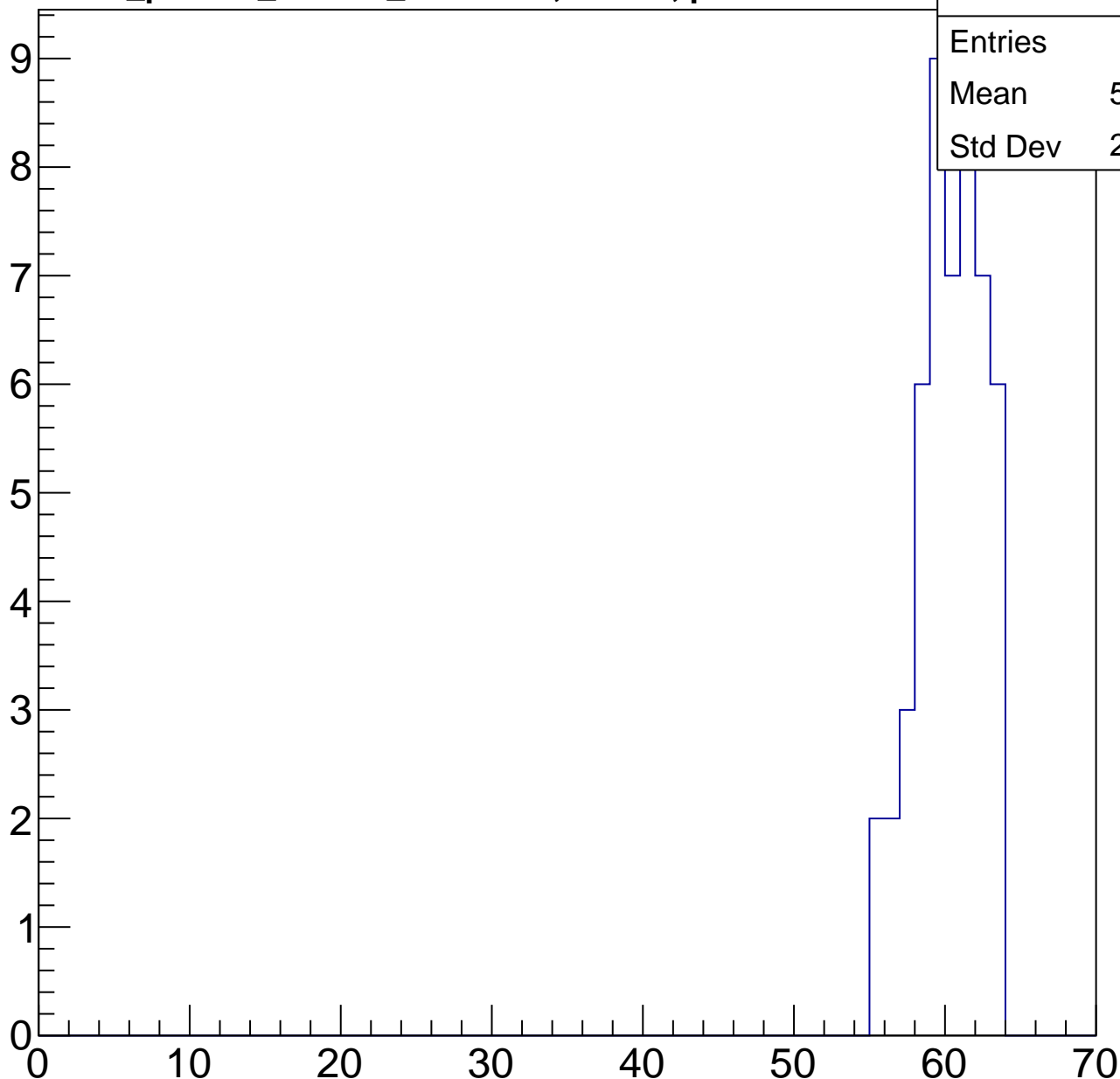
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.86
Std Dev	2.133

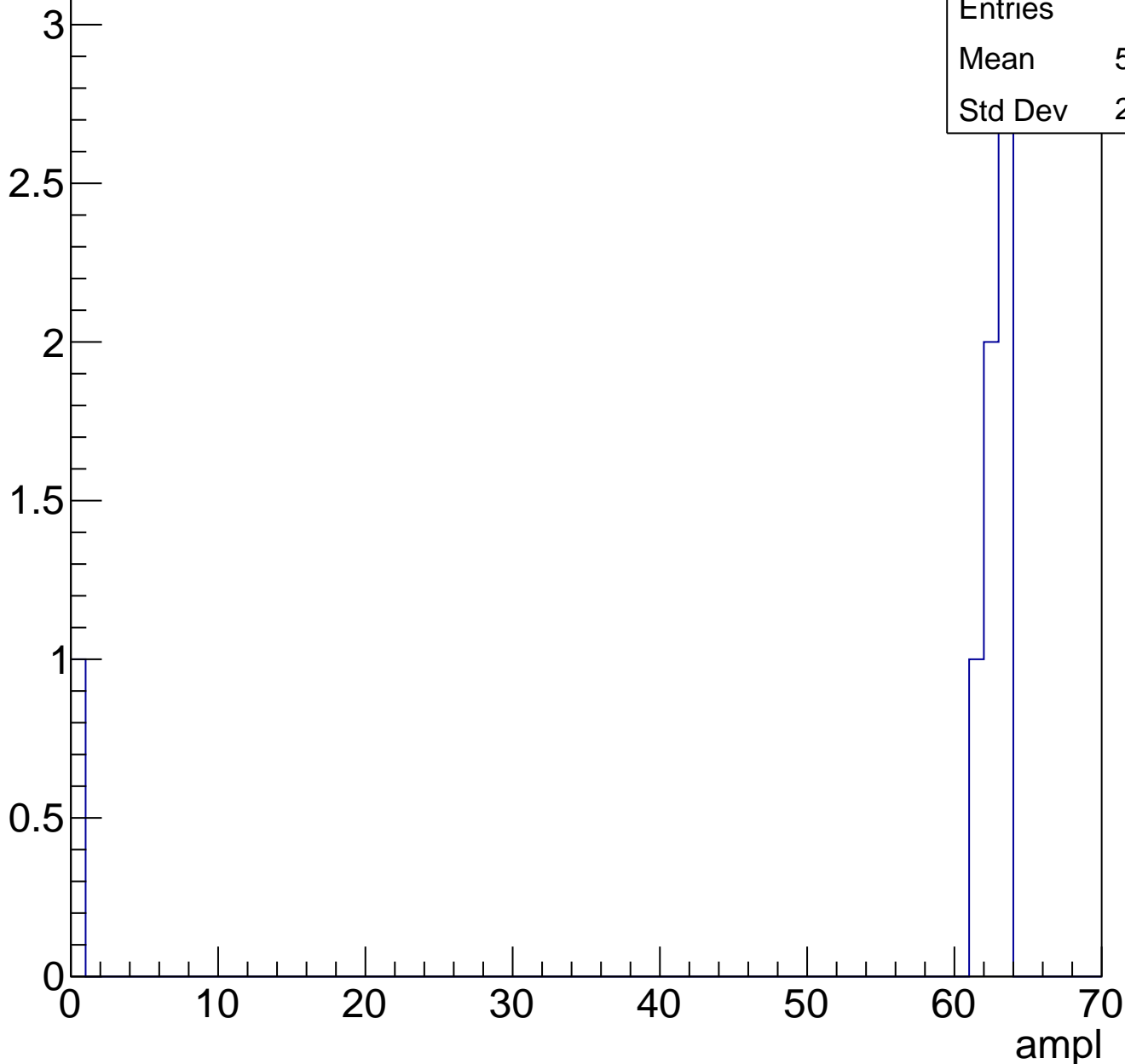
ampl



B1L003S, U21-ch6, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	7
Mean	53.43
Std Dev	21.82

B1L003S, U21-ch6, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U21-ch7, adc0

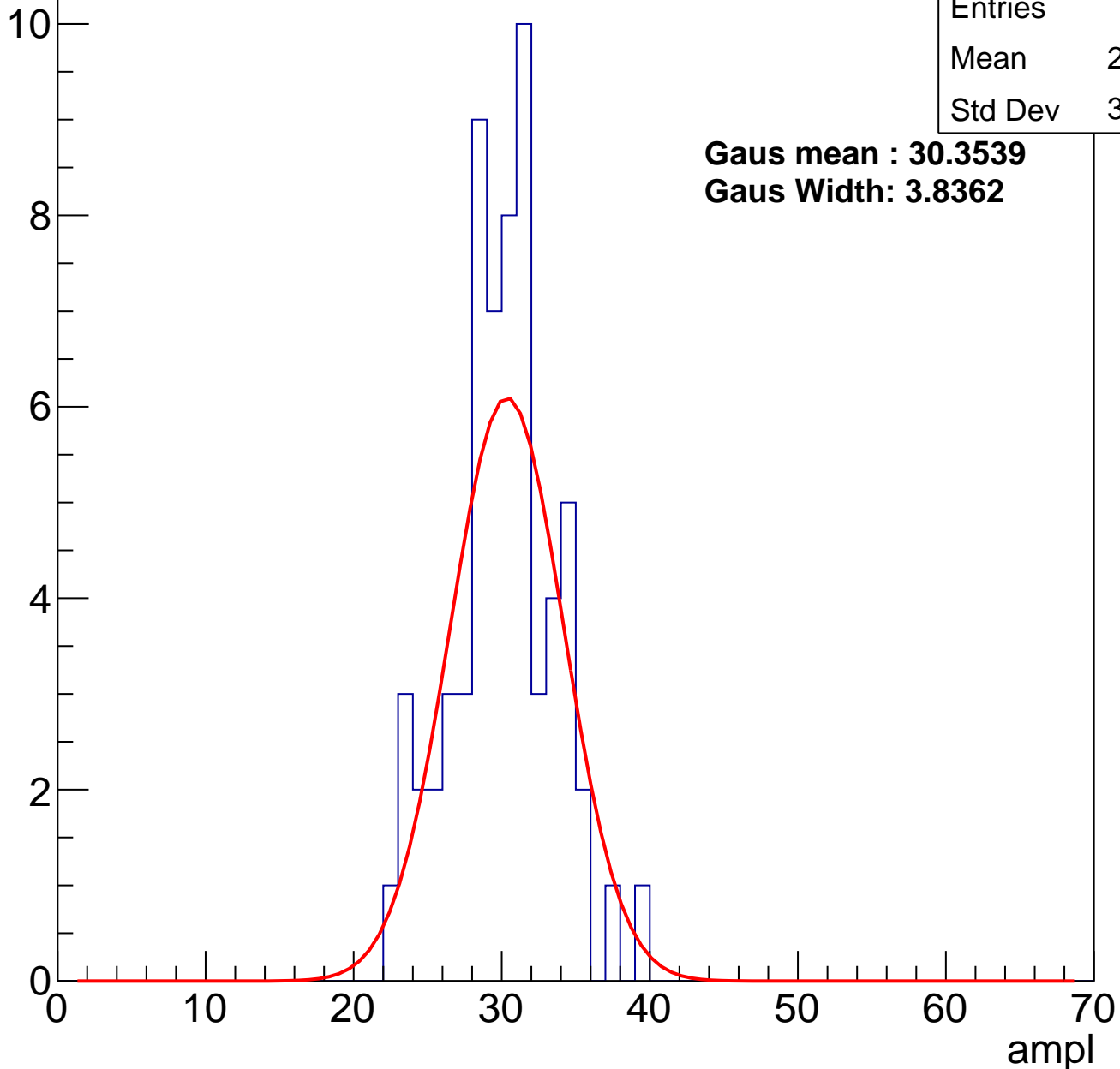
calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	29.64
Std Dev	3.448

Gaus mean : 30.3539

Gaus Width: 3.8362

Entry



B1L003S, U21-ch7, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	35.93
Std Dev	3.205

Gaus mean : 36.1310

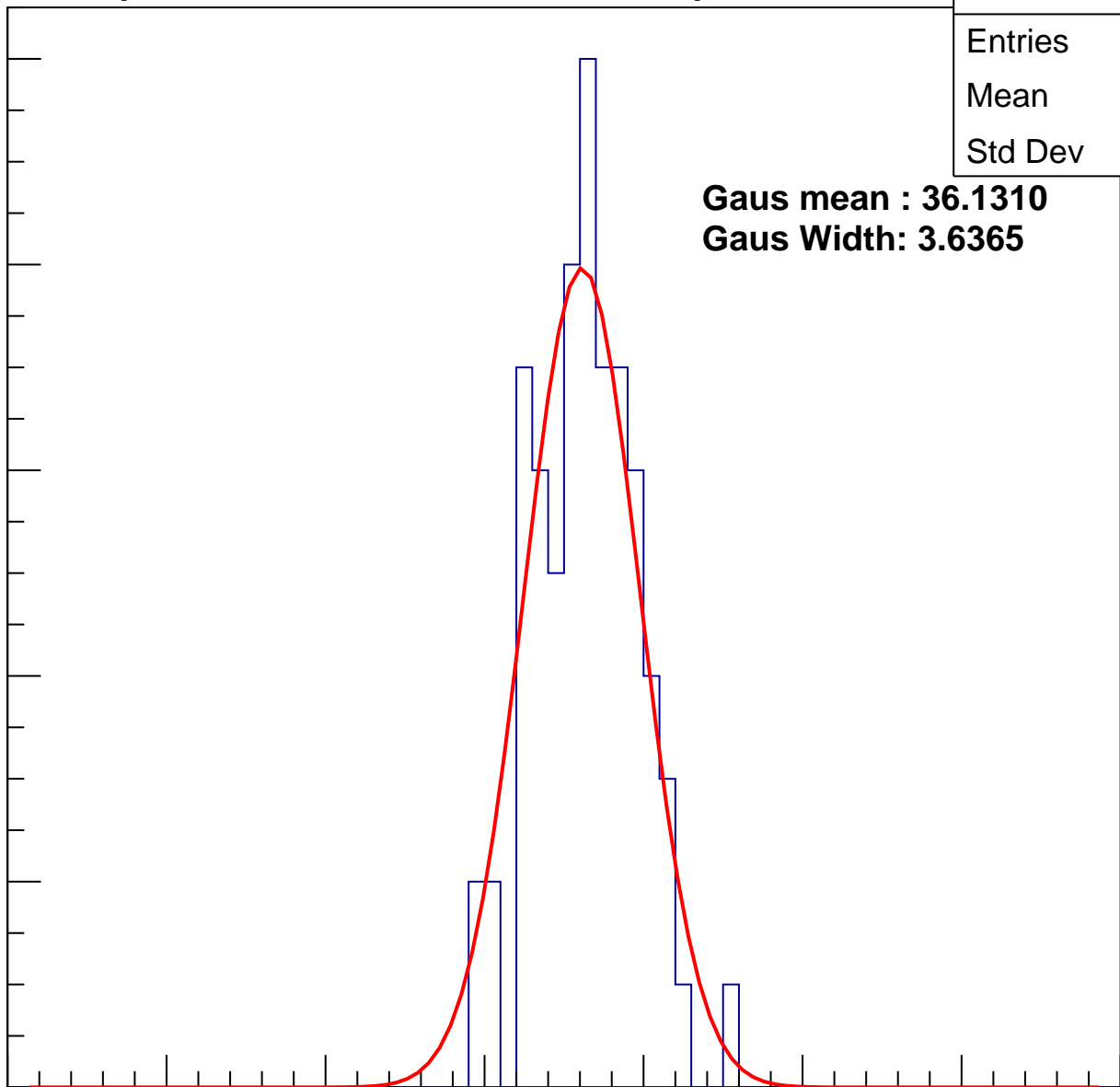
Gaus Width: 3.6365

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch7, adc2

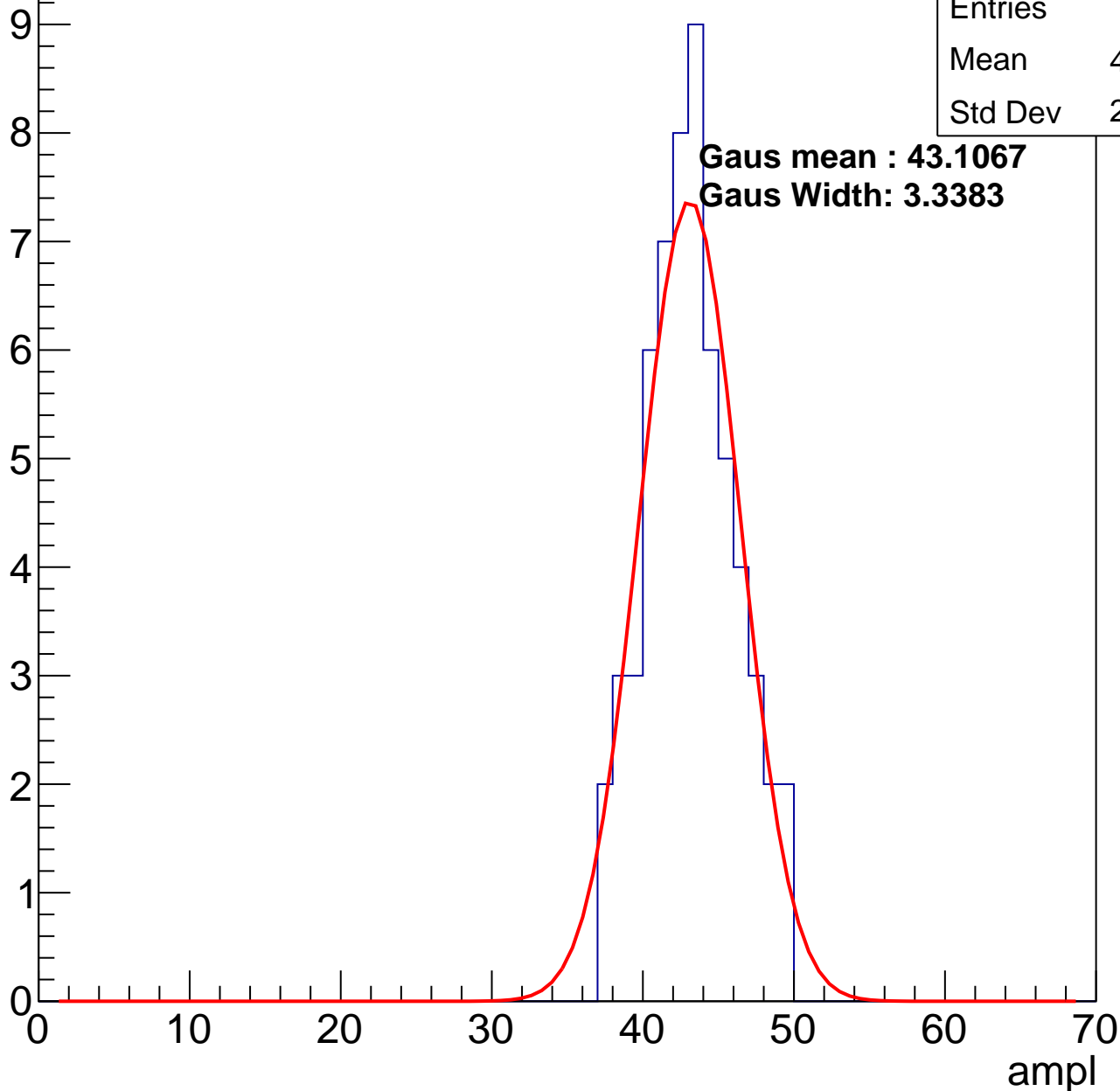
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.72
Std Dev	2.922

Gaus mean : 43.1067

Gaus Width: 3.3383

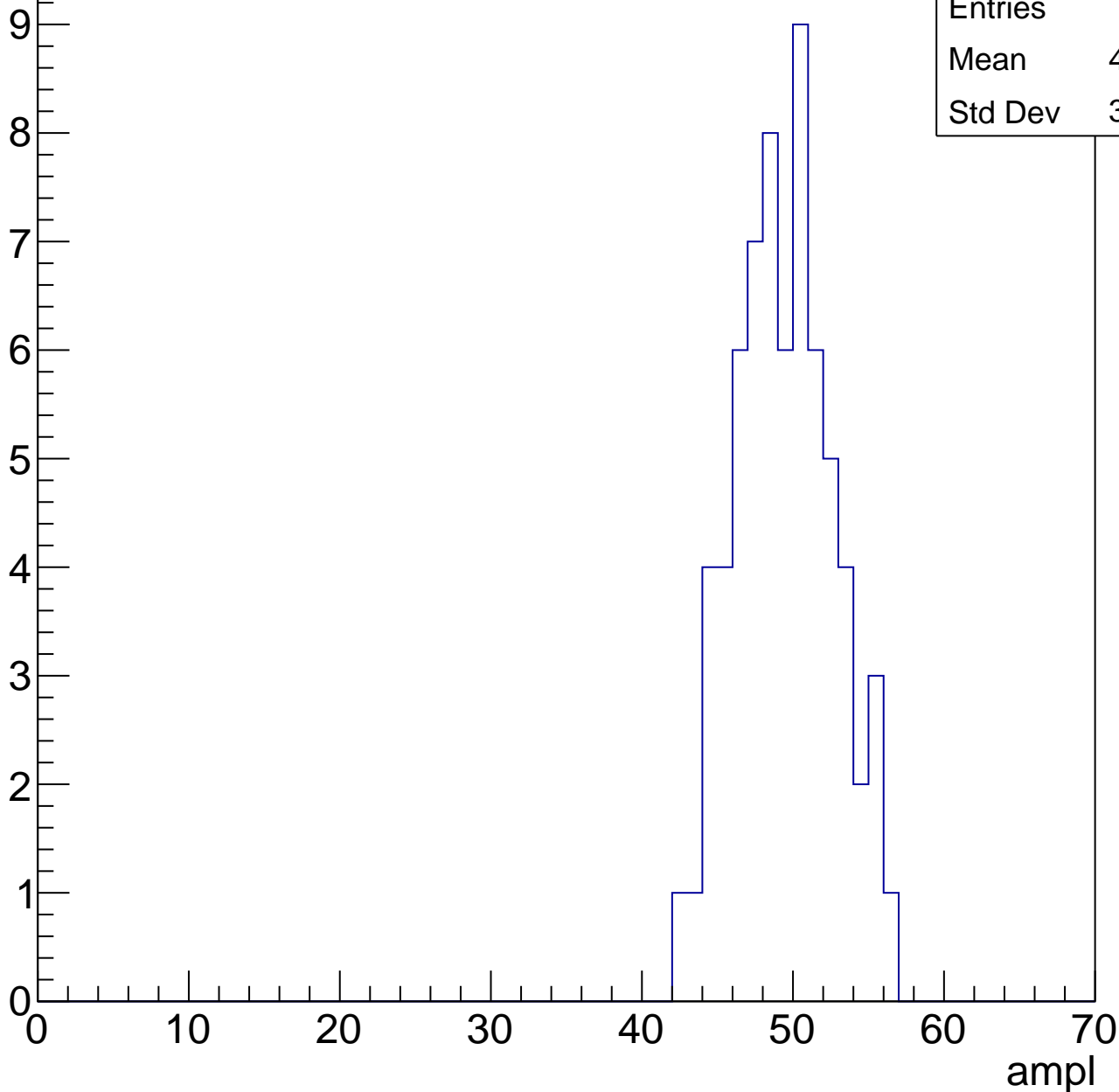


B1L003S, U21-ch7, adc3

calib_packv5_042523_0143.root, FC#13, port D2

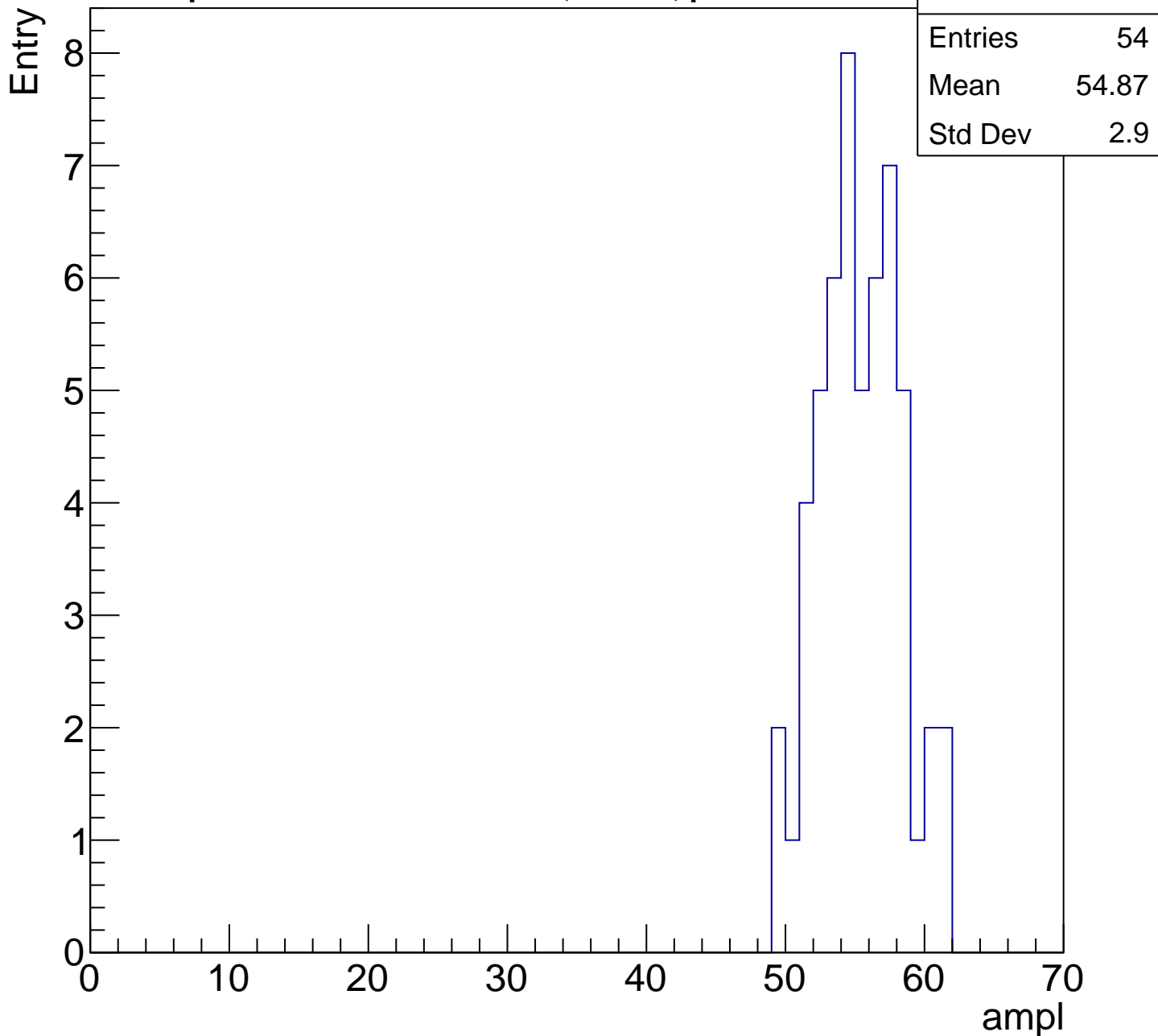
Entry

Entries	67
Mean	48.97
Std Dev	3.204



B1L003S, U21-ch7, adc4

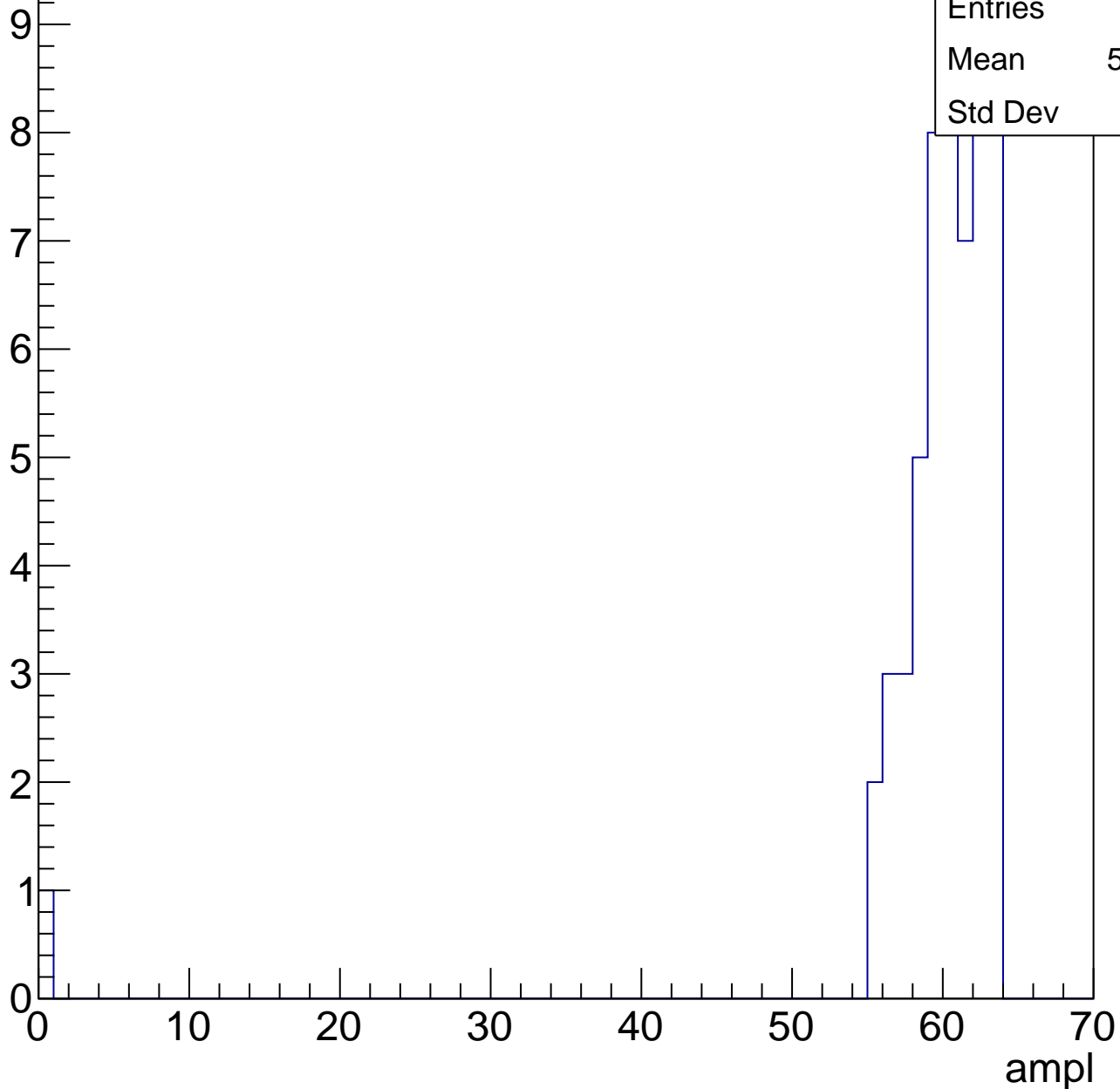
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U21-ch7, adc5

calib_packv5_042523_0143.root, FC#13, port D2

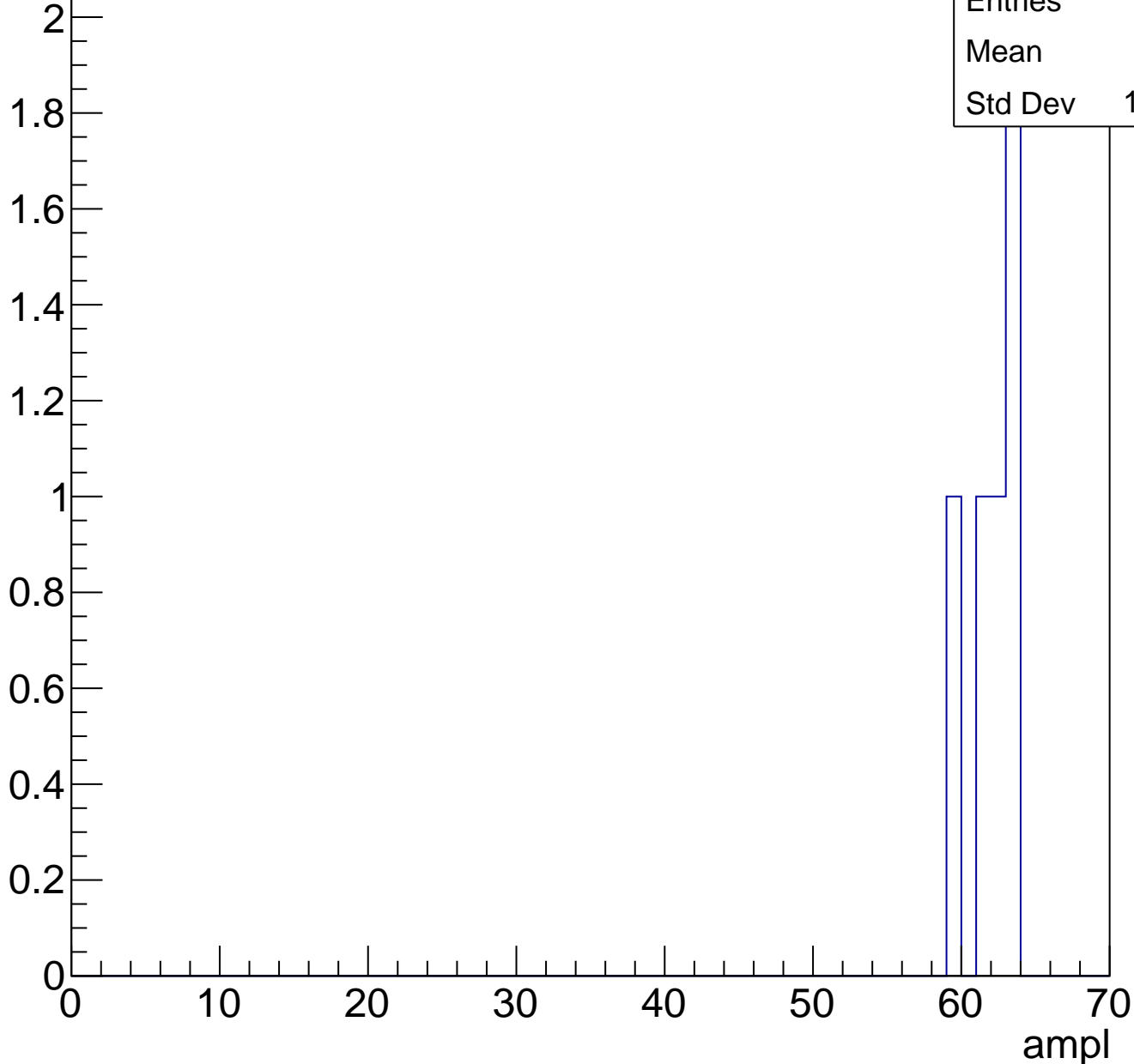
Entry



B1L003S, U21-ch7, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch7, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch8, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	29.44
Std Dev	4.69

Gaus mean : 30.2512

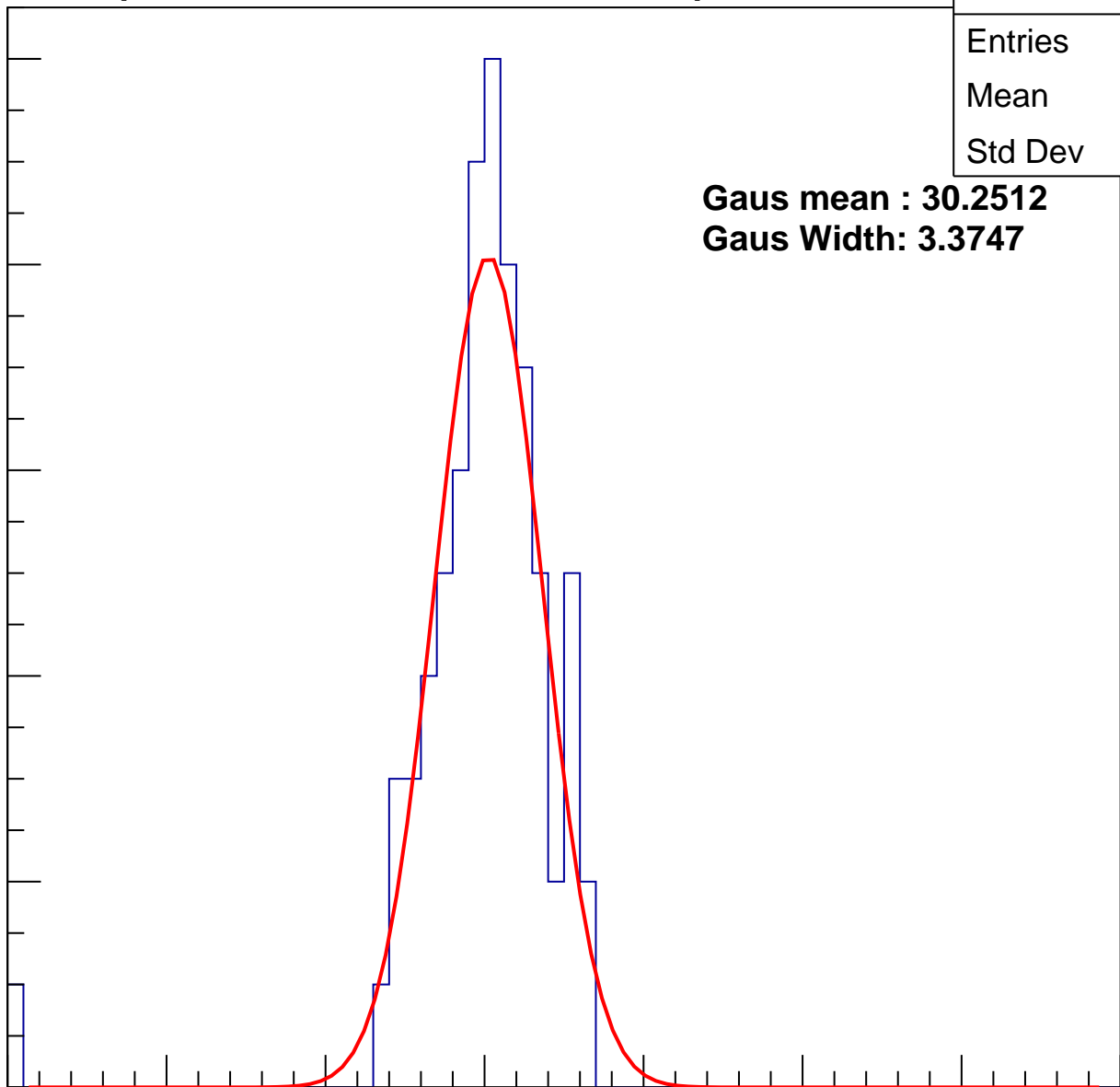
Gaus Width: 3.3747

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch8, adc1

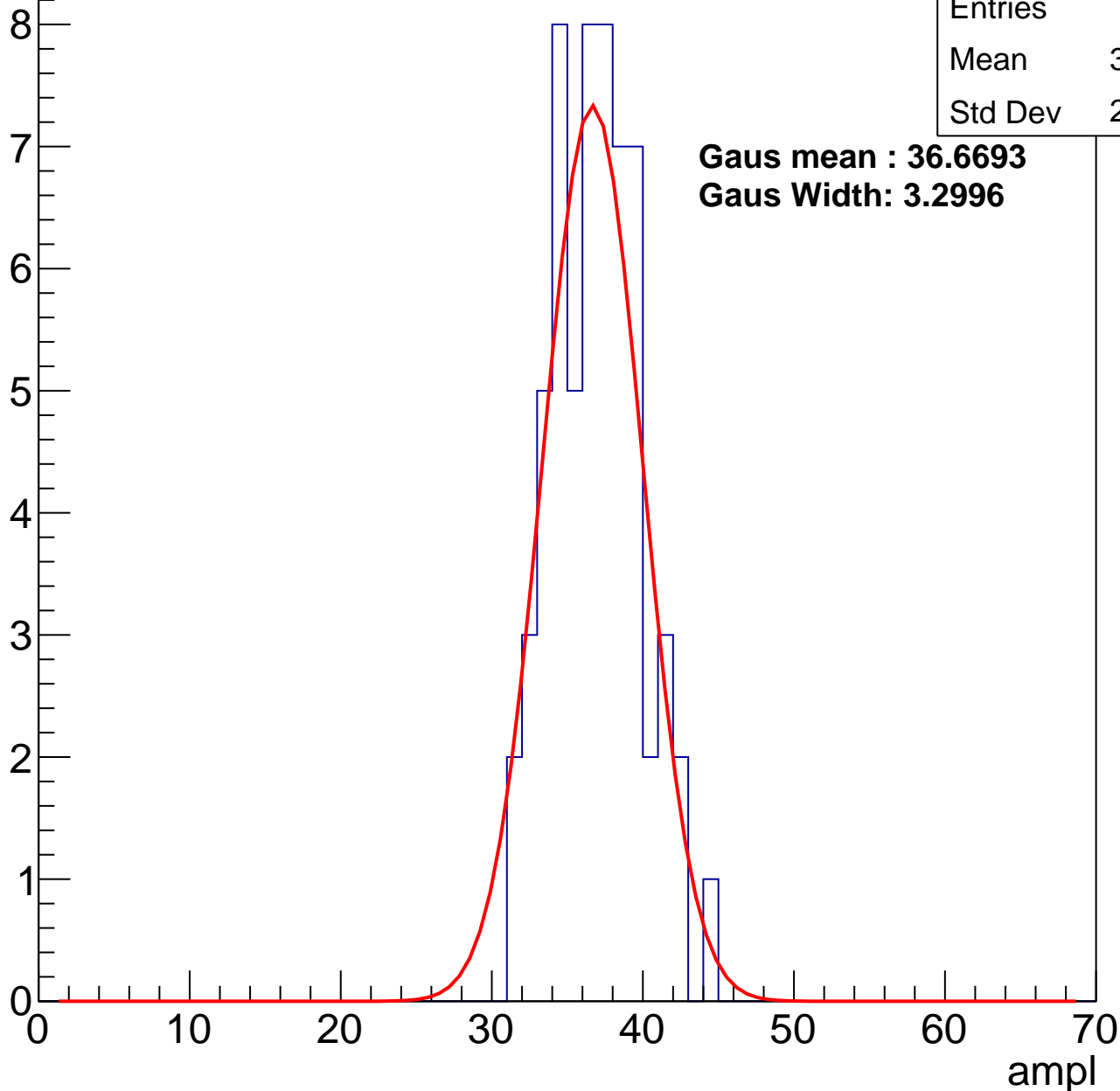
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.46
Std Dev	2.889

Gaus mean : 36.6693

Gaus Width: 3.2996



B1L003S, U21-ch8, adc2

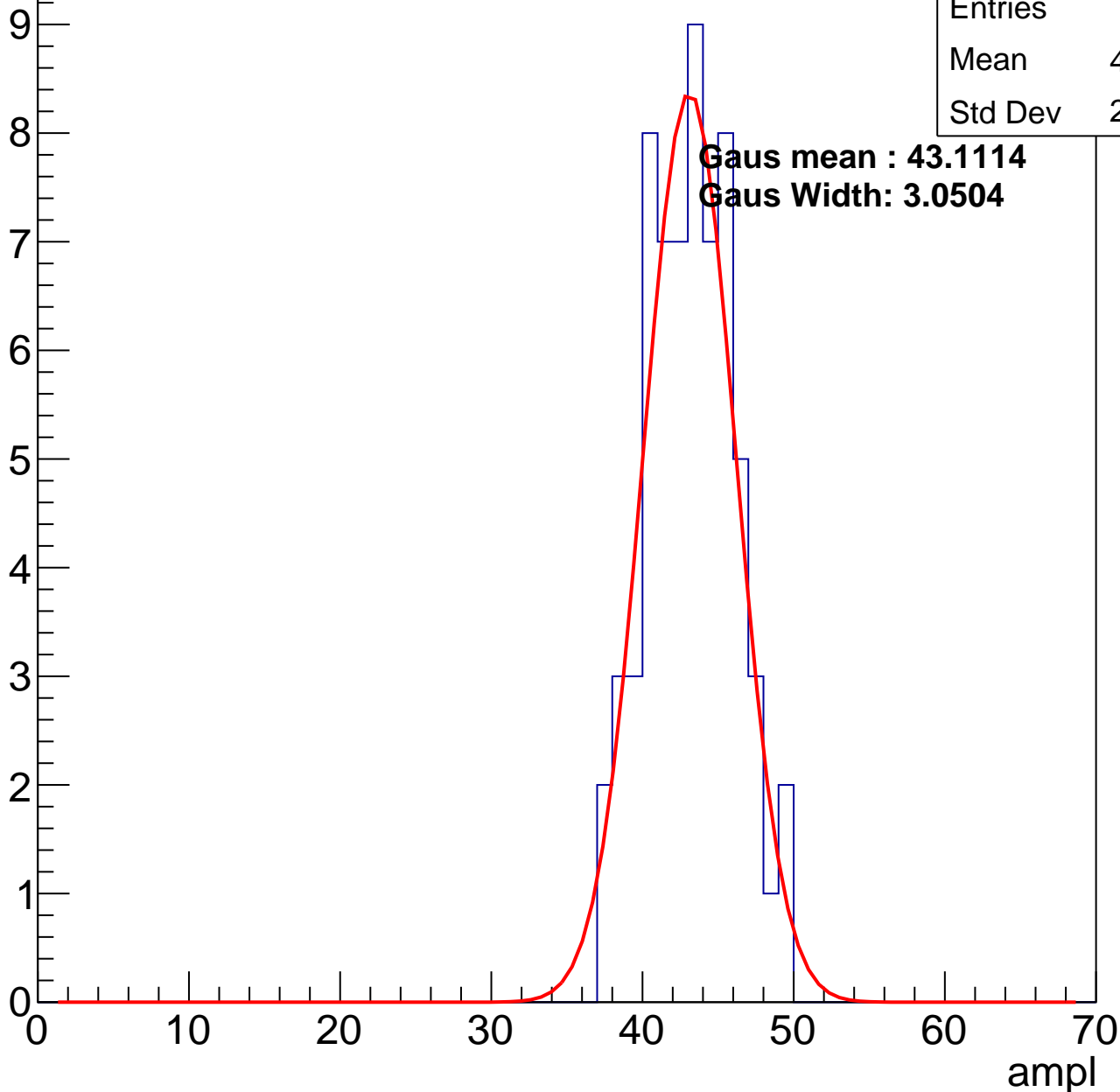
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.74
Std Dev	2.846

Gaus mean : 43.1114

Gaus Width: 3.0504



B1L003S, U21-ch8, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	49.6
Std Dev	3.173

Entry

10

8

6

4

2

0

0

10

20

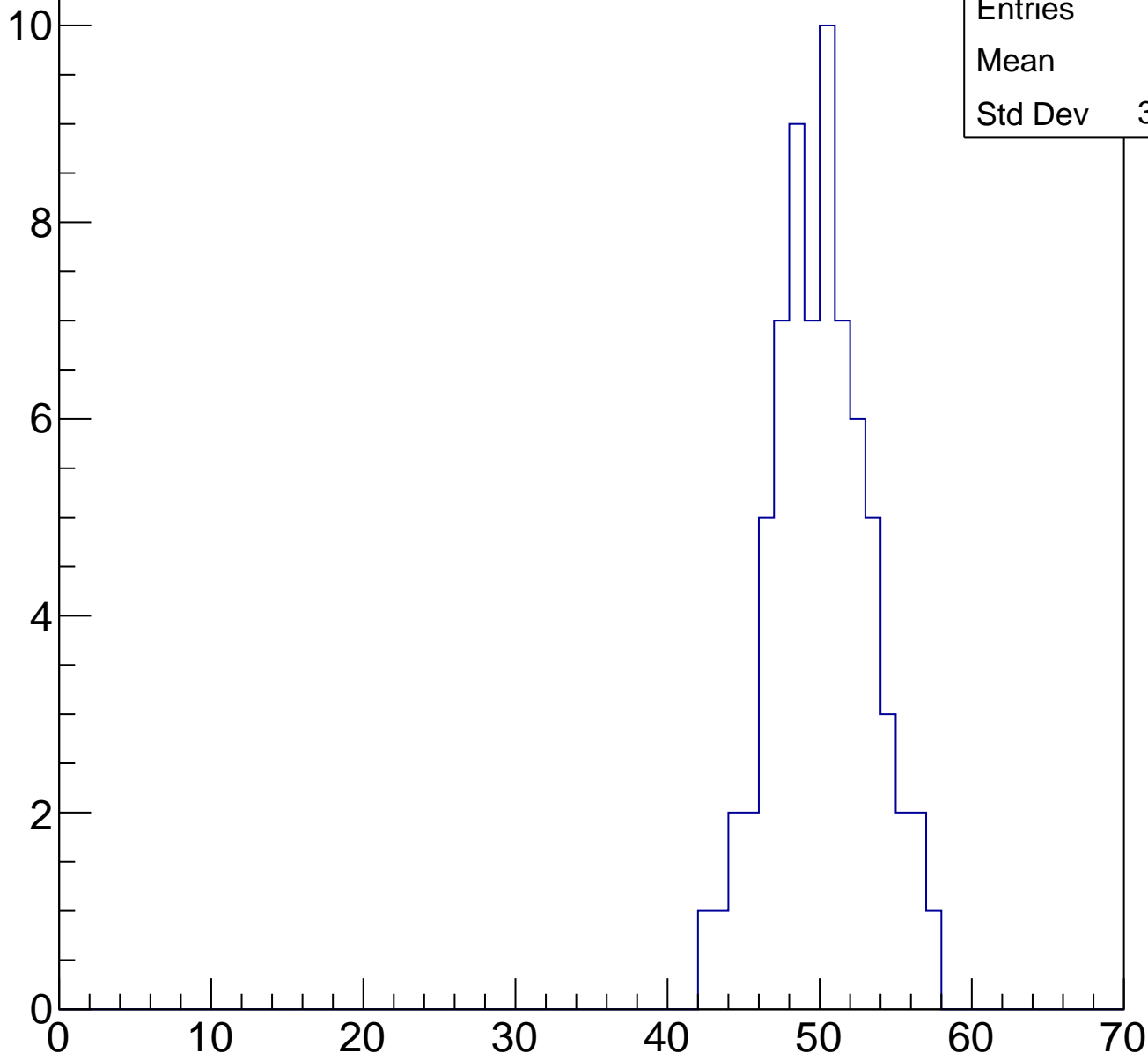
30

40

50

60

ampl

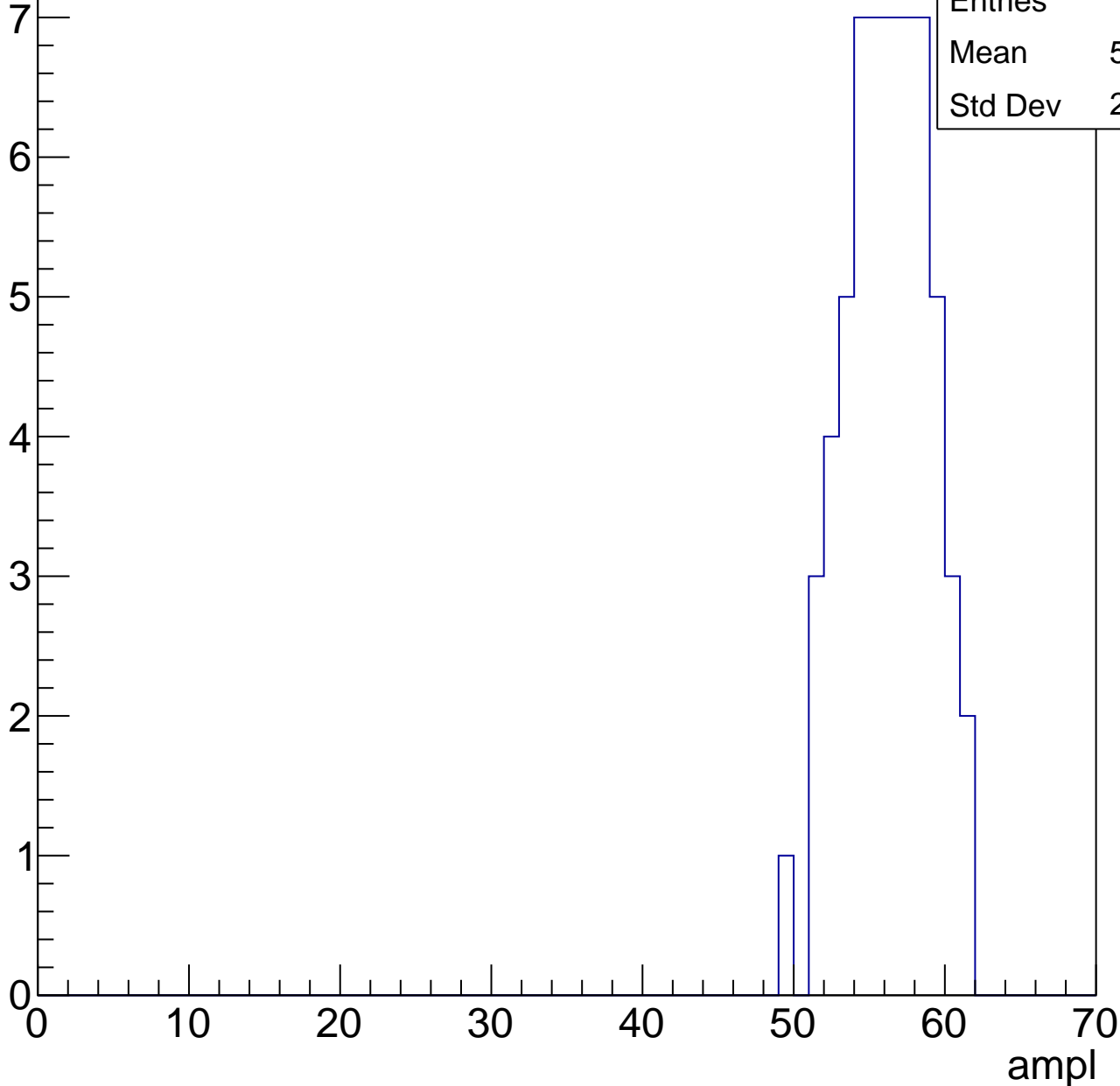


B1L003S, U21-ch8, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.72
Std Dev	2.759

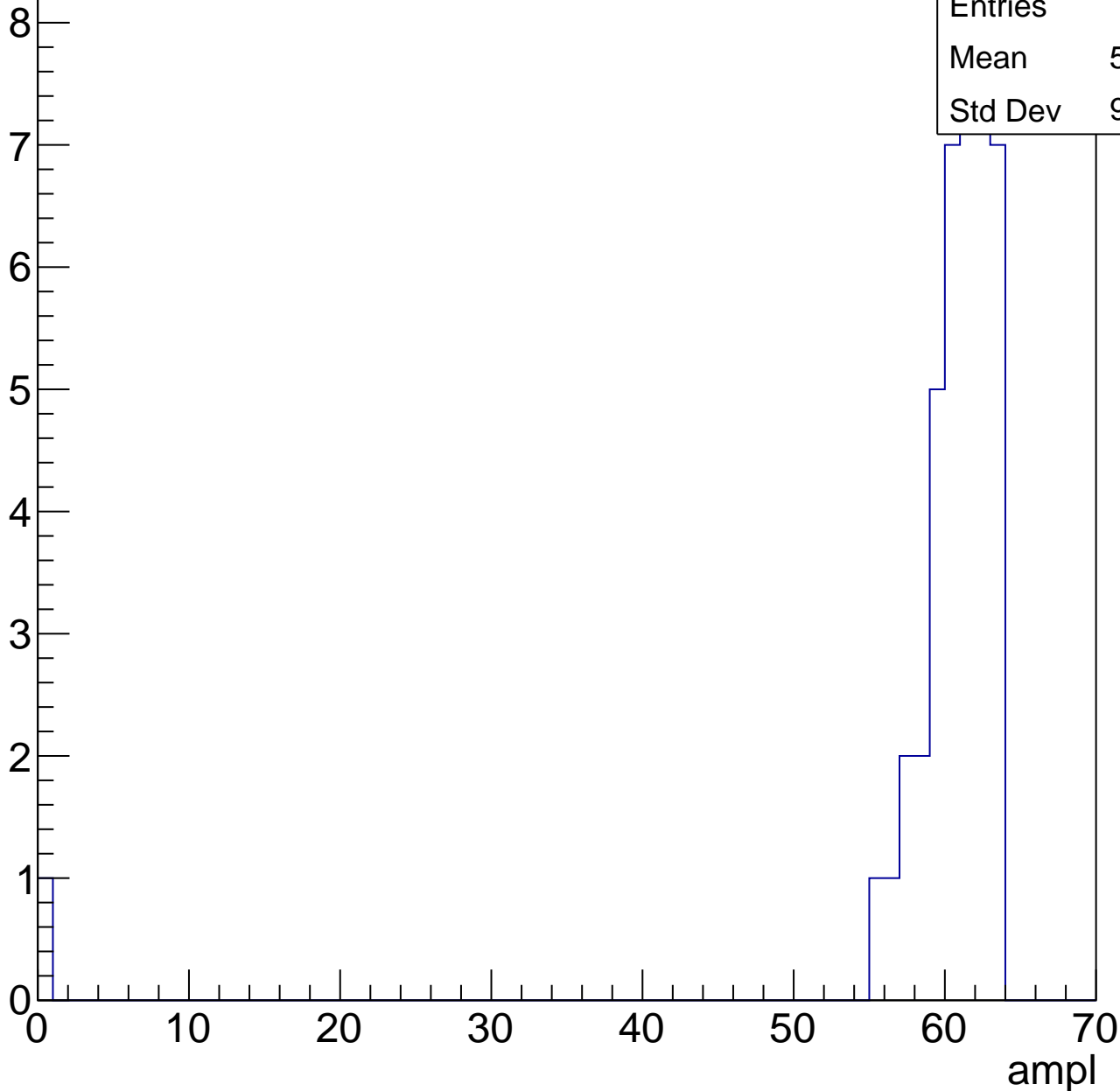


B1L003S, U21-ch8, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

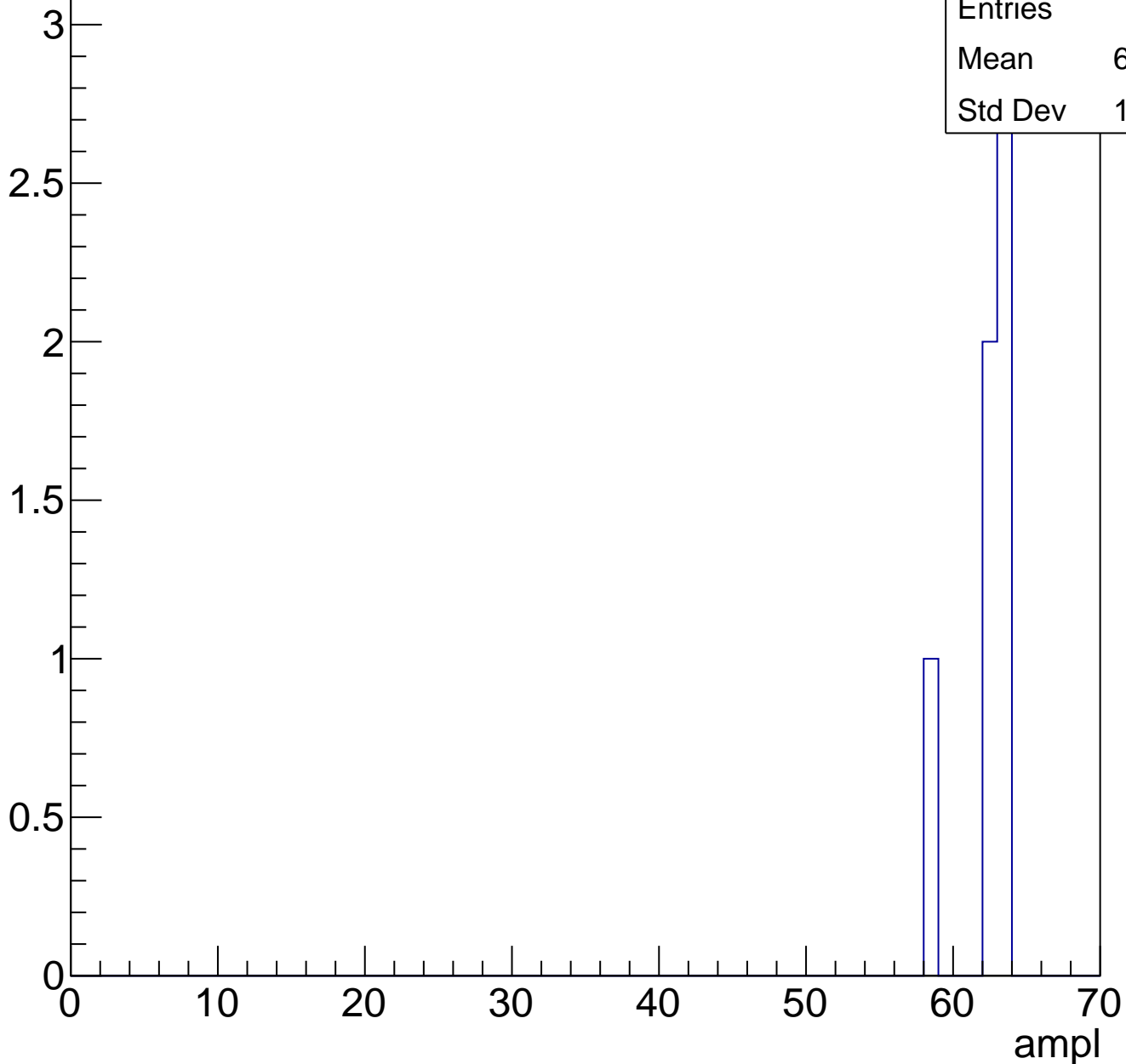
Entries	42
Mean	59.07
Std Dev	9.435



B1L003S, U21-ch8, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch8, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch9, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	95
Mean	30.13
Std Dev	3.614

Gaus mean : 30.5515

Gaus Width: 3.4287

Entry

10

8

6

4

2

0

0

10

20

30

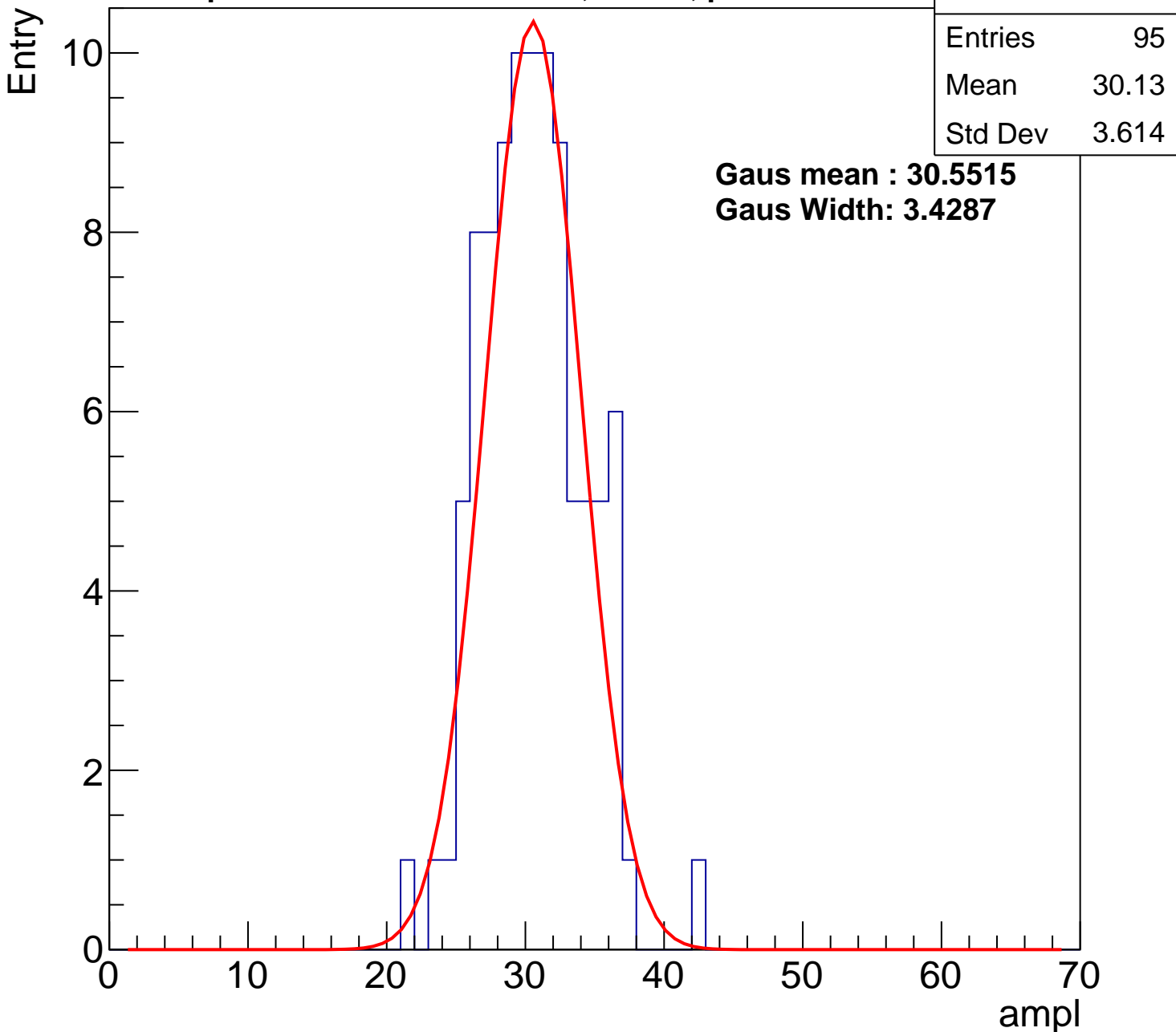
40

50

60

70

ampl



B1L003S, U21-ch9, adc1

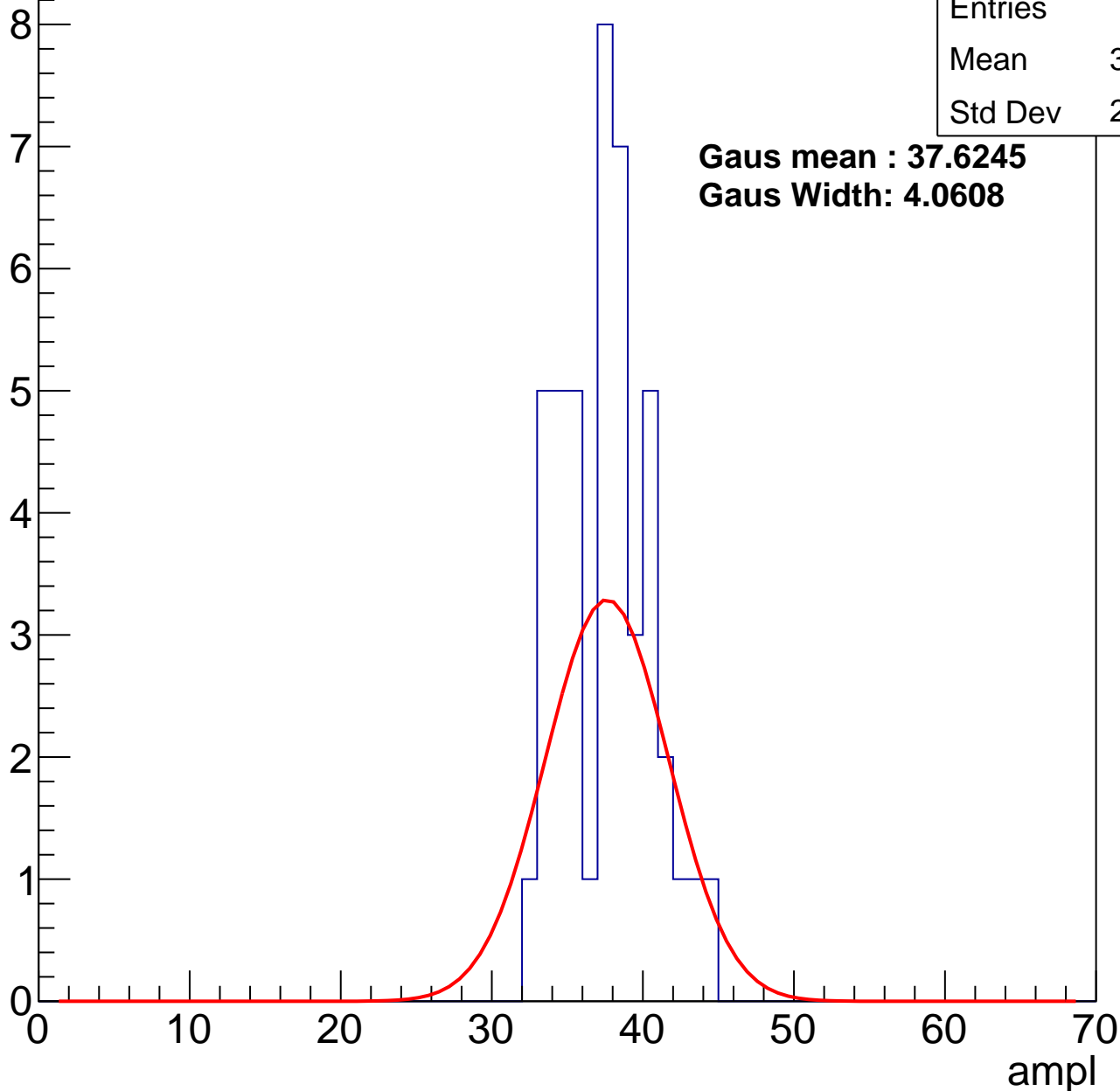
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	37.07
Std Dev	2.894

Gaus mean : 37.6245

Gaus Width: 4.0608



B1L003S, U21-ch9, adc2

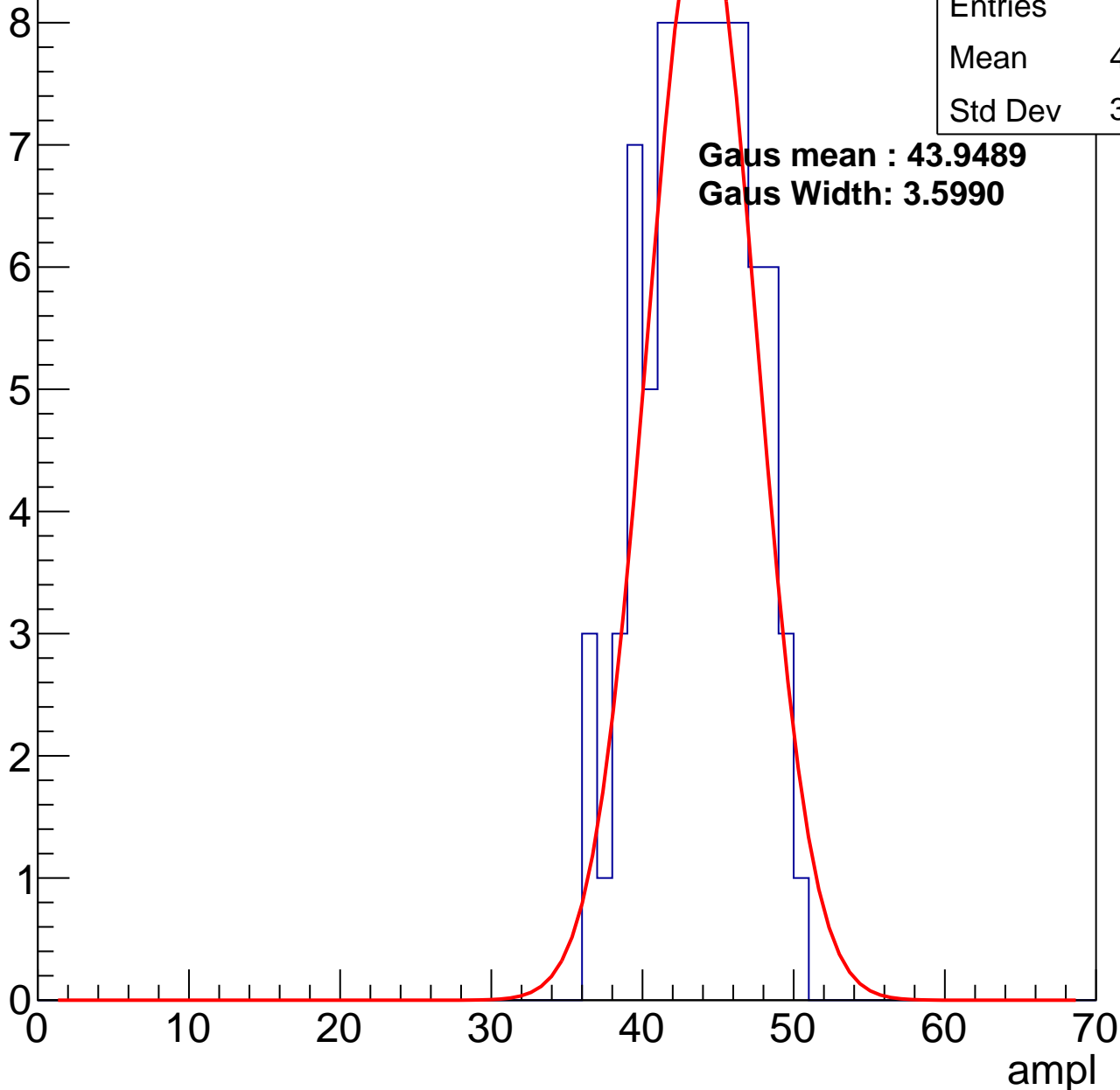
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	43.22
Std Dev	3.412

Gaus mean : 43.9489

Gaus Width: 3.5990

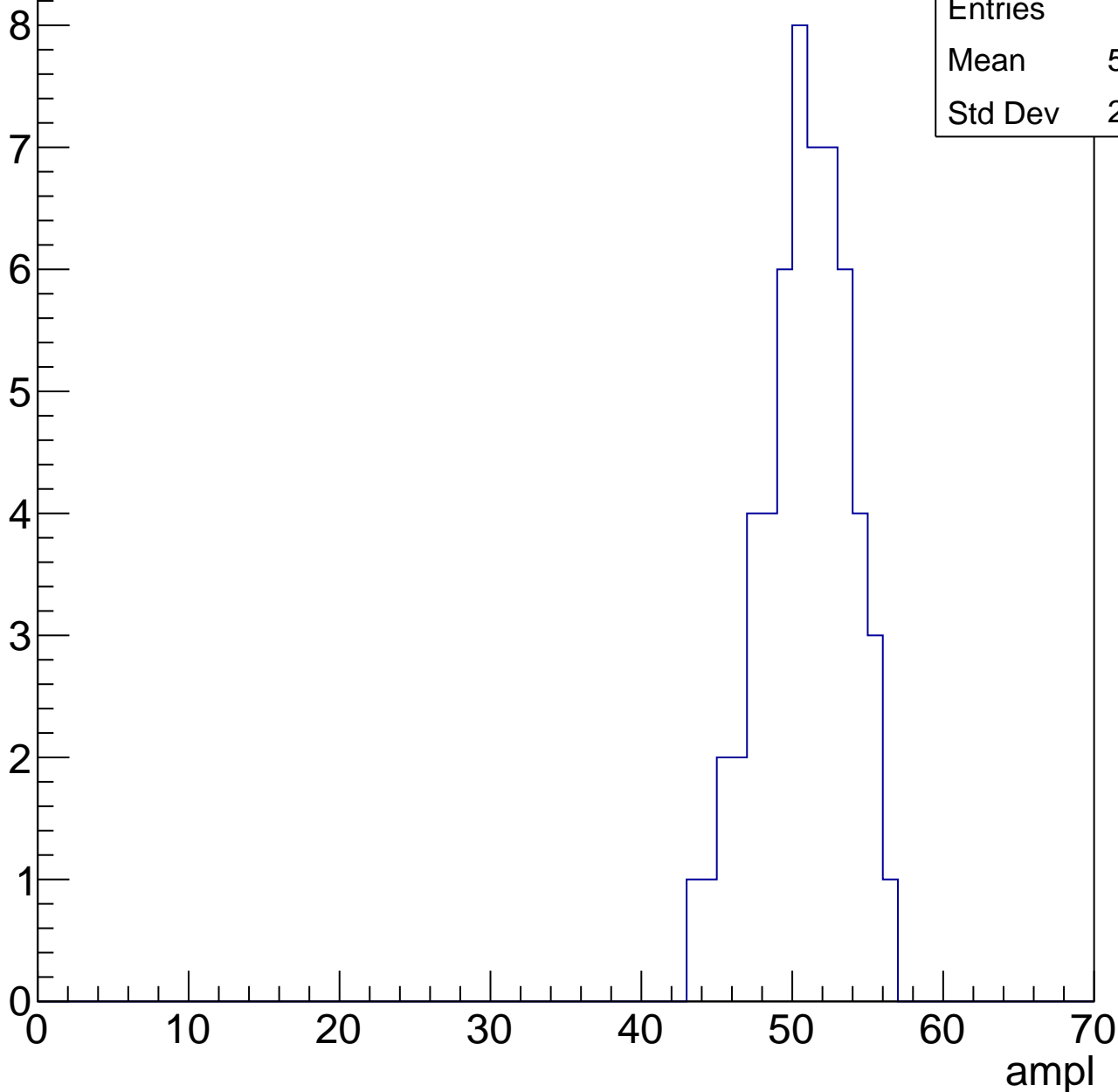


B1L003S, U21-ch9, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	50.34
Std Dev	2.935

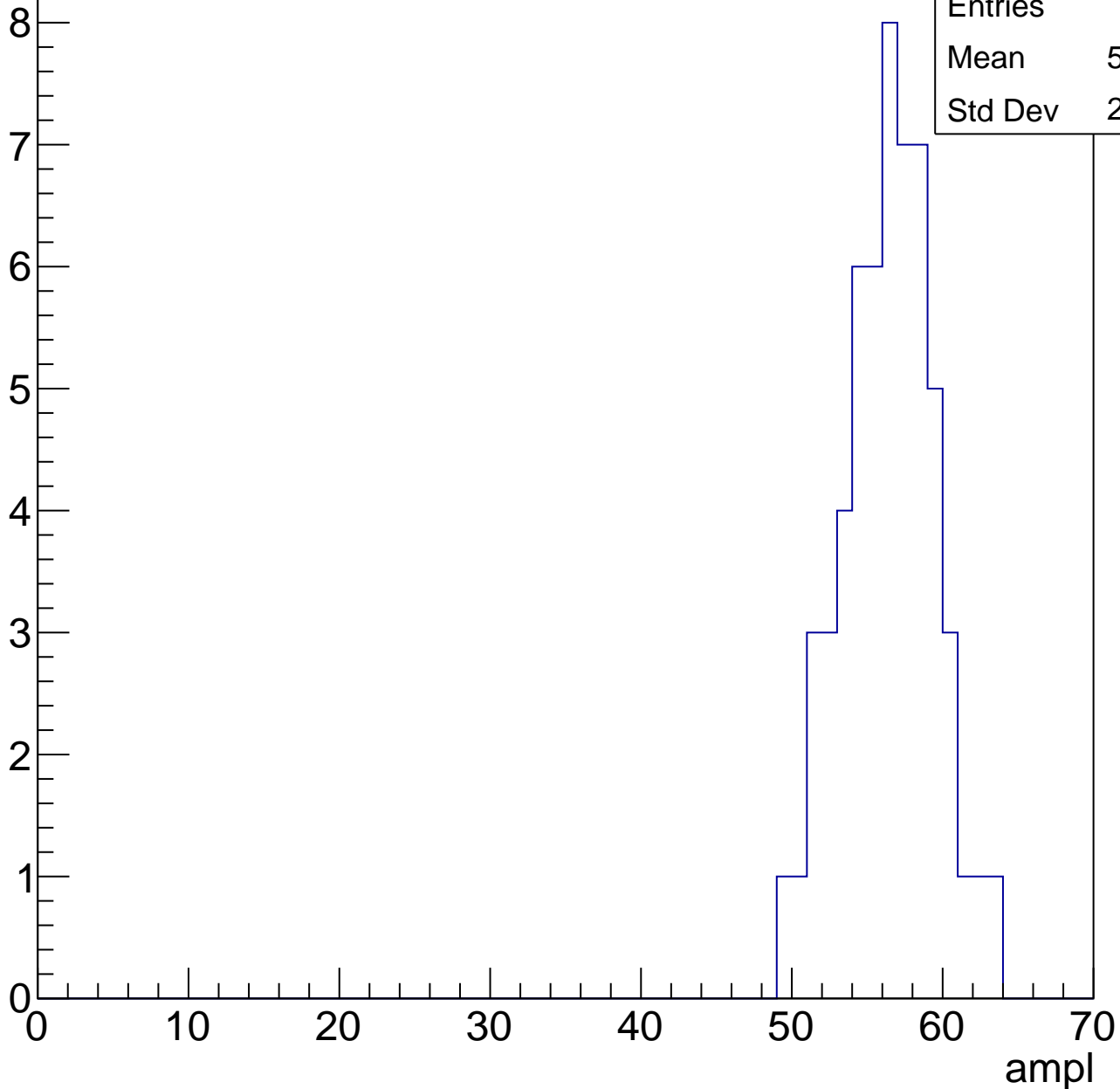


B1L003S, U21-ch9, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	55.93
Std Dev	2.996

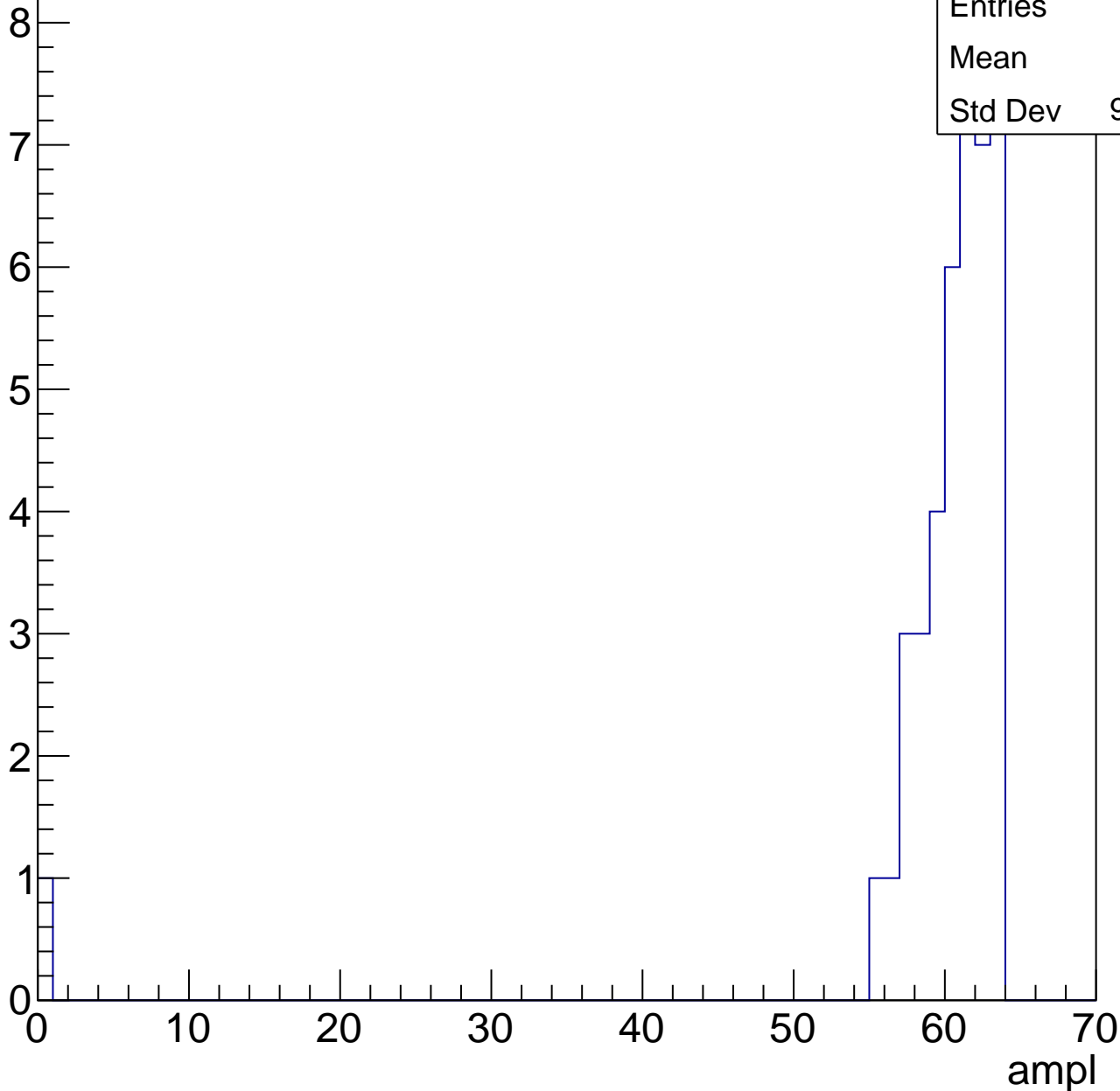


B1L003S, U21-ch9, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	59
Std Dev	9.449



B1L003S, U21-ch9, adc6

calib_packv5_042523_0143.root, FC#13, port D2

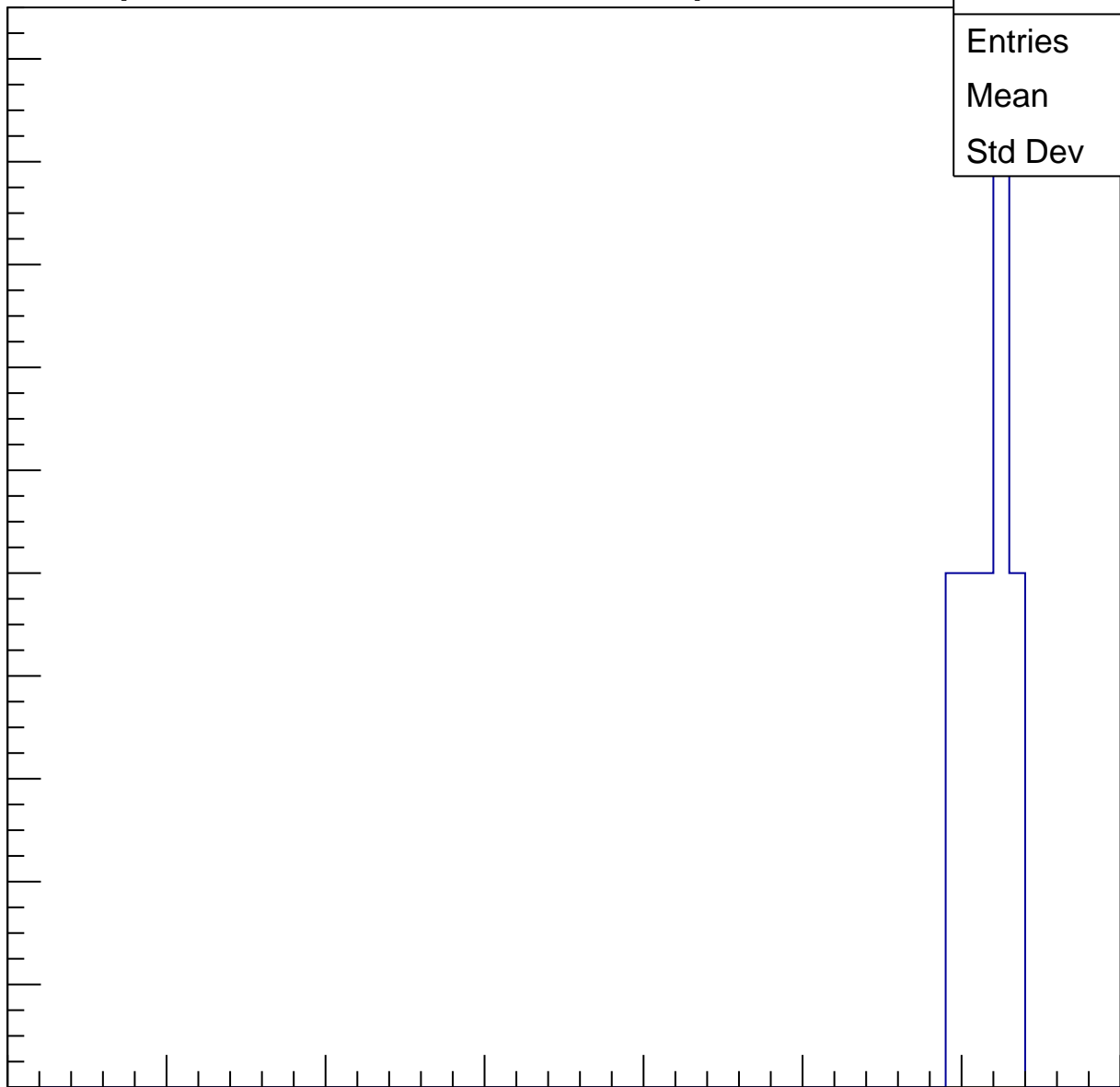
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61.17
Std Dev	1.344

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch9, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch10, adc0

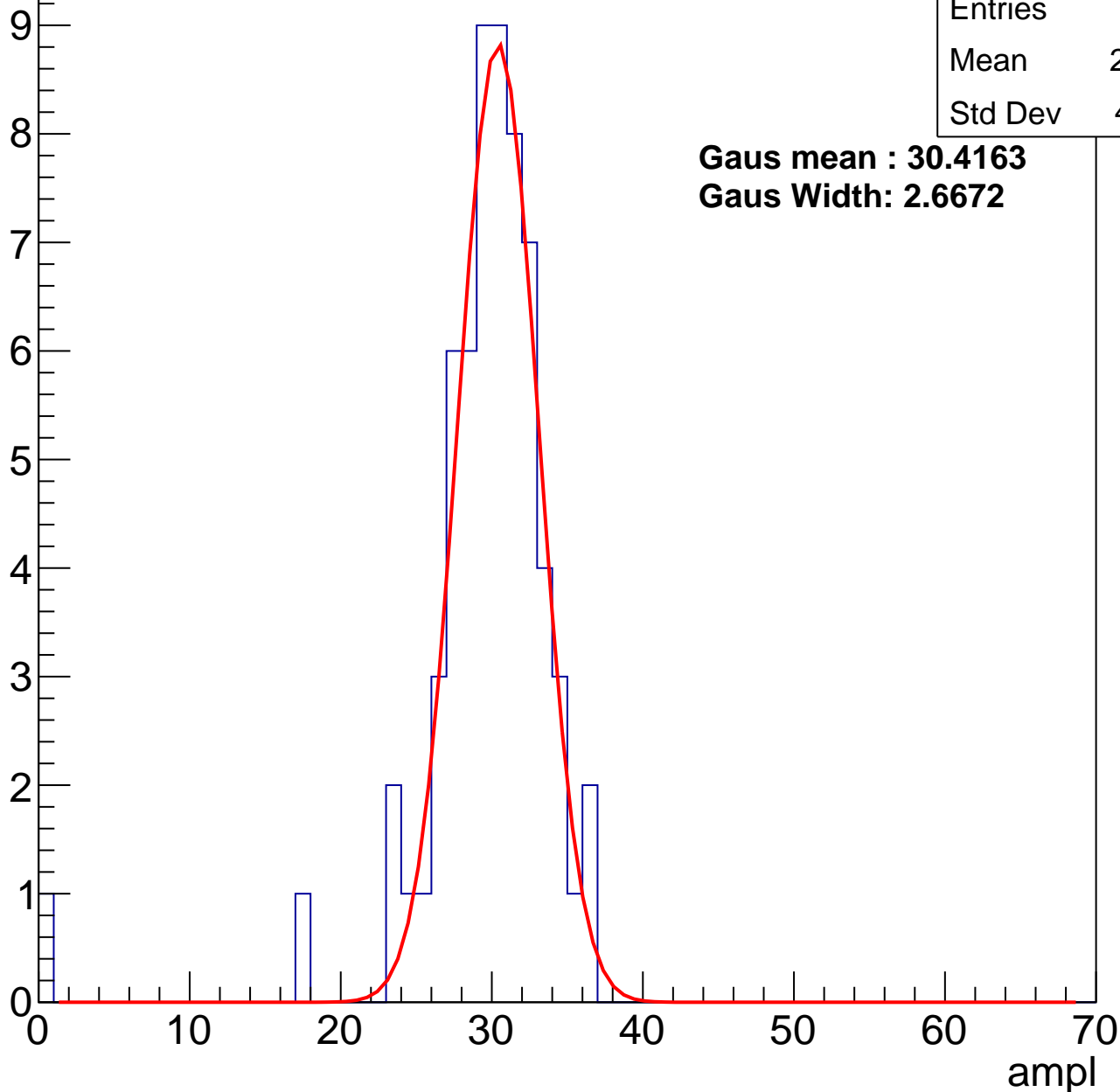
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	29.12
Std Dev	4.891

Gaus mean : 30.4163

Gaus Width: 2.6672



B1L003S, U21-ch10, adc1

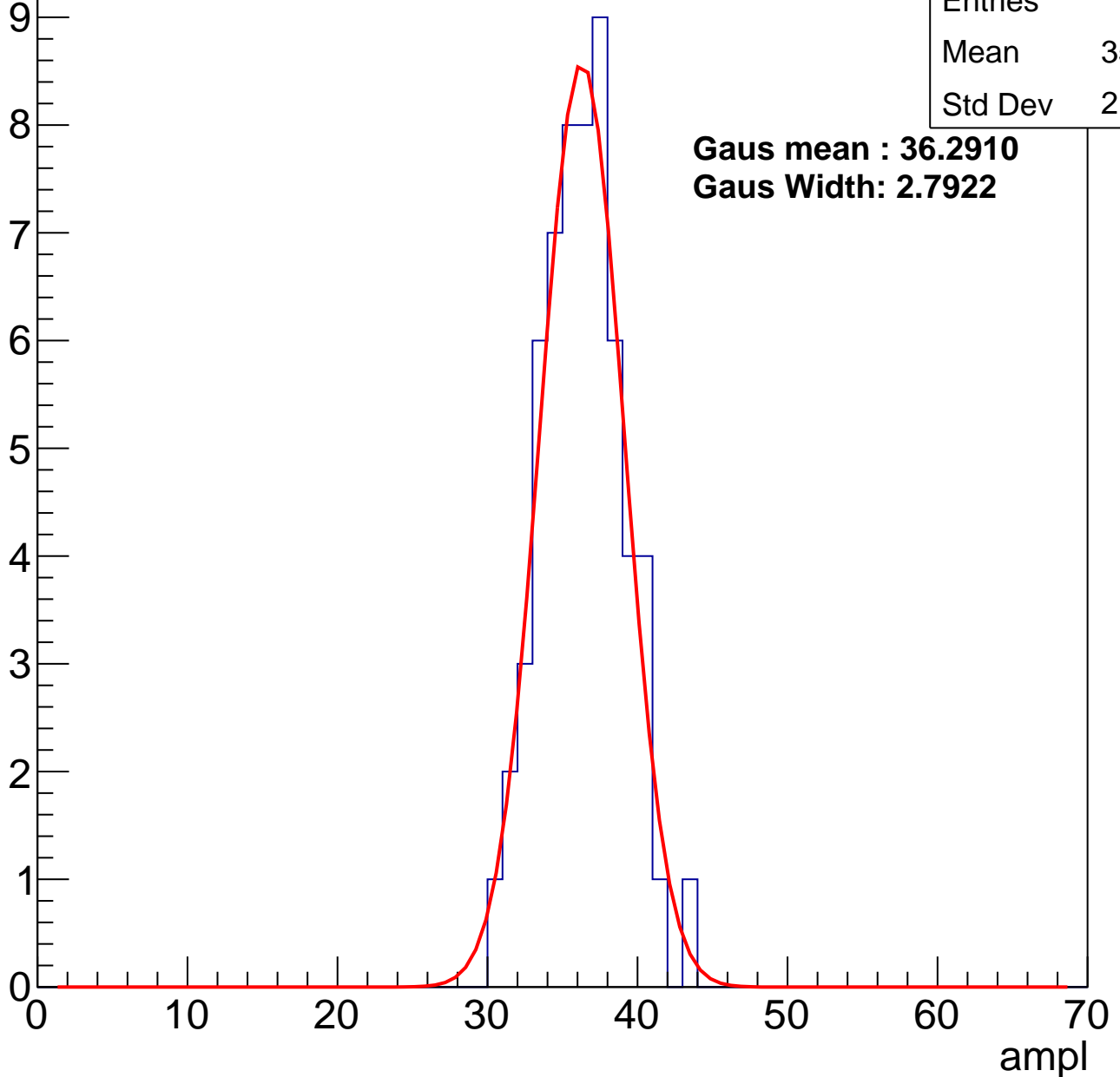
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	35.88
Std Dev	2.678

Gaus mean : 36.2910

Gaus Width: 2.7922



B1L003S, U21-ch10, adc2

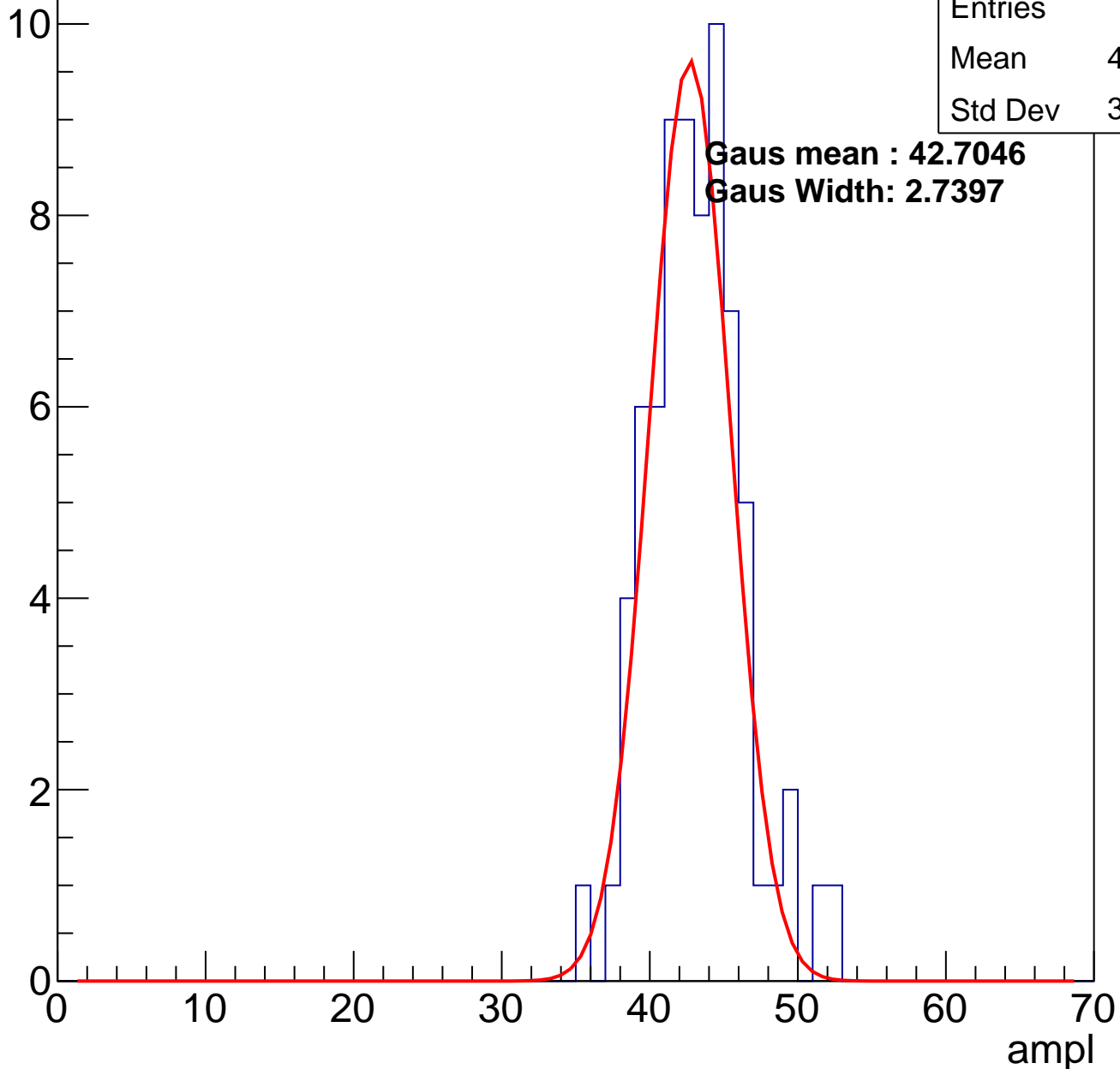
calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	42.64
Std Dev	3.177

Gaus mean : 42.7046

Gaus Width: 2.7397

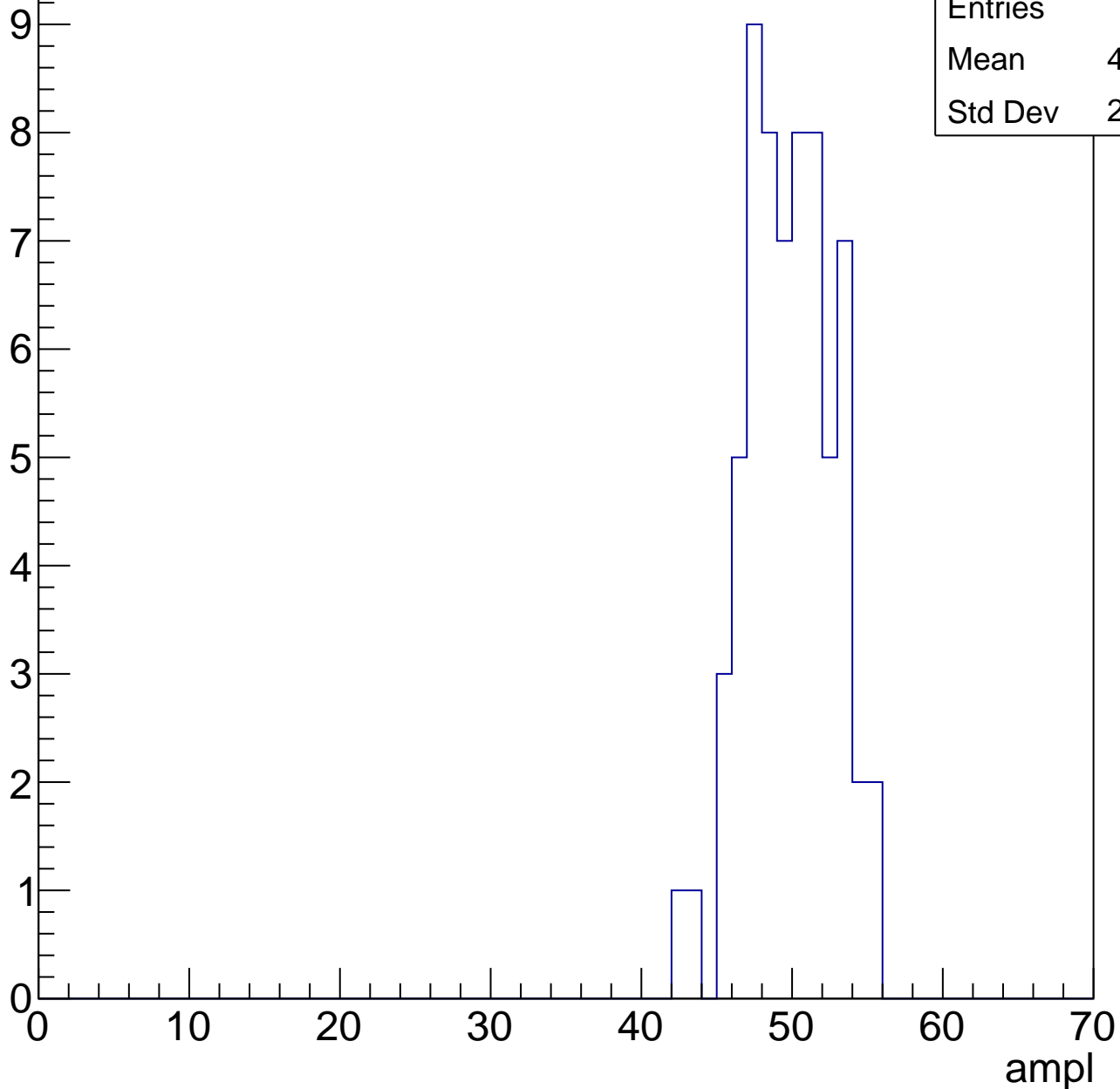
Entry



B1L003S, U21-ch10, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

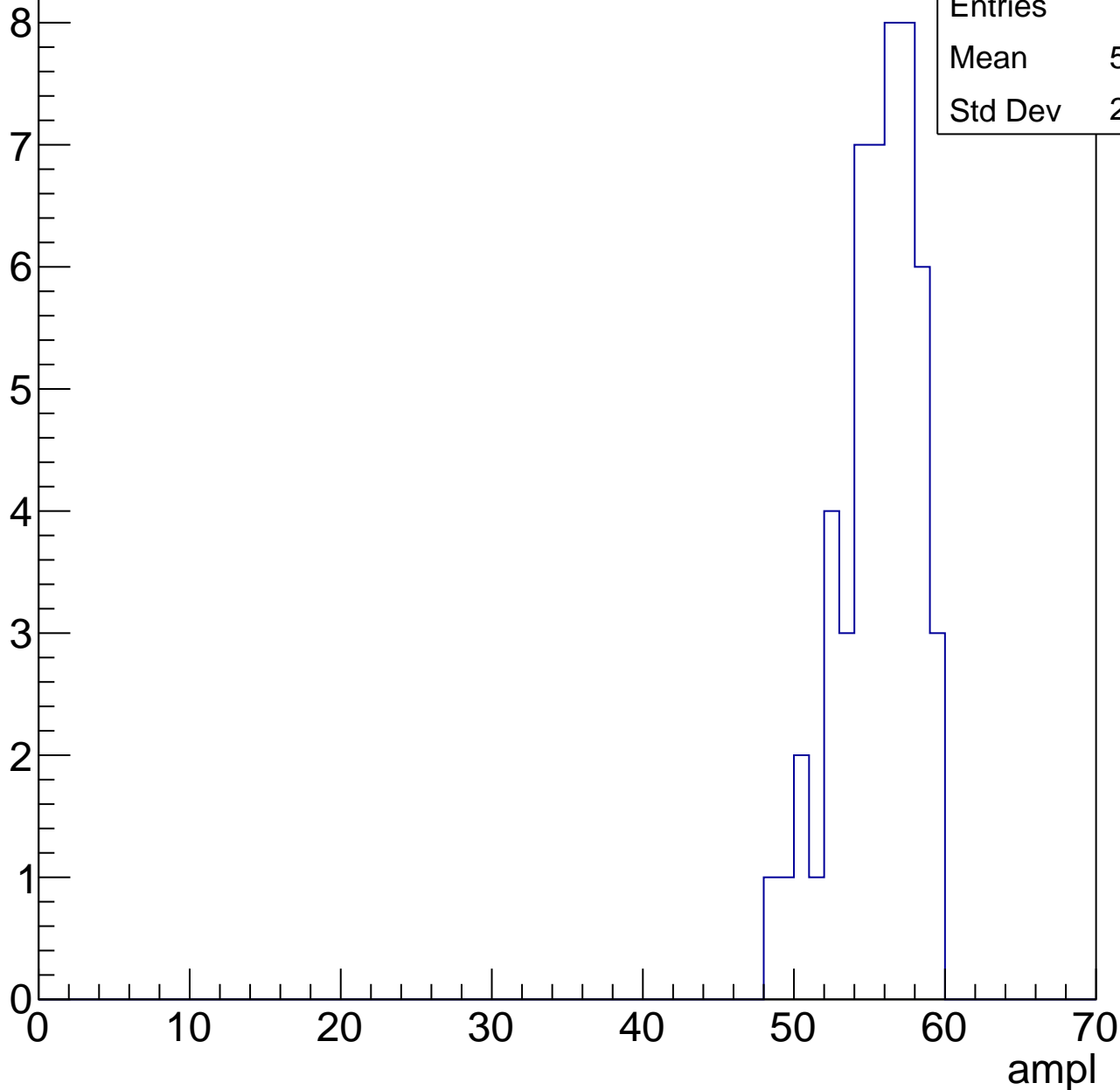


Entries	66
Mean	49.35
Std Dev	2.842

B1L003S, U21-ch10, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

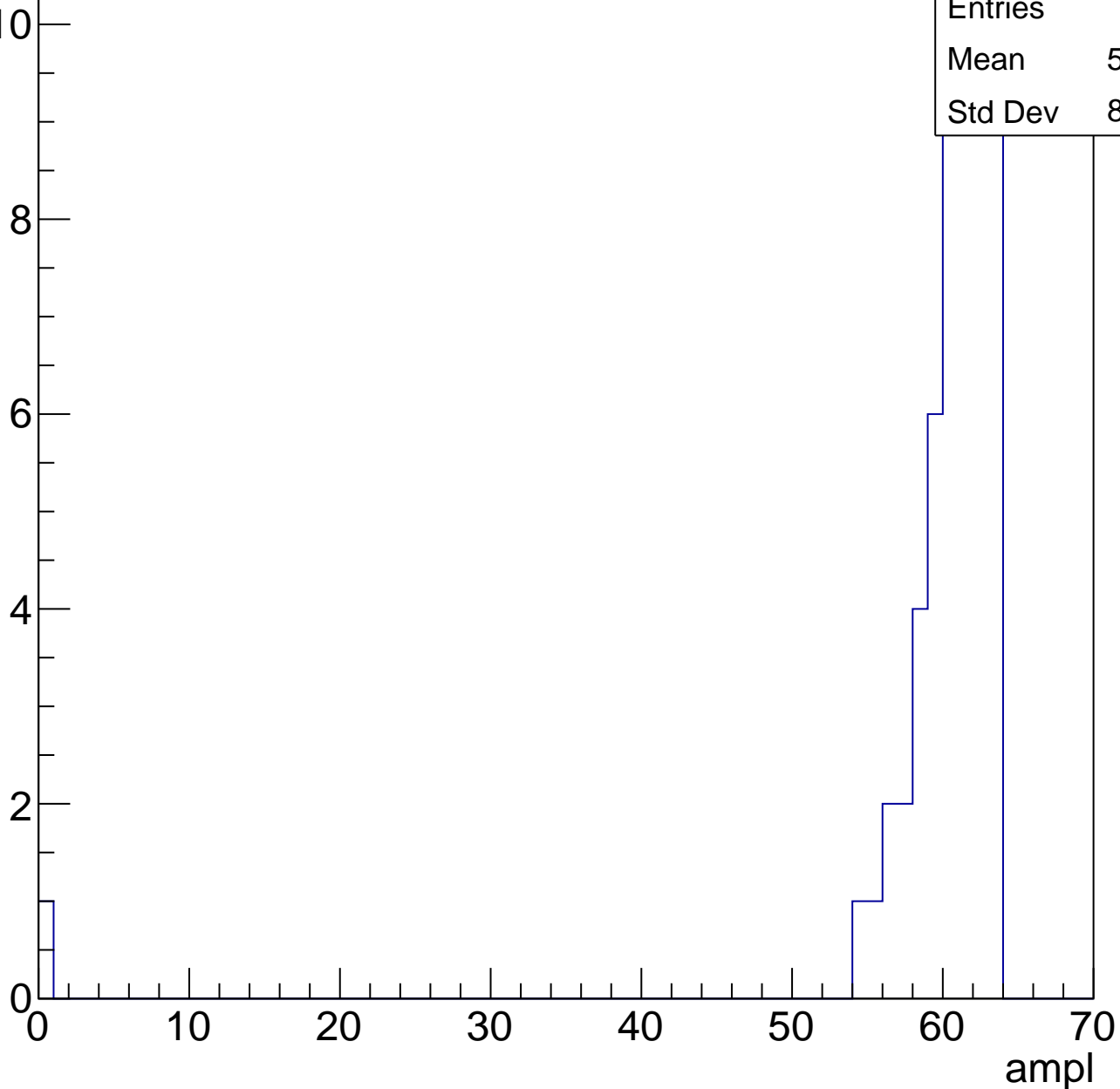


B1L003S, U21-ch10, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

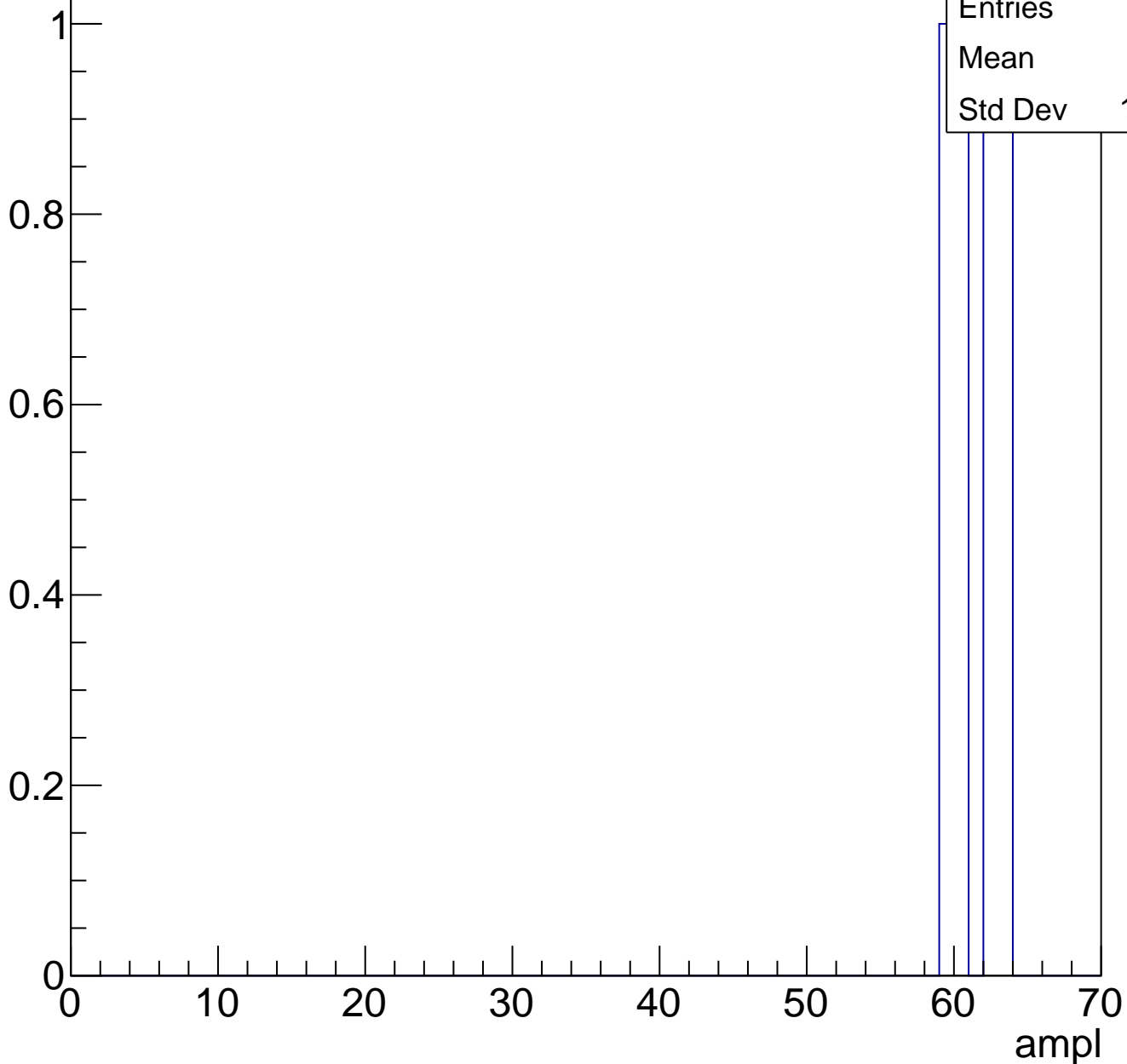
Entries	54
Mean	59.19
Std Dev	8.415



B1L003S, U21-ch10, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch10, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch11, adc0

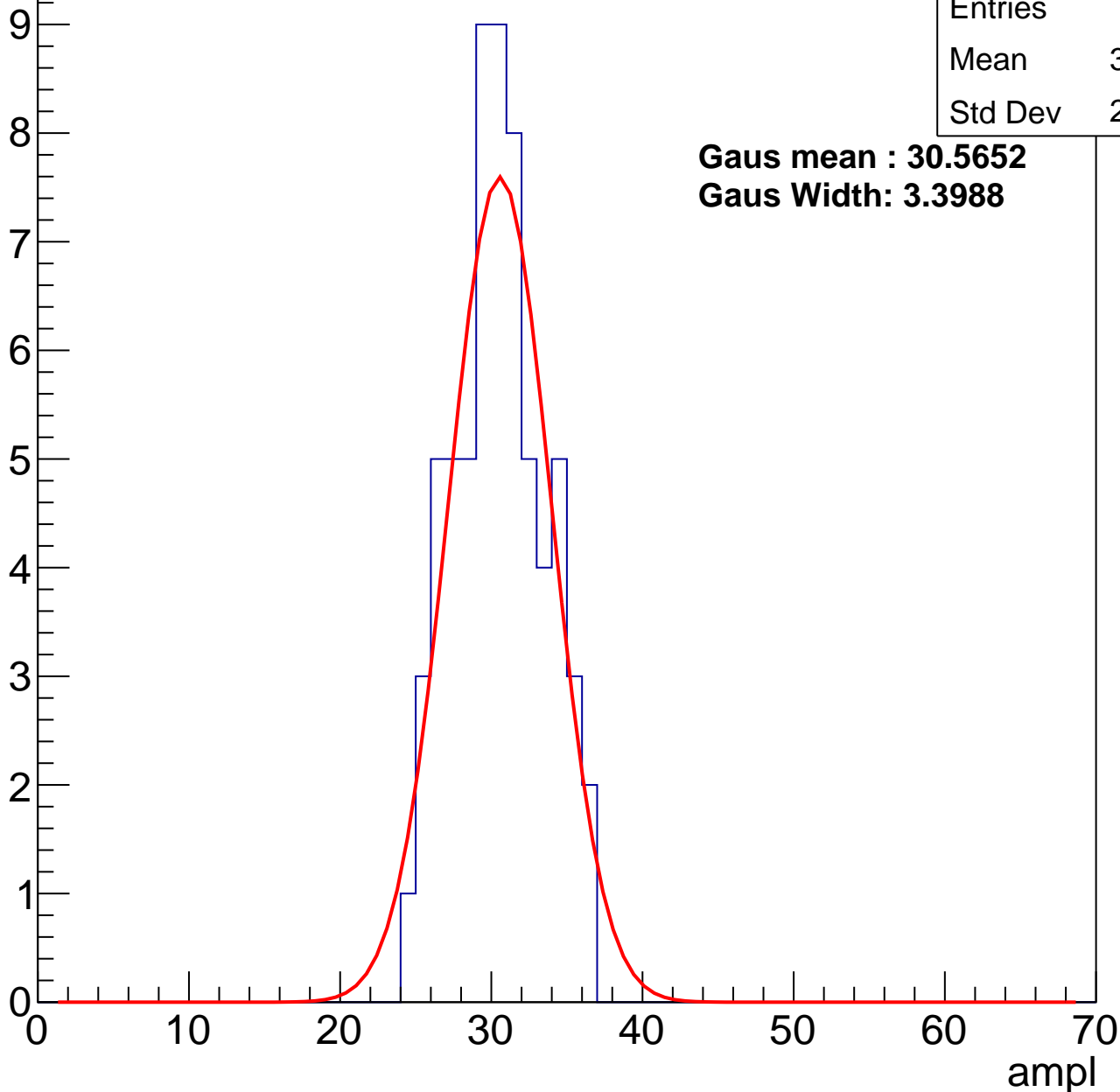
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	30.03
Std Dev	2.947

Gaus mean : 30.5652

Gaus Width: 3.3988



B1L003S, U21-ch11, adc1

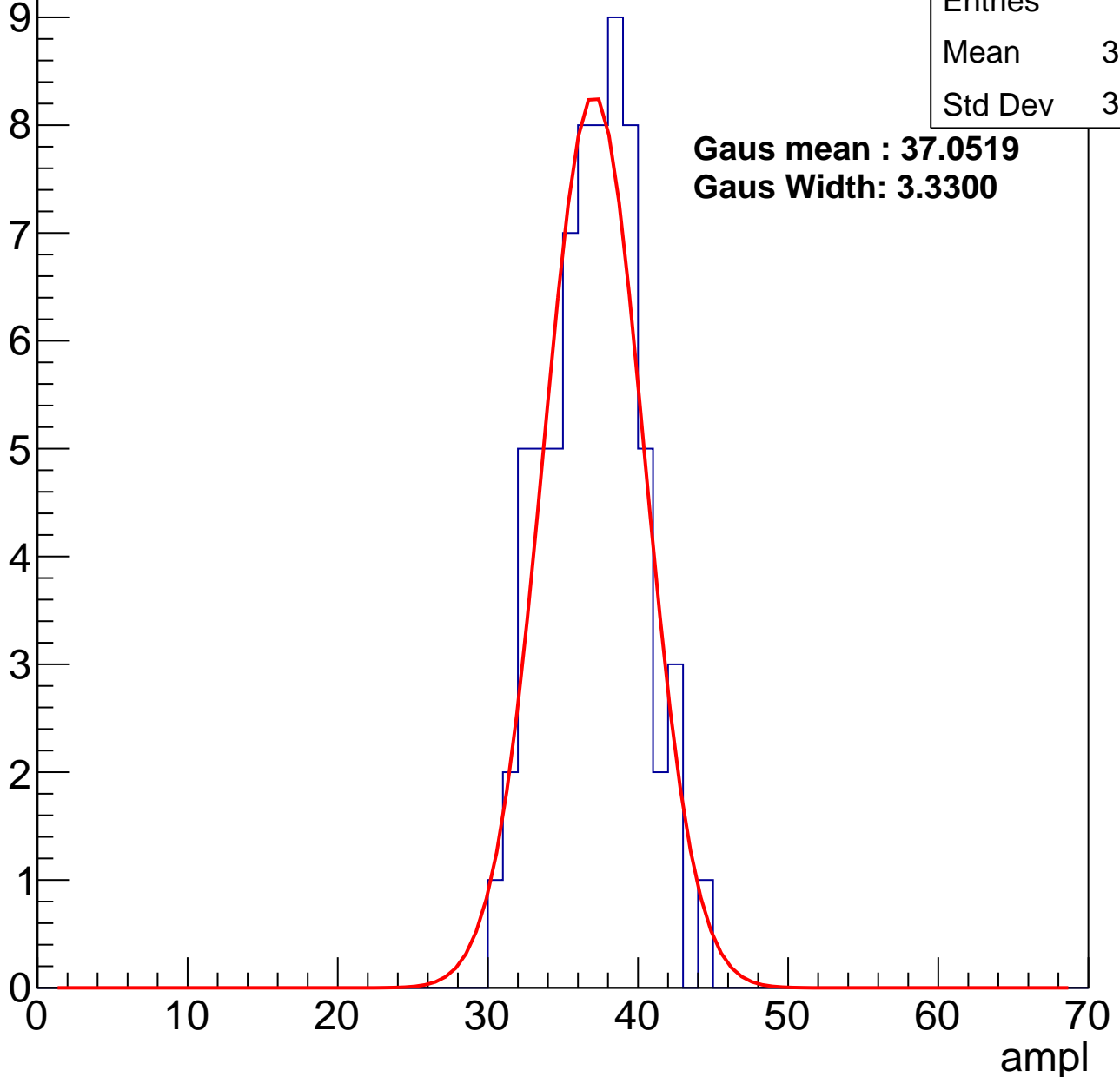
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	36.55
Std Dev	3.034

Gaus mean : 37.0519

Gaus Width: 3.3300



B1L003S, U21-ch11, adc2

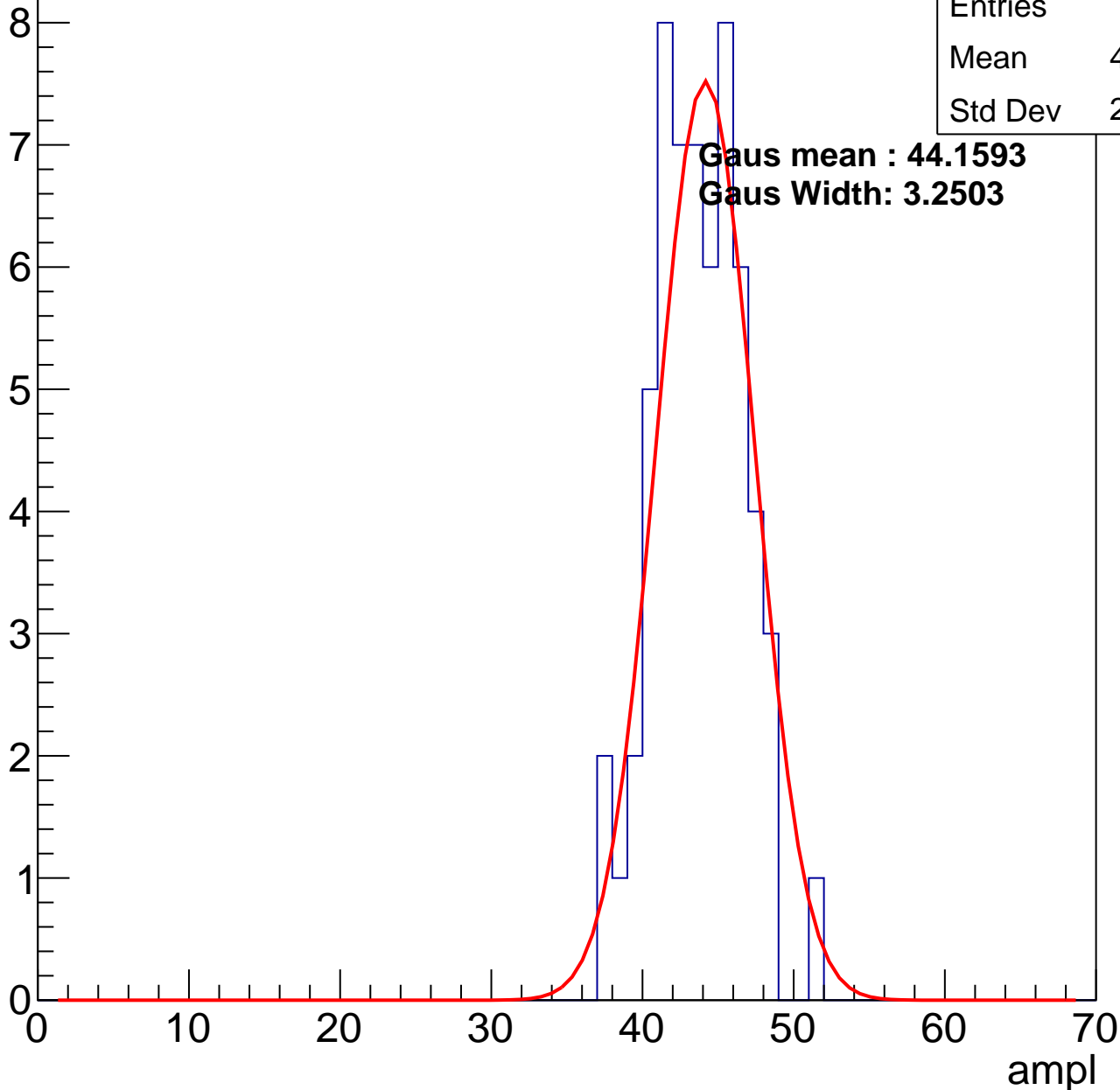
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.27
Std Dev	2.898

Gaus mean : 44.1593

Gaus Width: 3.2503

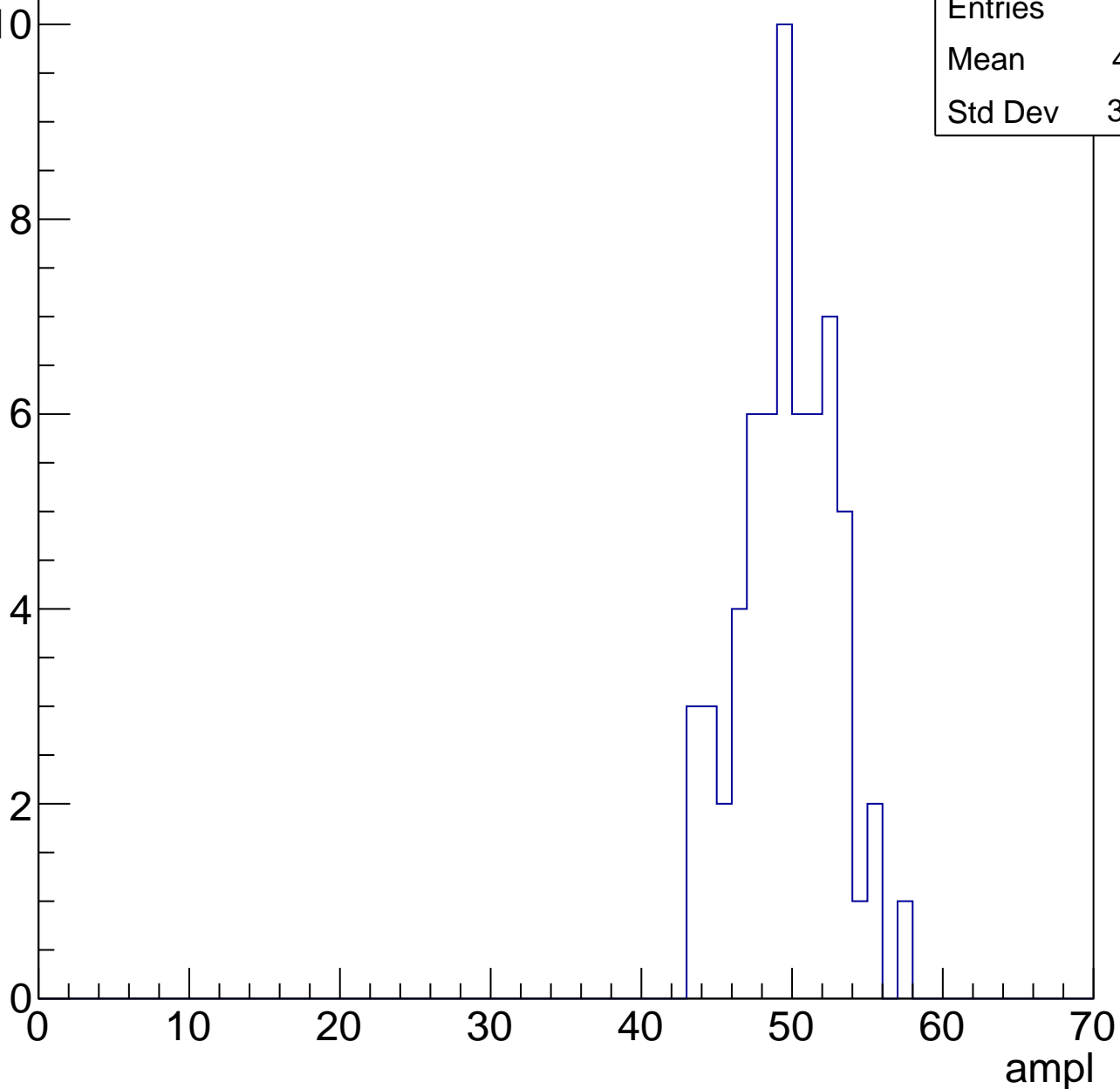


B1L003S, U21-ch11, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.21
Std Dev	3.143

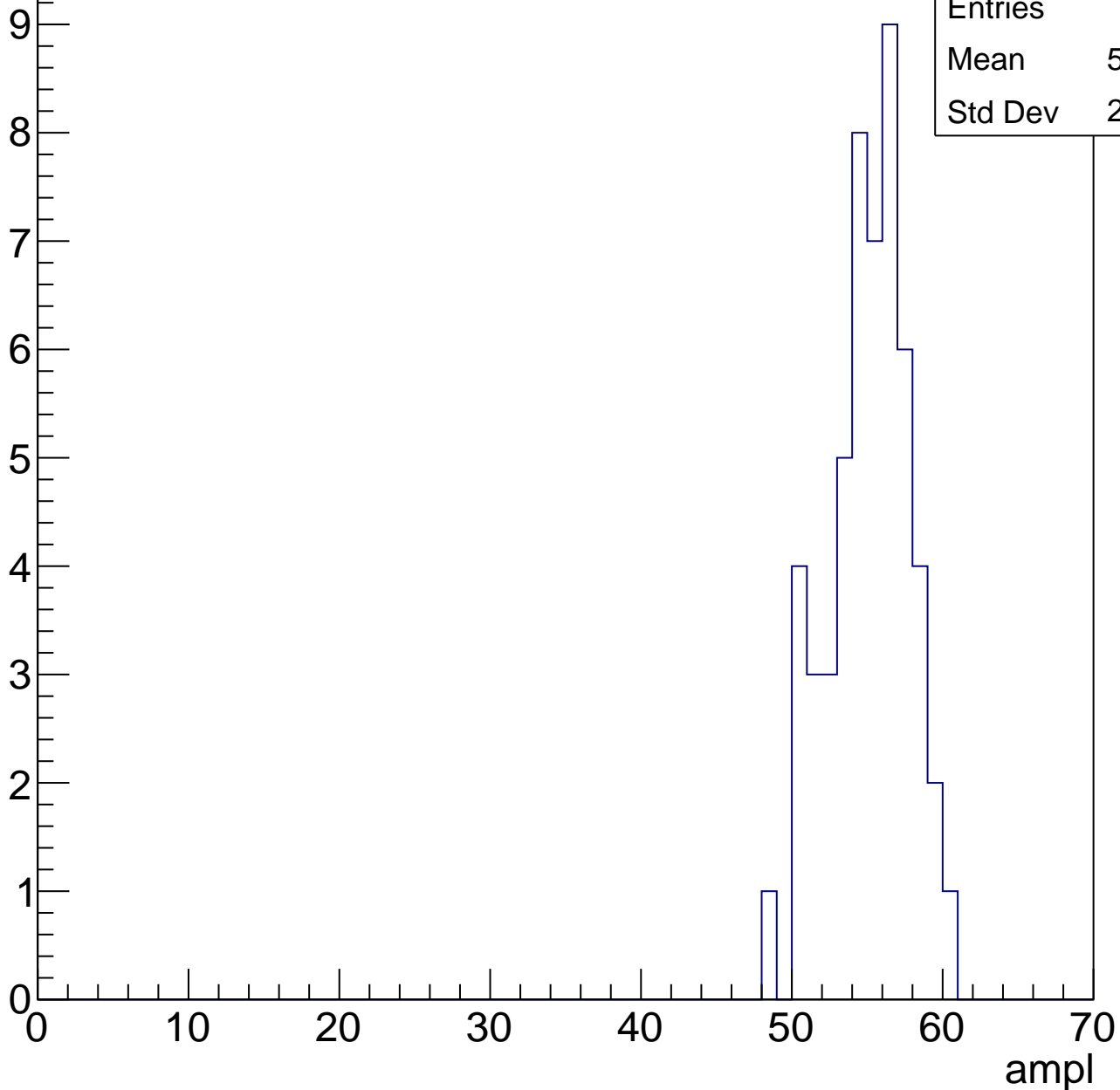


B1L003S, U21-ch11, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	54.62
Std Dev	2.644

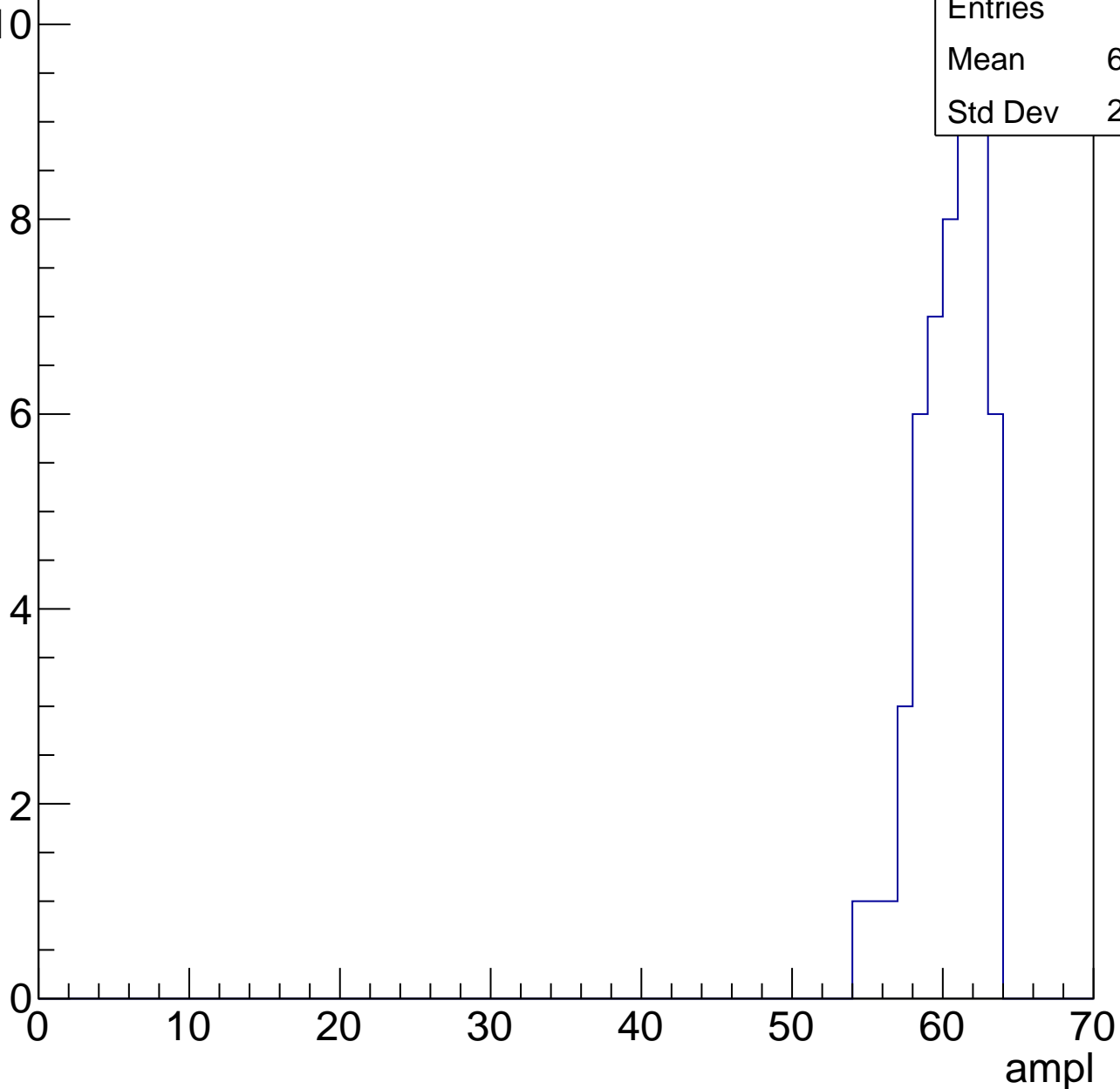


B1L003S, U21-ch11, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

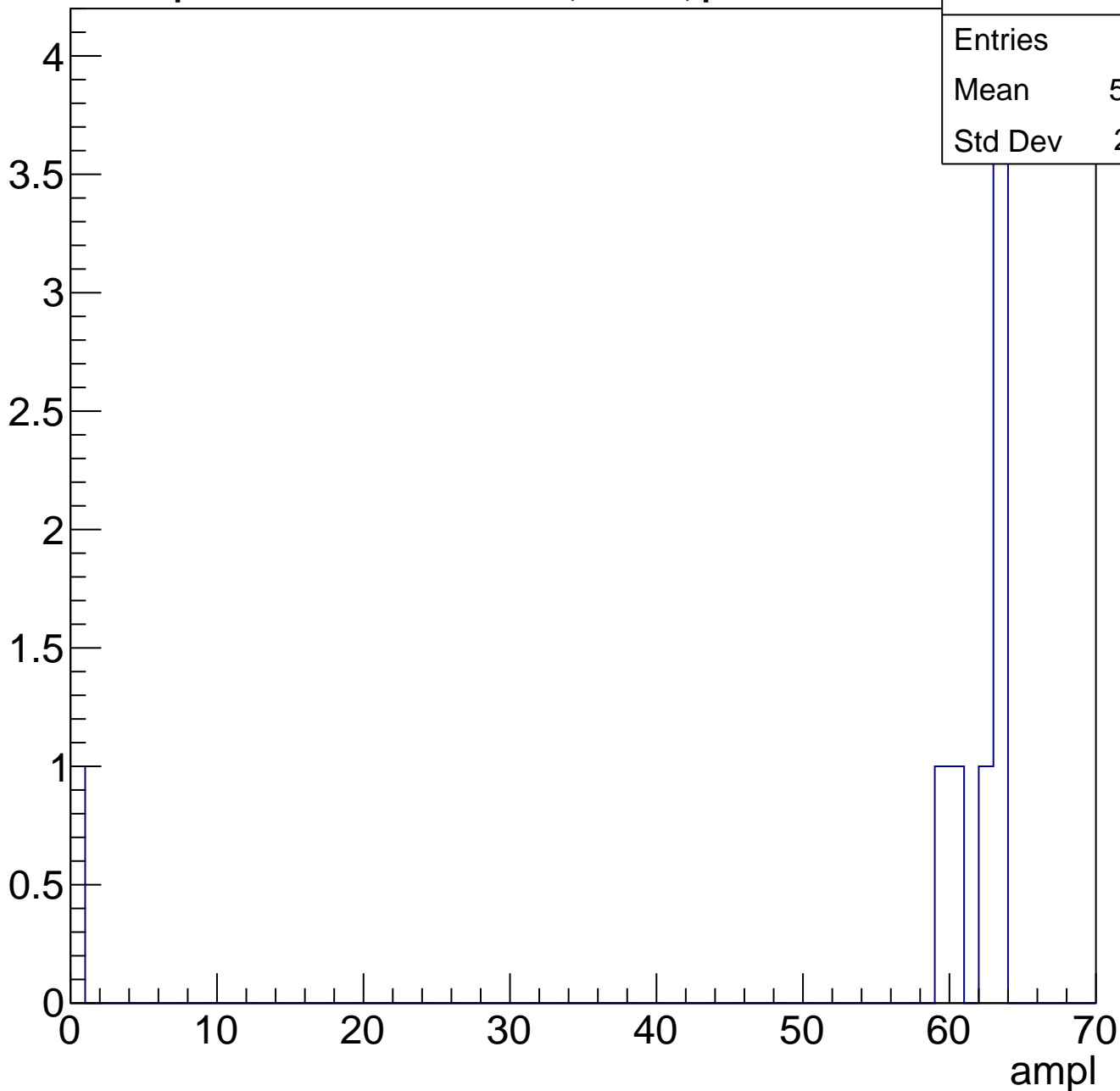
Entries	52
Mean	60.06
Std Dev	2.125



B1L003S, U21-ch11, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch11, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch12, adc0

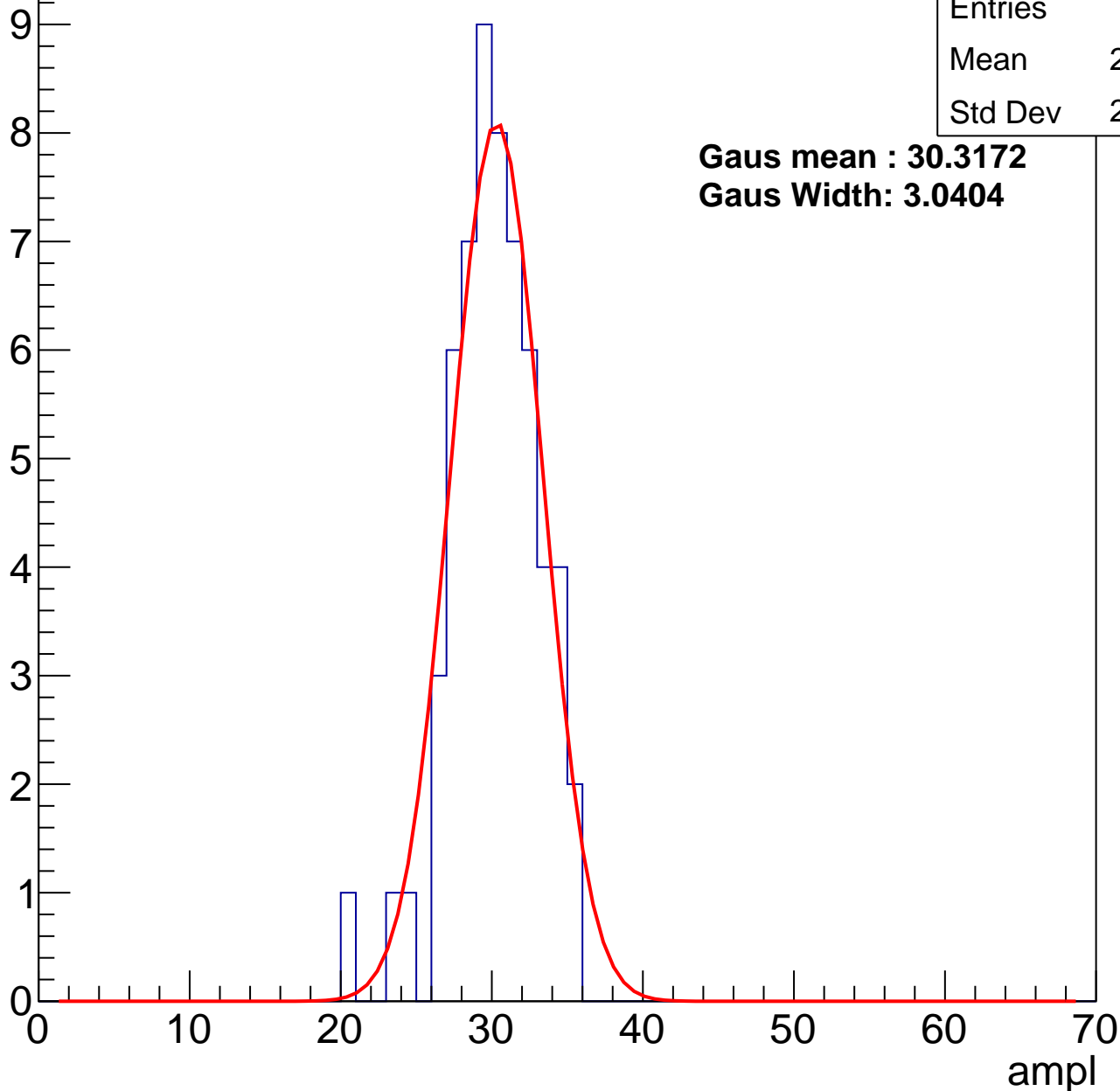
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	29.68
Std Dev	2.908

Gaus mean : 30.3172

Gaus Width: 3.0404



B1L003S, U21-ch12, adc1

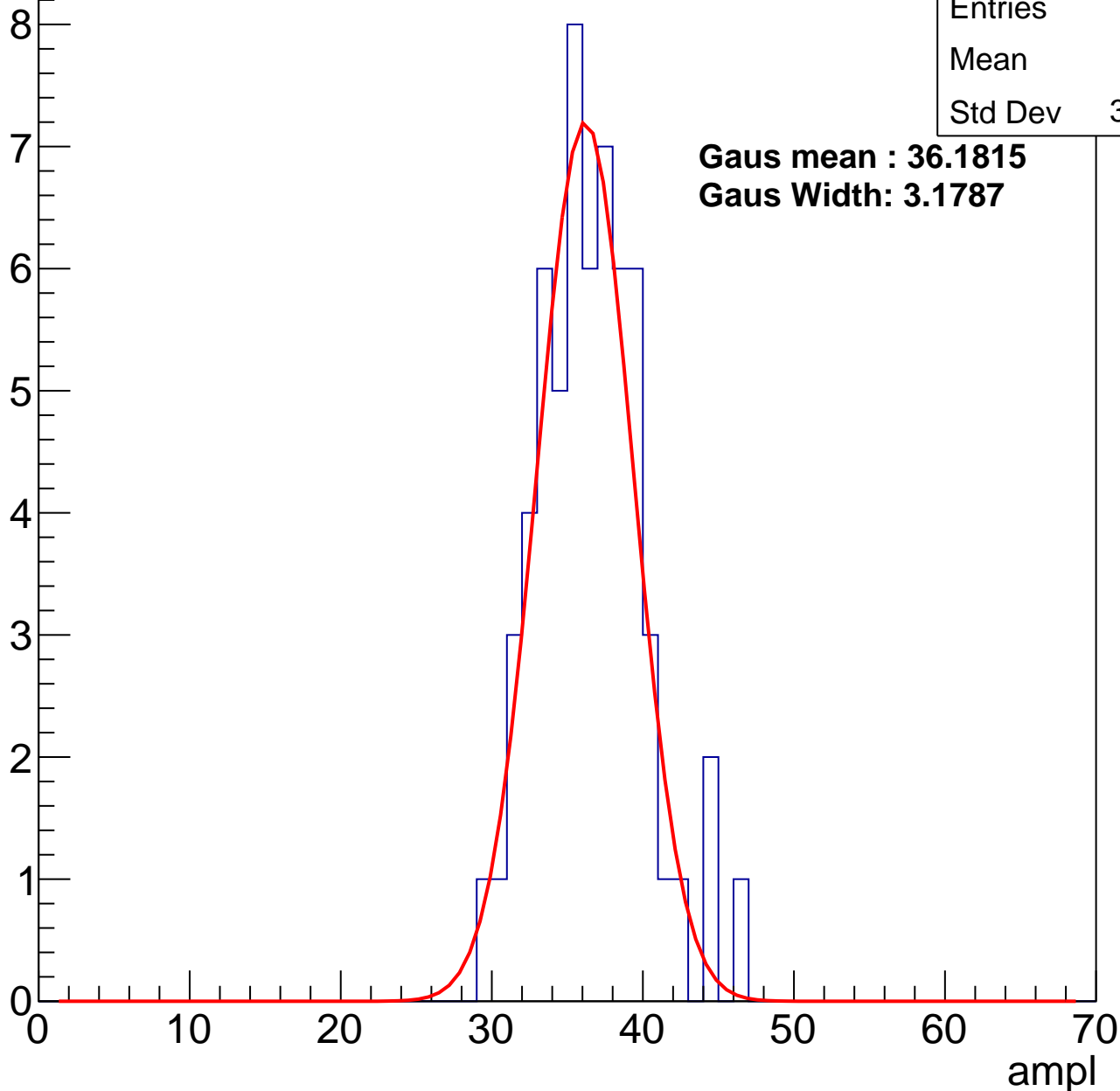
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.1
Std Dev	3.453

Gaus mean : 36.1815

Gaus Width: 3.1787



B1L003S, U21-ch12, adc2

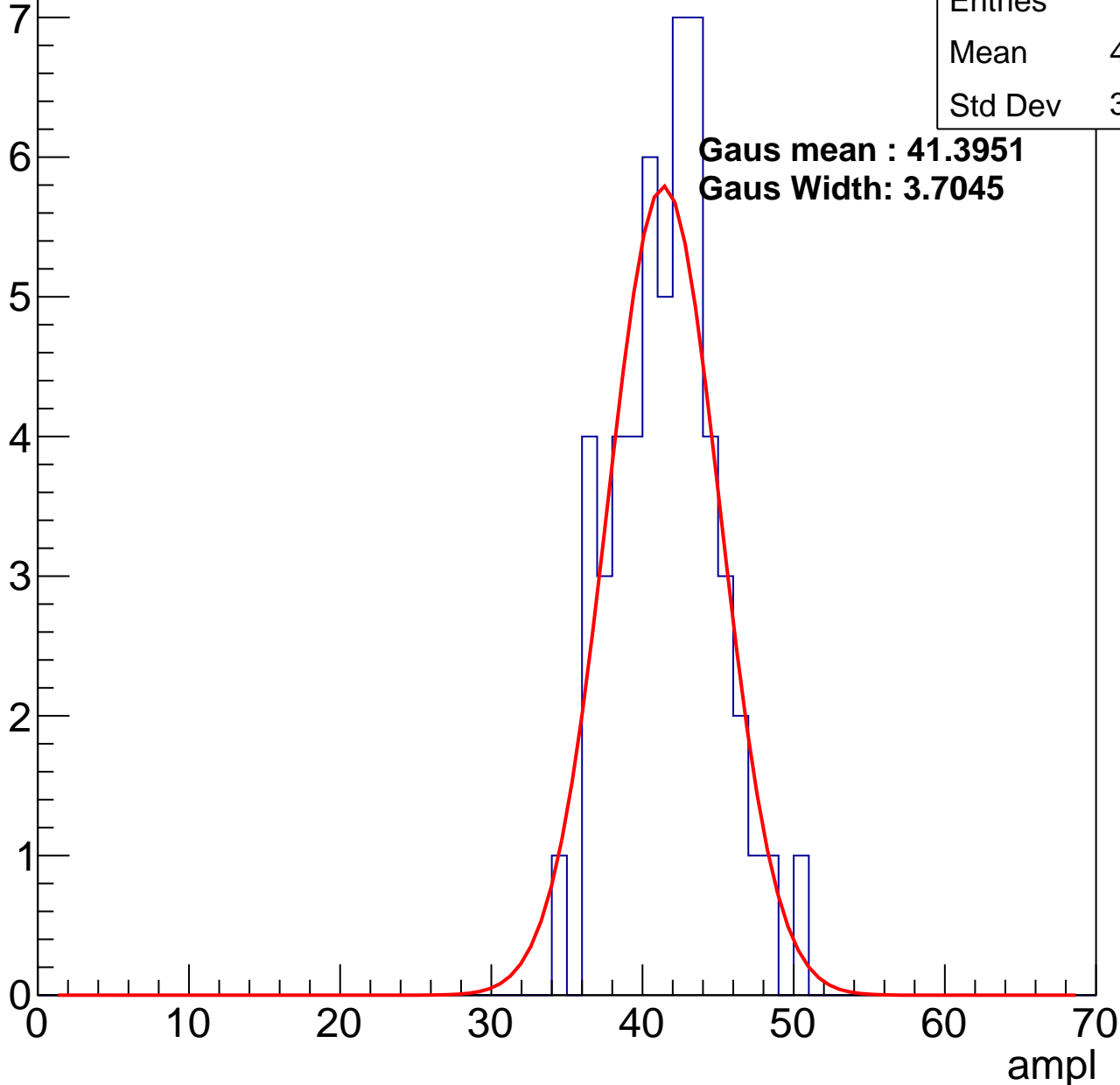
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	41.23
Std Dev	3.329

Gaus mean : 41.3951

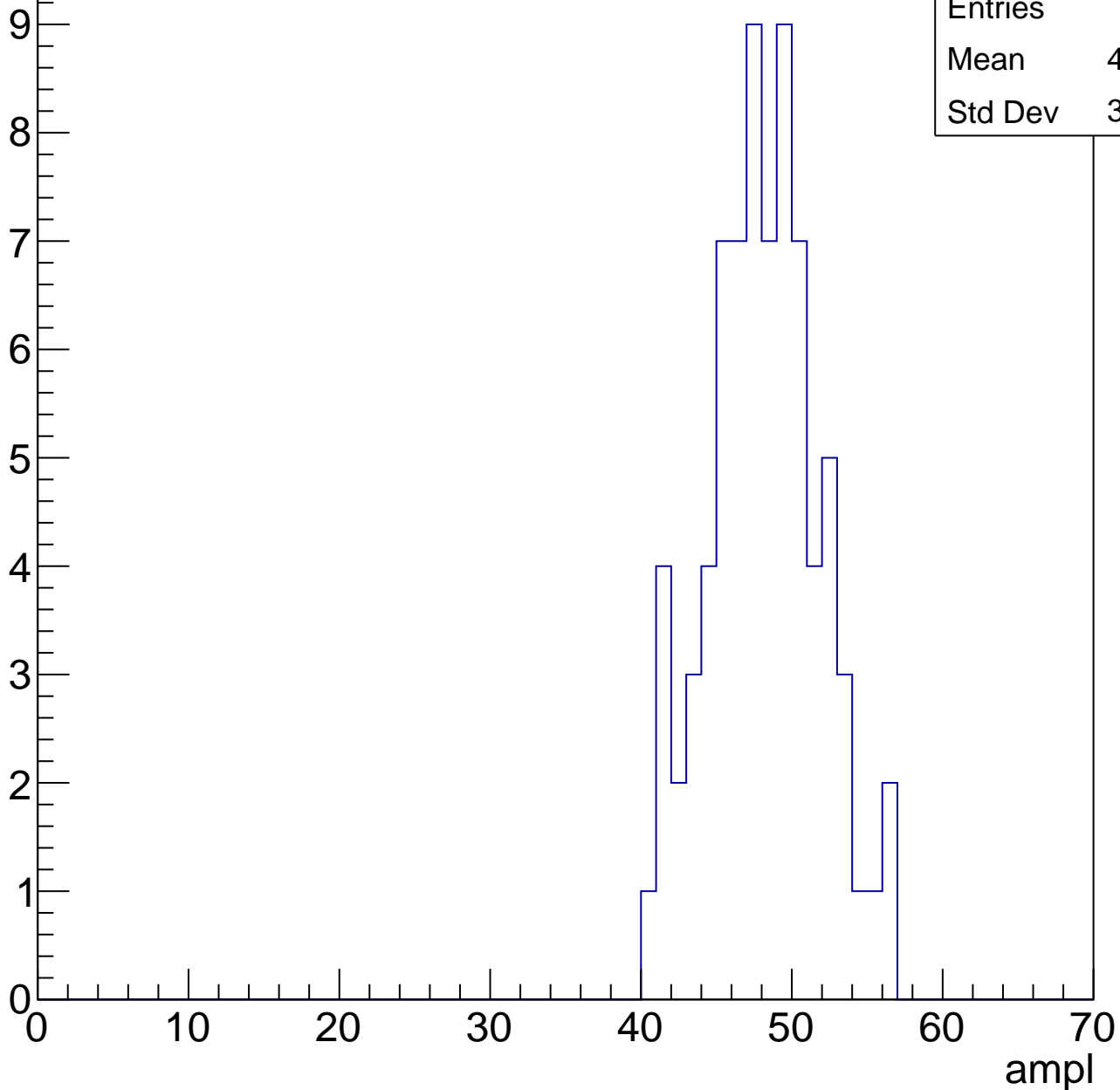
Gaus Width: 3.7045



B1L003S, U21-ch12, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



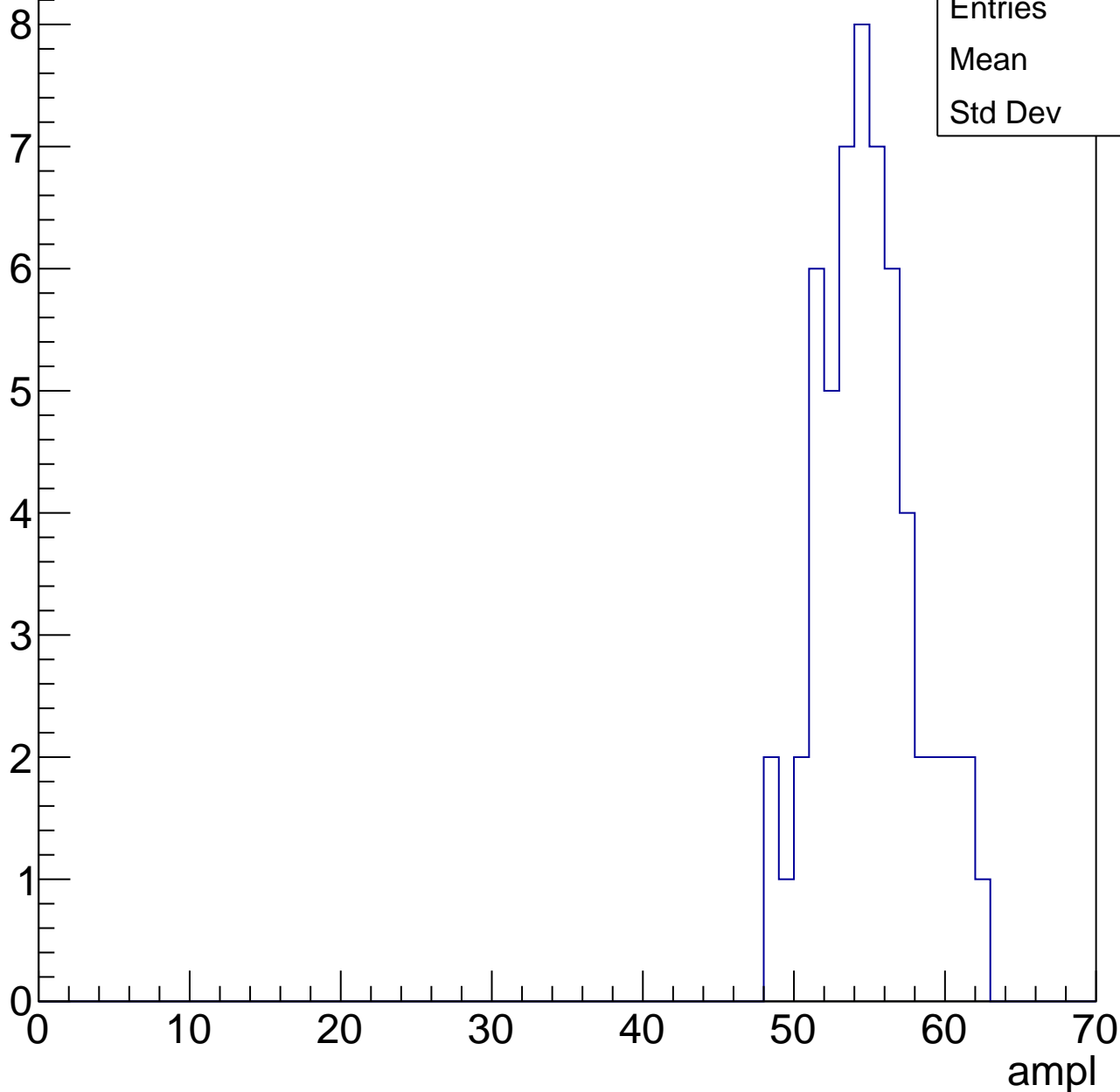
Entries	76
Mean	47.68
Std Dev	3.635

B1L003S, U21-ch12, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

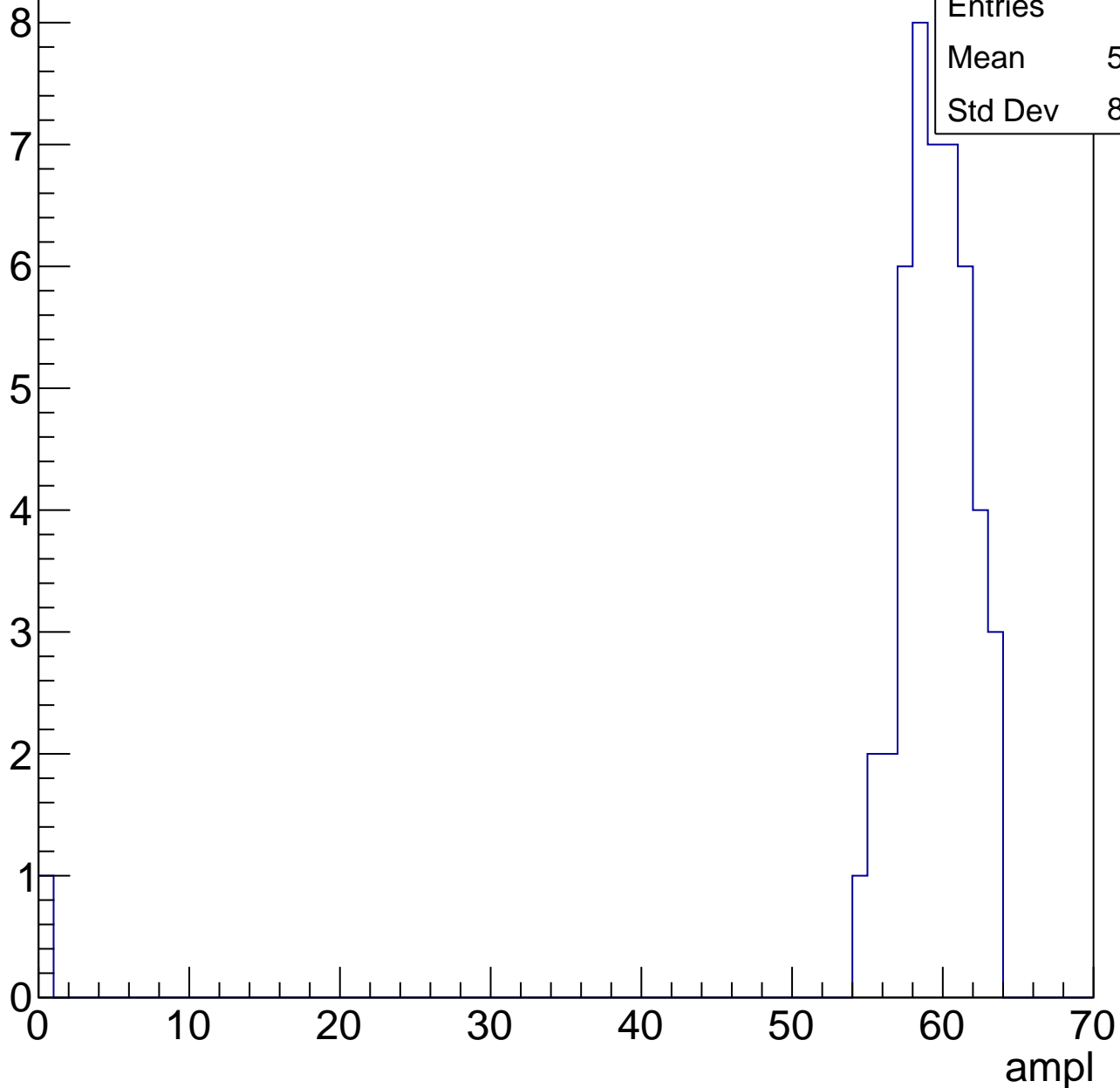
Entries	57
Mean	54.4
Std Dev	3.2



B1L003S, U21-ch12, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

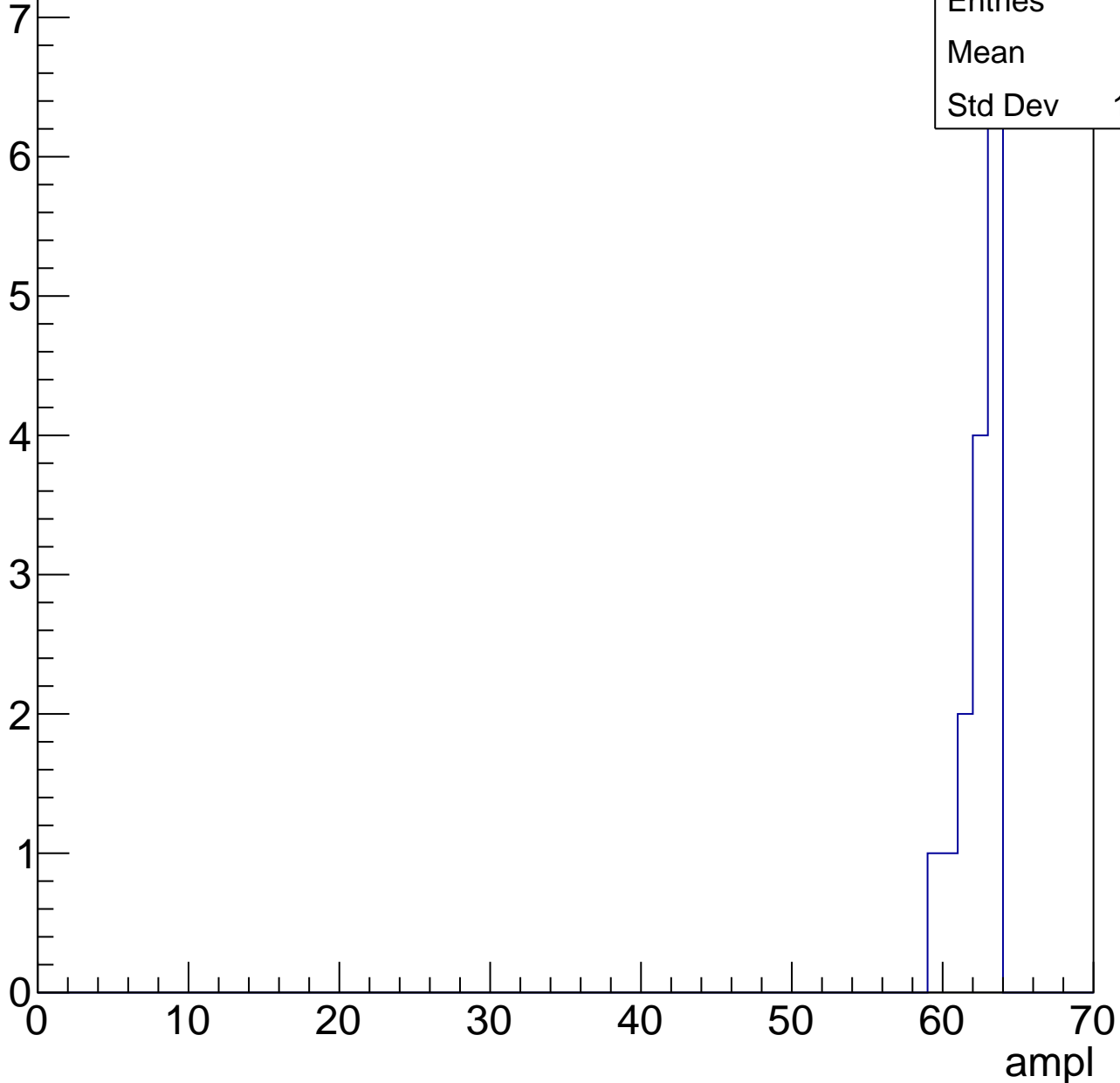


B1L003S, U21-ch12, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	62
Std Dev	1.211



B1L003S, U21-ch12, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch13, adc0

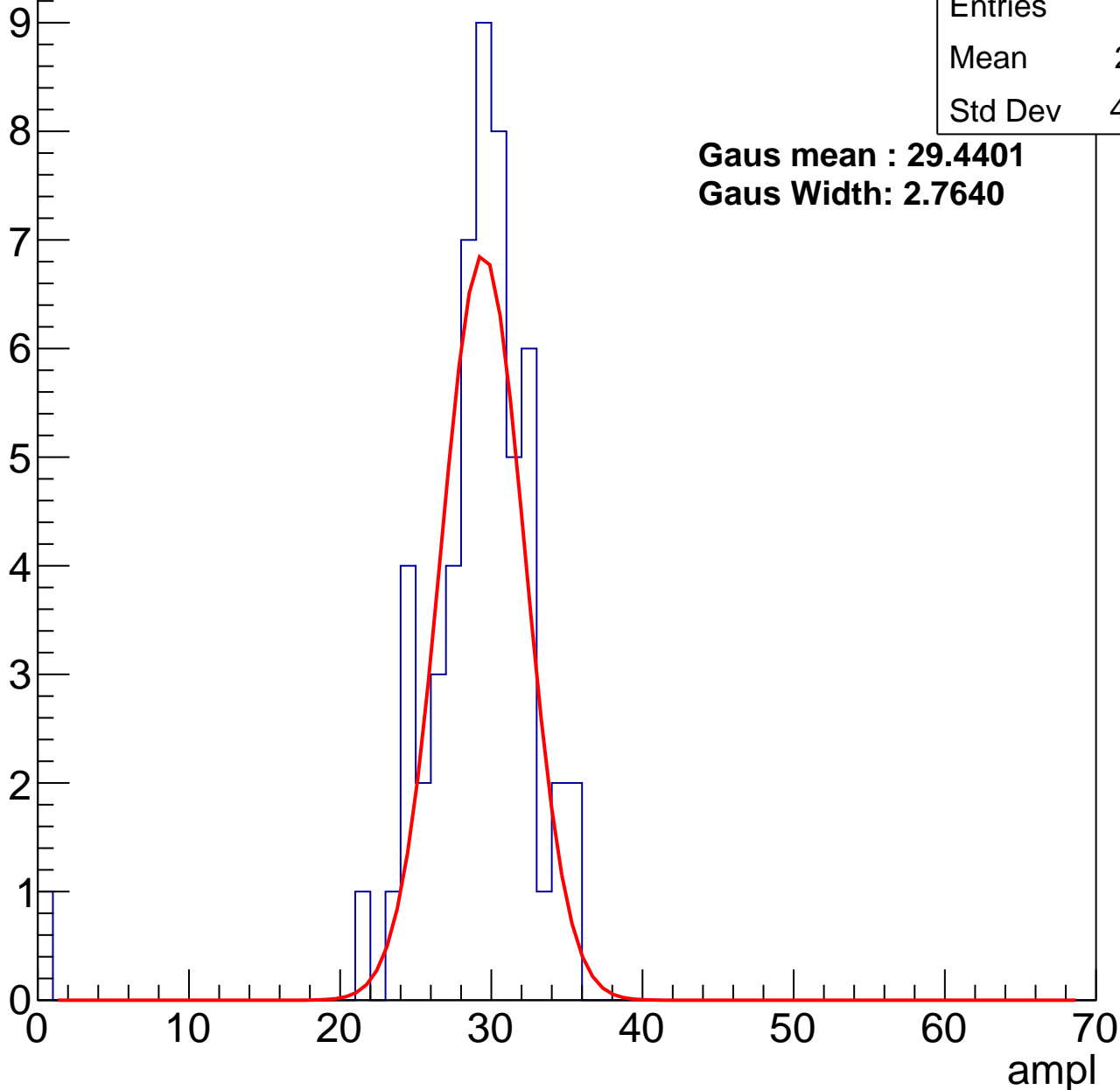
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	28.41
Std Dev	4.862

Gaus mean : 29.4401

Gaus Width: 2.7640



B1L003S, U21-ch13, adc1

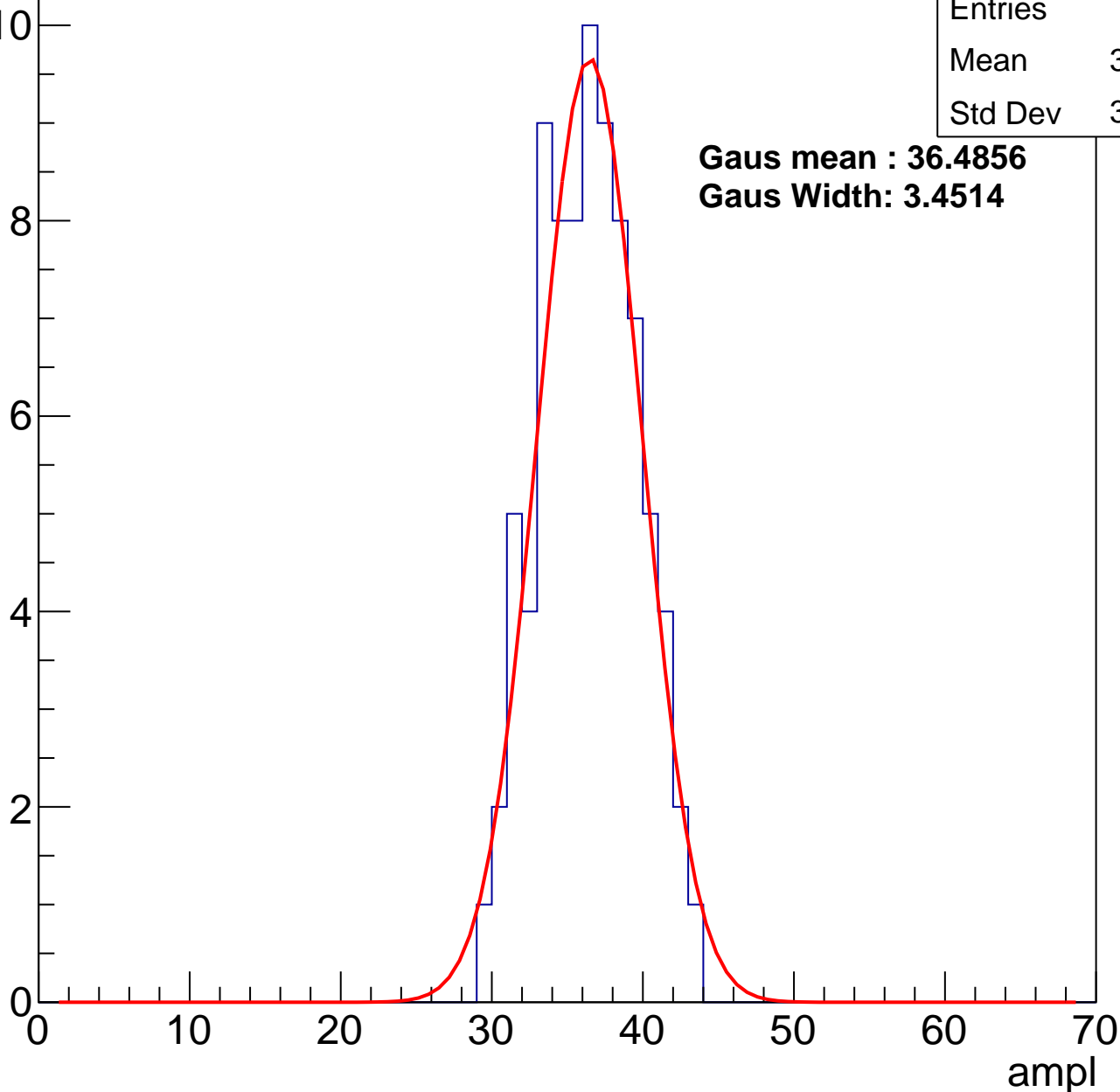
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	83
Mean	35.93
Std Dev	3.173

Gaus mean : 36.4856

Gaus Width: 3.4514



B1L003S, U21-ch13, adc2

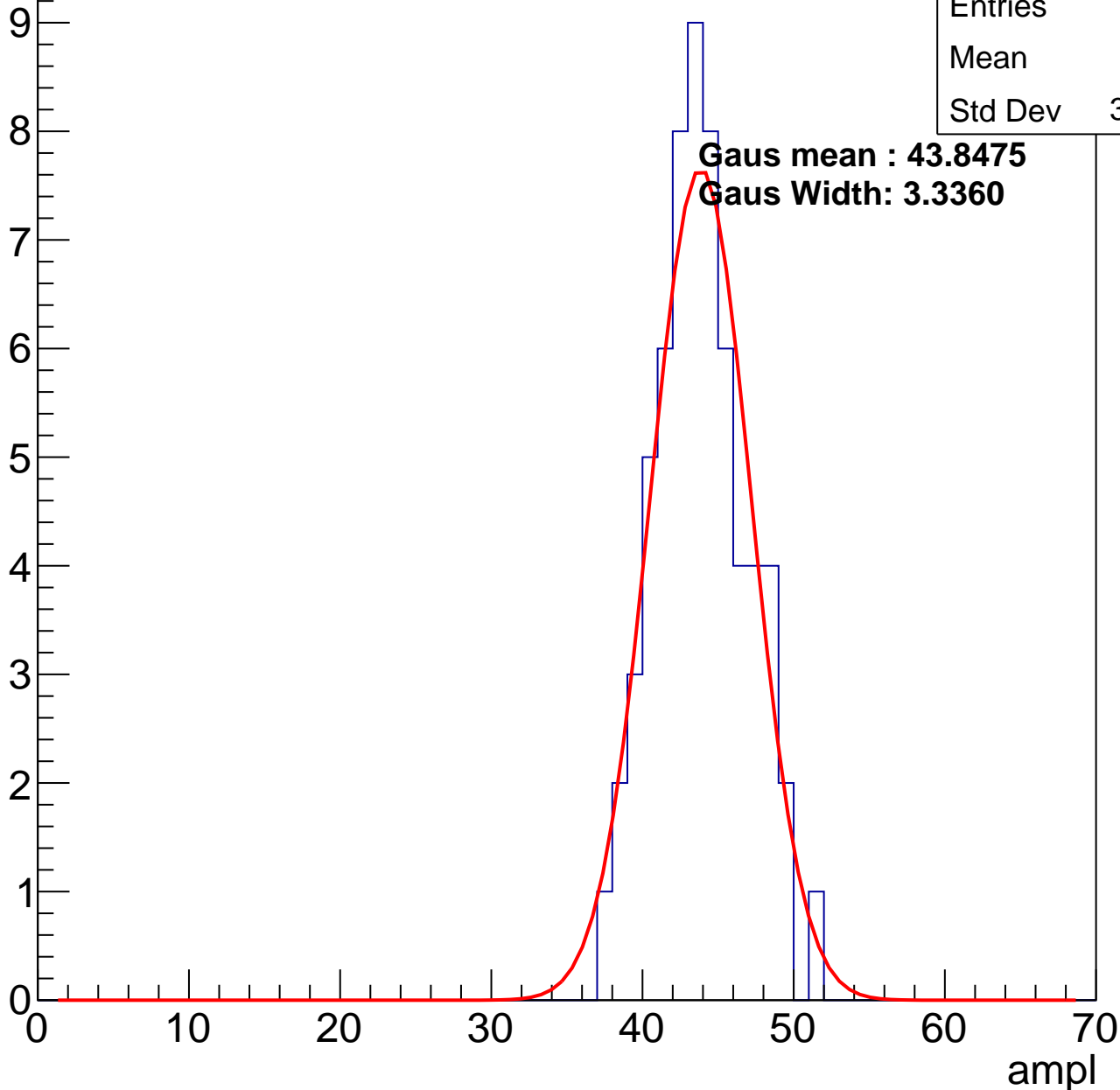
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	43.4
Std Dev	3.005

Gaus mean : 43.8475

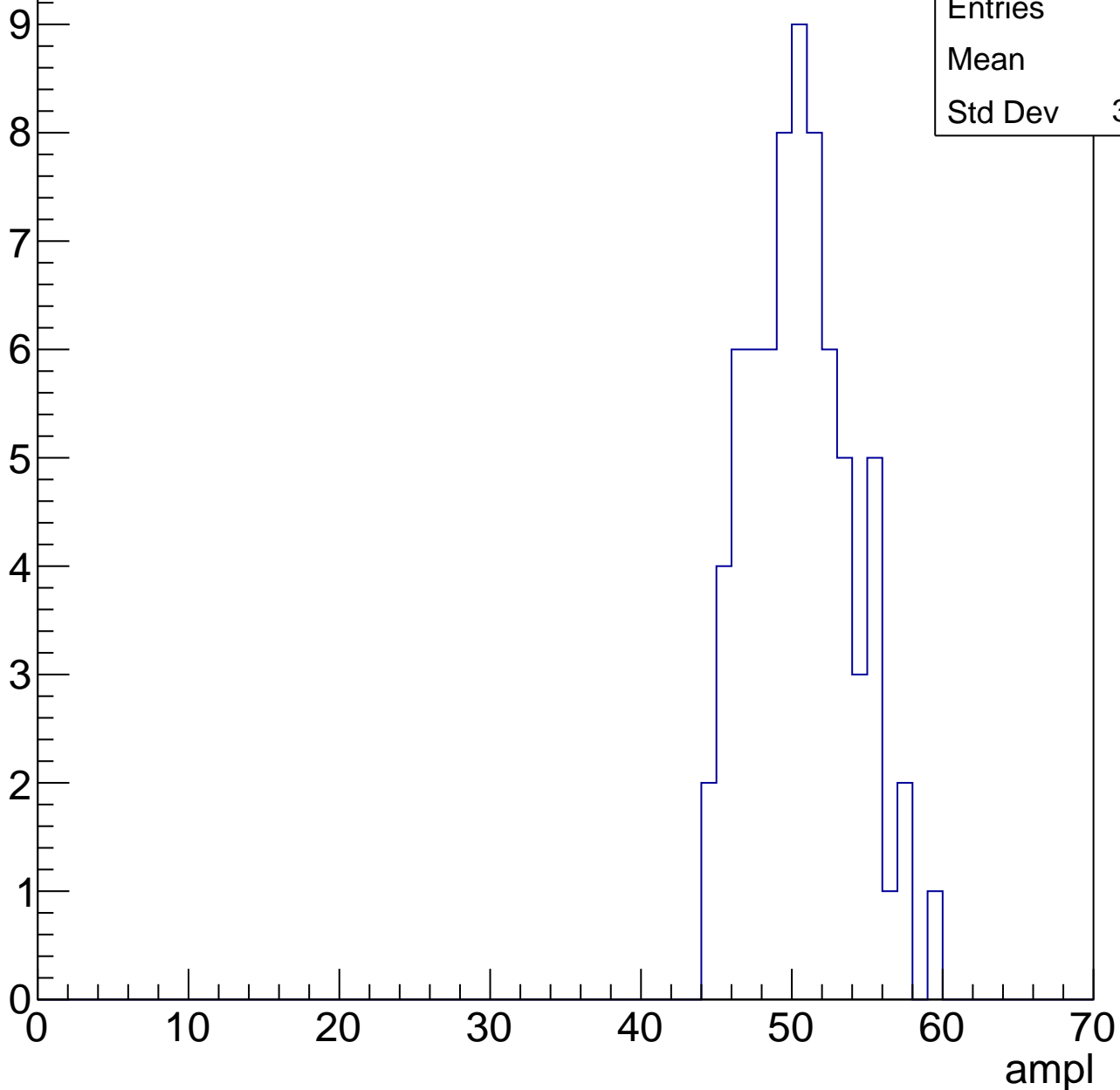
Gaus Width: 3.3360



B1L003S, U21-ch13, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



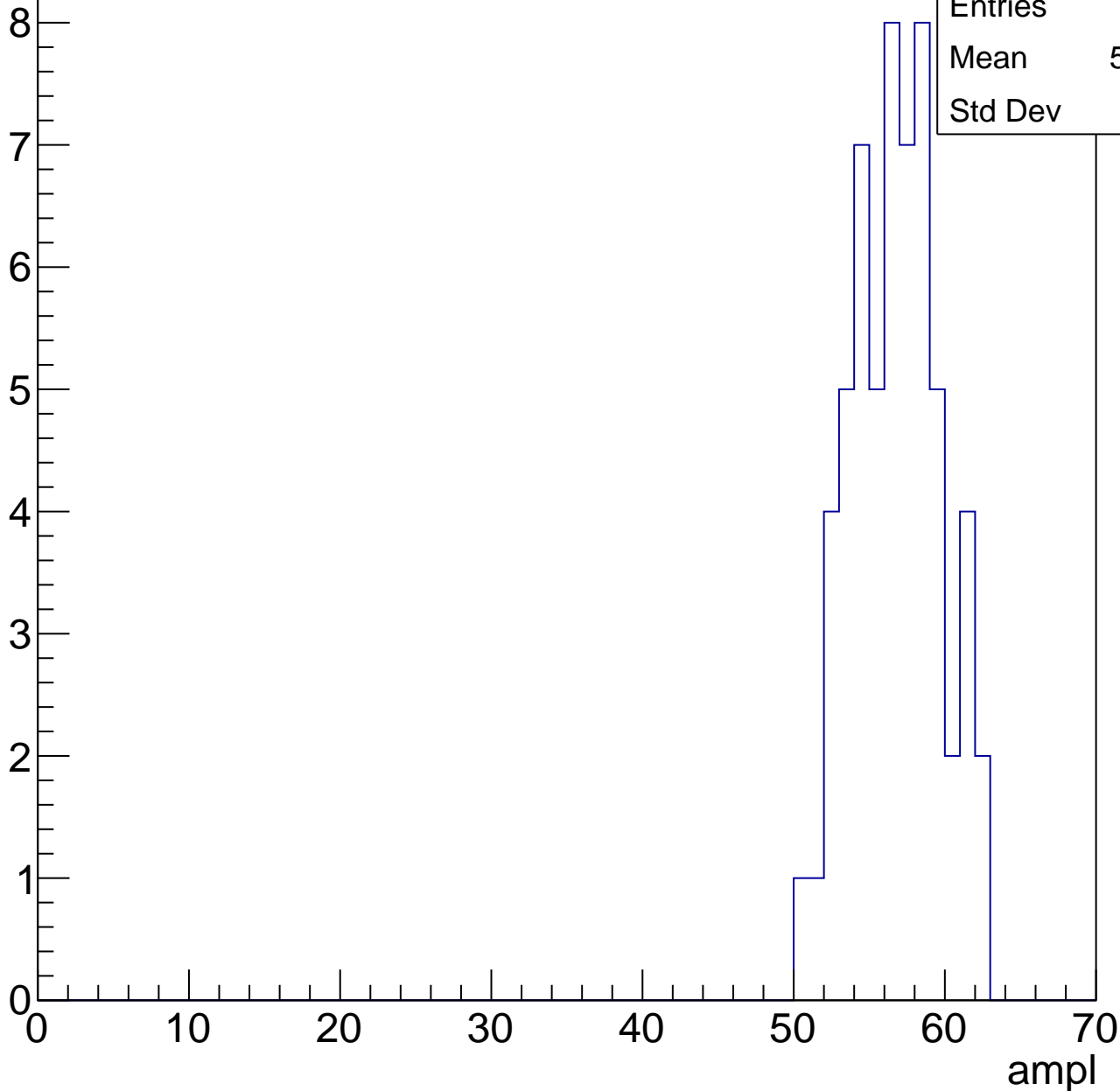
Entries	72
Mean	50.1
Std Dev	3.371

B1L003S, U21-ch13, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.29
Std Dev	2.87

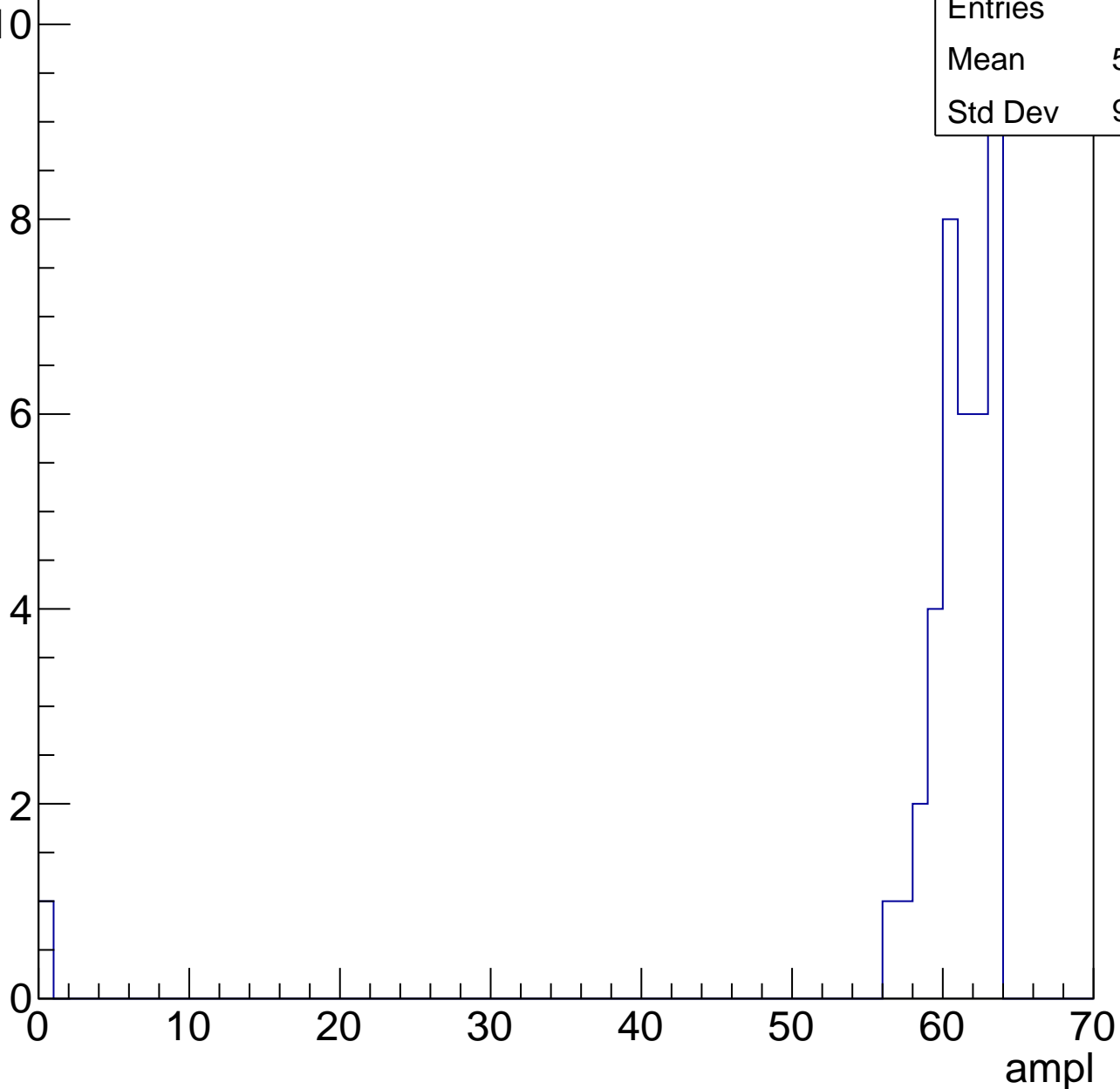


B1L003S, U21-ch13, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	59.31
Std Dev	9.791



B1L003S, U21-ch13, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	62
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U21-ch13, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch14, adc0

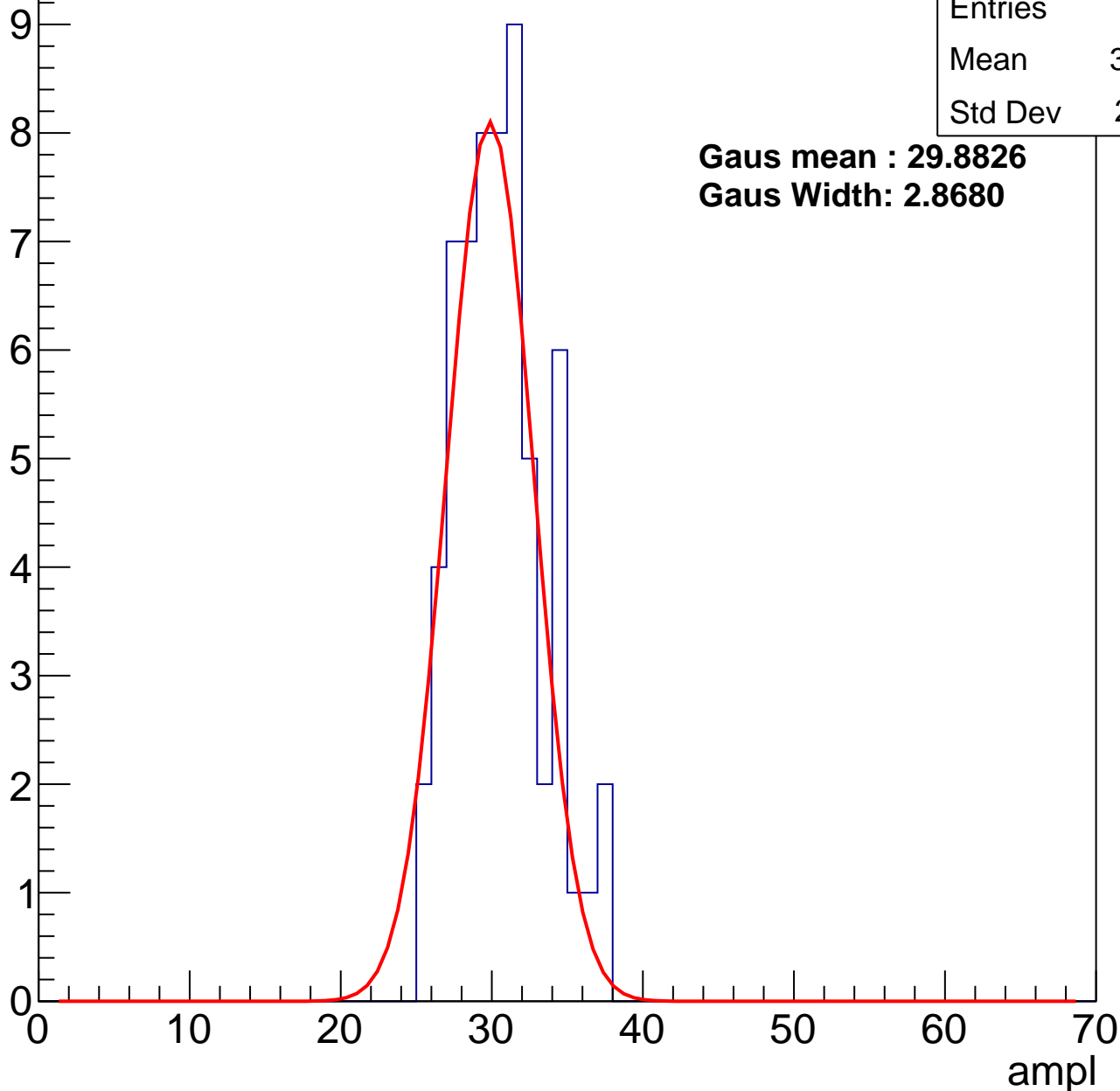
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	30.08
Std Dev	2.881

Gaus mean : 29.8826

Gaus Width: 2.8680



B1L003S, U21-ch14, adc1

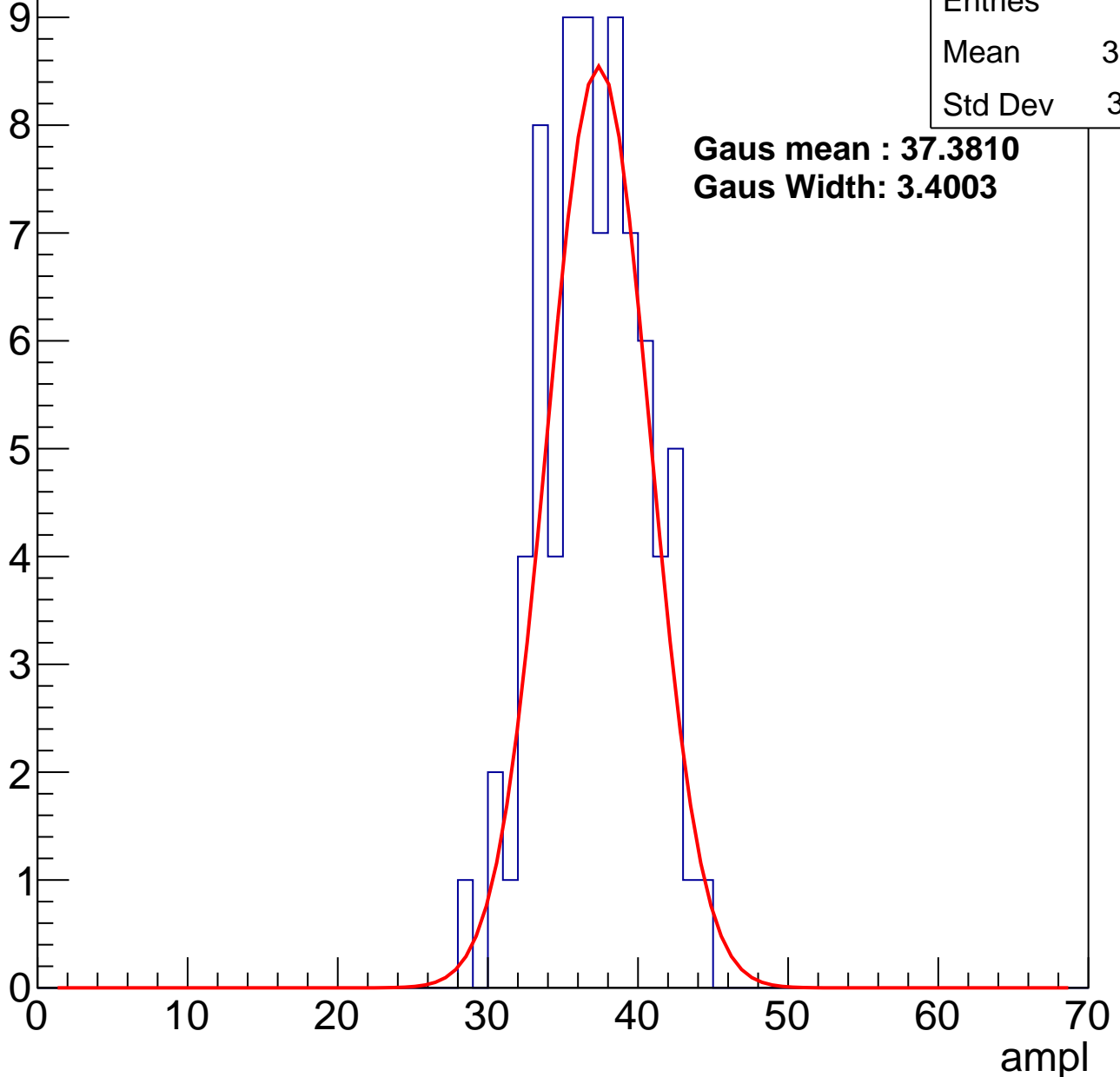
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	36.68
Std Dev	3.361

Gaus mean : 37.3810

Gaus Width: 3.4003



B1L003S, U21-ch14, adc2

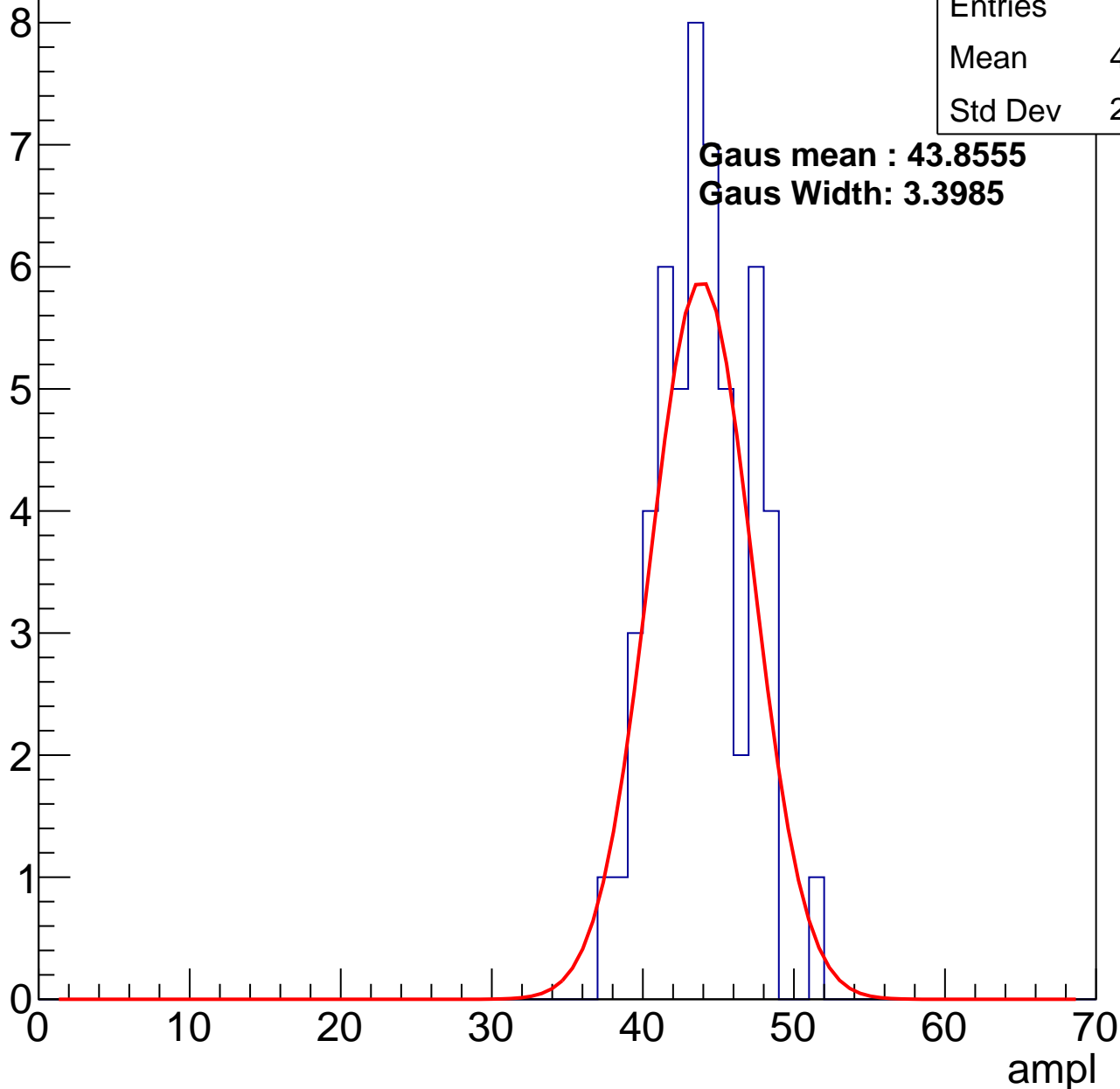
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.43
Std Dev	2.975

Gaus mean : 43.8555

Gaus Width: 3.3985

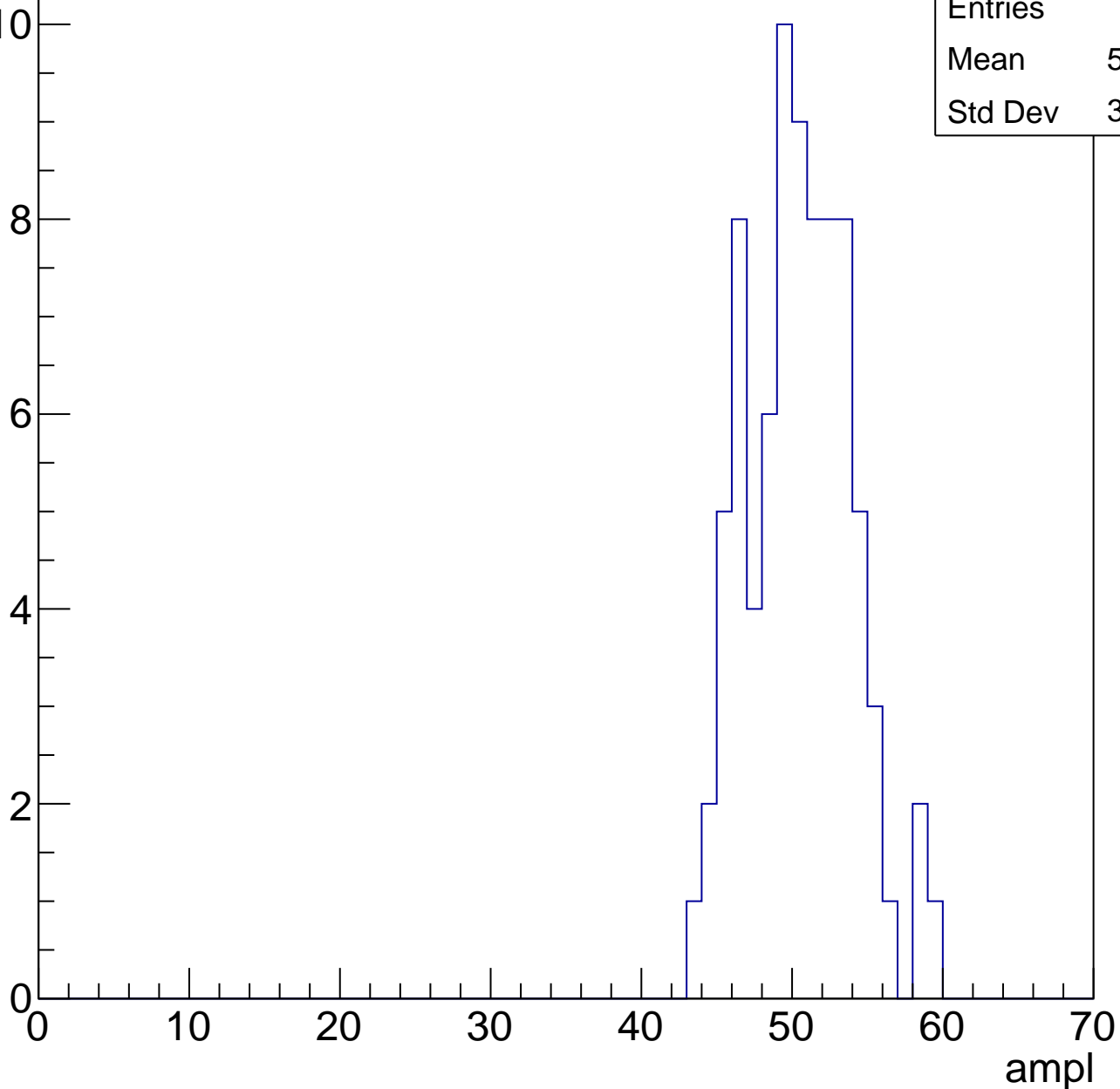


B1L003S, U21-ch14, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	50.05
Std Dev	3.435

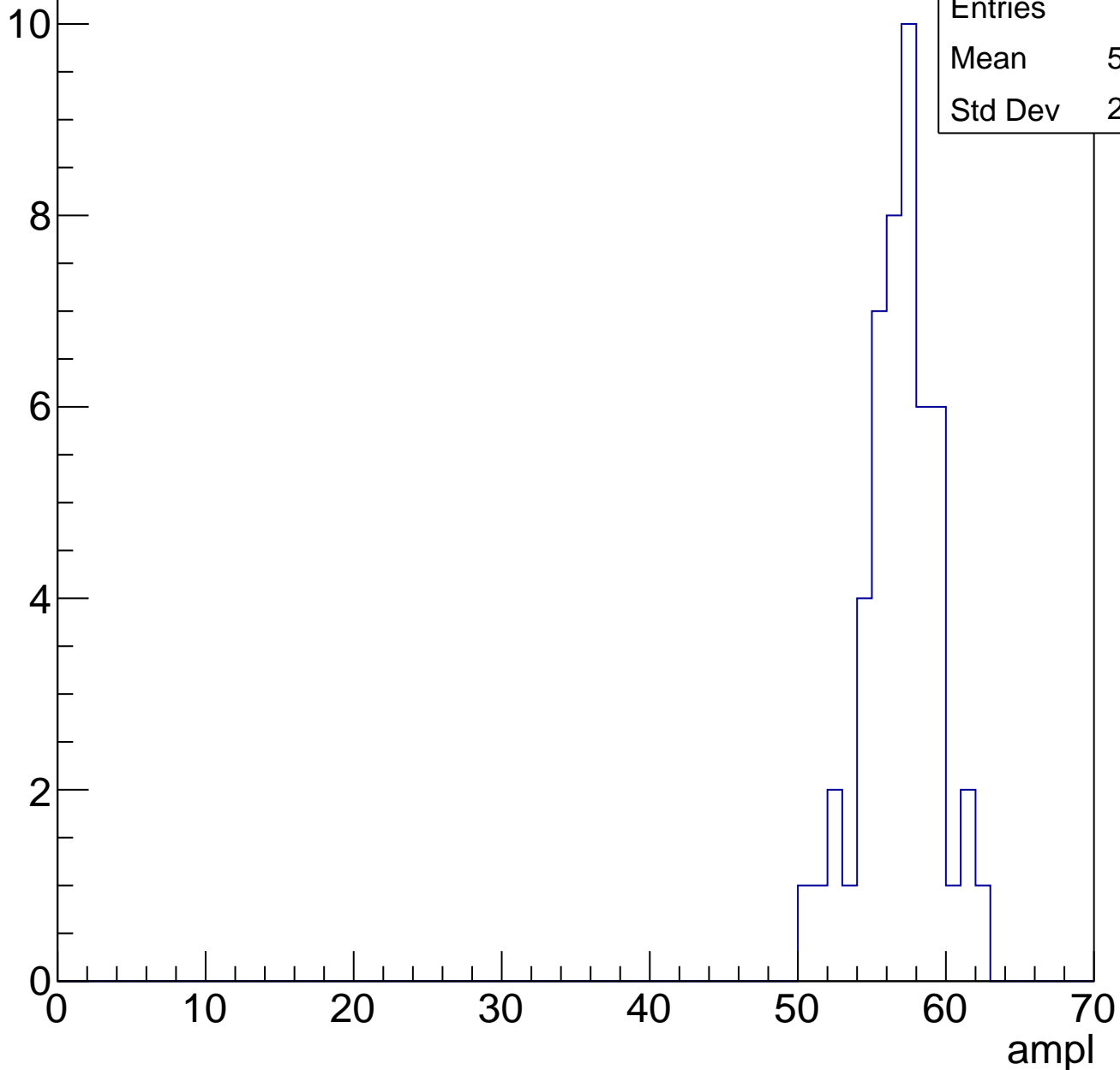


B1L003S, U21-ch14, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	50
Mean	56.46
Std Dev	2.467

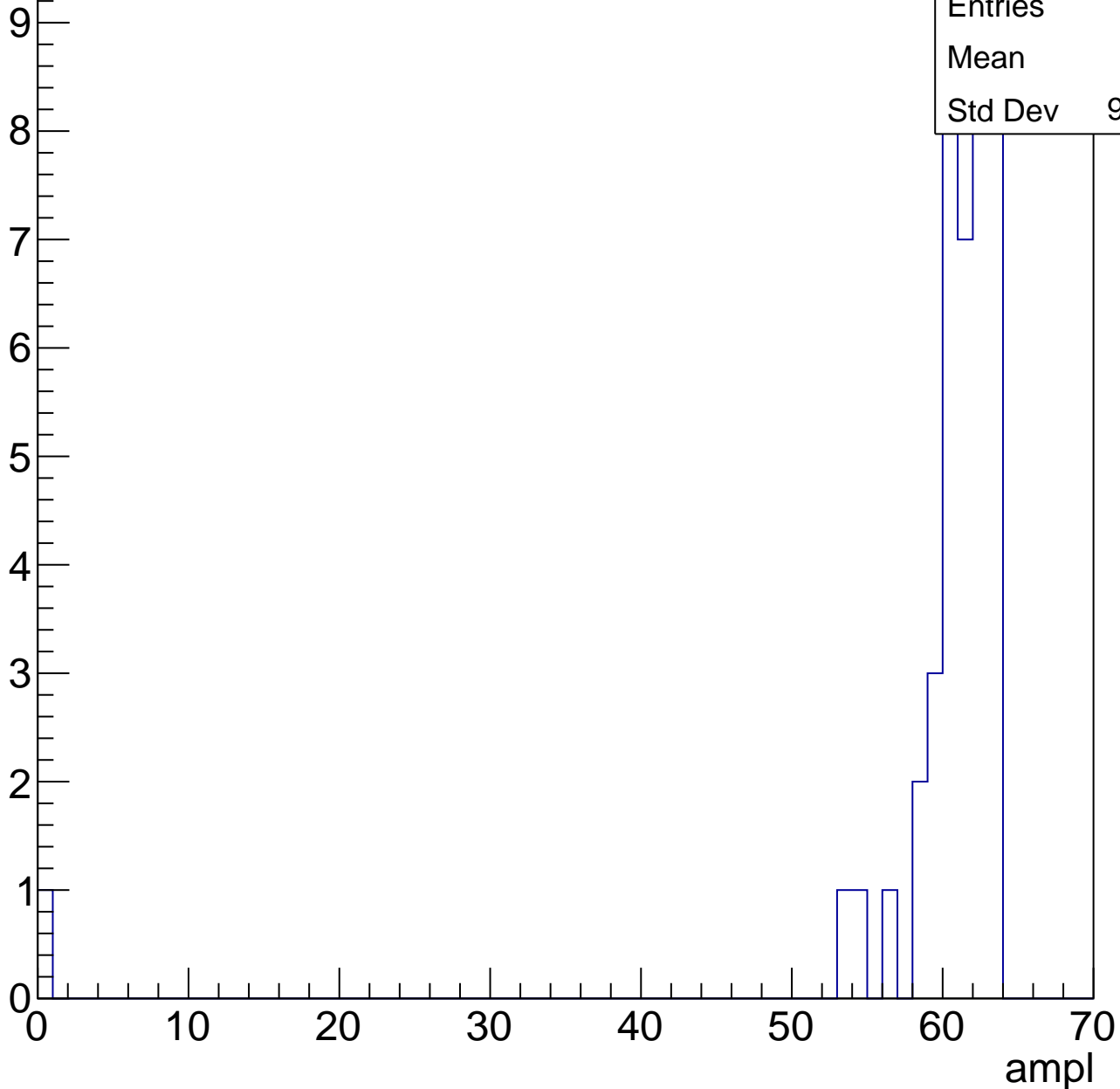
Entry



B1L003S, U21-ch14, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch14, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch14, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L003S, U21-ch15, adc0

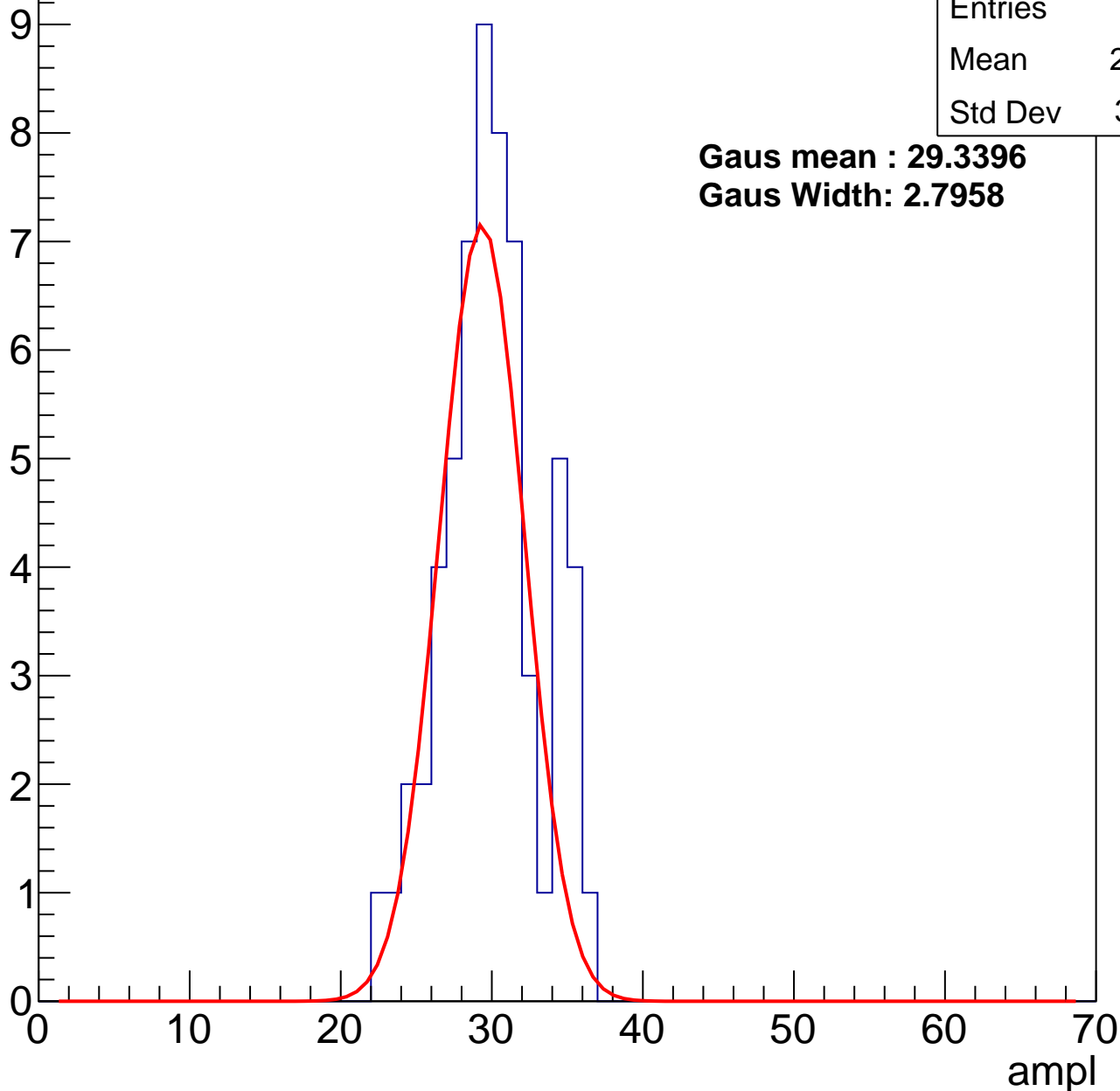
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.52
Std Dev	3.191

Gaus mean : 29.3396

Gaus Width: 2.7958



B1L003S, U21-ch15, adc1

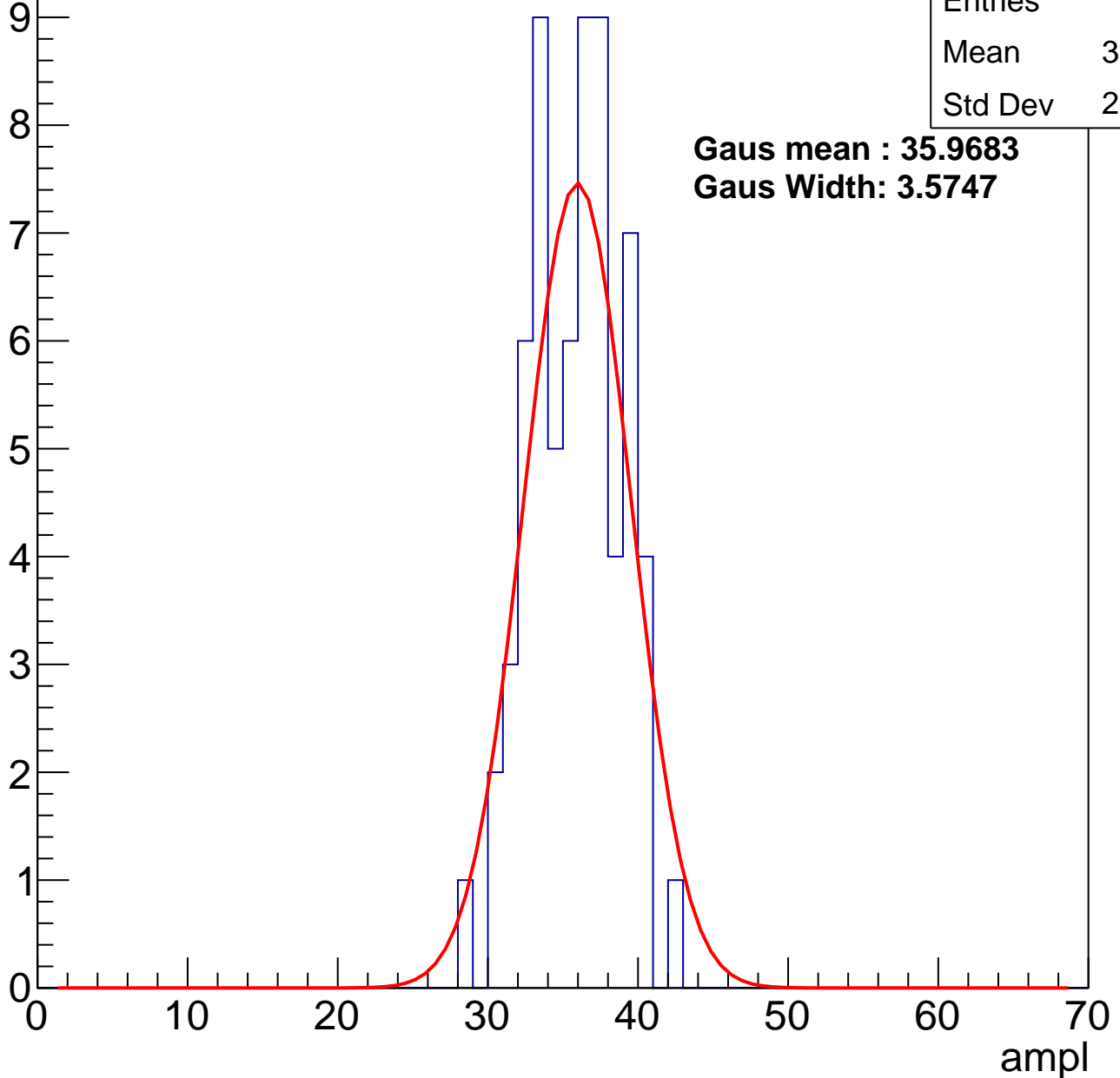
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.36
Std Dev	2.952

Gaus mean : 35.9683

Gaus Width: 3.5747



B1L003S, U21-ch15, adc2

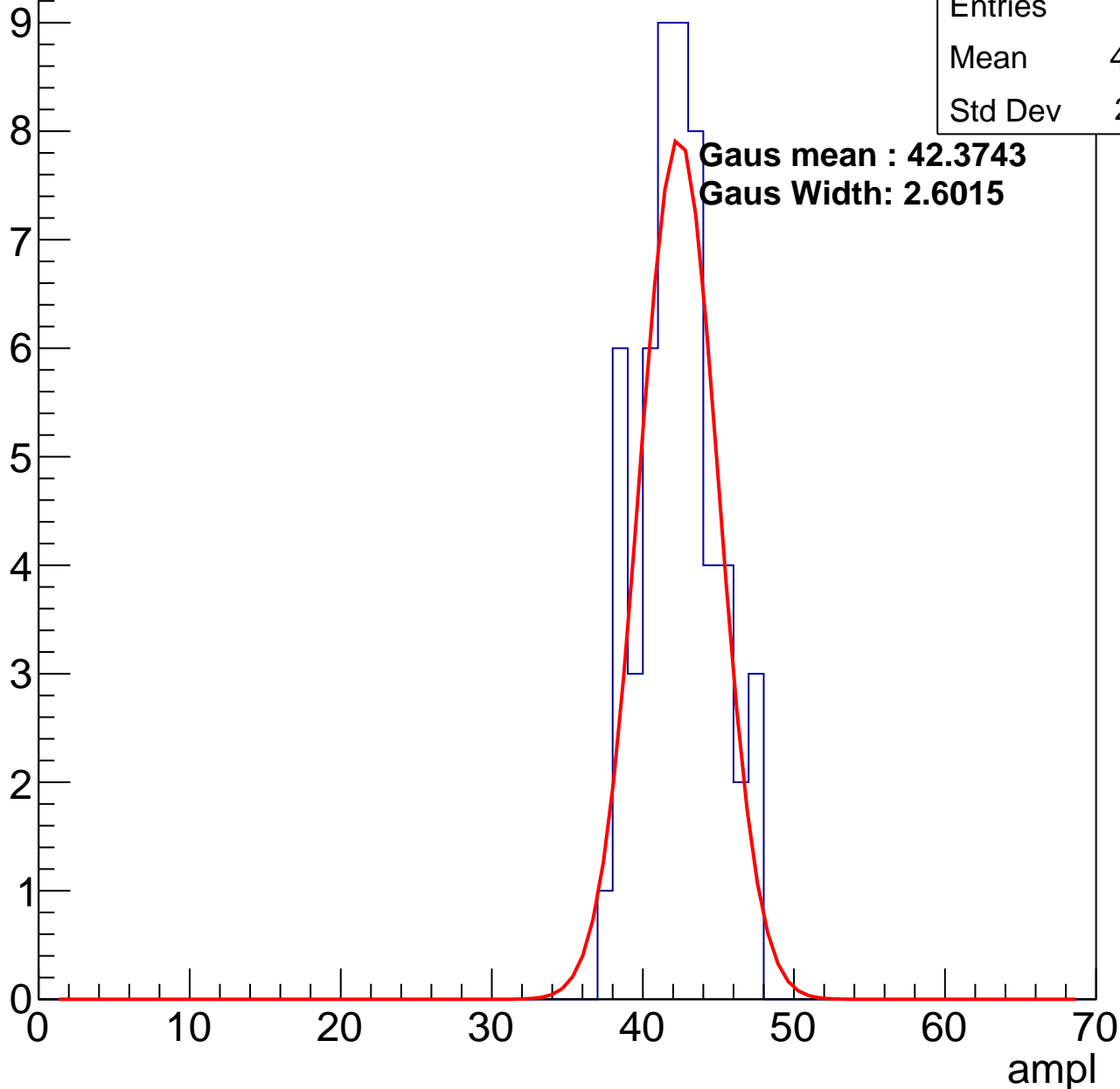
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	41.85
Std Dev	2.511

Gaus mean : 42.3743

Gaus Width: 2.6015

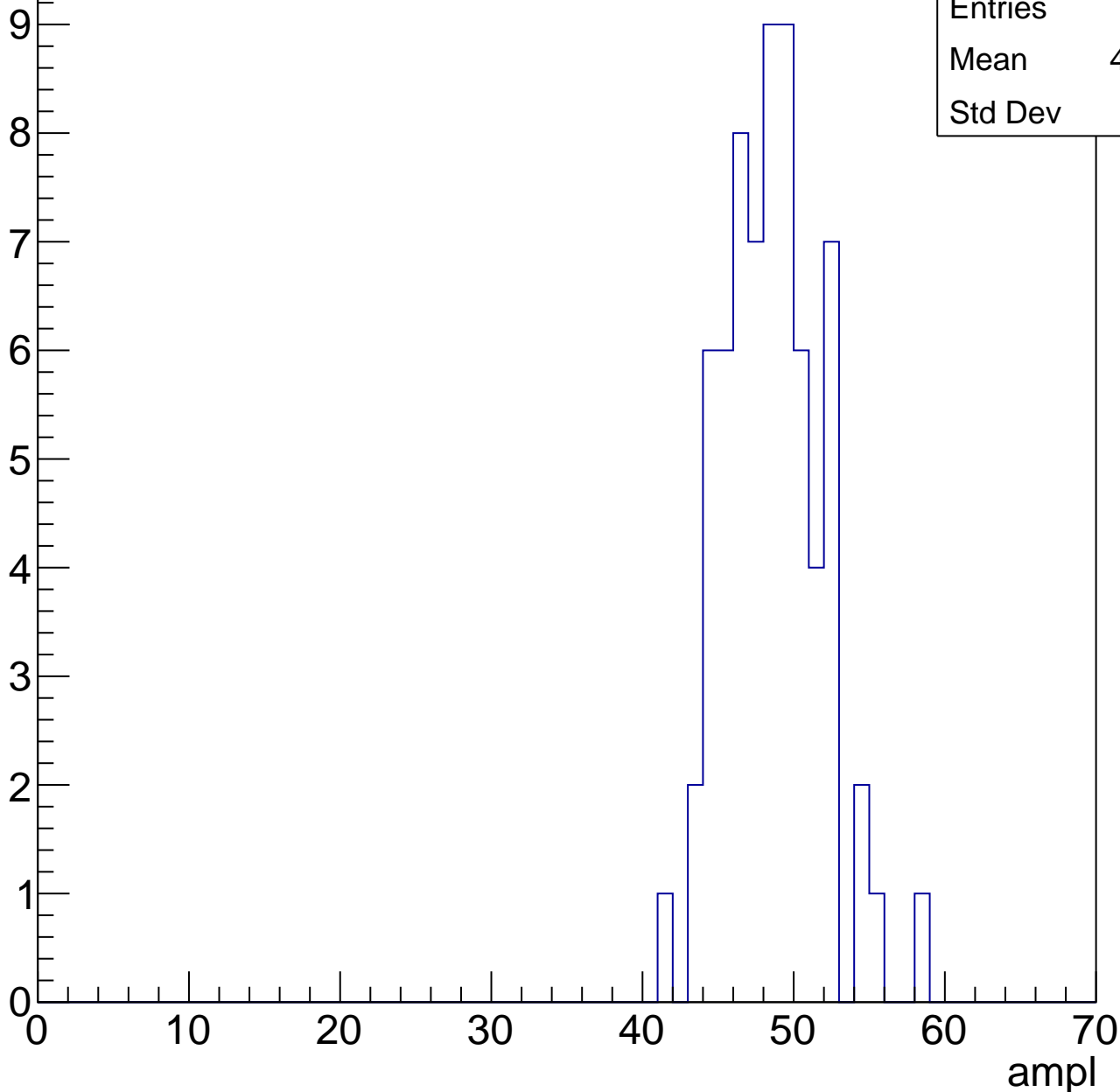


B1L003S, U21-ch15, adc3

calib_packv5_042523_0143.root, FC#13, port D2

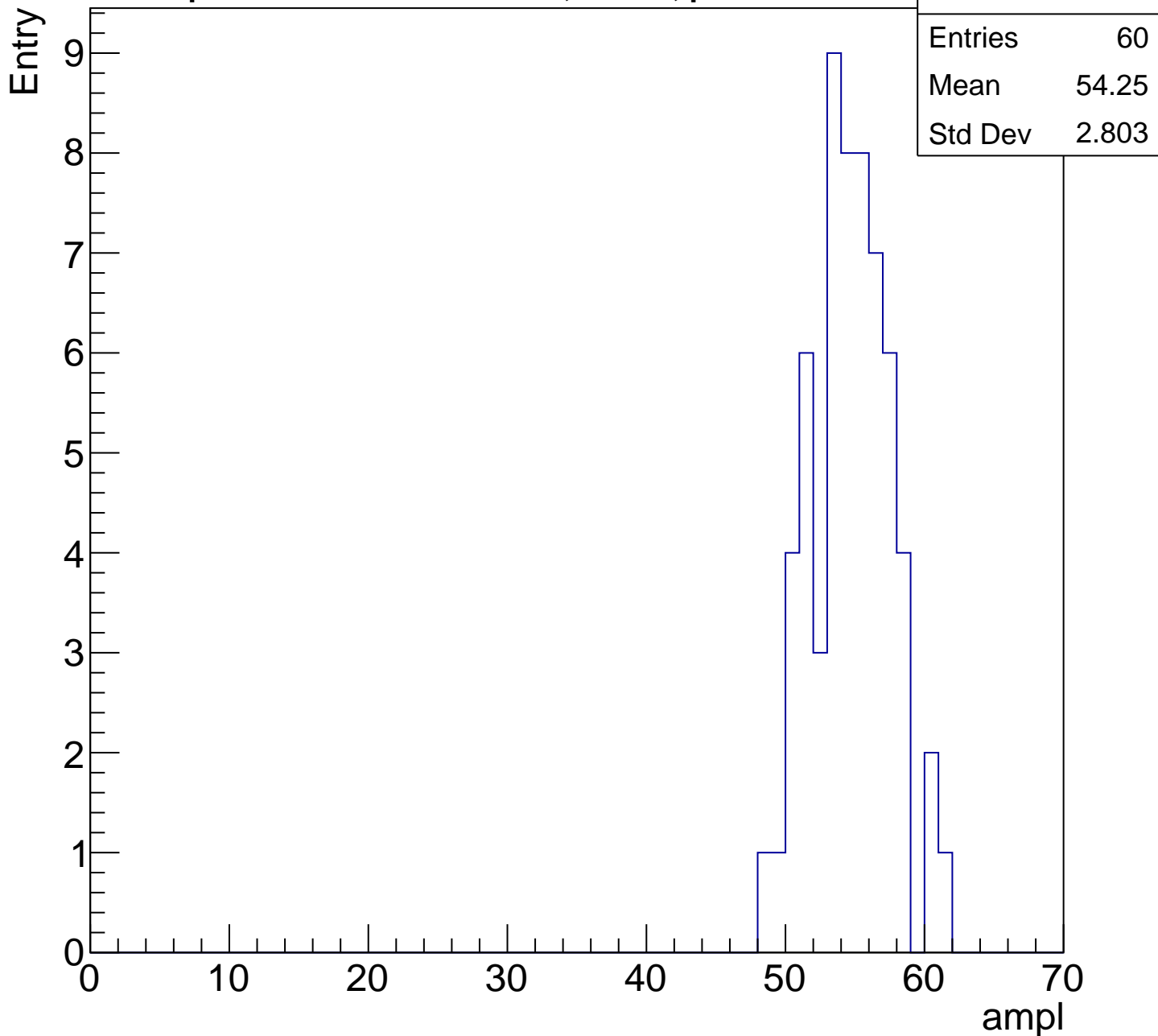
Entry

Entries	69
Mean	48.12
Std Dev	3.16



B1L003S, U21-ch15, adc4

calib_packv5_042523_0143.root, FC#13, port D2

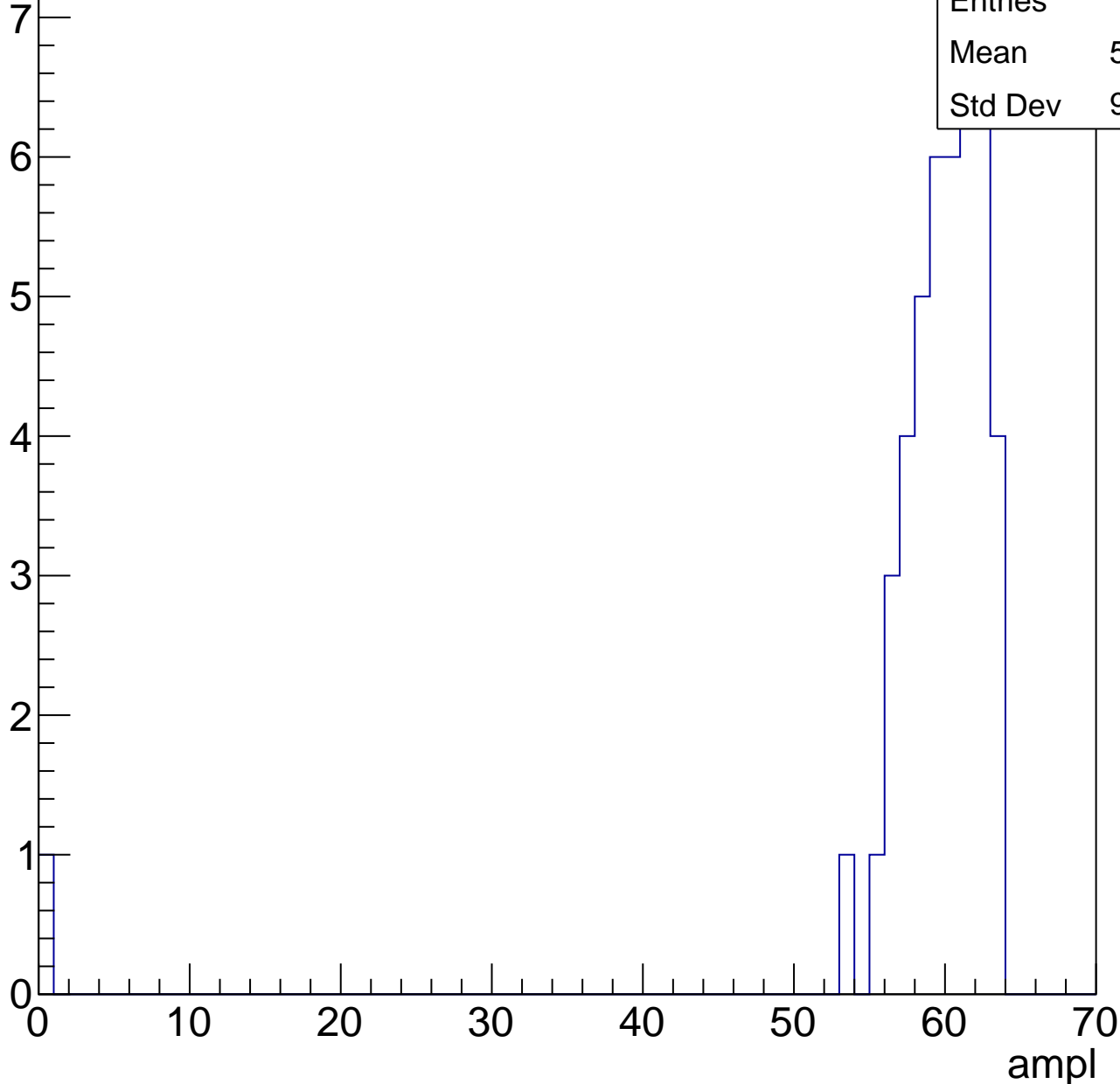


B1L003S, U21-ch15, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.24
Std Dev	9.088

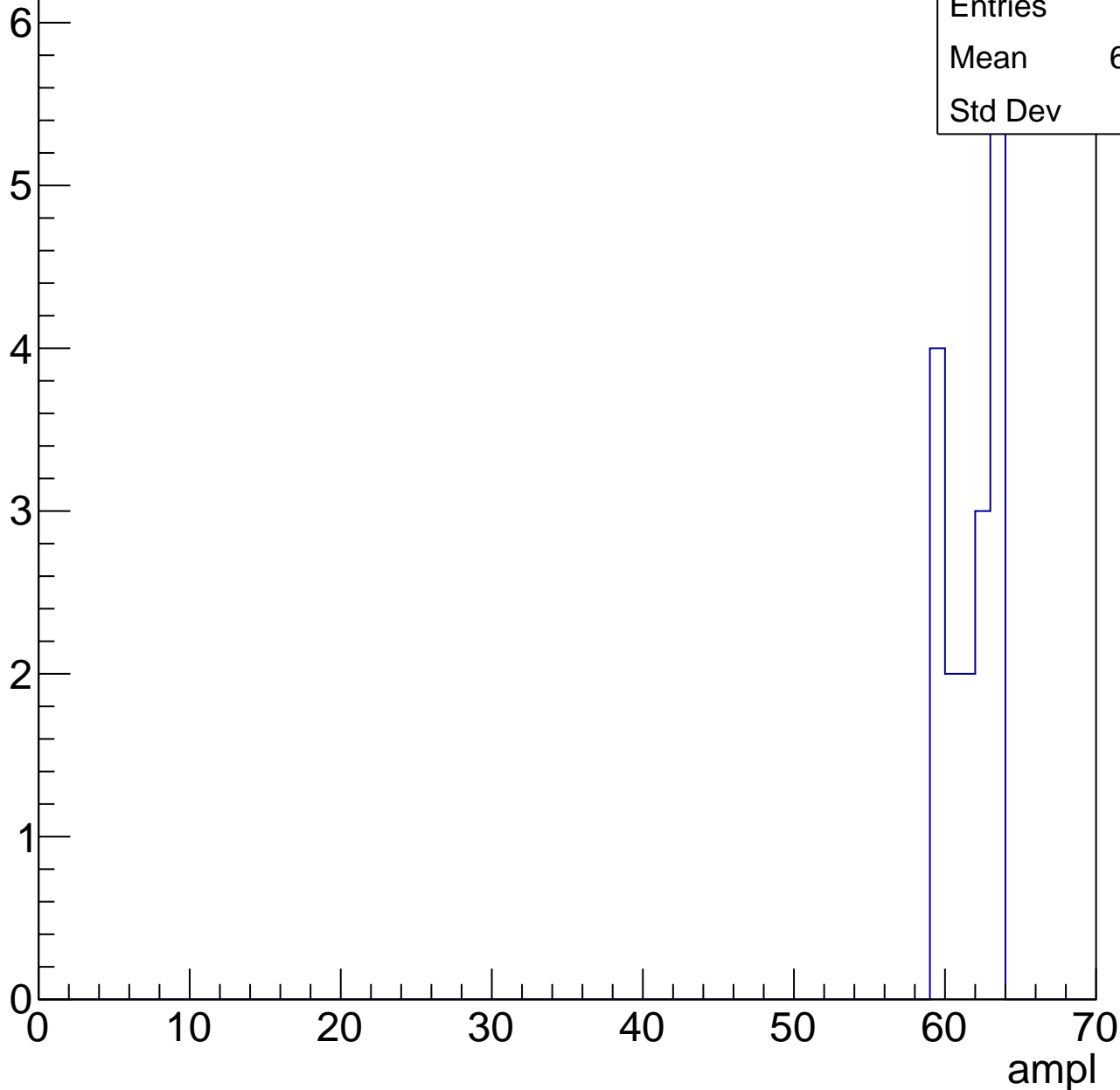


B1L003S, U21-ch15, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	61.29
Std Dev	1.6



B1L003S, U21-ch15, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch16, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	30.06
Std Dev	4.579

Gaus mean : 30.9751

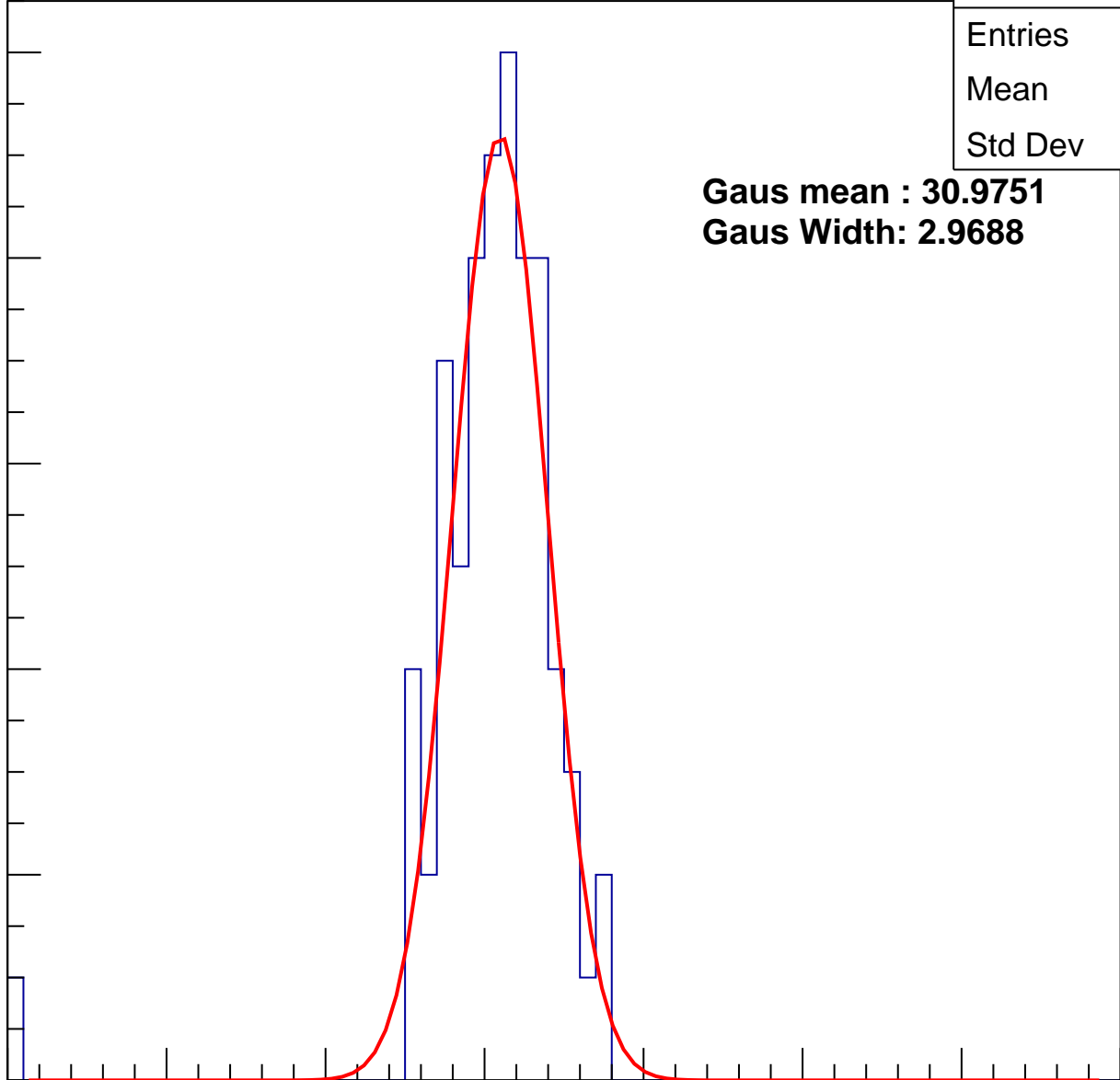
Gaus Width: 2.9688

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch16, adc1

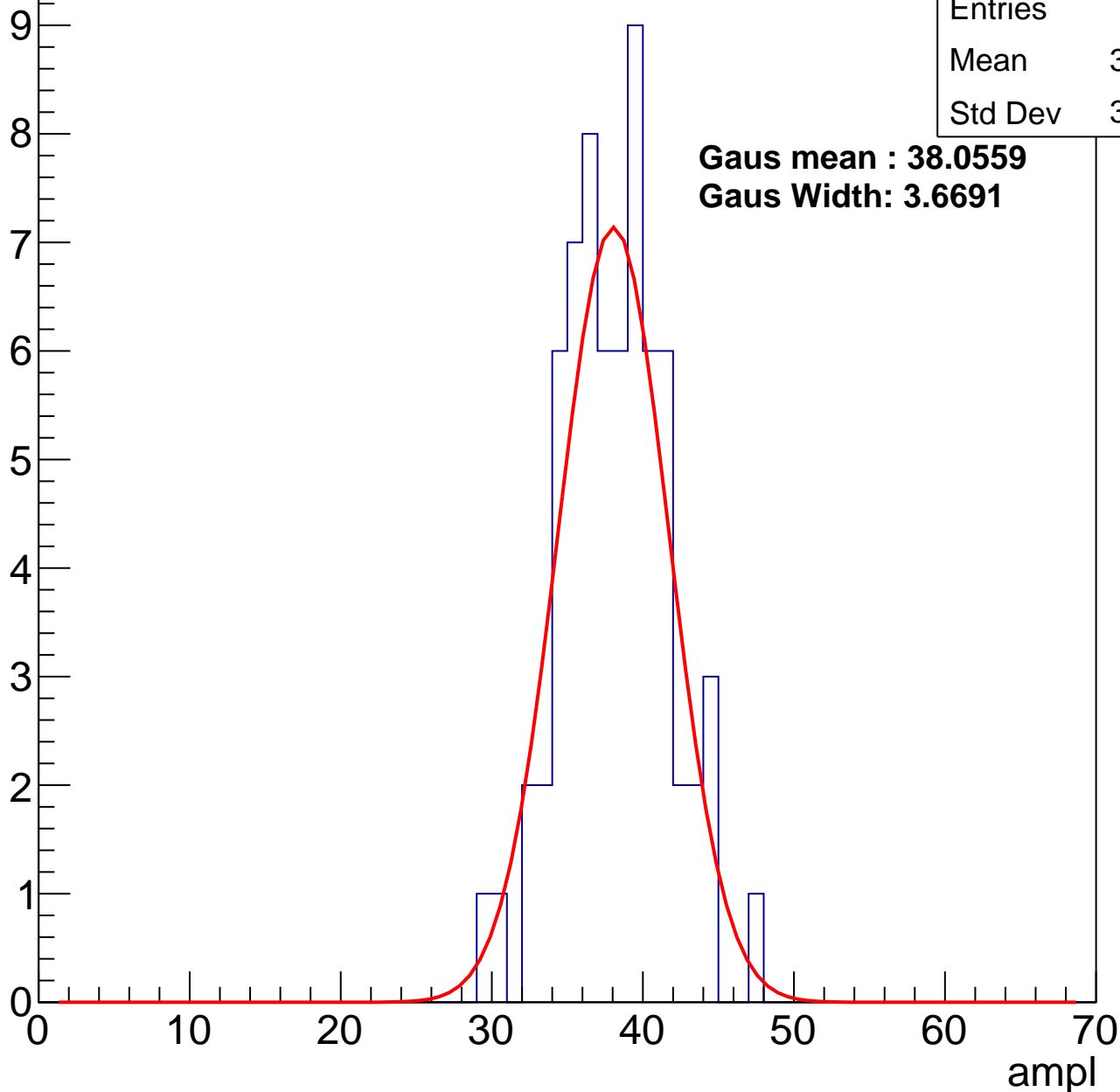
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.68
Std Dev	3.462

Gaus mean : 38.0559

Gaus Width: 3.6691



B1L003S, U21-ch16, adc2

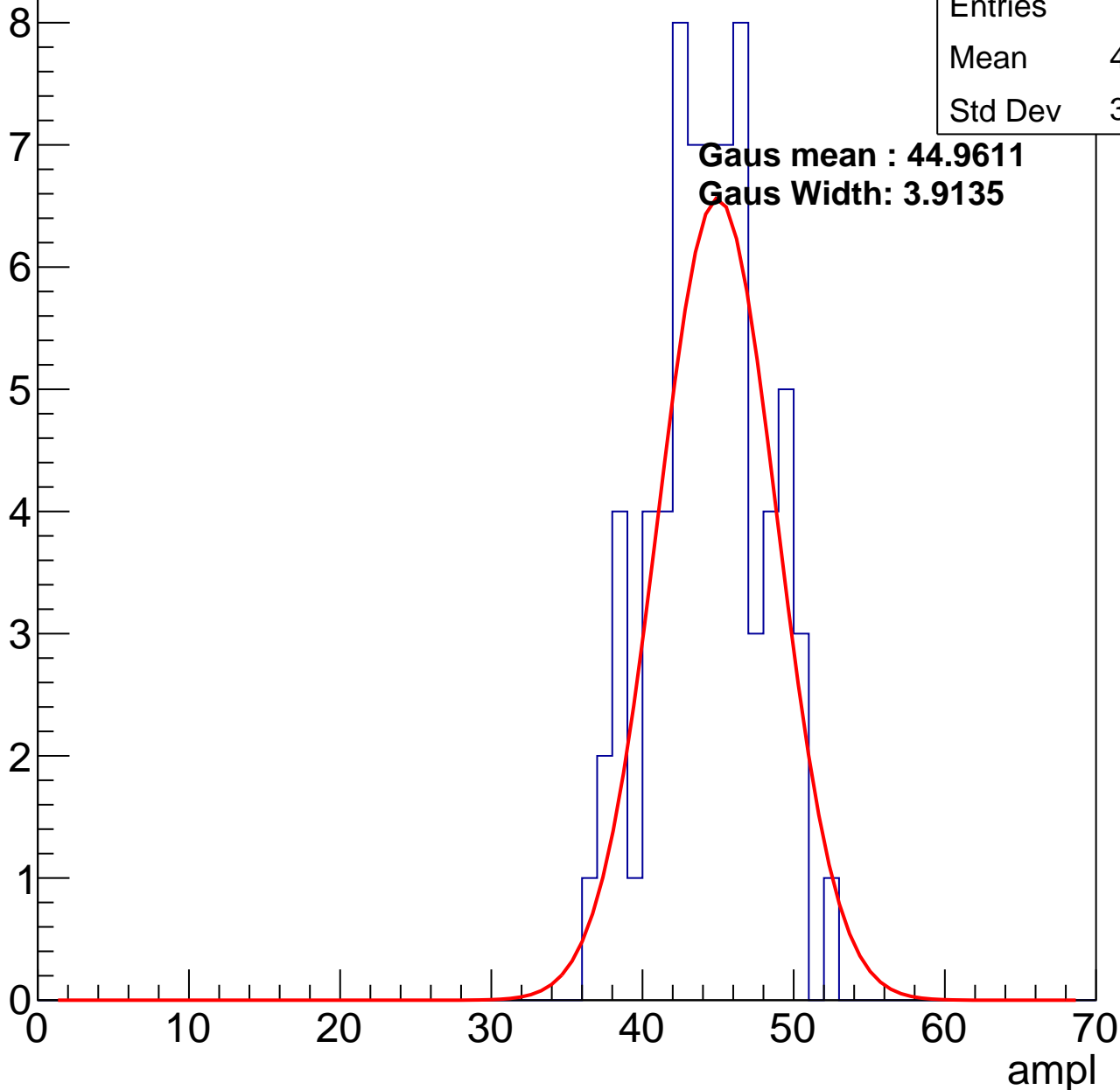
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	43.96
Std Dev	3.605

Gaus mean : 44.9611

Gaus Width: 3.9135

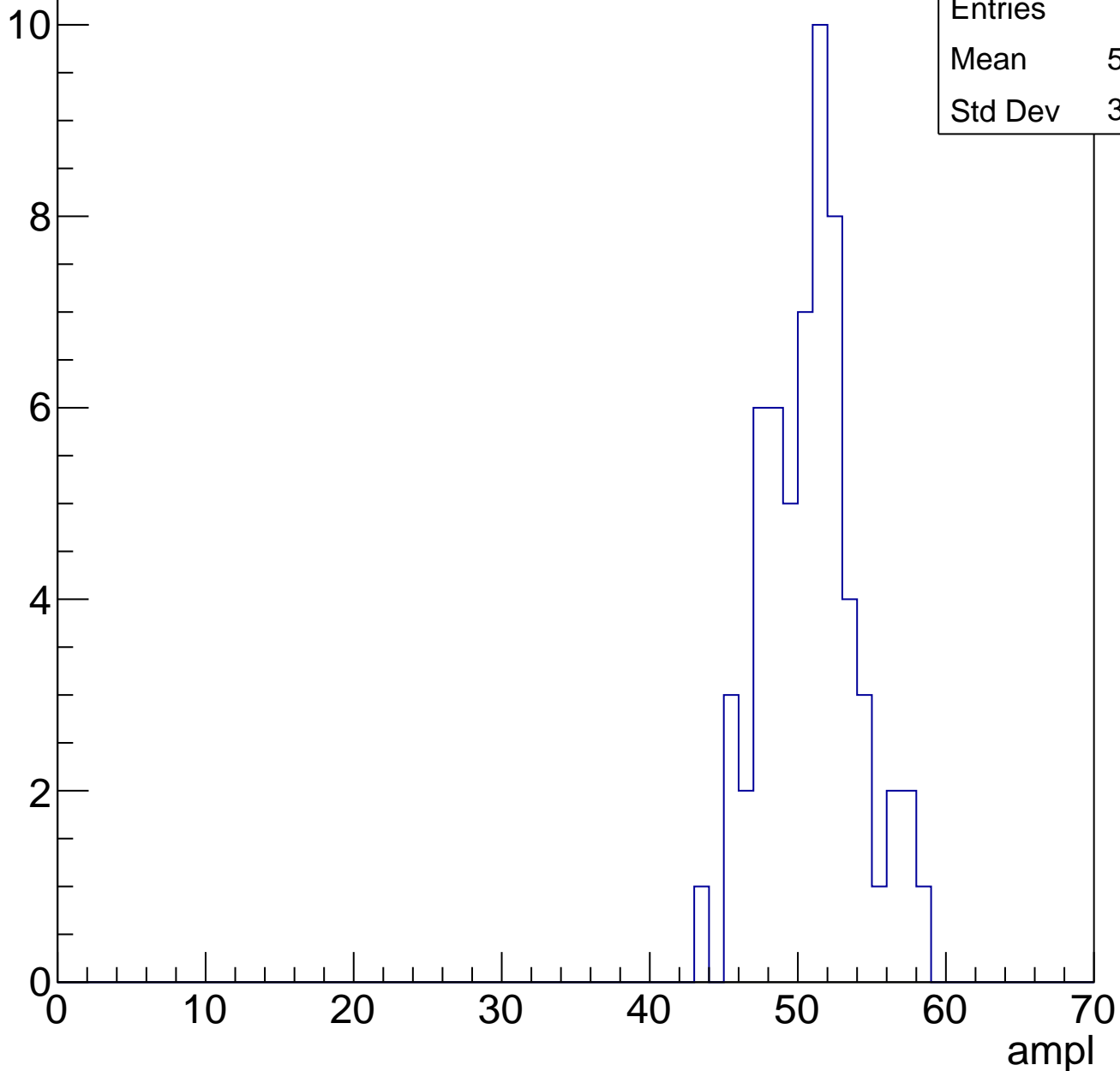


B1L003S, U21-ch16, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	50.39
Std Dev	3.174

Entry

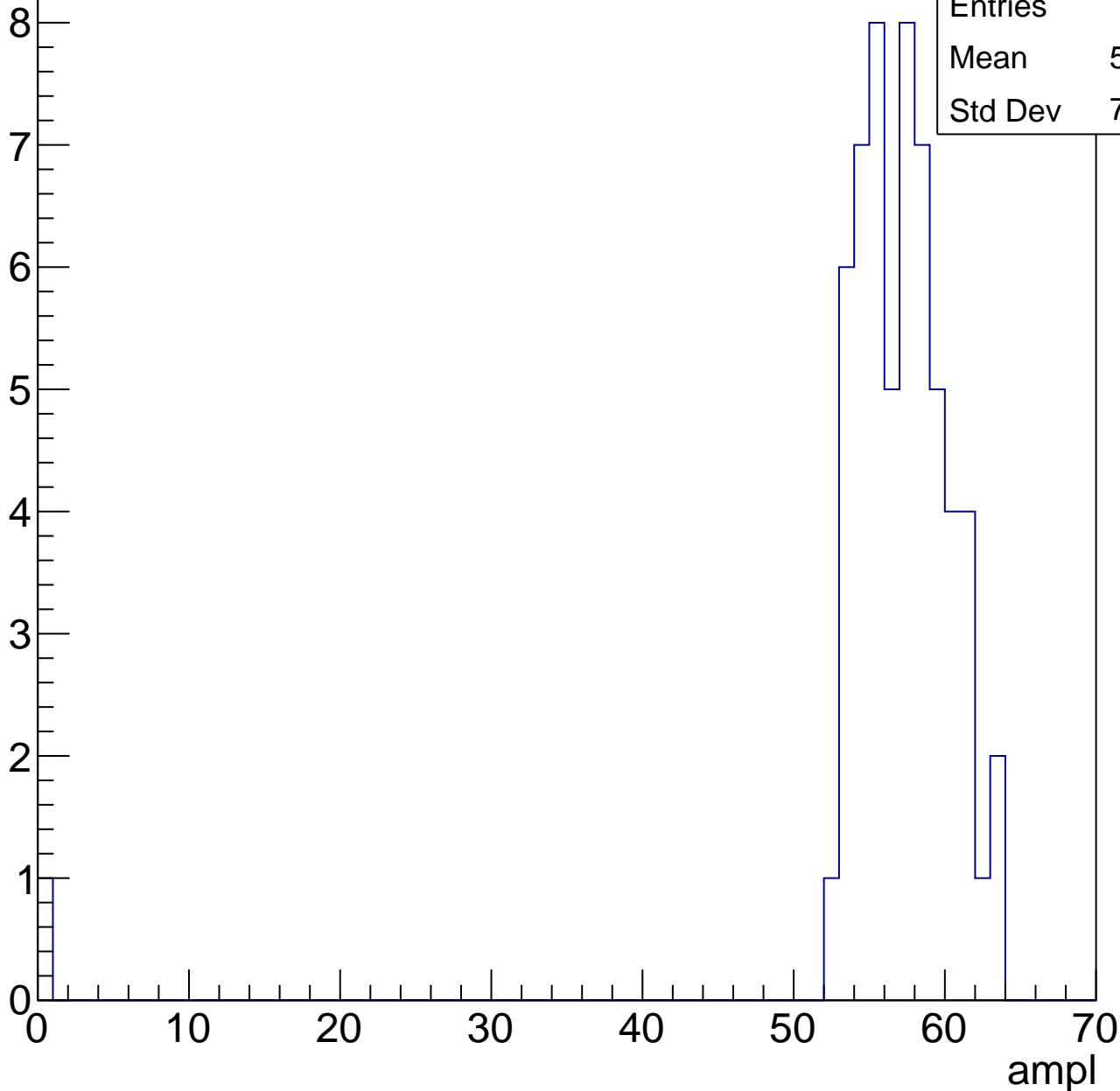


B1L003S, U21-ch16, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.88
Std Dev	7.833



B1L003S, U21-ch16, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries

36

Mean

60.39

Std Dev

2.215

ampl

0

10

20

30

40

50

60

70

B1L003S, U21-ch16, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch16, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	24
Std Dev	0

ampl

B1L003S, U21-ch17, adc0

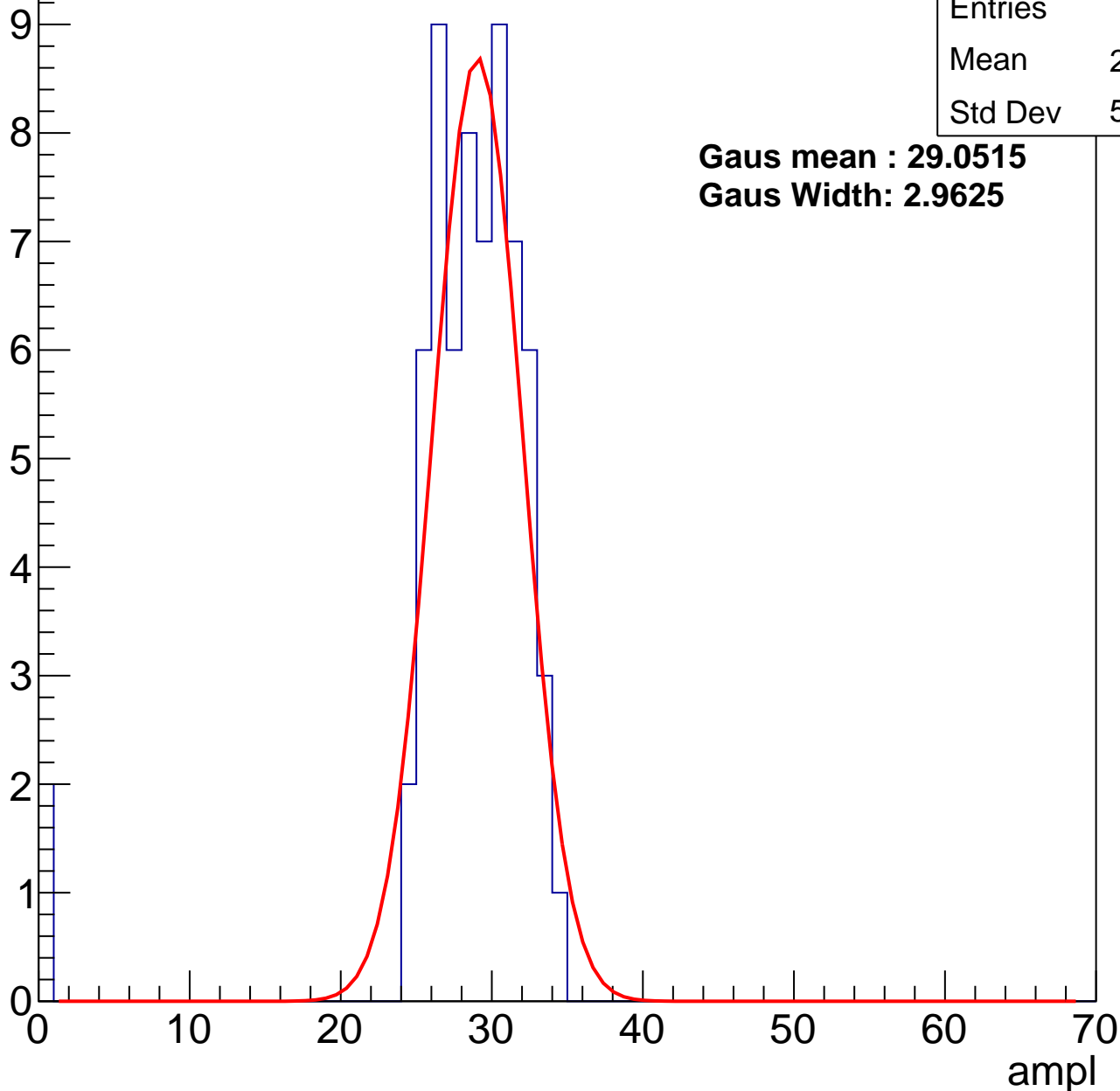
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.77
Std Dev	5.513

Gaus mean : 29.0515

Gaus Width: 2.9625



B1L003S, U21-ch17, adc1

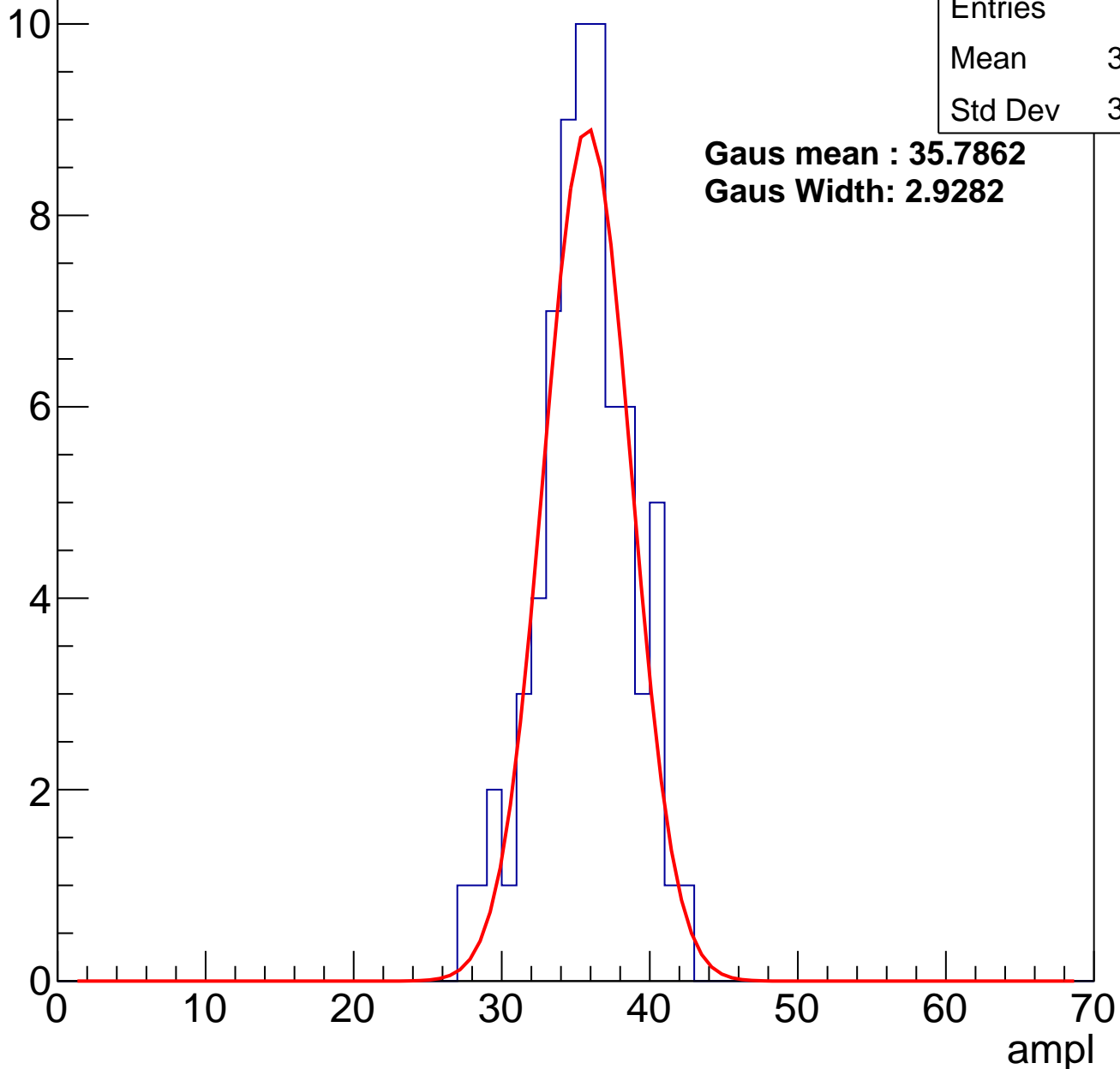
calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	35.16
Std Dev	3.106

Gaus mean : 35.7862

Gaus Width: 2.9282

Entry



B1L003S, U21-ch17, adc2

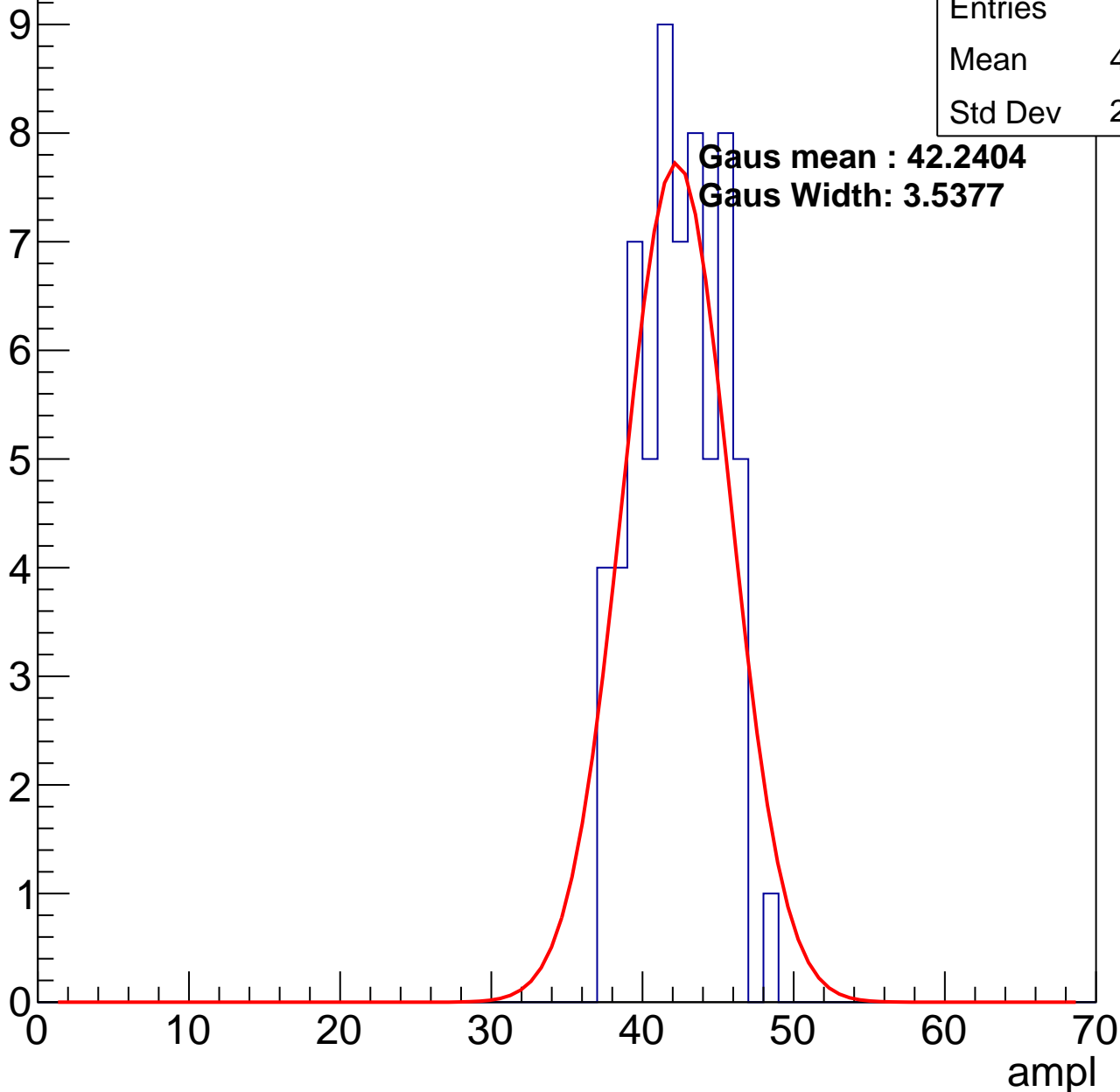
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.87
Std Dev	2.734

Gaus mean : 42.2404

Gaus Width: 3.5377



B1L003S, U21-ch17, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	48.46
Std Dev	2.996

Entry

10

8

6

4

2

0

0

10

20

30

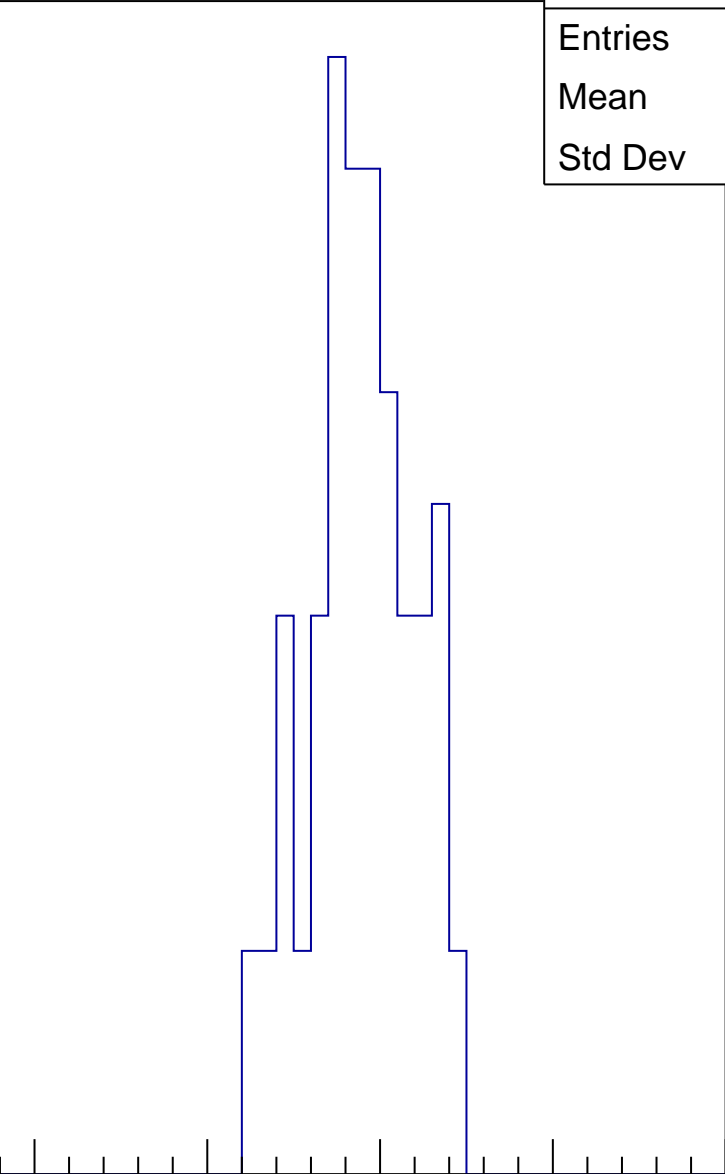
40

50

60

70

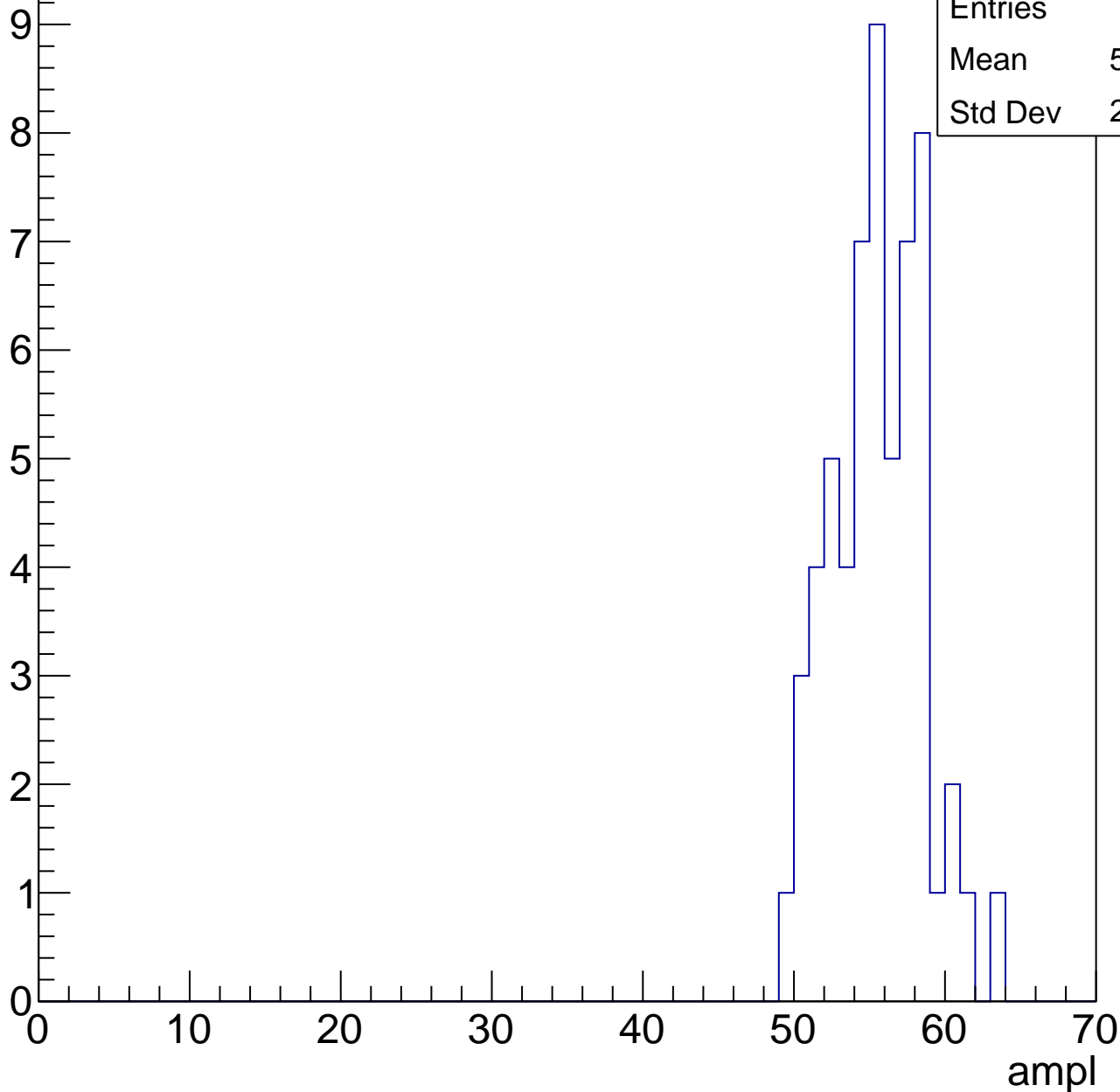
ampl



B1L003S, U21-ch17, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch17, adc5

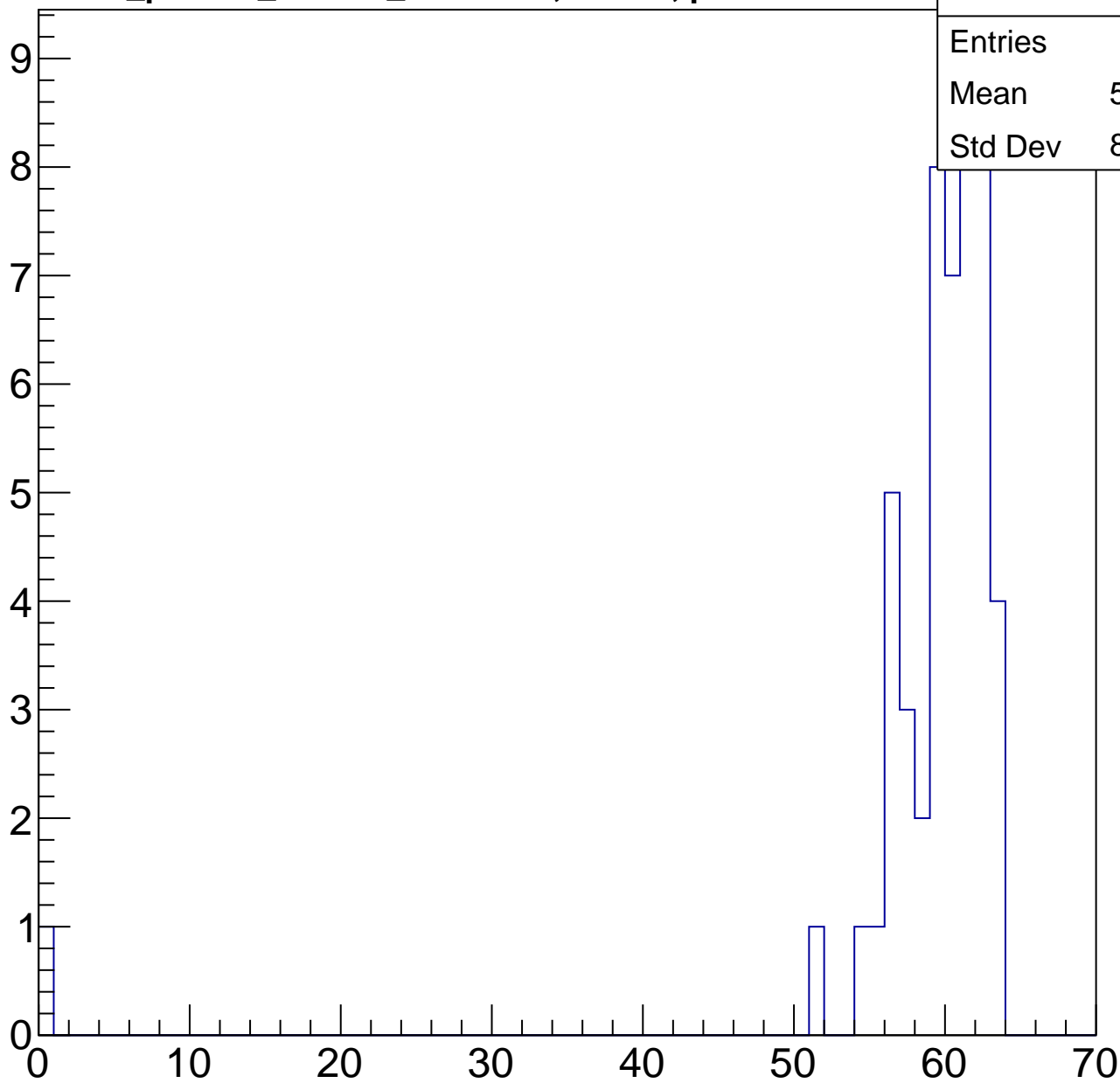
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	50
Mean	58.32
Std Dev	8.719

ampl

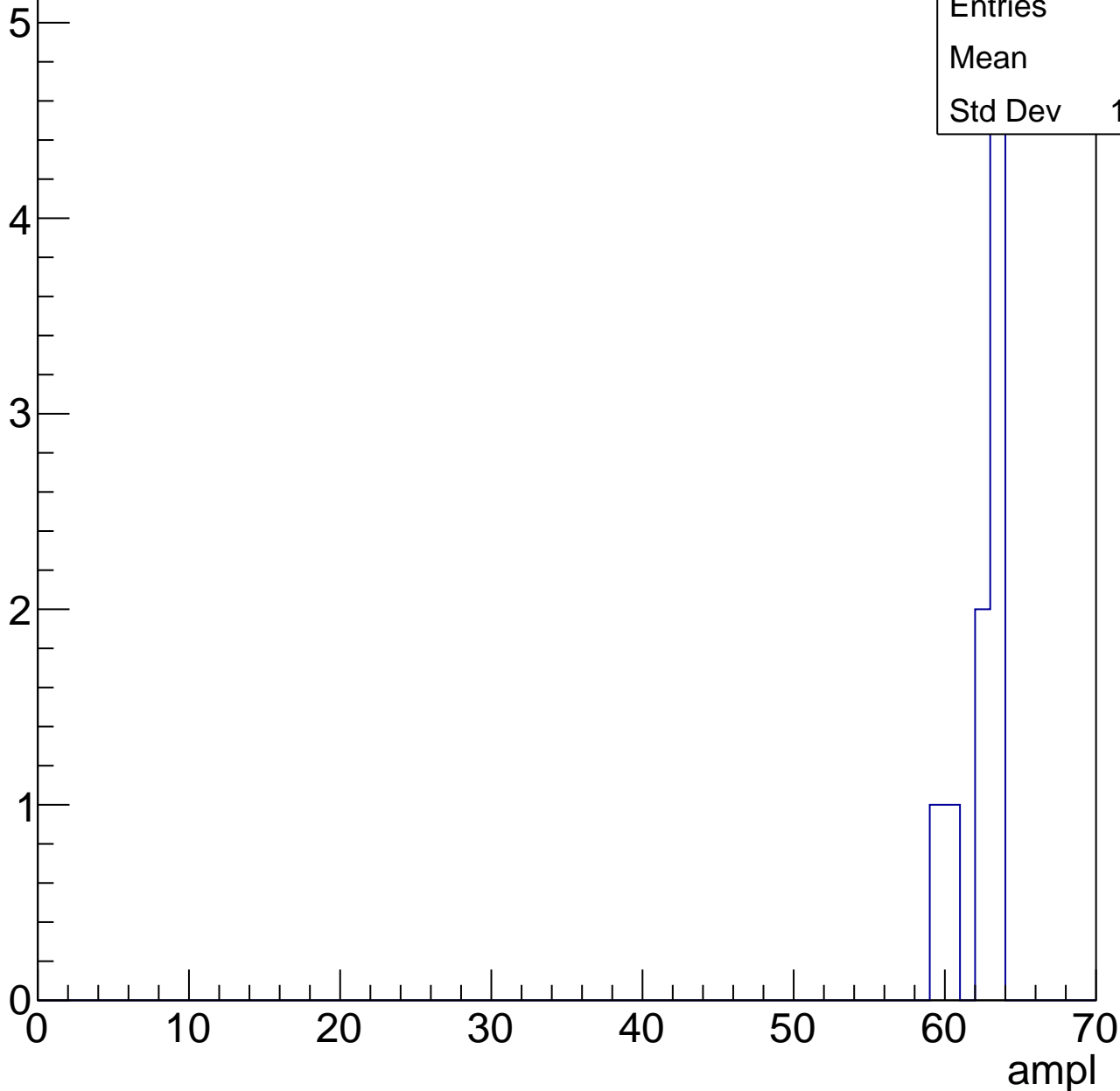


B1L003S, U21-ch17, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	9
Mean	62
Std Dev	1.414



B1L003S, U21-ch17, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch18, adc0

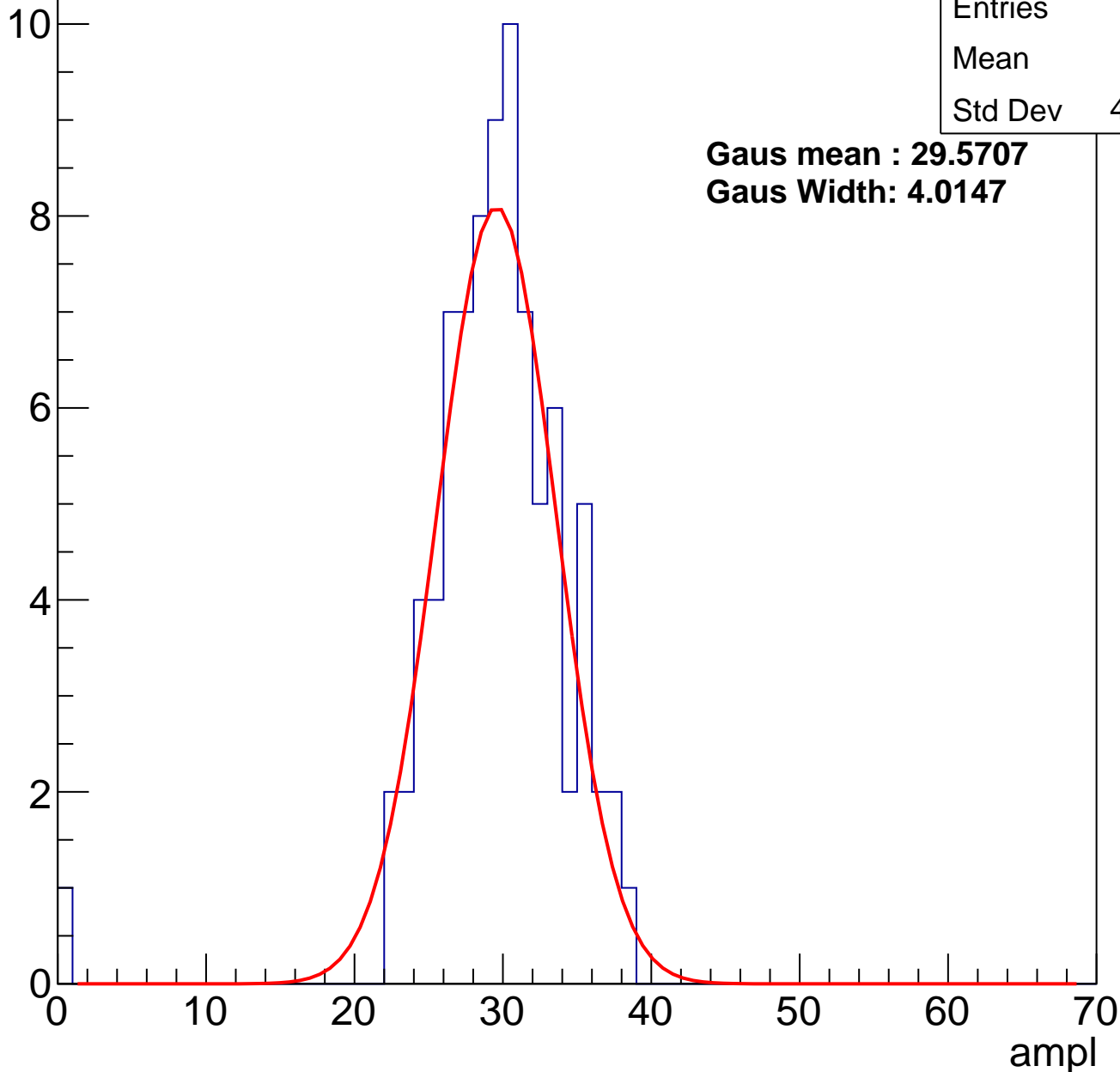
calib_packv5_042523_0143.root, FC#13, port D2

Entries	84
Mean	29.1
Std Dev	4.866

Gaus mean : 29.5707

Gaus Width: 4.0147

Entry



B1L003S, U21-ch18, adc1

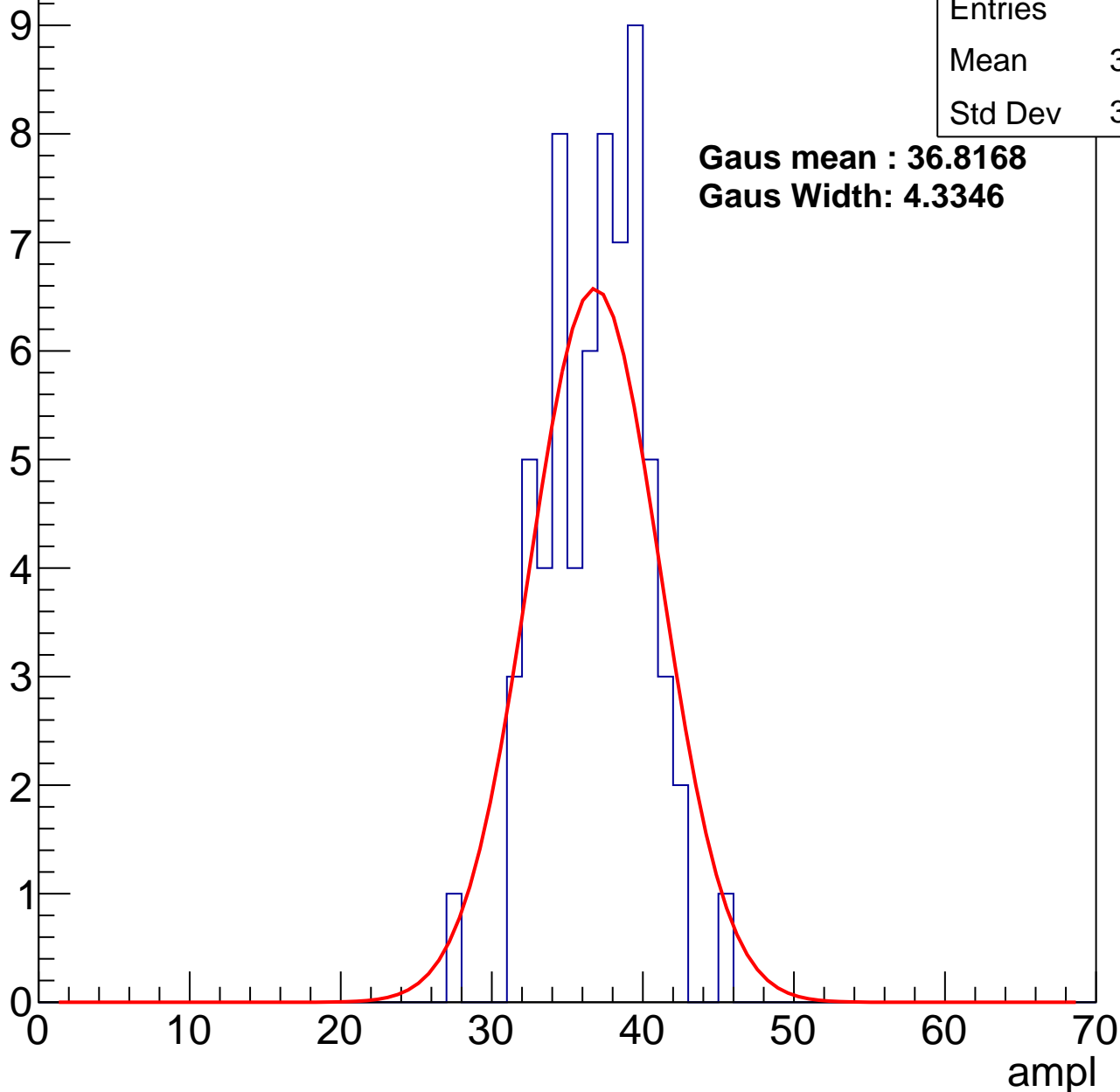
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.44
Std Dev	3.303

Gaus mean : 36.8168

Gaus Width: 4.3346



B1L003S, U21-ch18, adc2

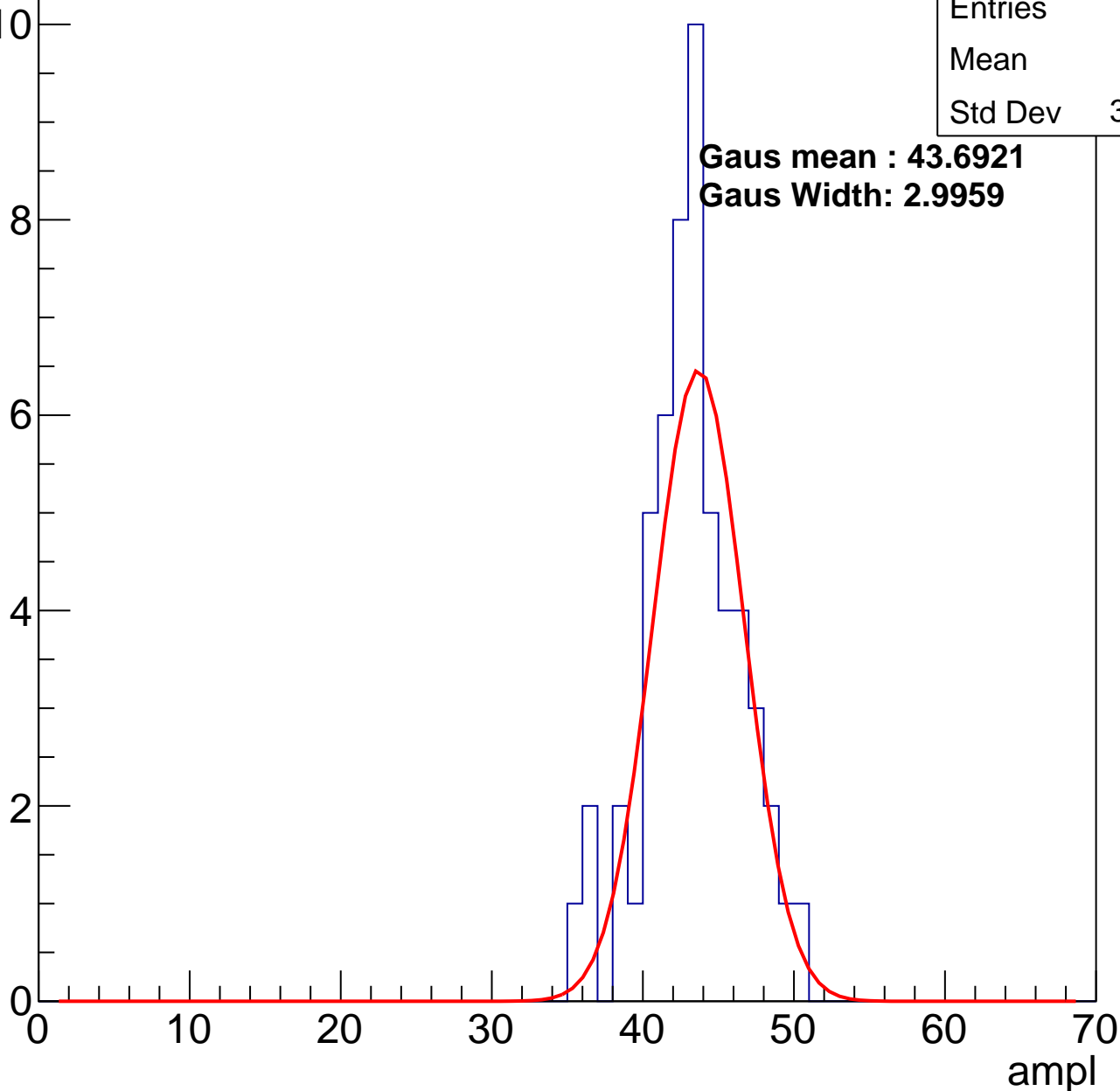
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.8
Std Dev	3.142

Gaus mean : 43.6921

Gaus Width: 2.9959

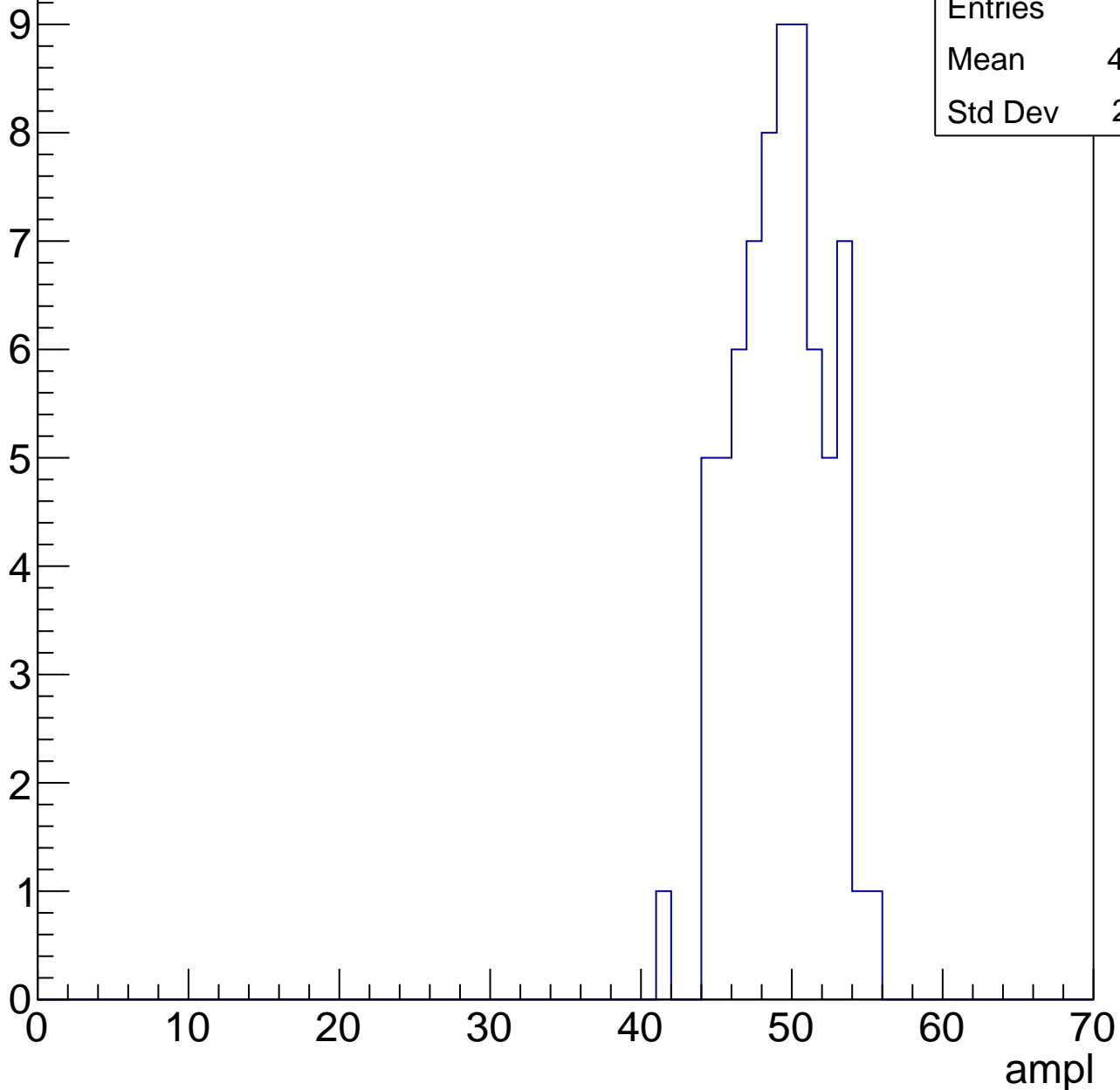


B1L003S, U21-ch18, adc3

calib_packv5_042523_0143.root, FC#13, port D2

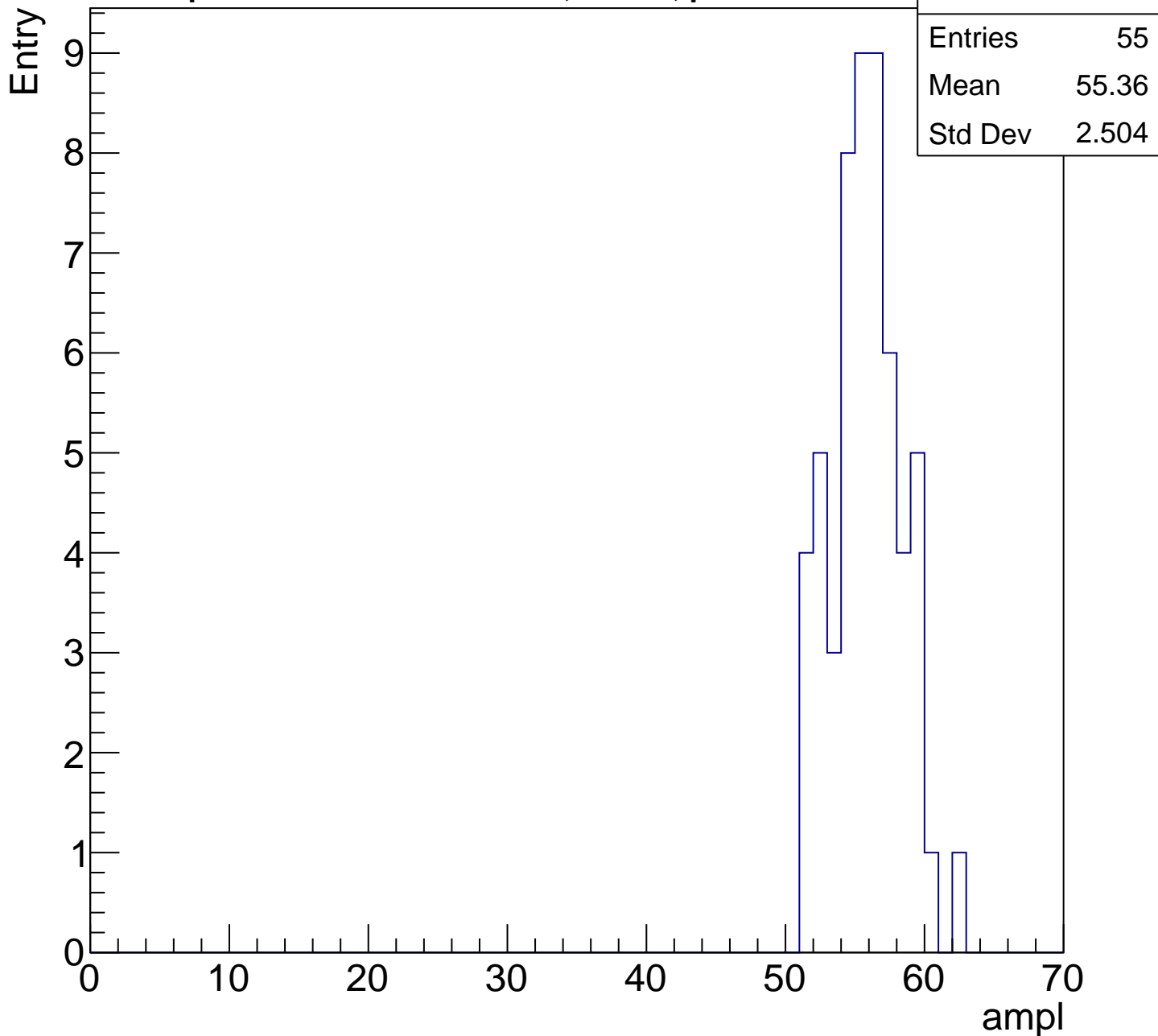
Entry

Entries	70
Mean	48.74
Std Dev	2.941



B1L003S, U21-ch18, adc4

calib_packv5_042523_0143.root, FC#13, port D2

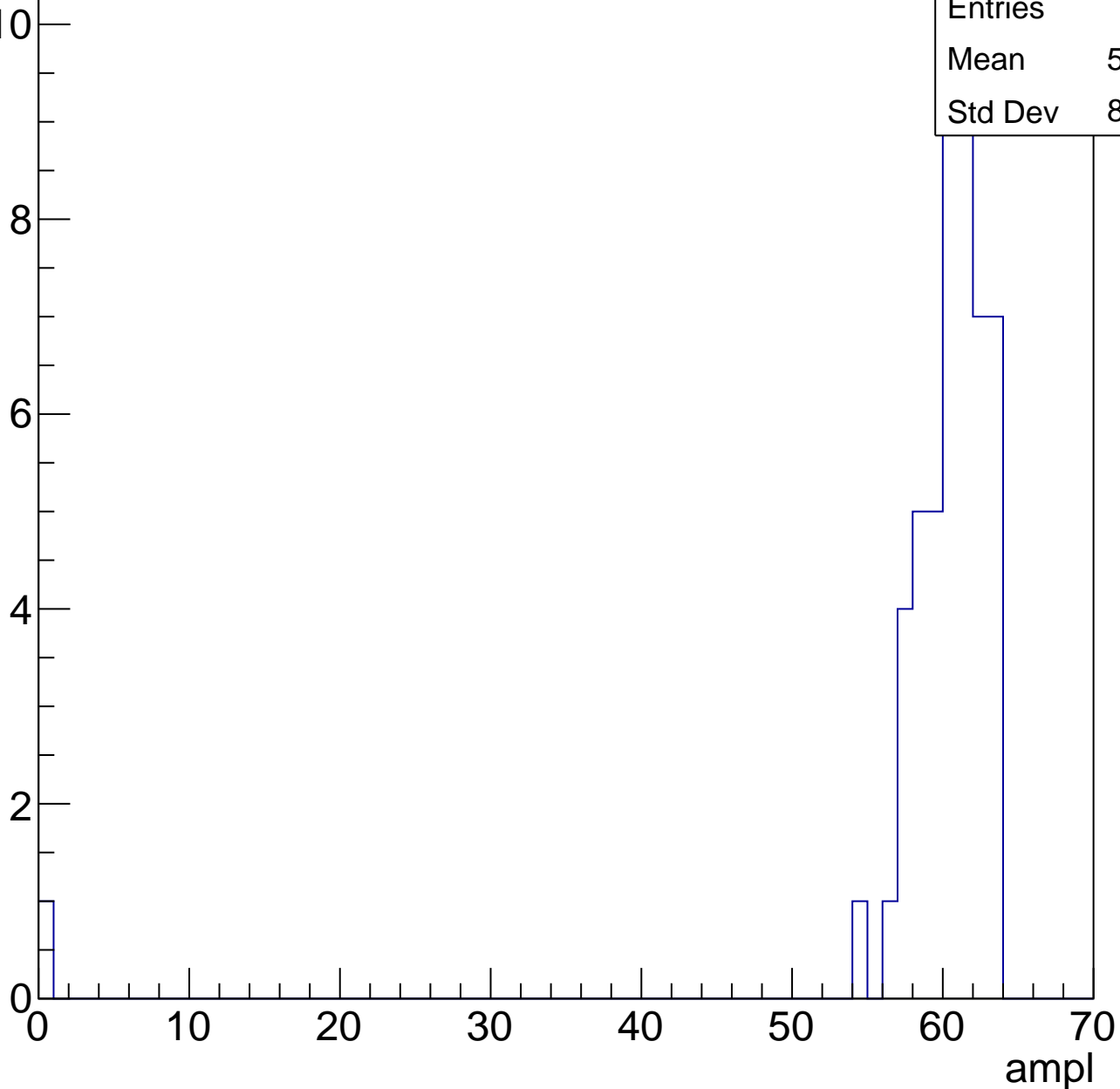


B1L003S, U21-ch18, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

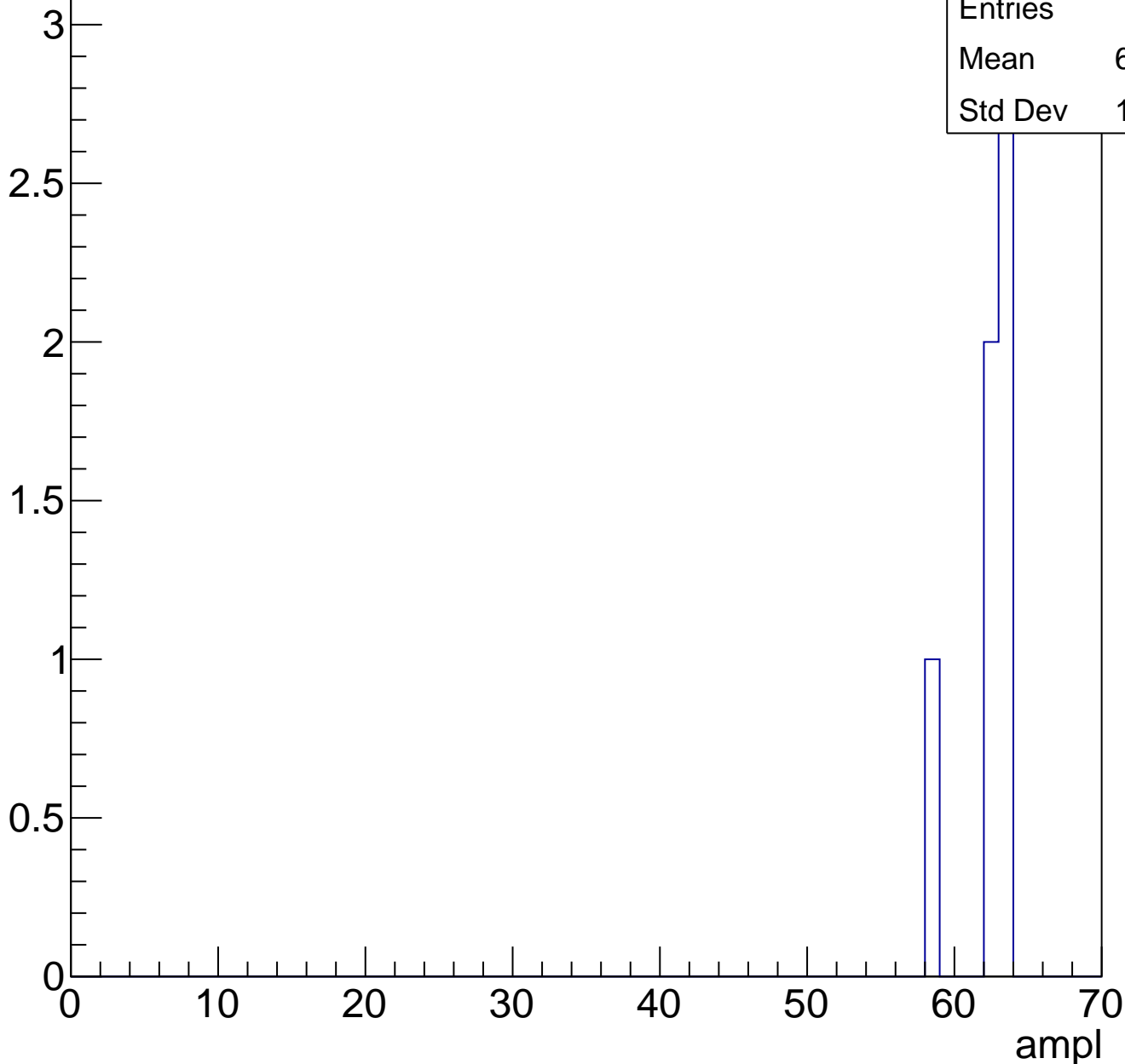
Entries	50
Mean	58.96
Std Dev	8.672



B1L003S, U21-ch18, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch18, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch19, adc0

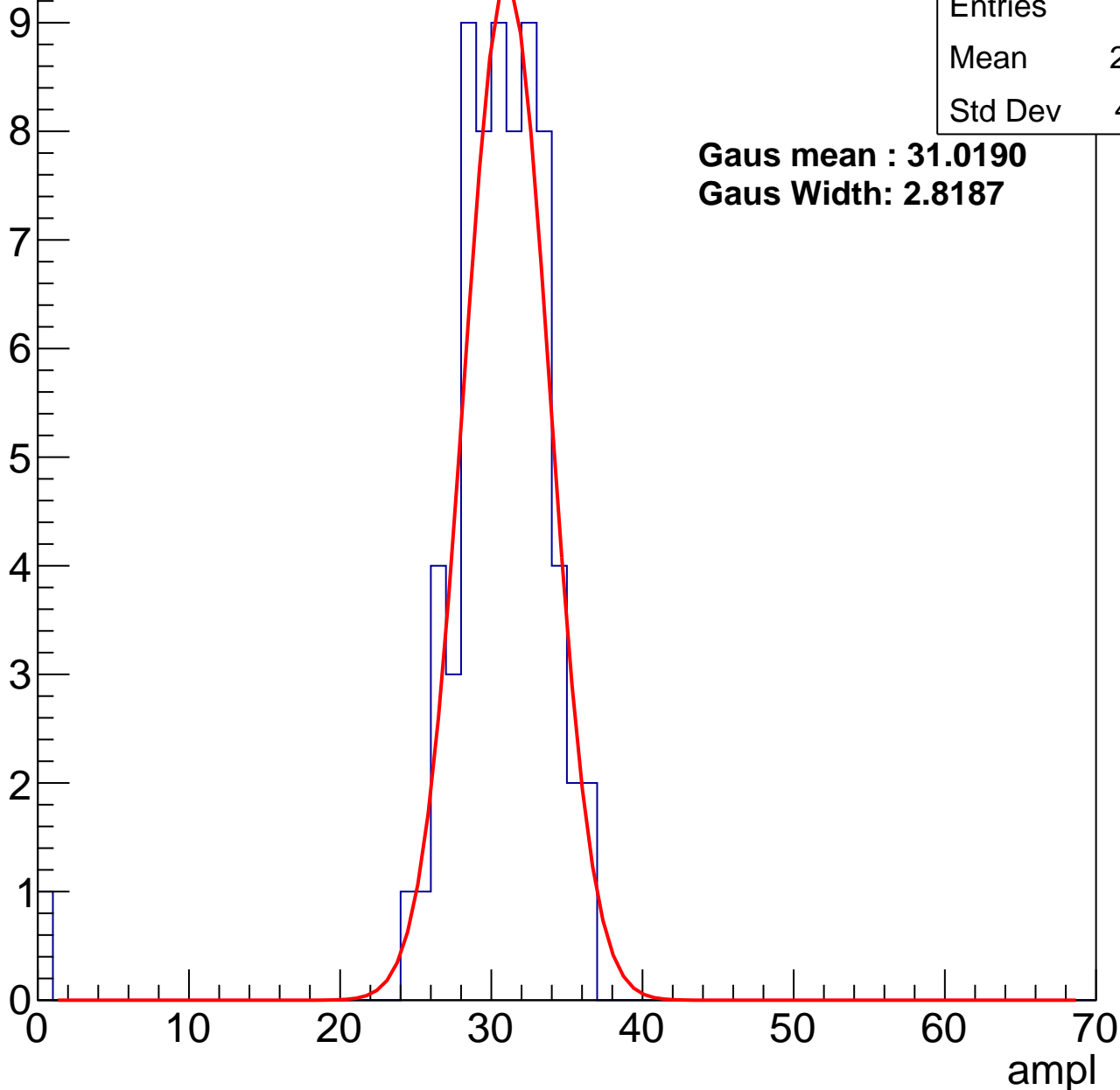
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	29.94
Std Dev	4.501

Gaus mean : 31.0190

Gaus Width: 2.8187



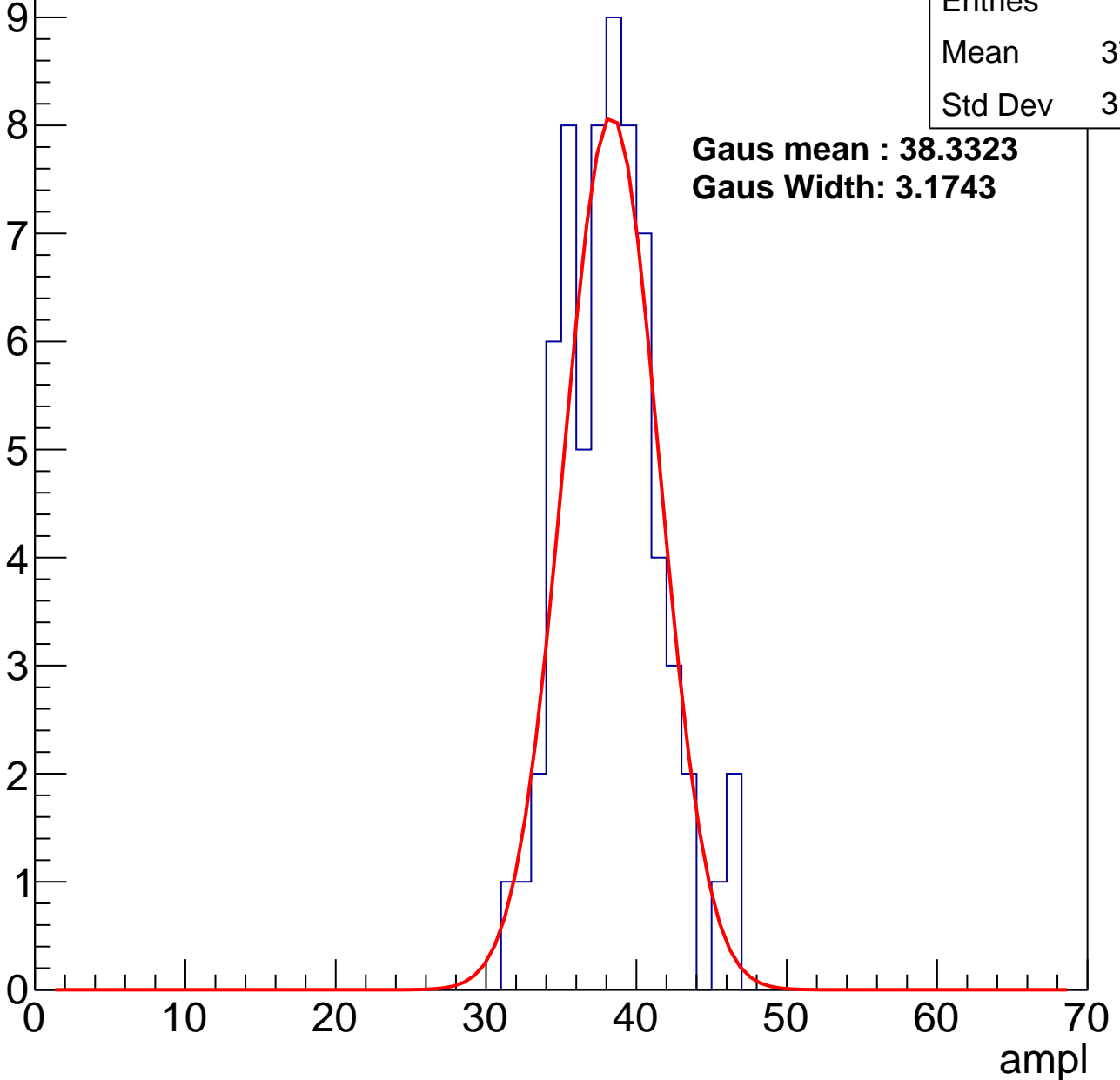
B1L003S, U21-ch19, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	37.85
Std Dev	3.178

Gaus mean : 38.3323
Gaus Width: 3.1743



B1L003S, U21-ch19, adc2

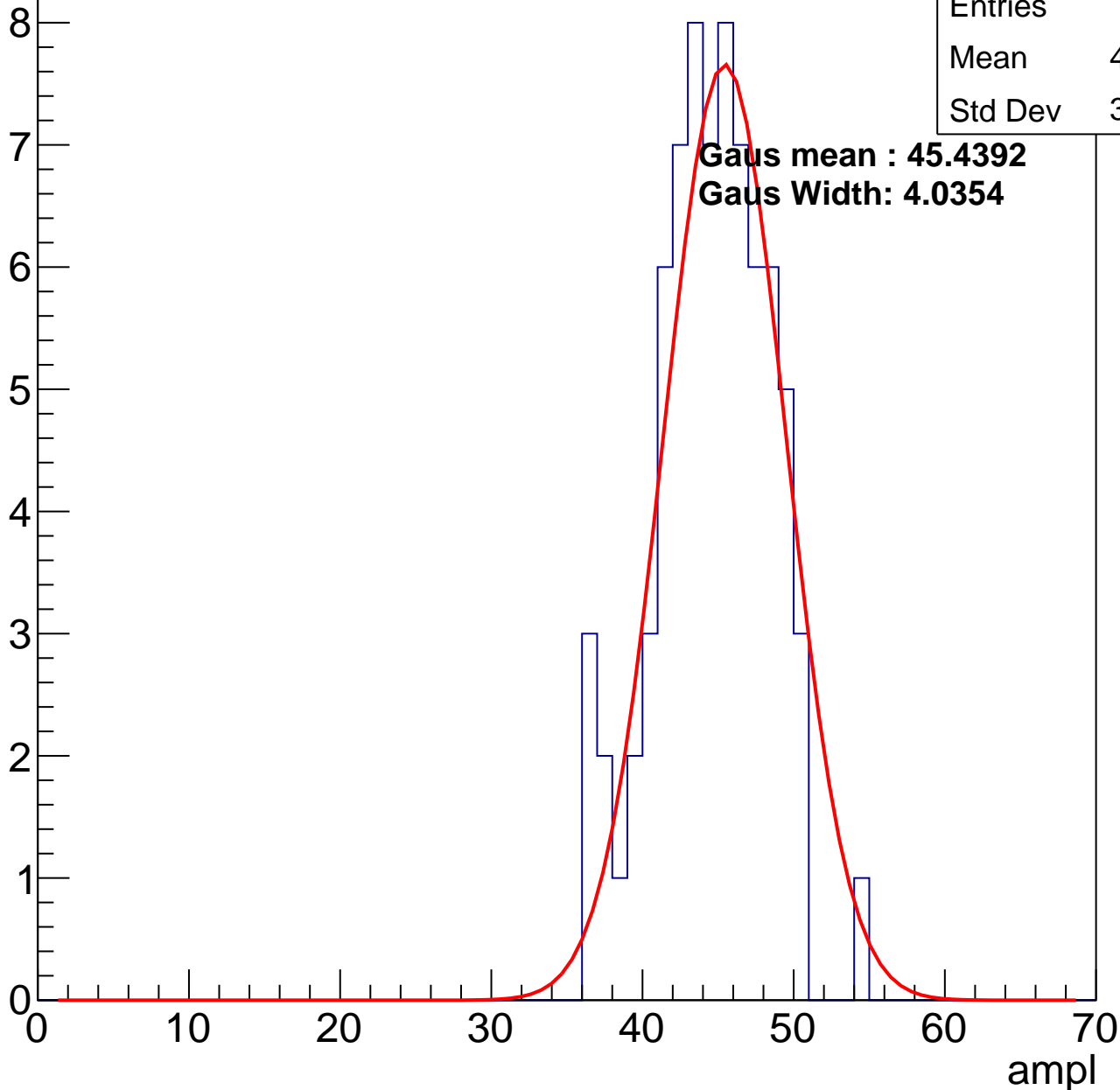
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	44.15
Std Dev	3.708

Gaus mean : 45.4392

Gaus Width: 4.0354



B1L003S, U21-ch19, adc3

calib_packv5_042523_0143.root, FC#13, port D2

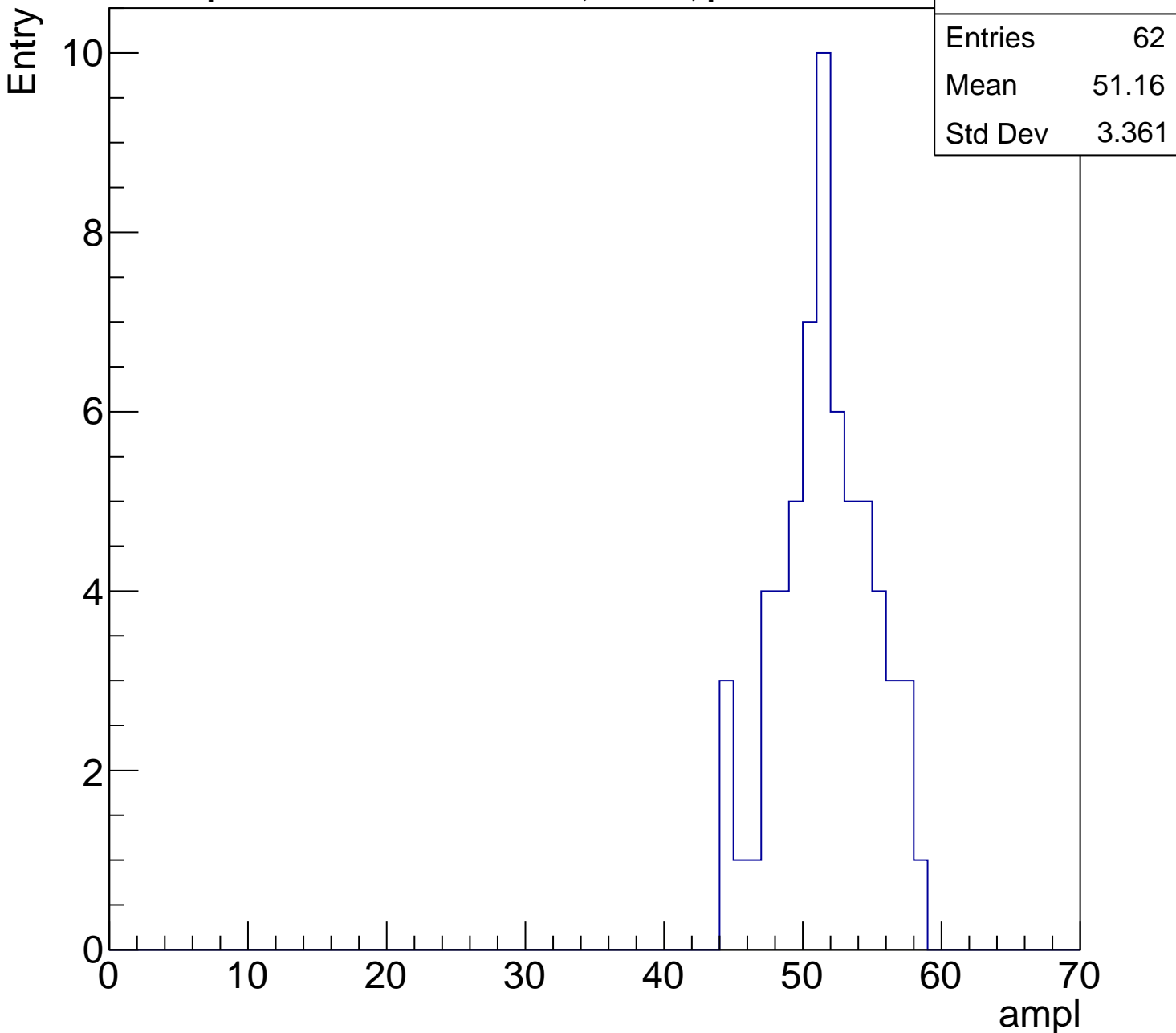
Entries	62
Mean	51.16
Std Dev	3.361

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

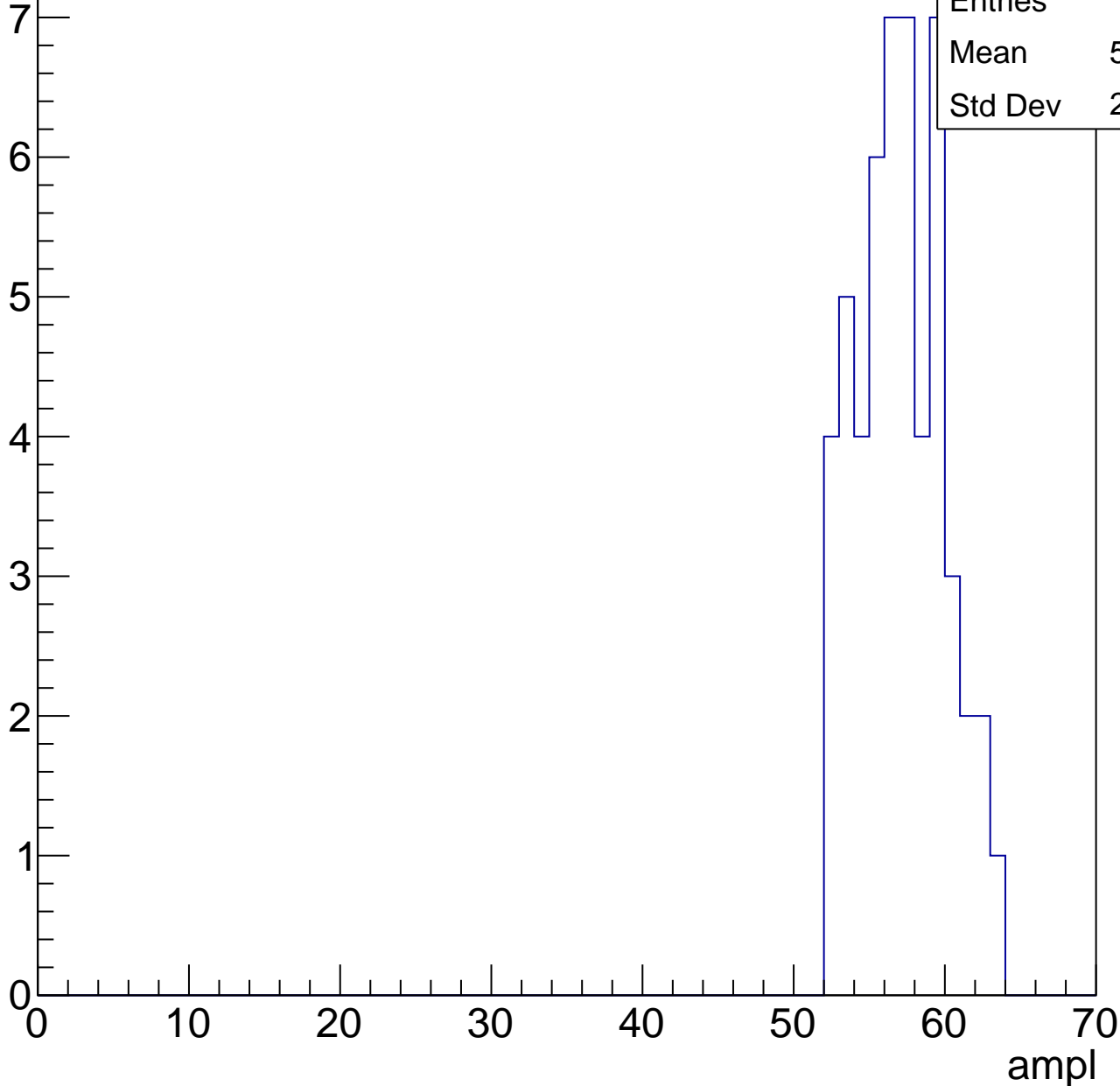


B1L003S, U21-ch19, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	56.62
Std Dev	2.829

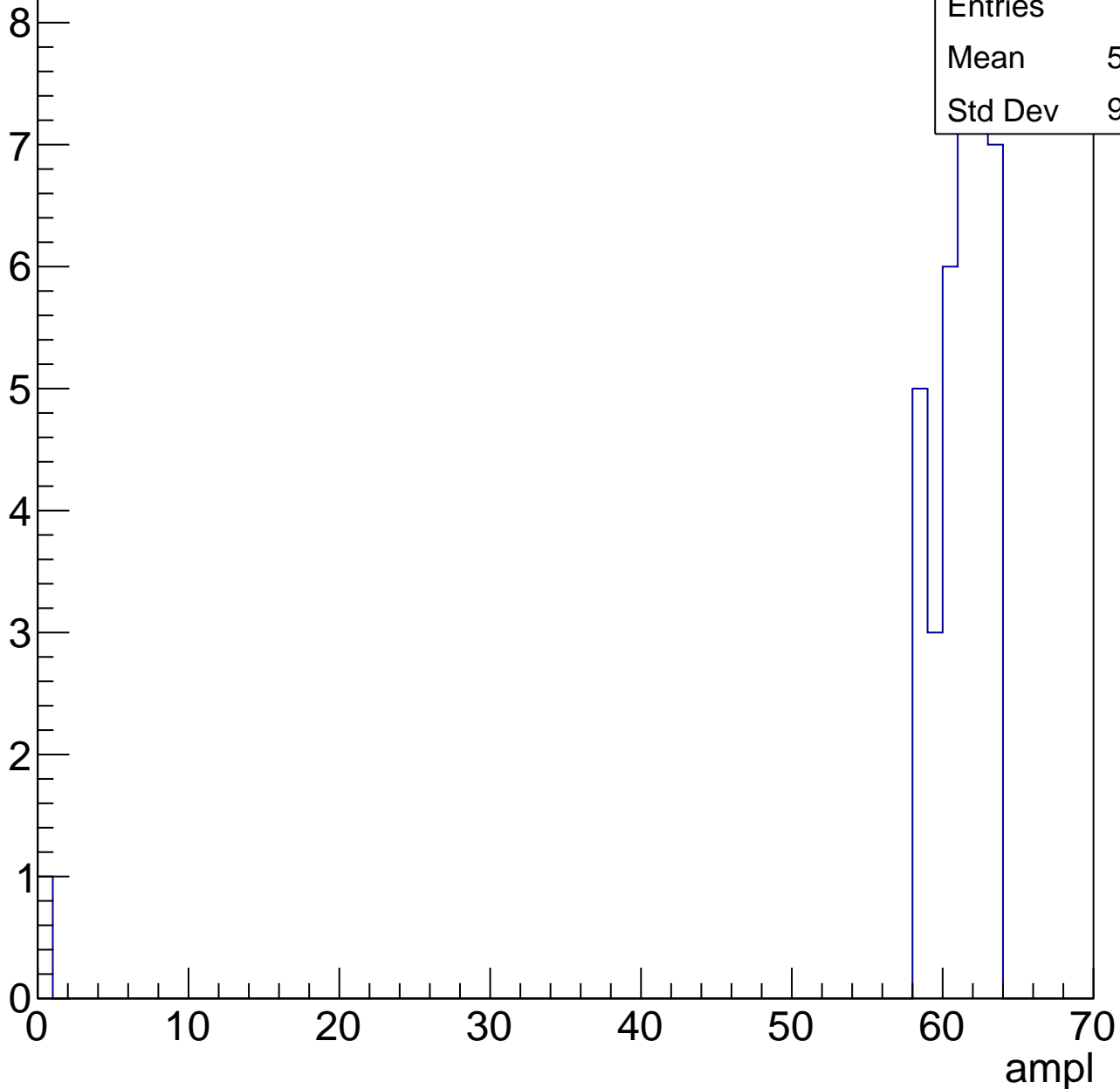


B1L003S, U21-ch19, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59.26
Std Dev	9.875



B1L003S, U21-ch19, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	62
Std Dev	1.414

B1L003S, U21-ch19, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch20, adc0

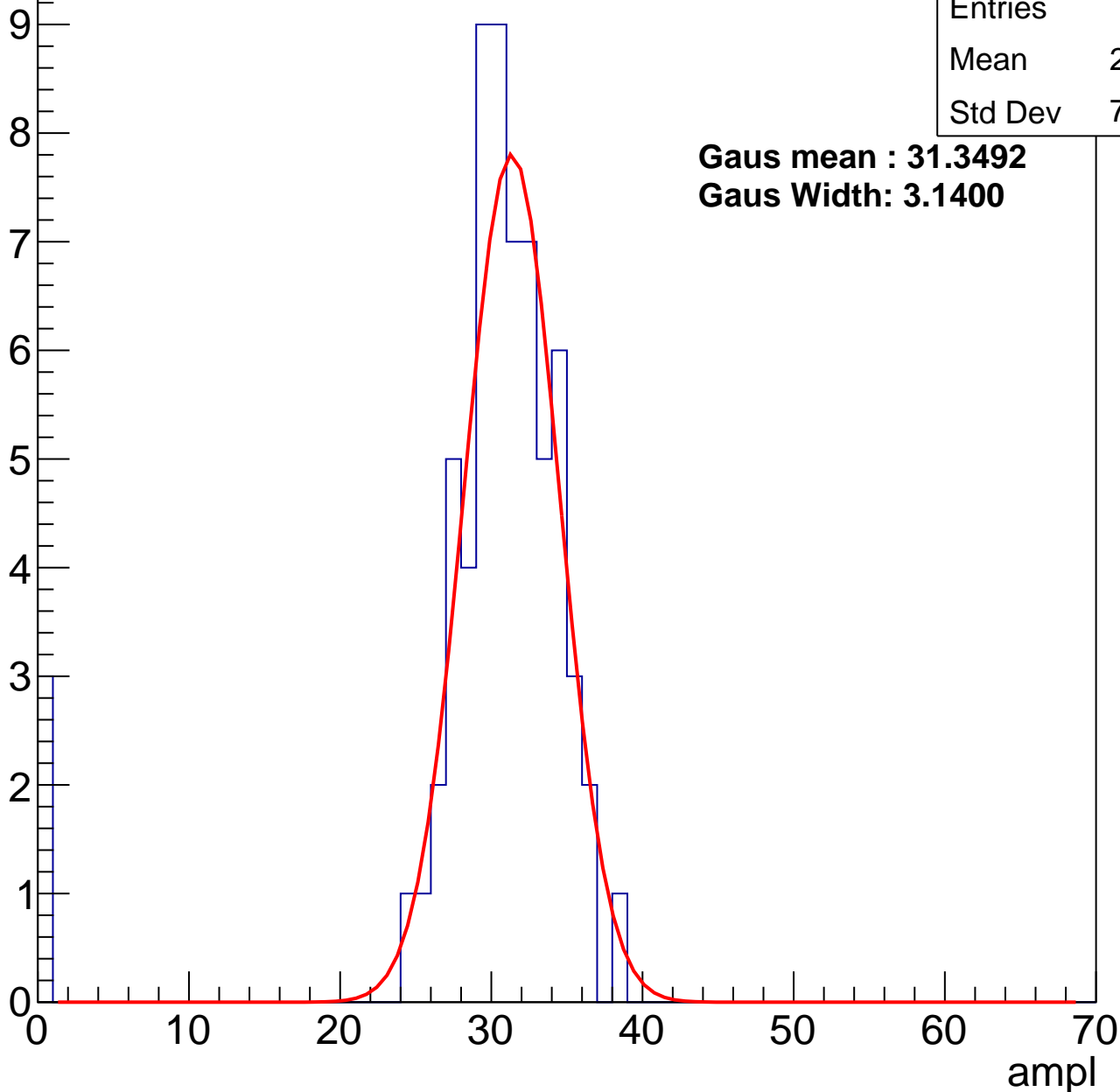
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.29
Std Dev	7.036

Gaus mean : 31.3492

Gaus Width: 3.1400



B1L003S, U21-ch20, adc1

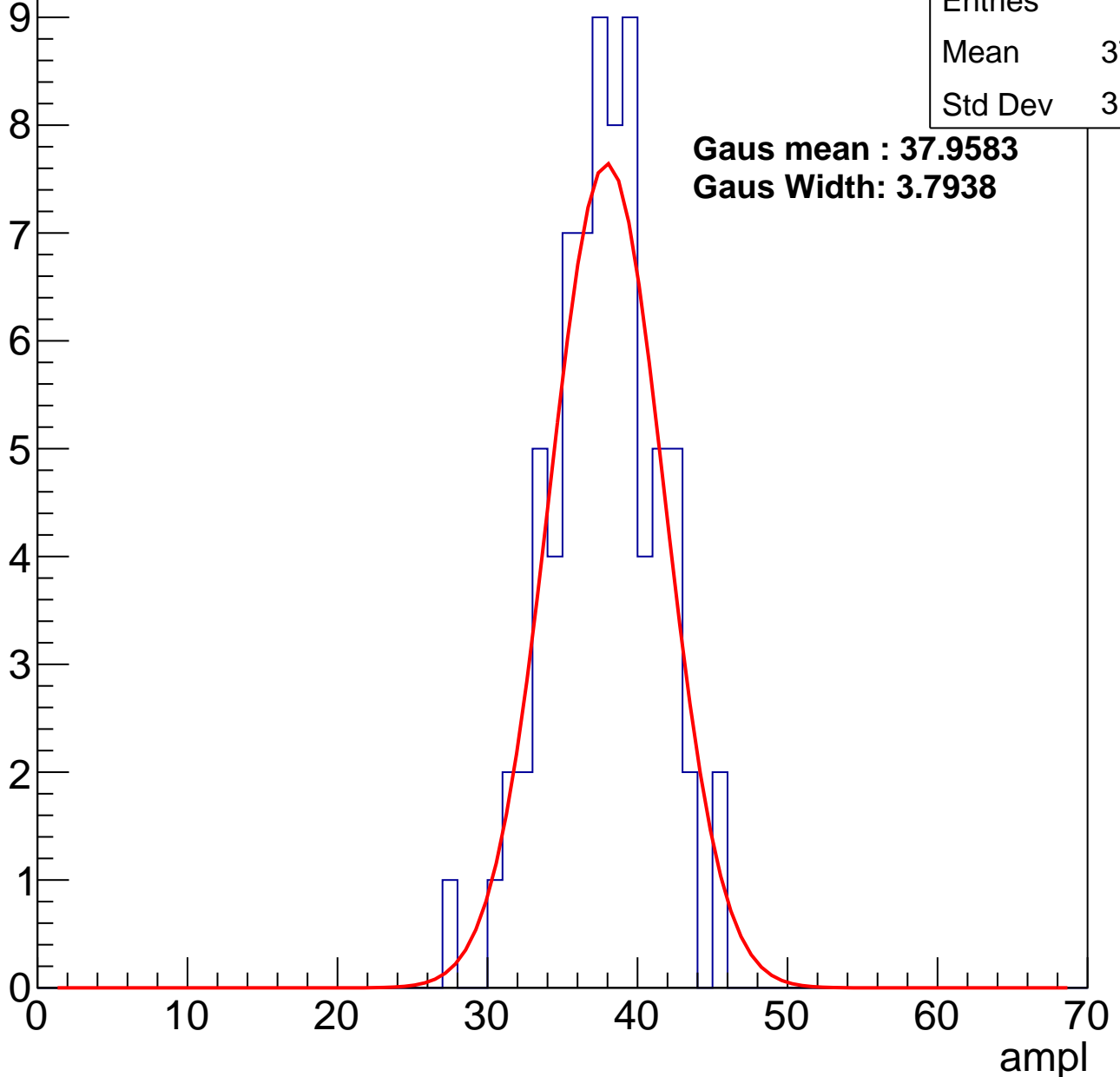
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.26
Std Dev	3.515

Gaus mean : 37.9583

Gaus Width: 3.7938



B1L003S, U21-ch20, adc2

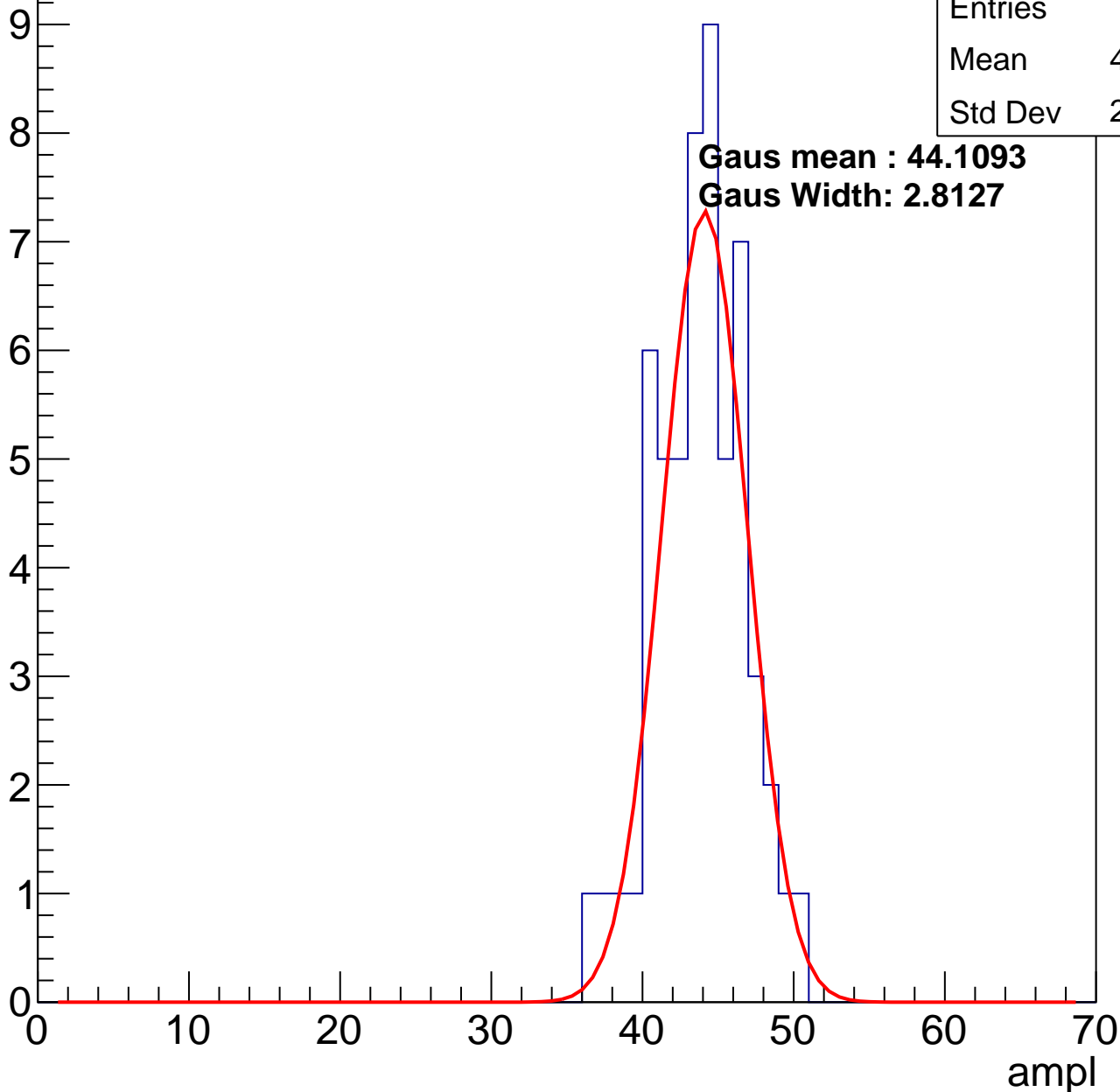
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.36
Std Dev	2.906

Gaus mean : 44.1093

Gaus Width: 2.8127

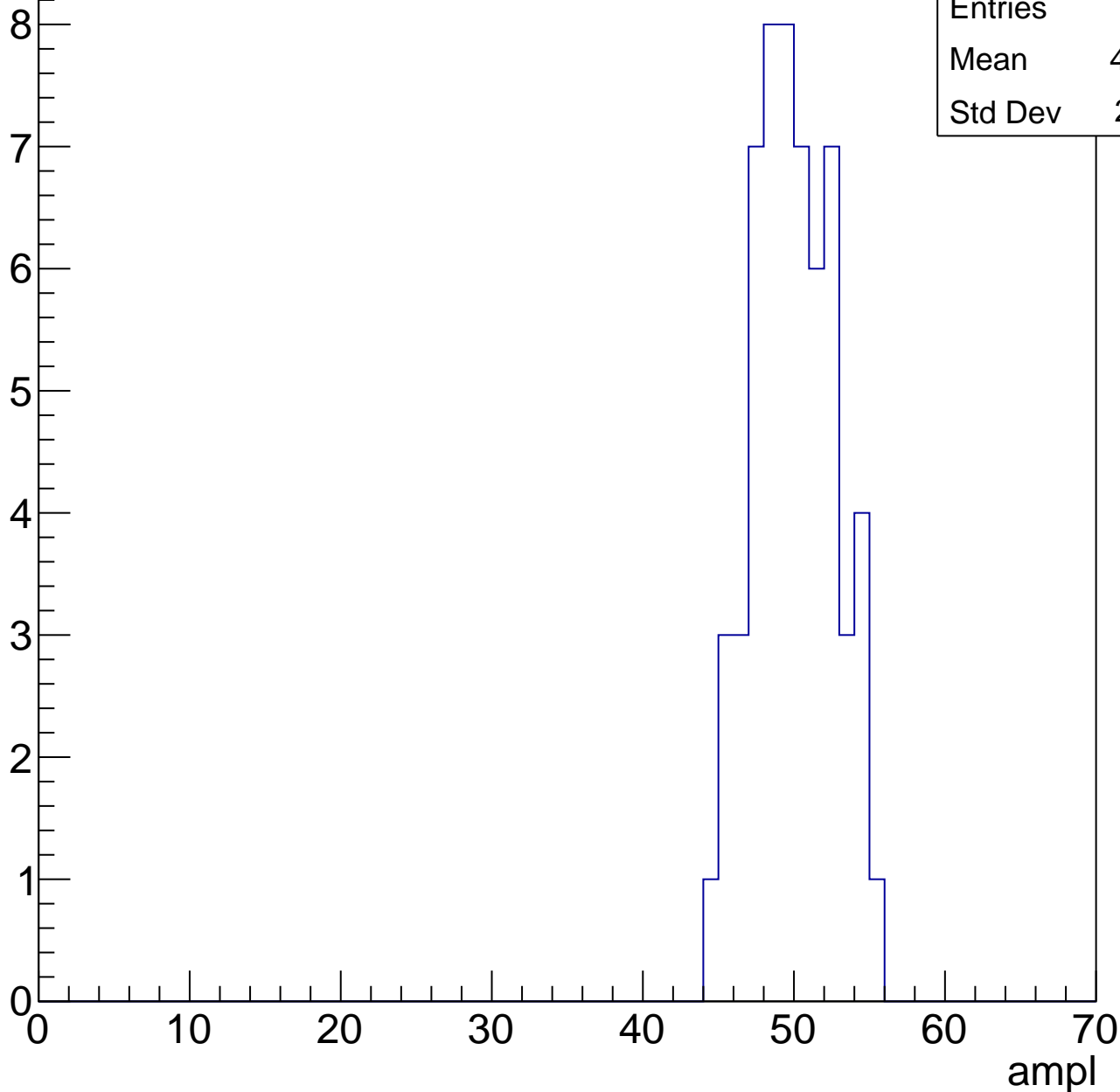


B1L003S, U21-ch20, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	49.52
Std Dev	2.621

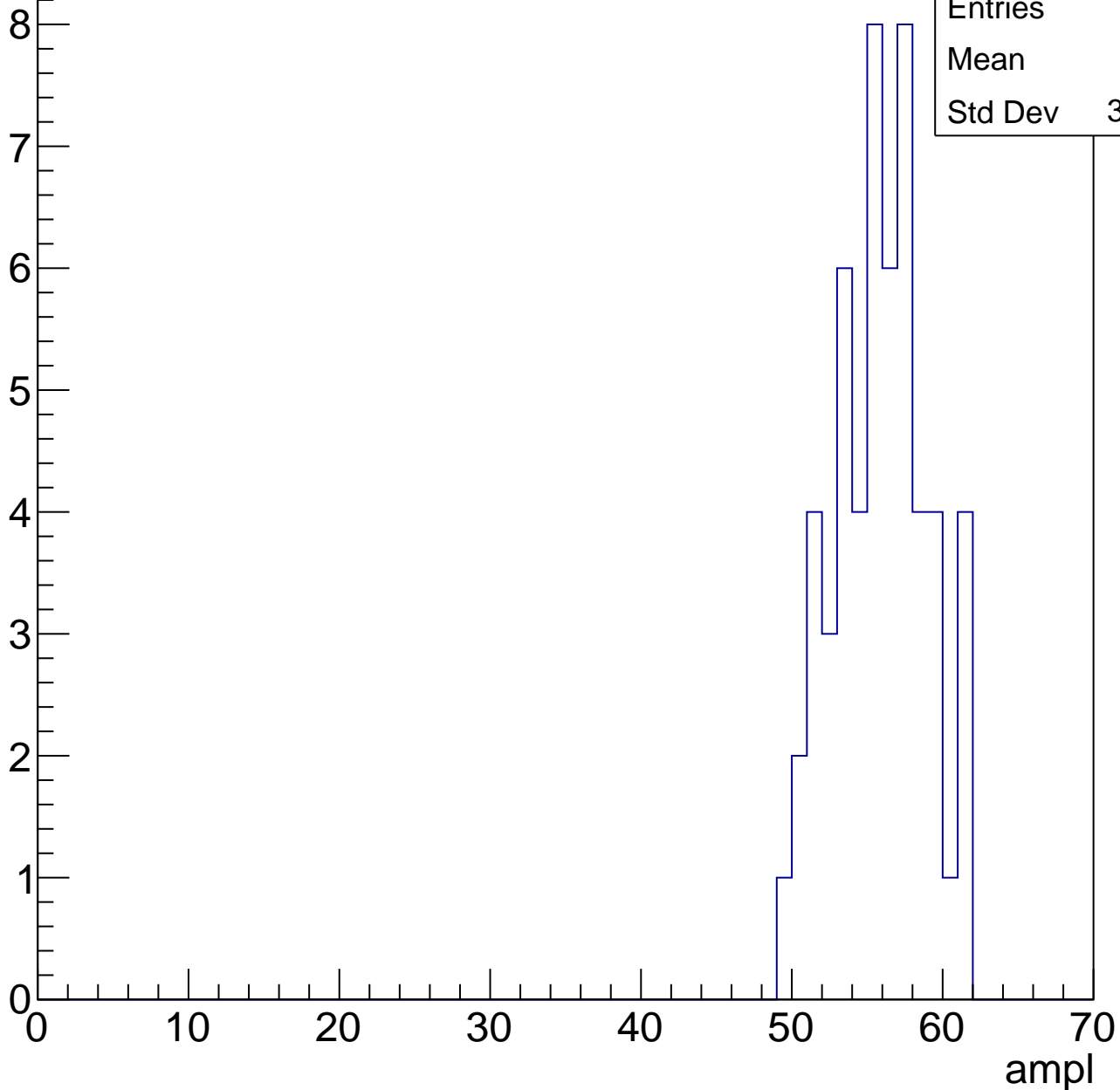


B1L003S, U21-ch20, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.4
Std Dev	3.025

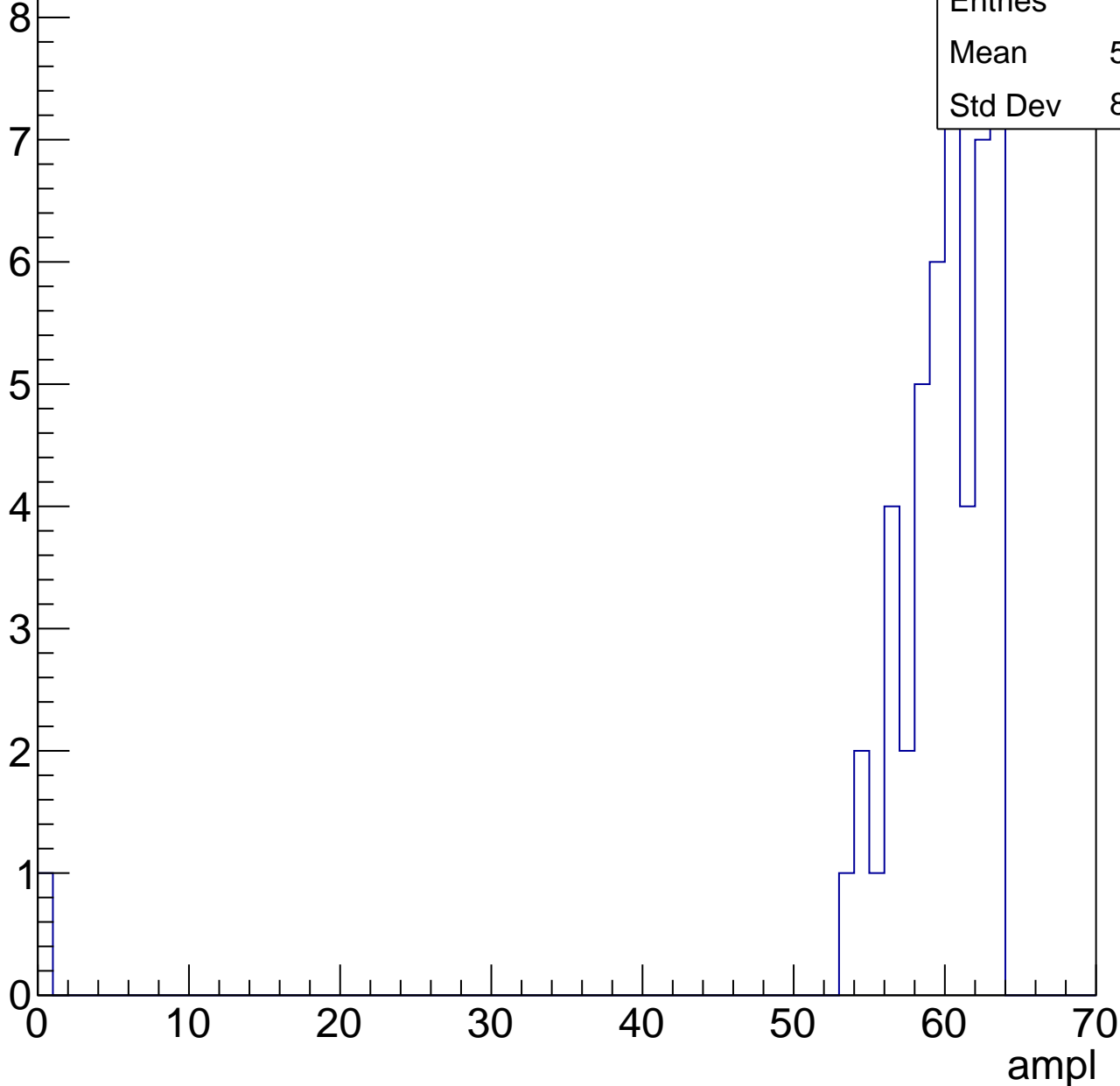


B1L003S, U21-ch20, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.37
Std Dev	8.838



B1L003S, U21-ch20, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	9
Mean	61.33
Std Dev	1.491

B1L003S, U21-ch20, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11
Std Dev	11

B1L003S, U21-ch21, adc0

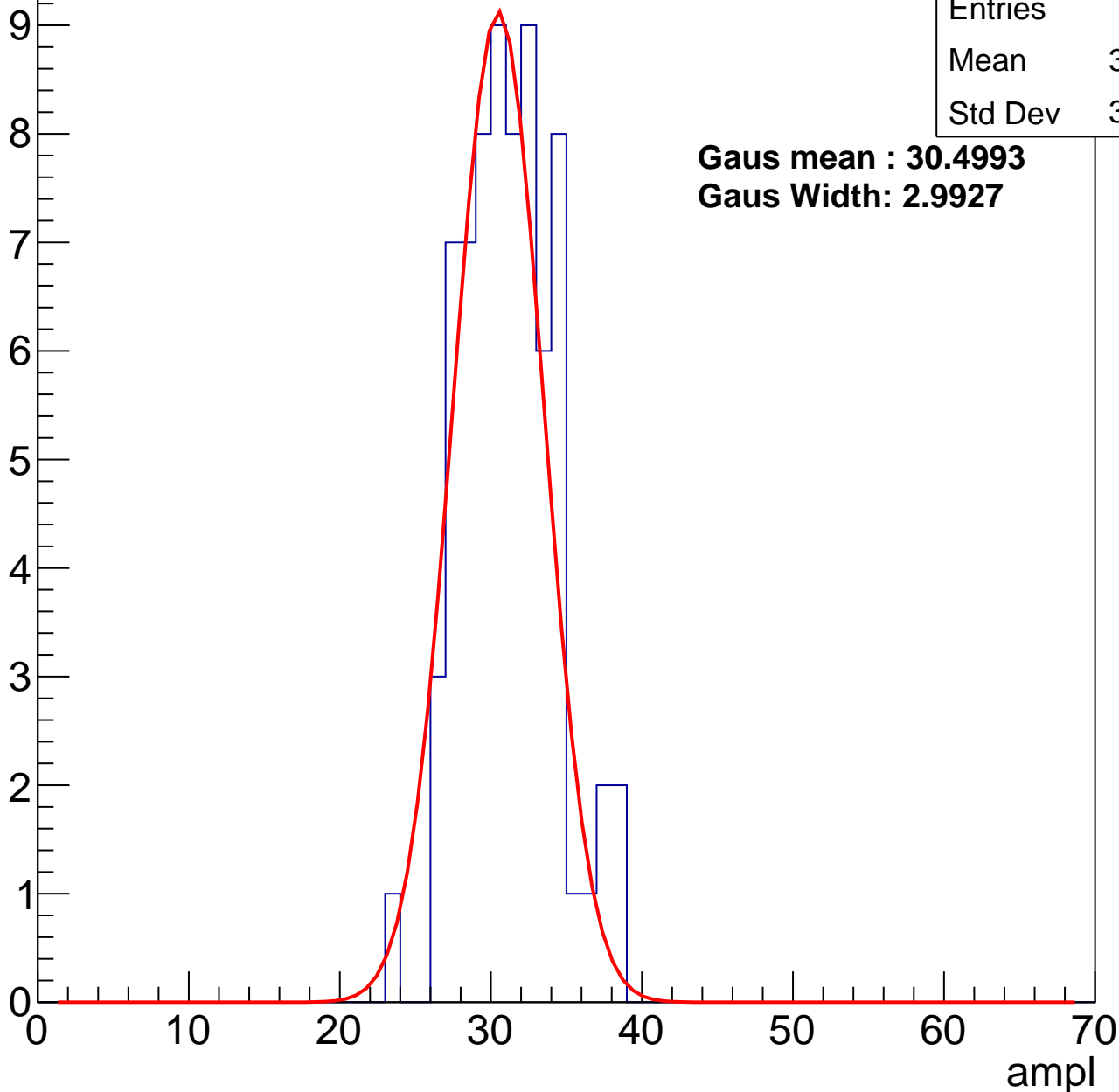
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	30.76
Std Dev	3.044

Gaus mean : 30.4993

Gaus Width: 2.9927



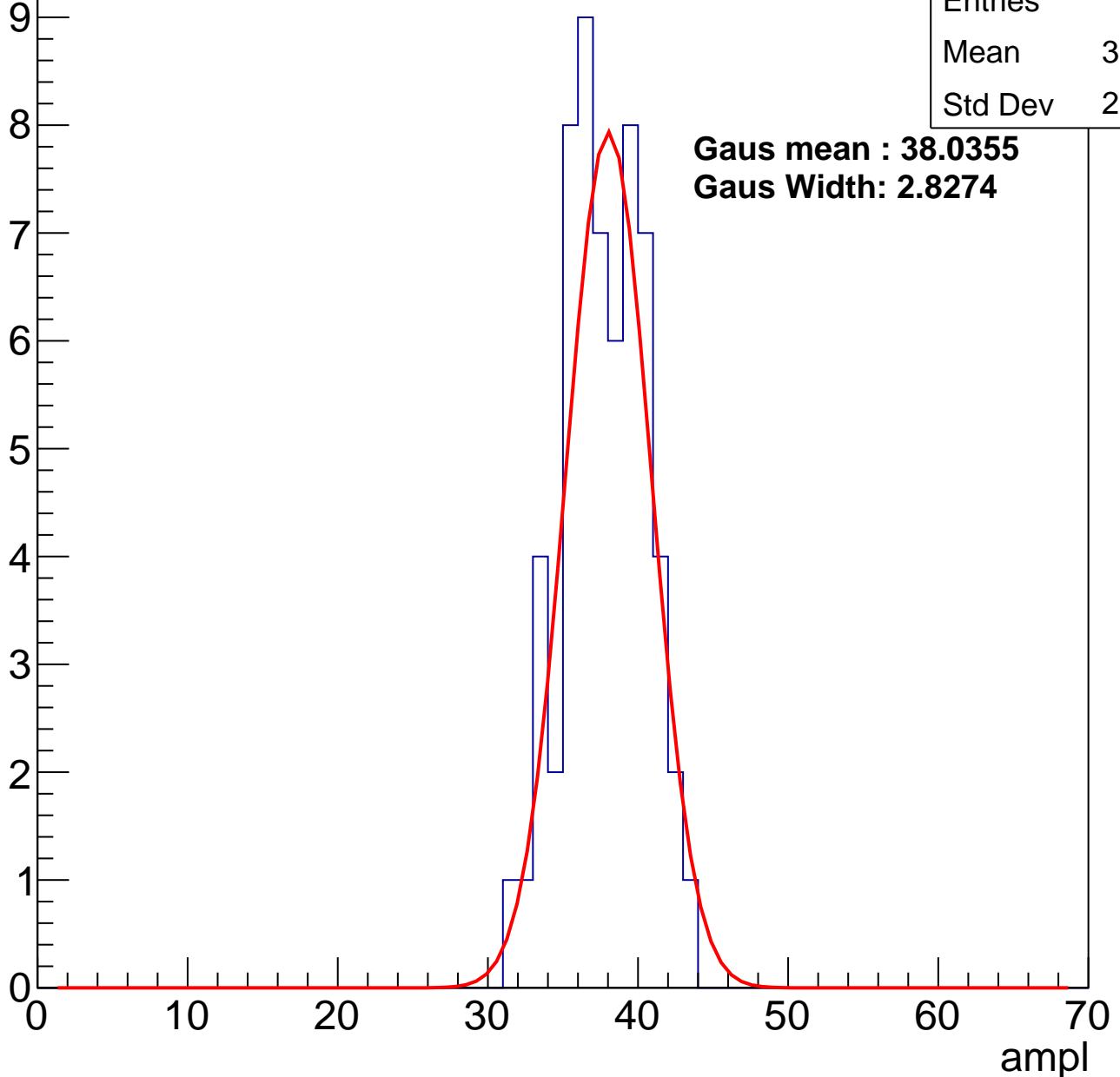
B1L003S, U21-ch21, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	37.28
Std Dev	2.678

Gaus mean : 38.0355
Gaus Width: 2.8274



B1L003S, U21-ch21, adc2

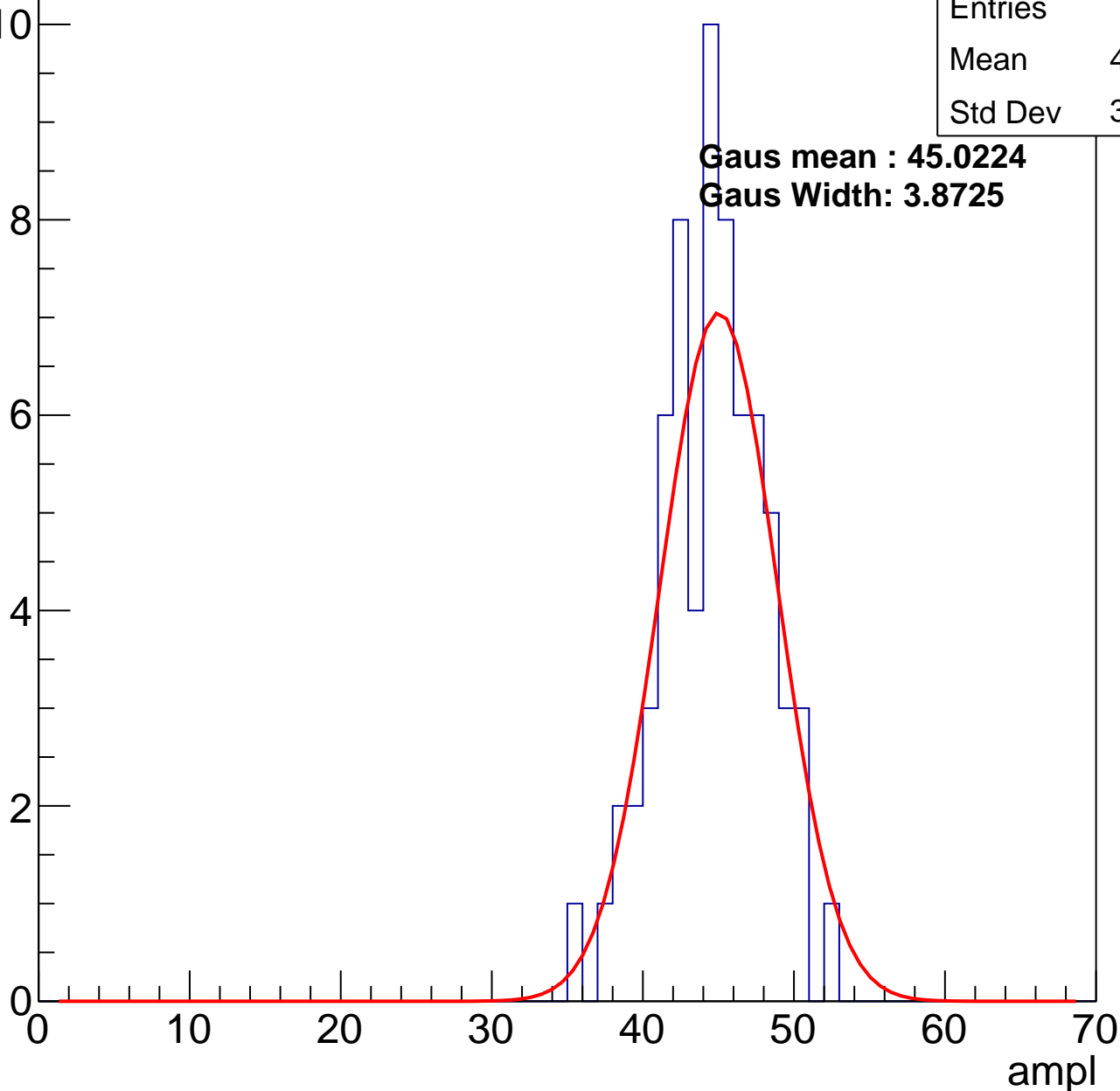
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	44.16
Std Dev	3.408

Gaus mean : 45.0224

Gaus Width: 3.8725

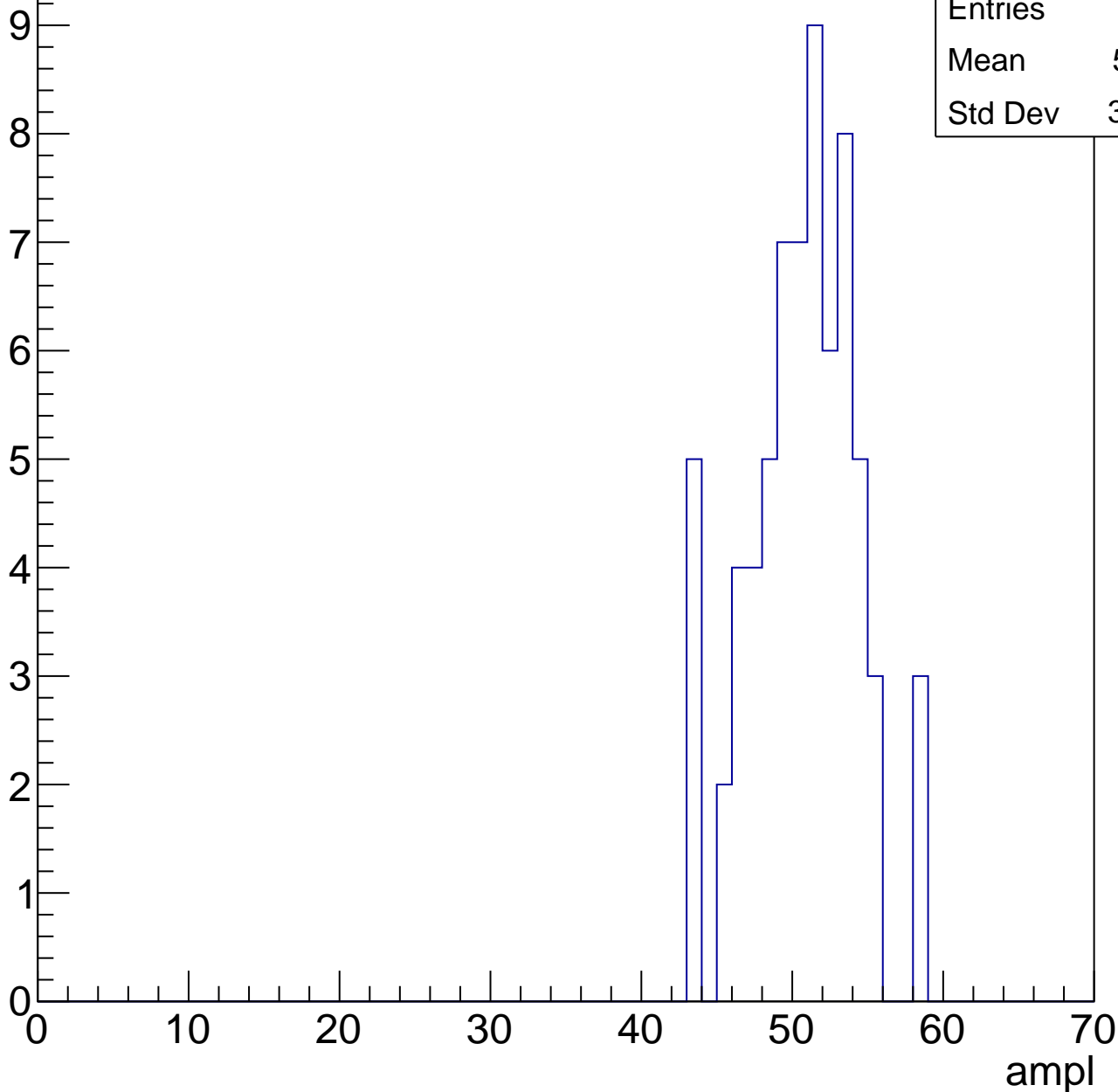


B1L003S, U21-ch21, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.21
Std Dev	3.579



B1L003S, U21-ch21, adc4

calib_packv5_042523_0143.root, FC#13, port D2

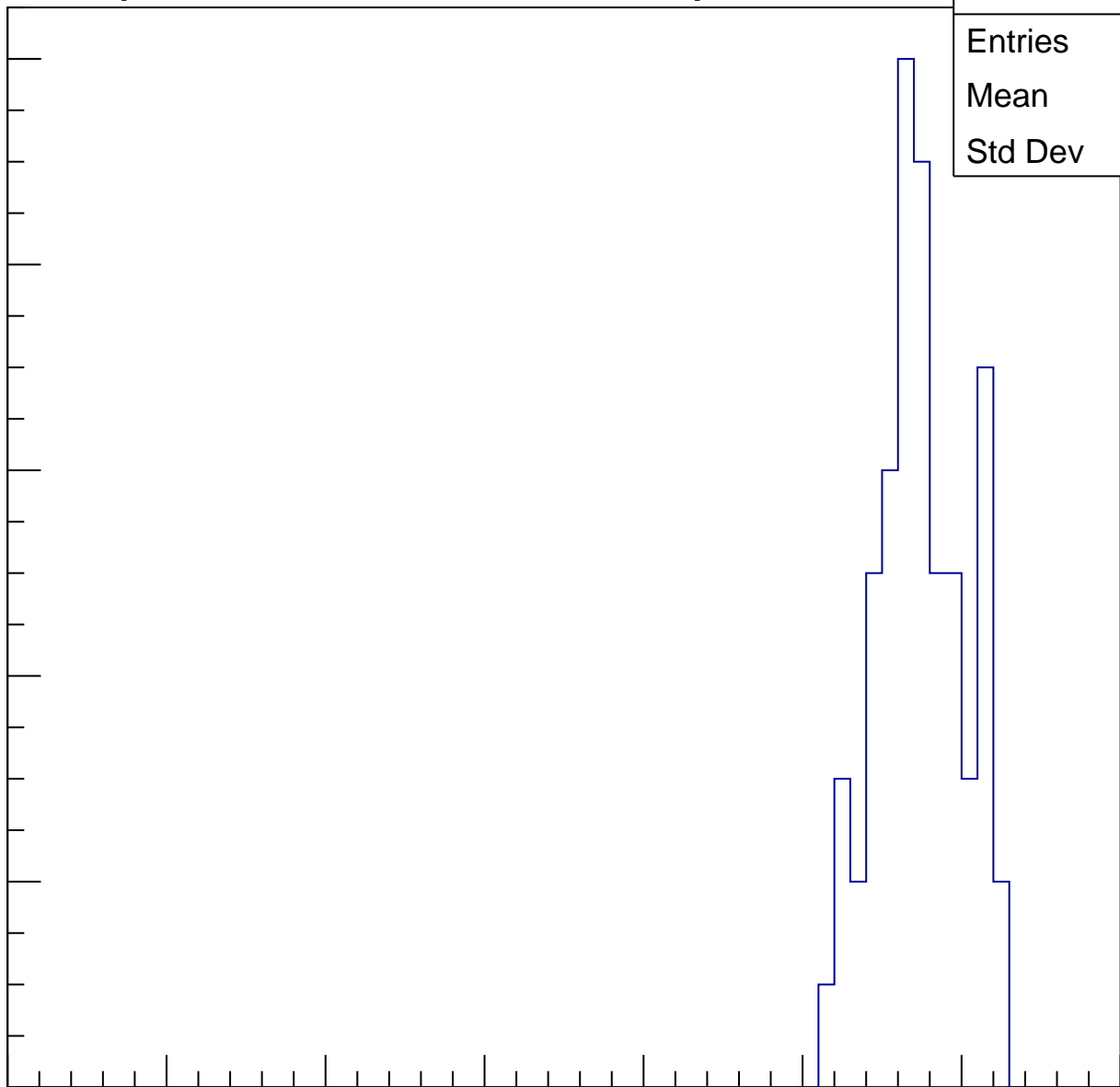
Entries	58
Mean	56.93
Std Dev	2.741

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

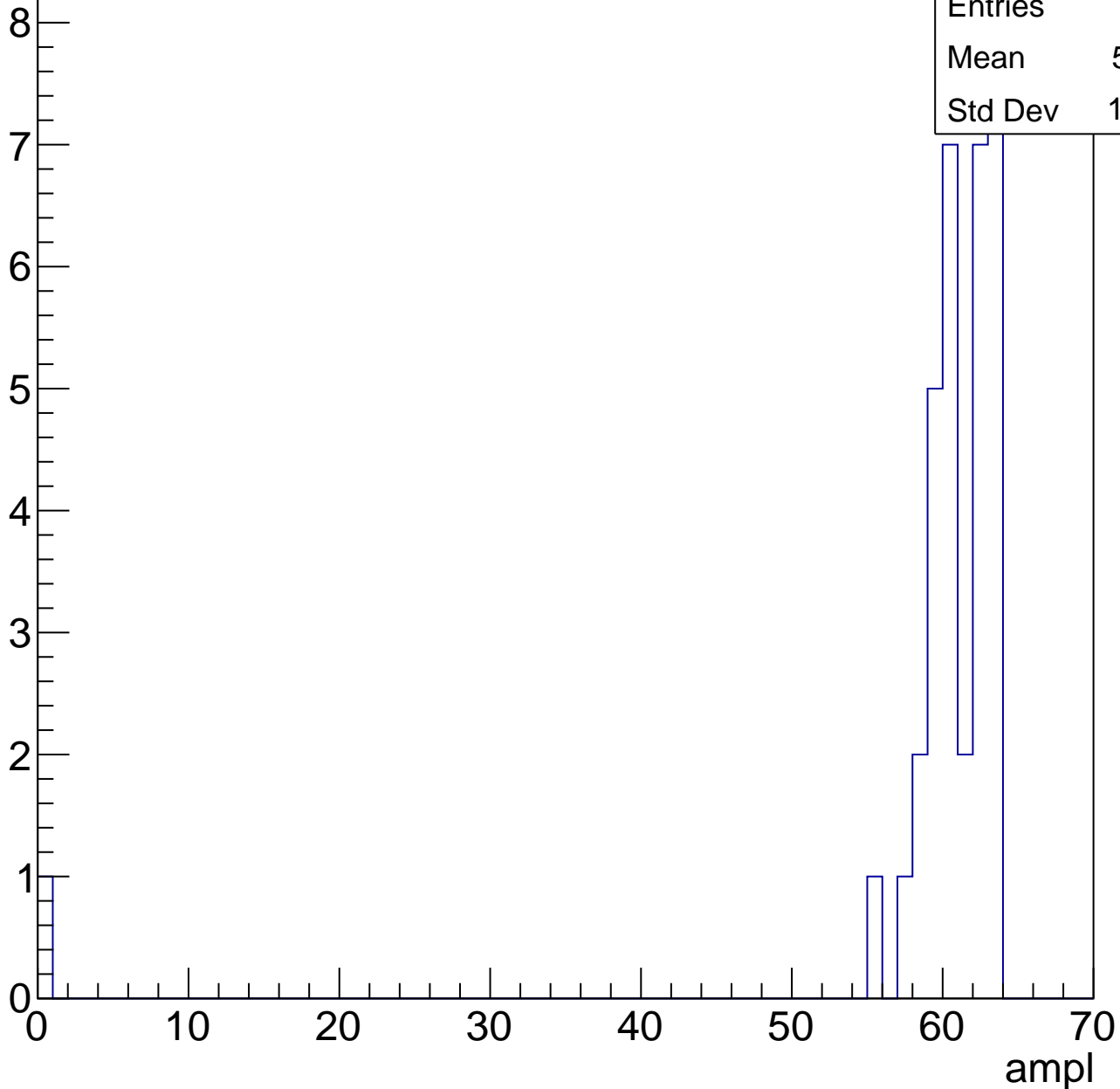


B1L003S, U21-ch21, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	34
Mean	58.91
Std Dev	10.44



B1L003S, U21-ch21, adc6

calib_packv5_042523_0143.root, FC#13, port D2

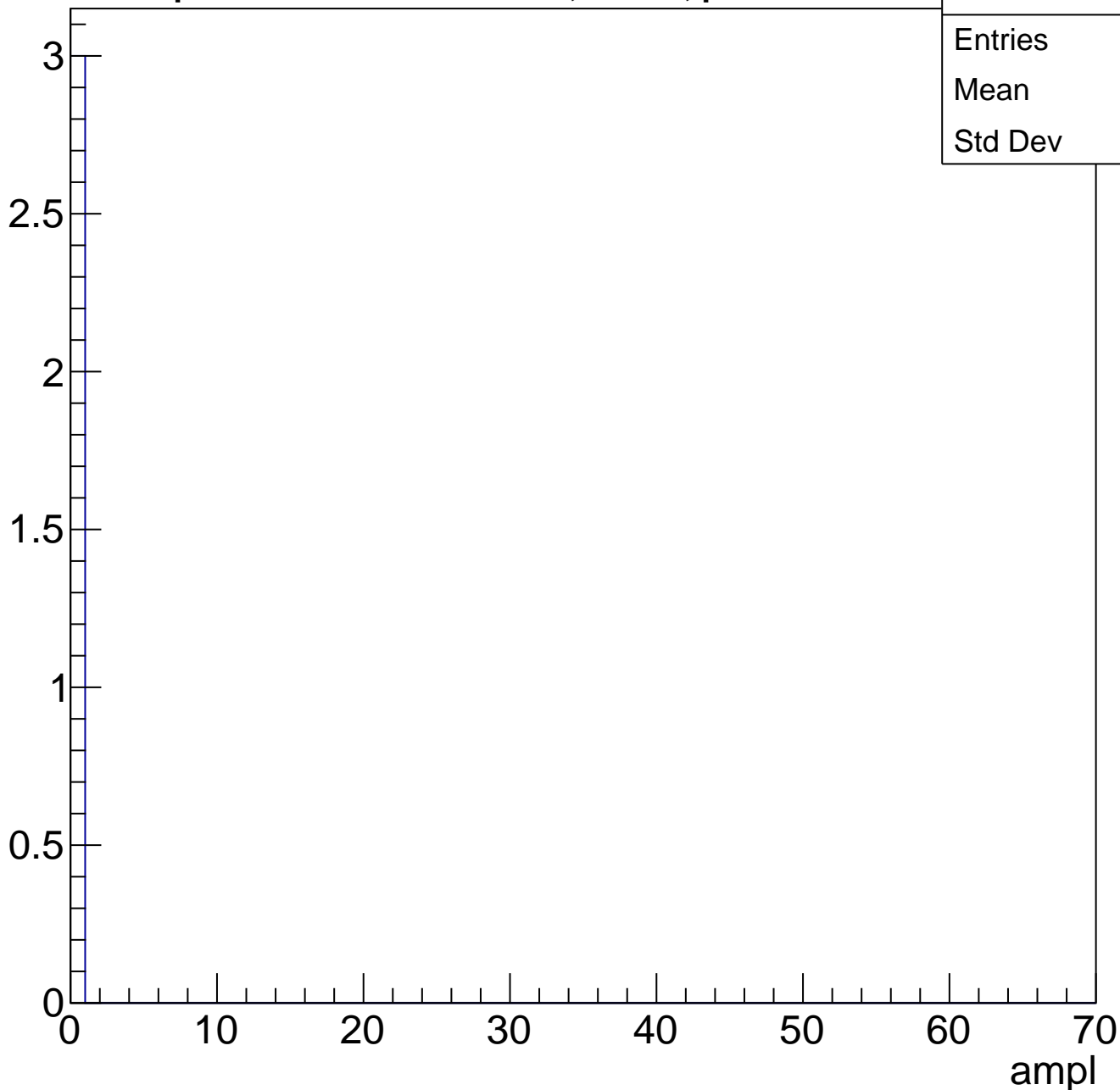
Entry



B1L003S, U21-ch21, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch22, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	29.27
Std Dev	3.068

Gaus mean : 29.8939

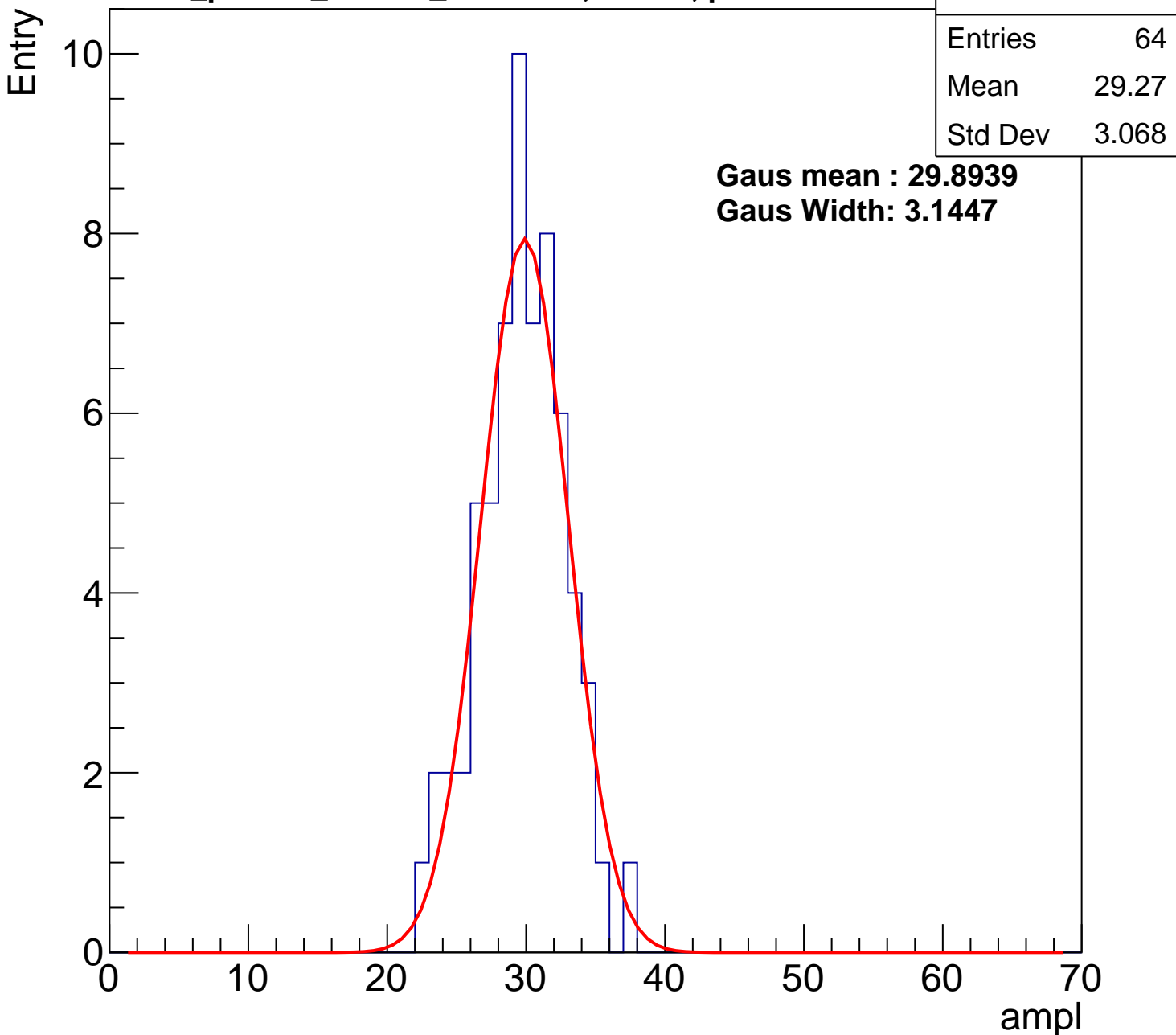
Gaus Width: 3.1447

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch22, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	36.84
Std Dev	3.477

Gaus mean : 37.1967

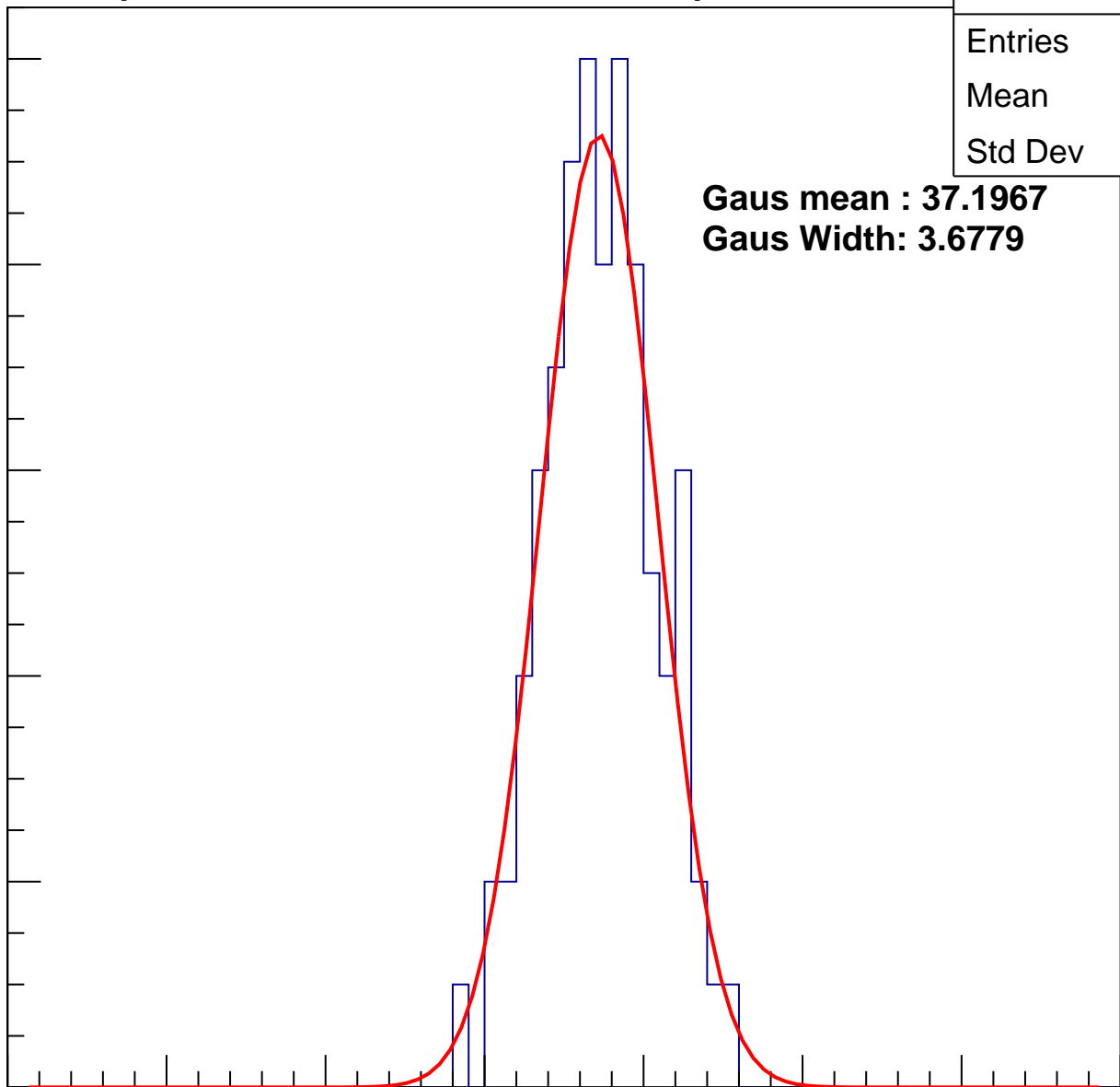
Gaus Width: 3.6779

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

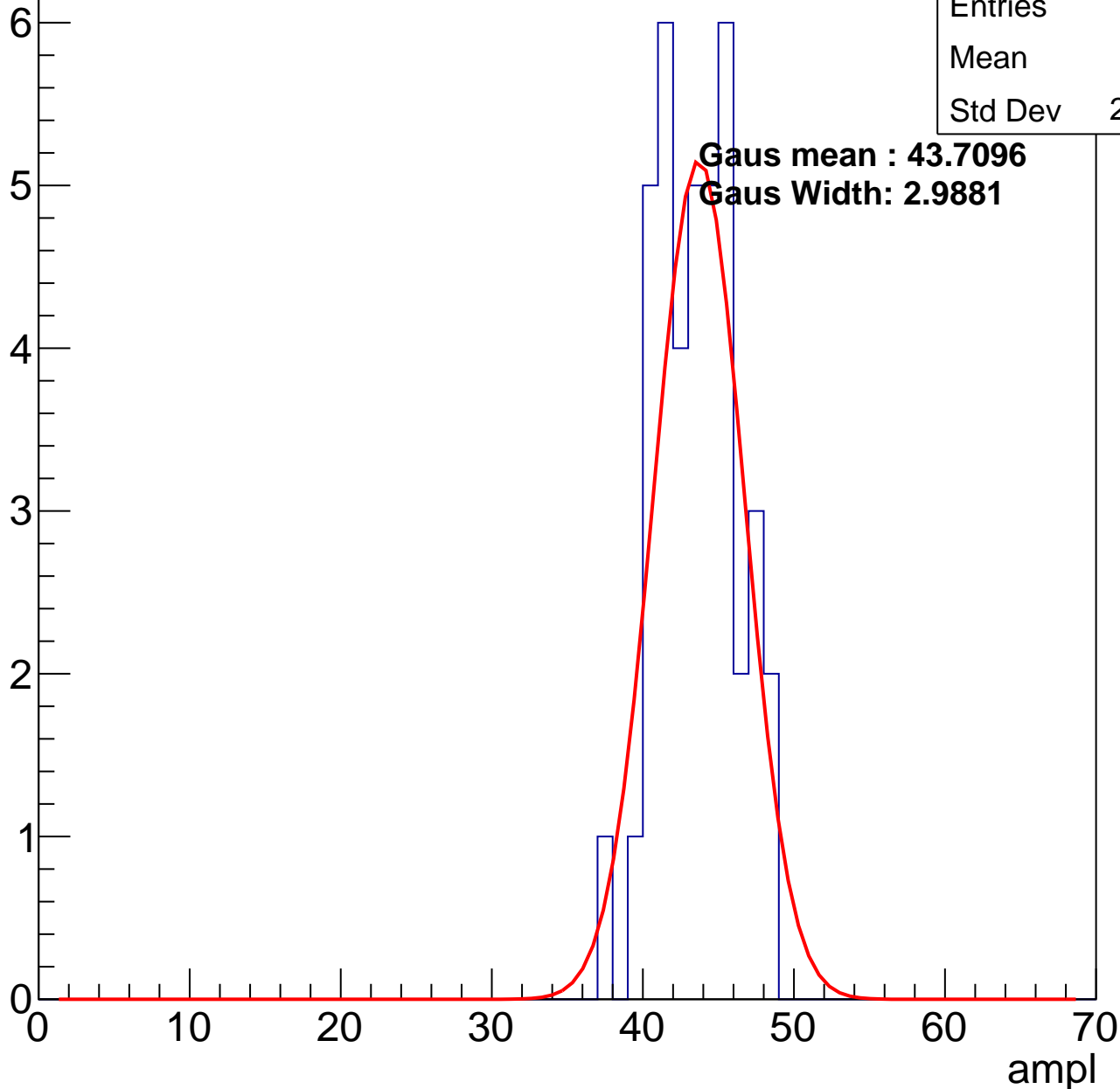


B1L003S, U21-ch22, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	43.1
Std Dev	2.596

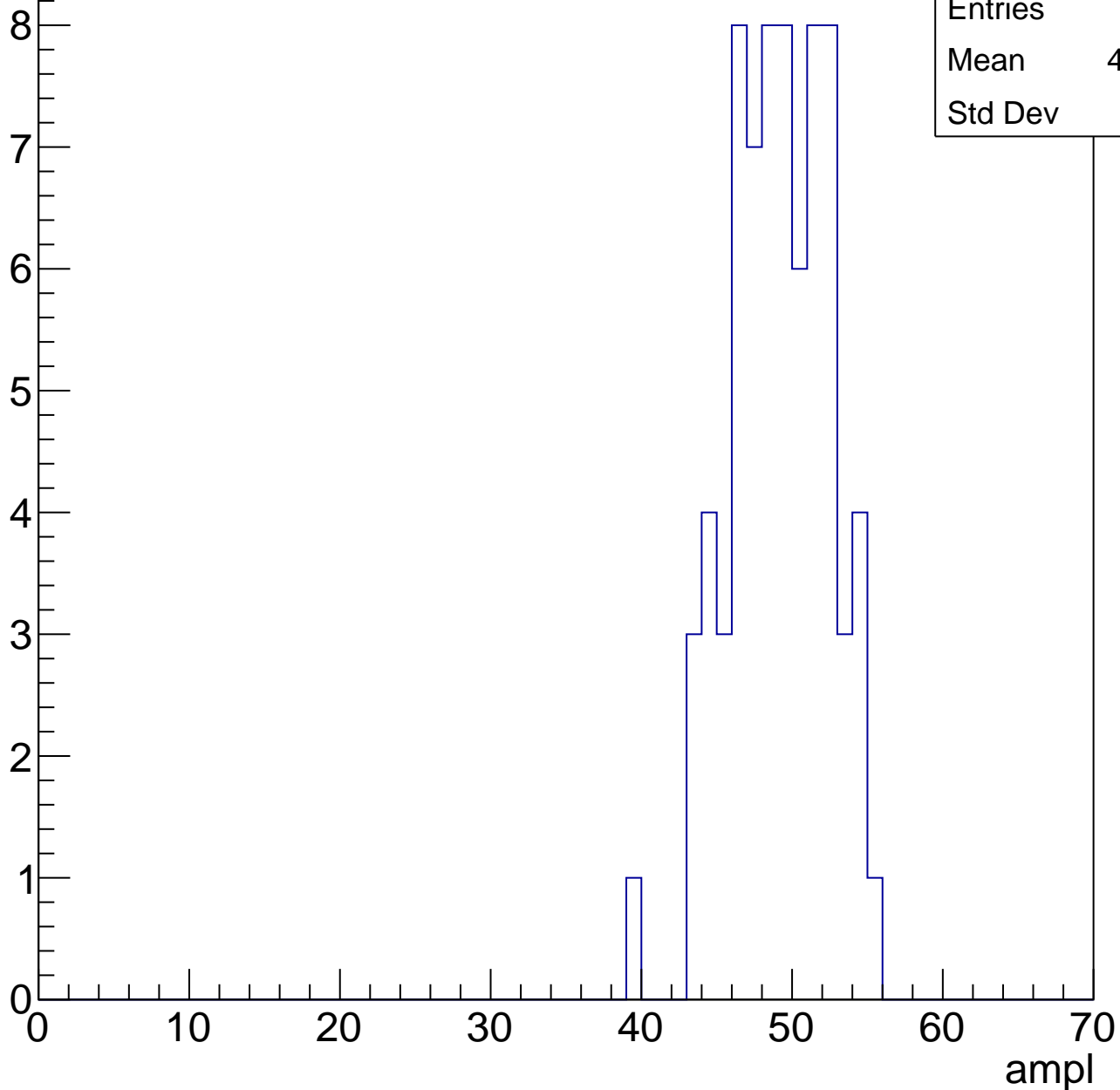


B1L003S, U21-ch22, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	48.69
Std Dev	3.23

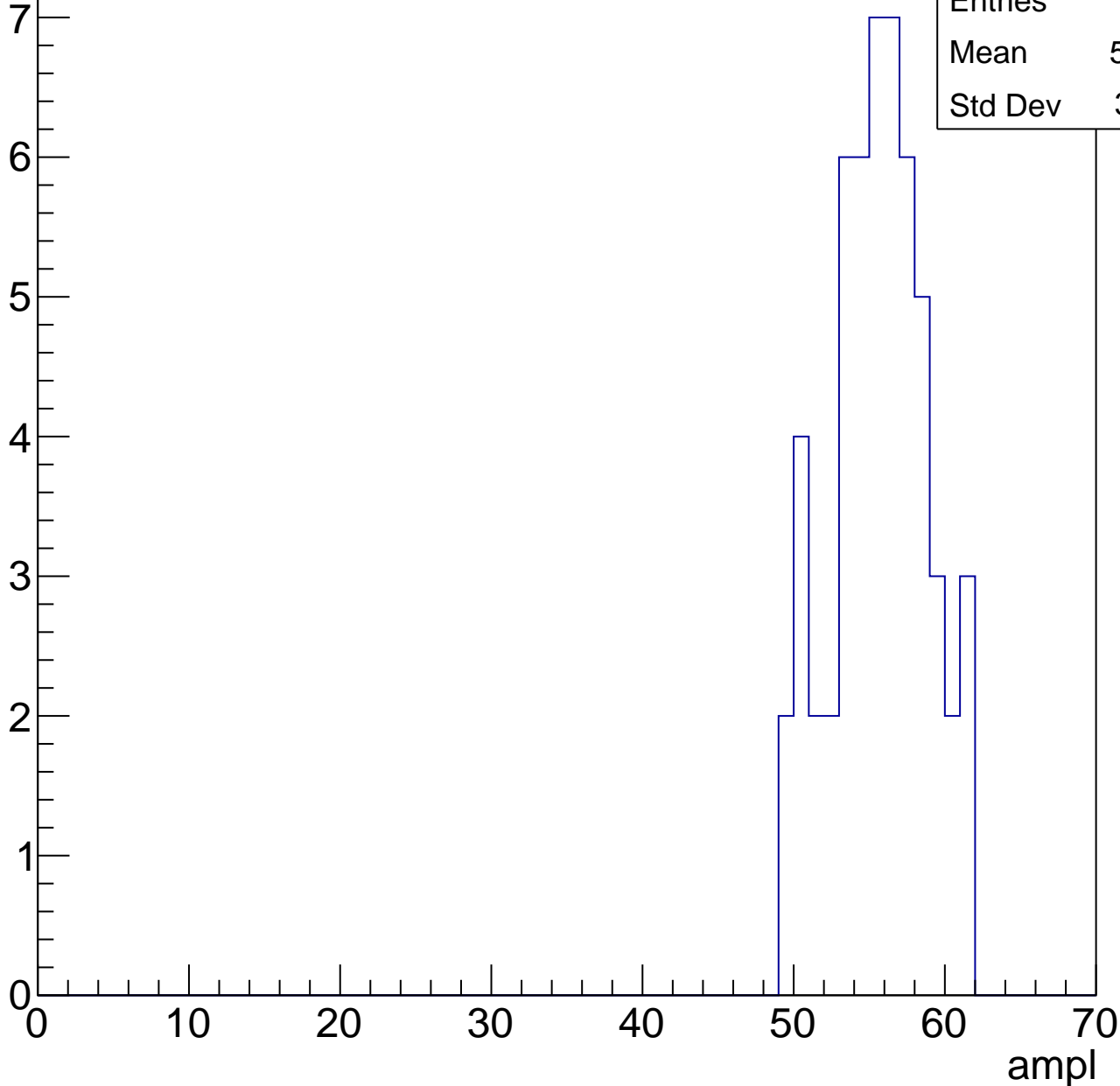


B1L003S, U21-ch22, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	55.18
Std Dev	3.111

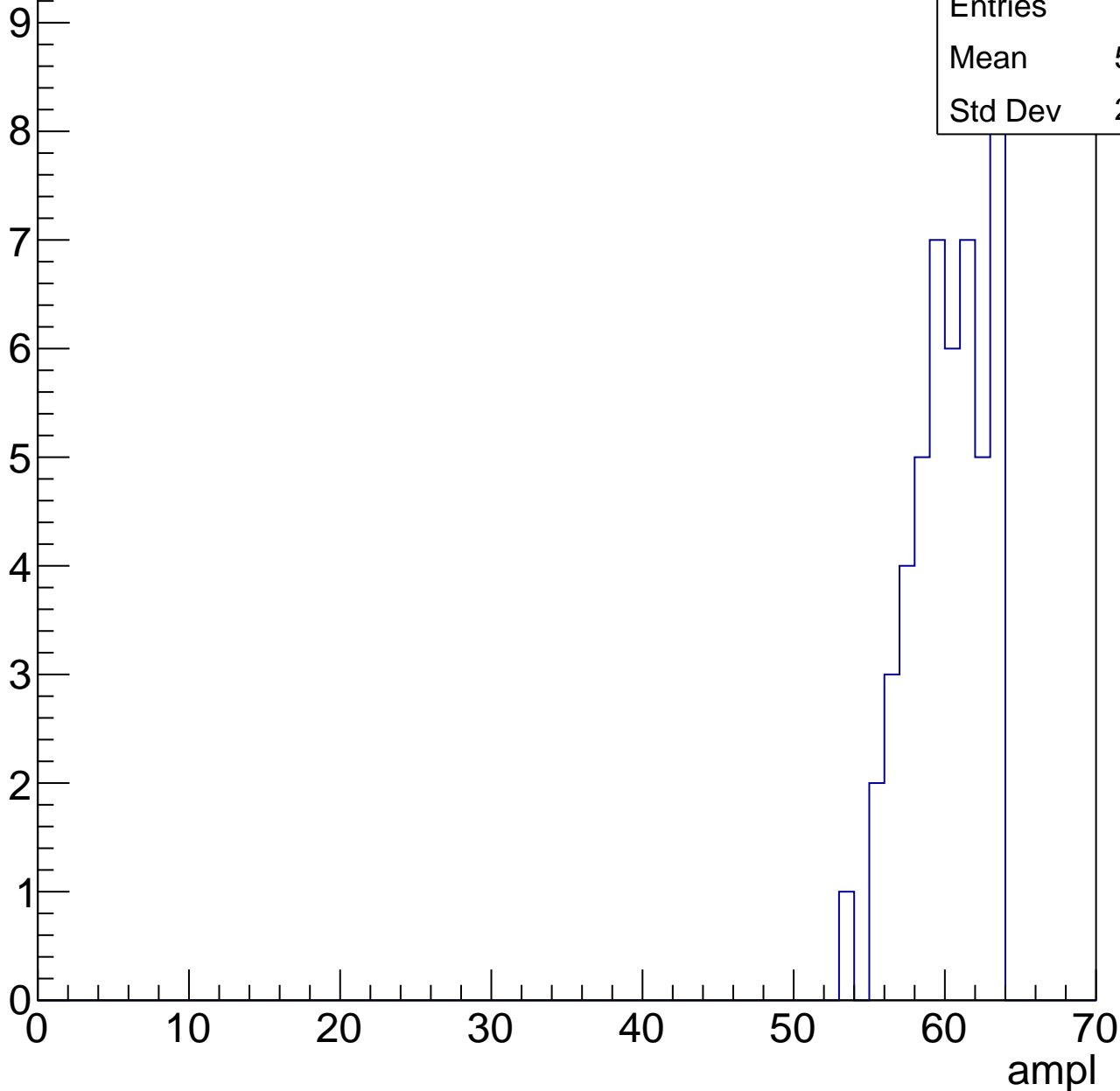


B1L003S, U21-ch22, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.71
Std Dev	2.531

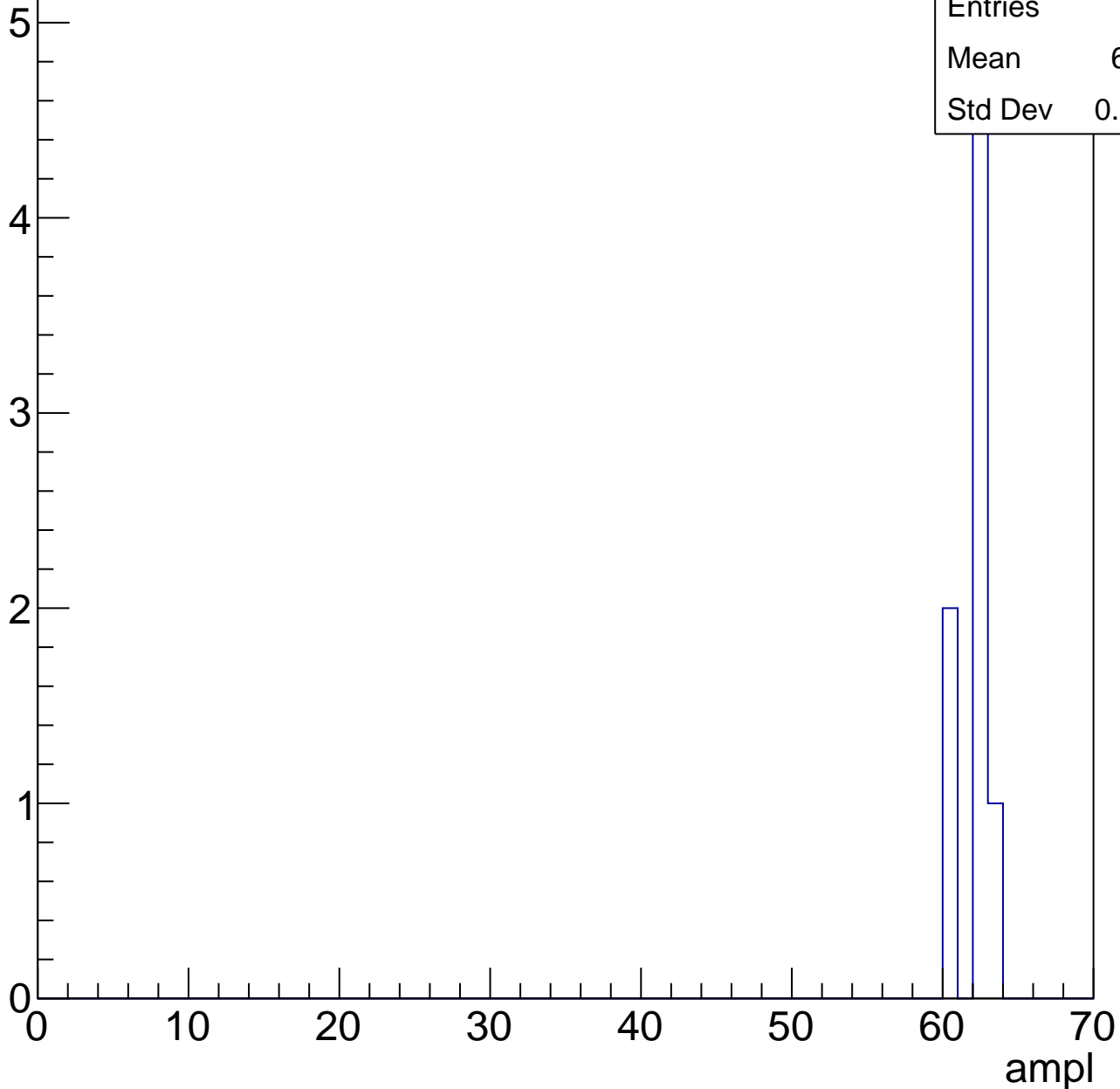


B1L003S, U21-ch22, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	8
Mean	61.62
Std Dev	0.9922



B1L003S, U21-ch22, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch23, adc0

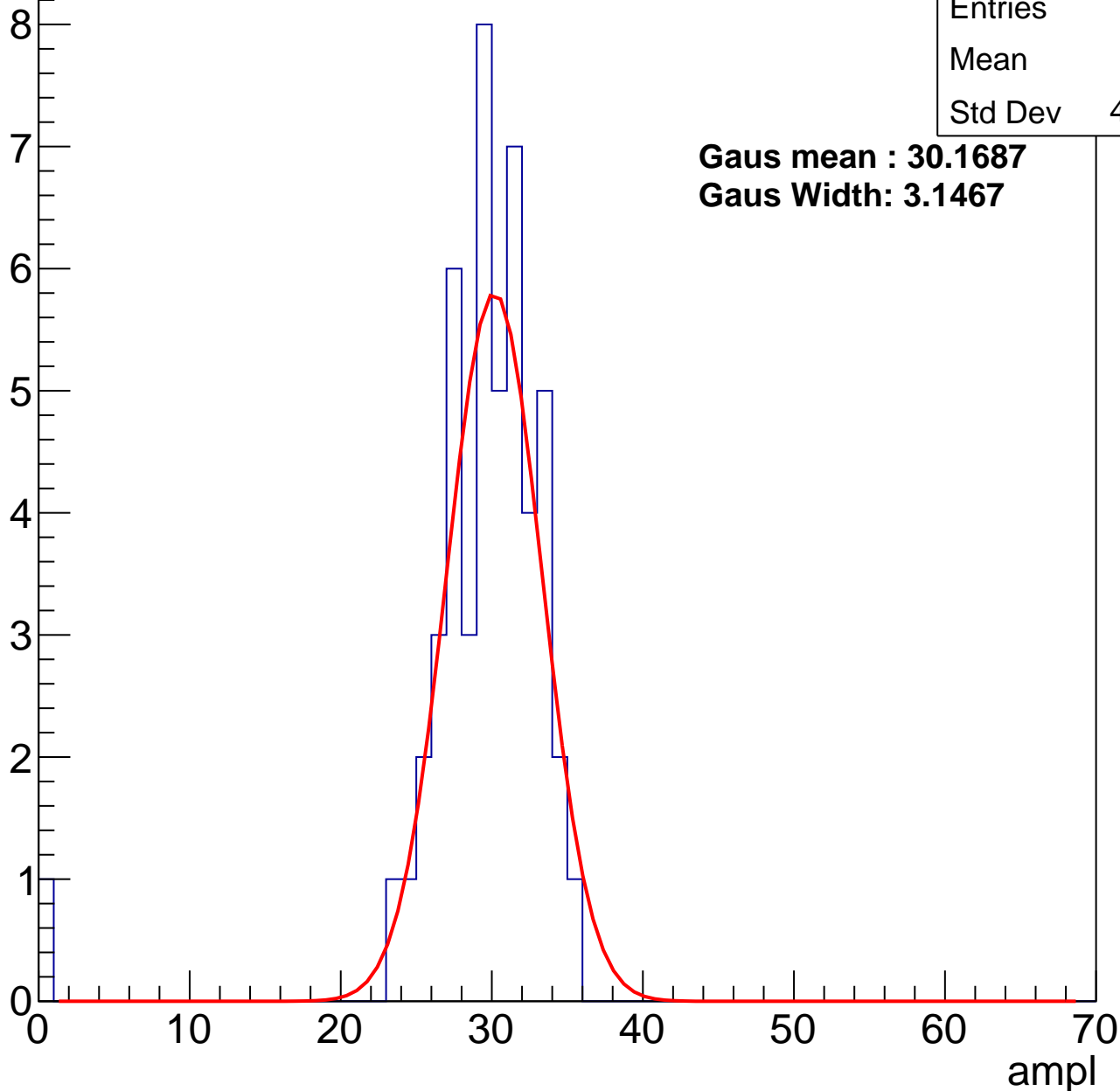
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	28.9
Std Dev	4.995

Gaus mean : 30.1687

Gaus Width: 3.1467



B1L003S, U21-ch23, adc1

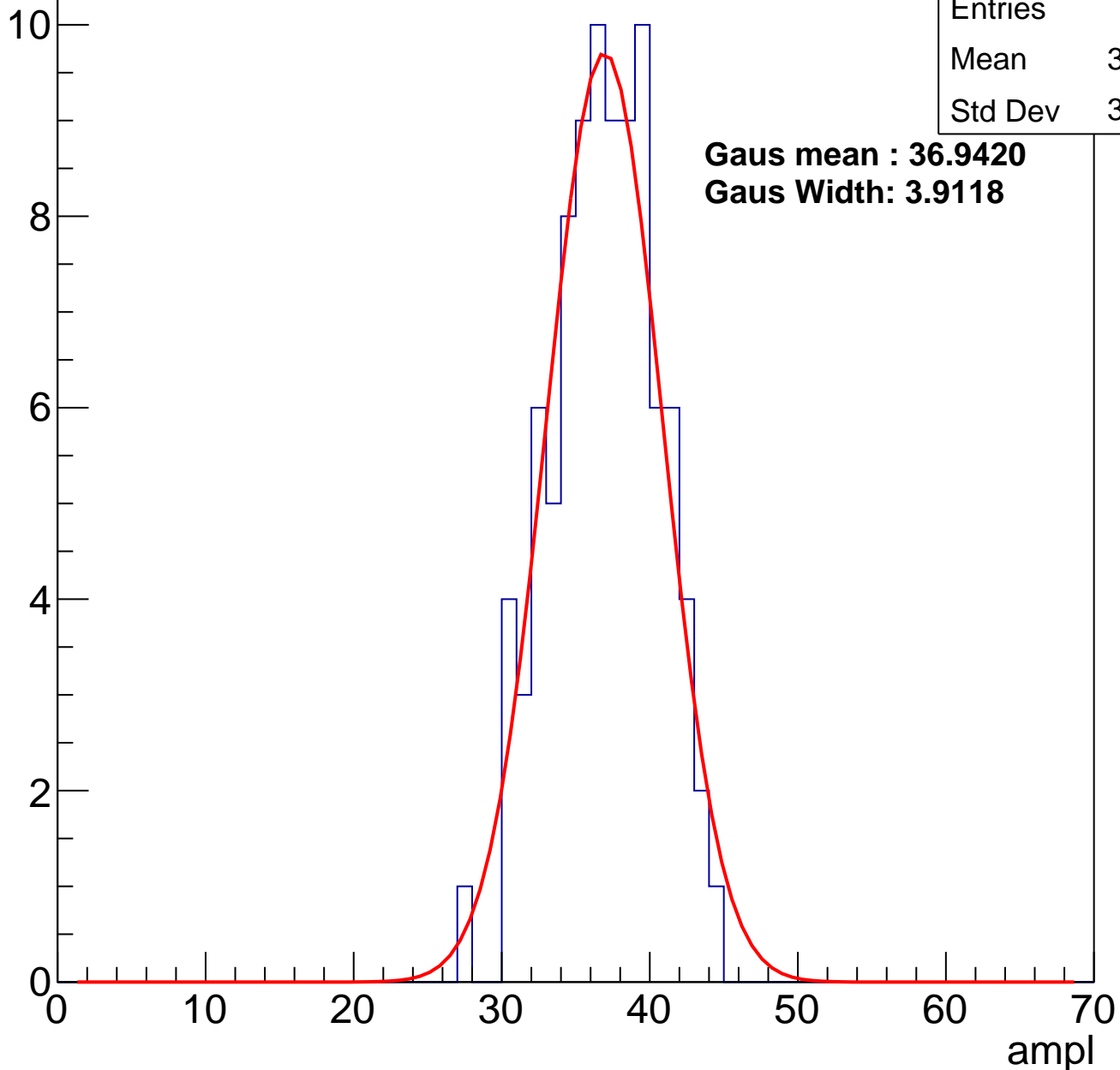
calib_packv5_042523_0143.root, FC#13, port D2

Entries	93
Mean	36.48
Std Dev	3.512

Gaus mean : 36.9420

Gaus Width: 3.9118

Entry



B1L003S, U21-ch23, adc2

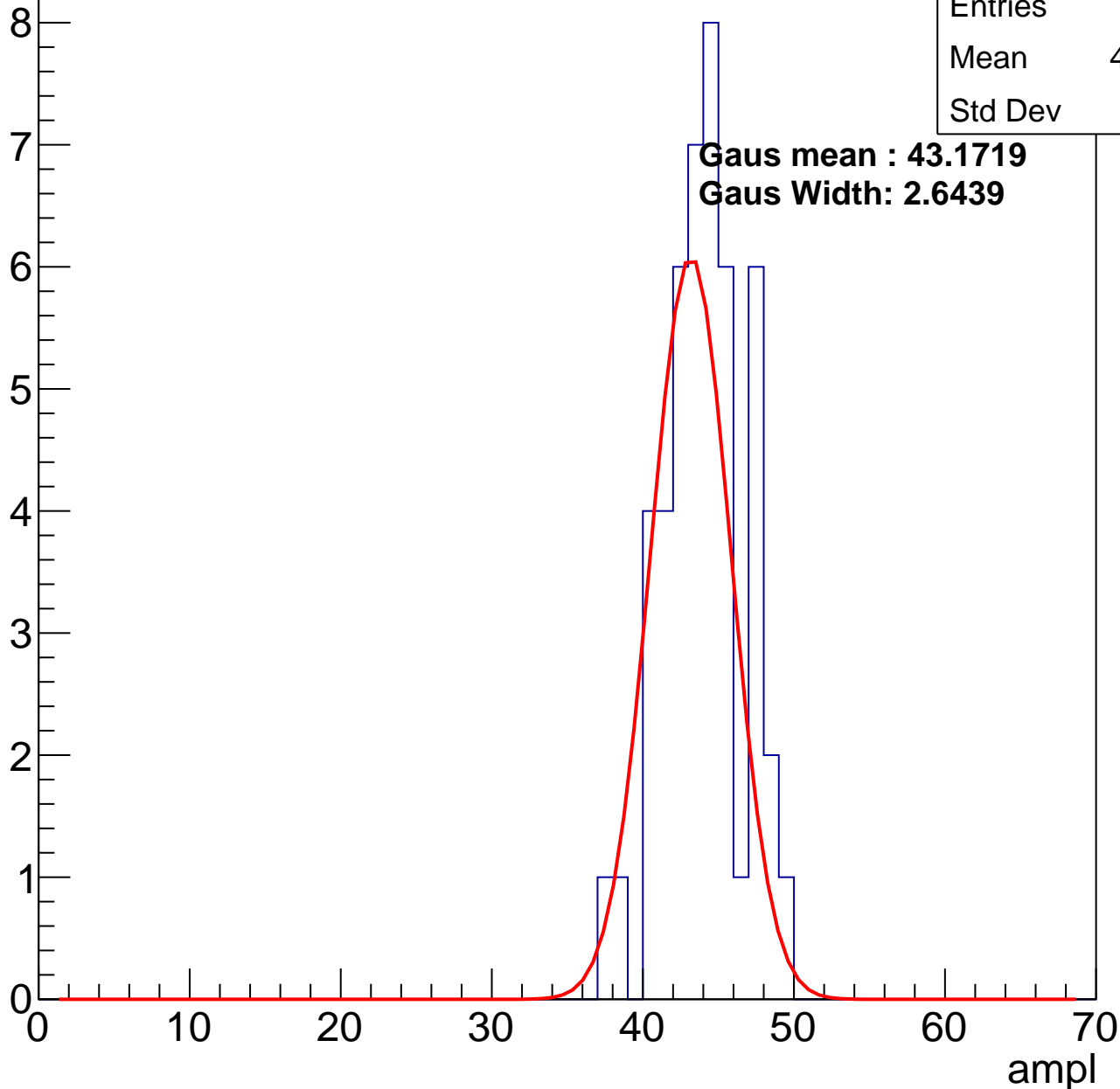
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	43.55
Std Dev	2.64

Gaus mean : 43.1719

Gaus Width: 2.6439

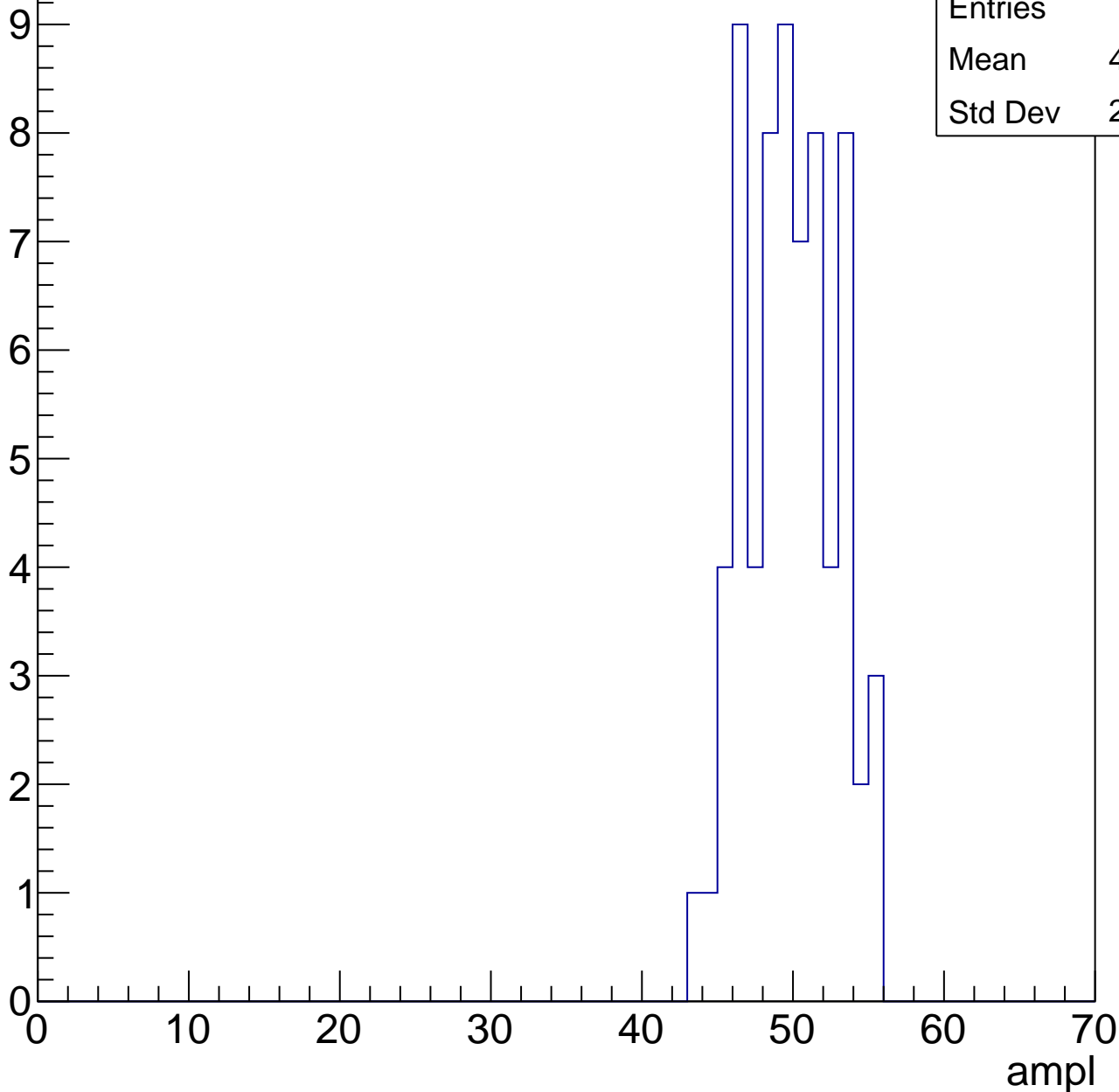


B1L003S, U21-ch23, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.37
Std Dev	2.925

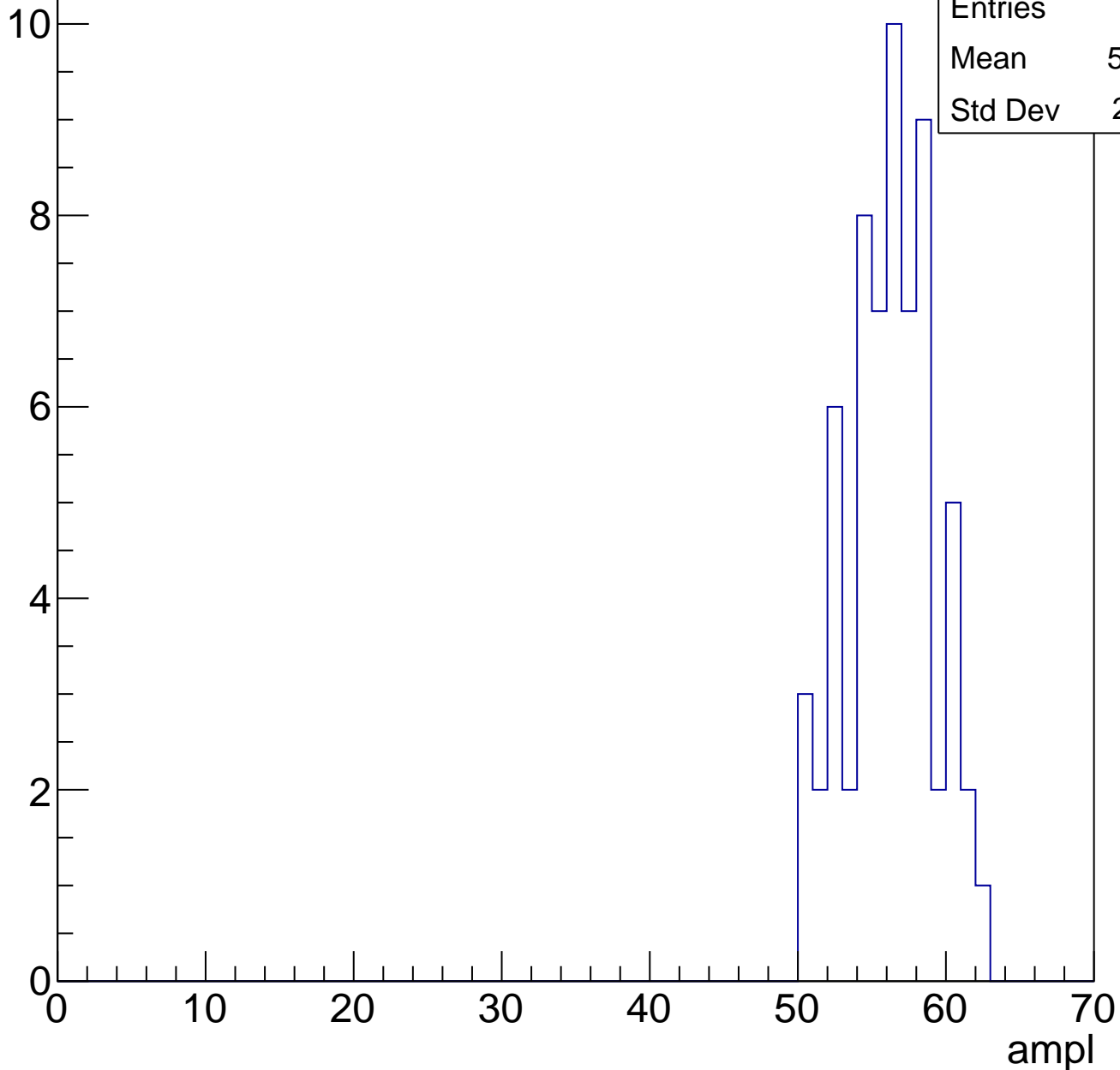


B1L003S, U21-ch23, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	55.78
Std Dev	2.891

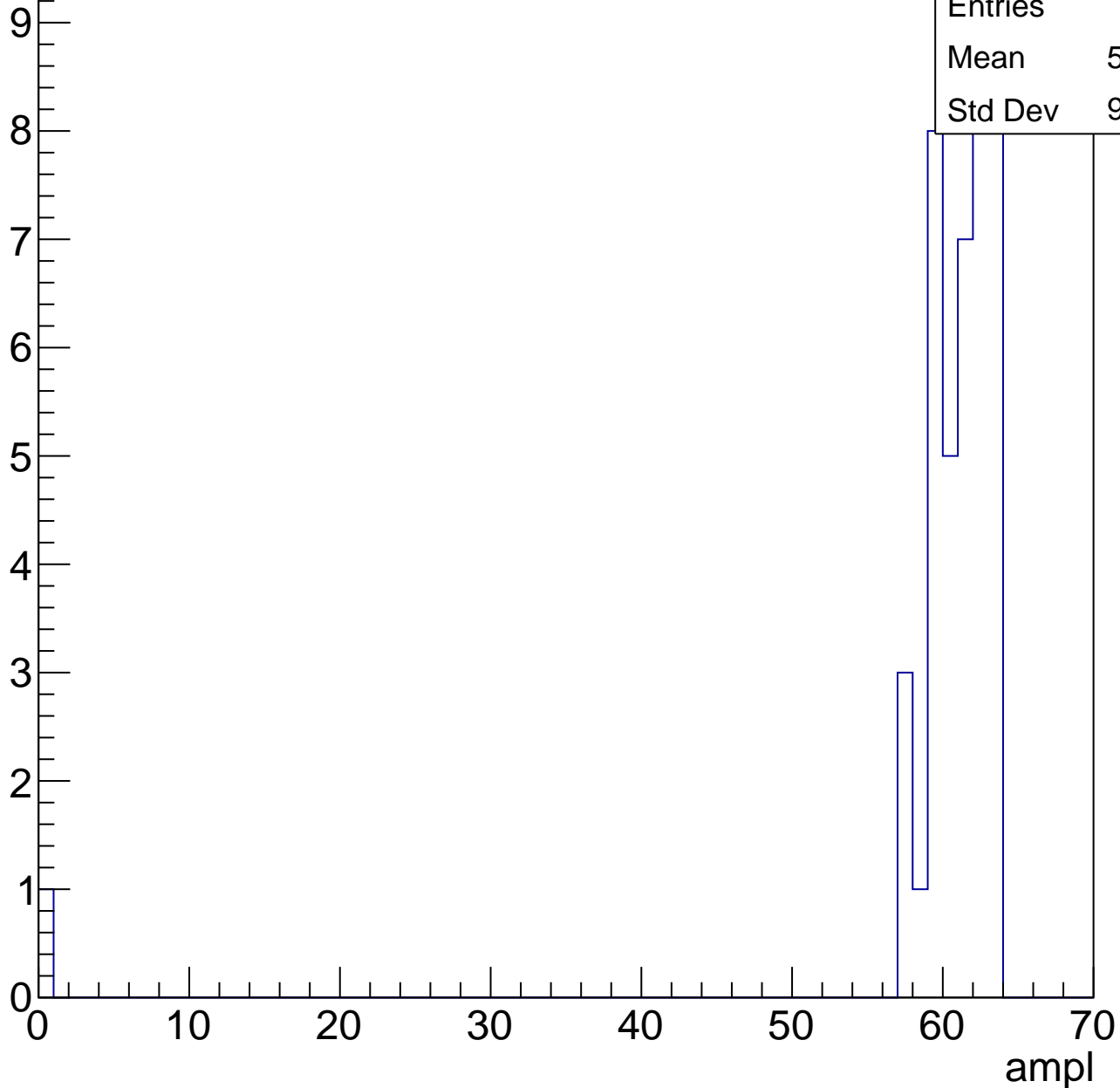
Entry



B1L003S, U21-ch23, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch23, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	62.33
Std Dev	0.9428

B1L003S, U21-ch23, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch24, adc0

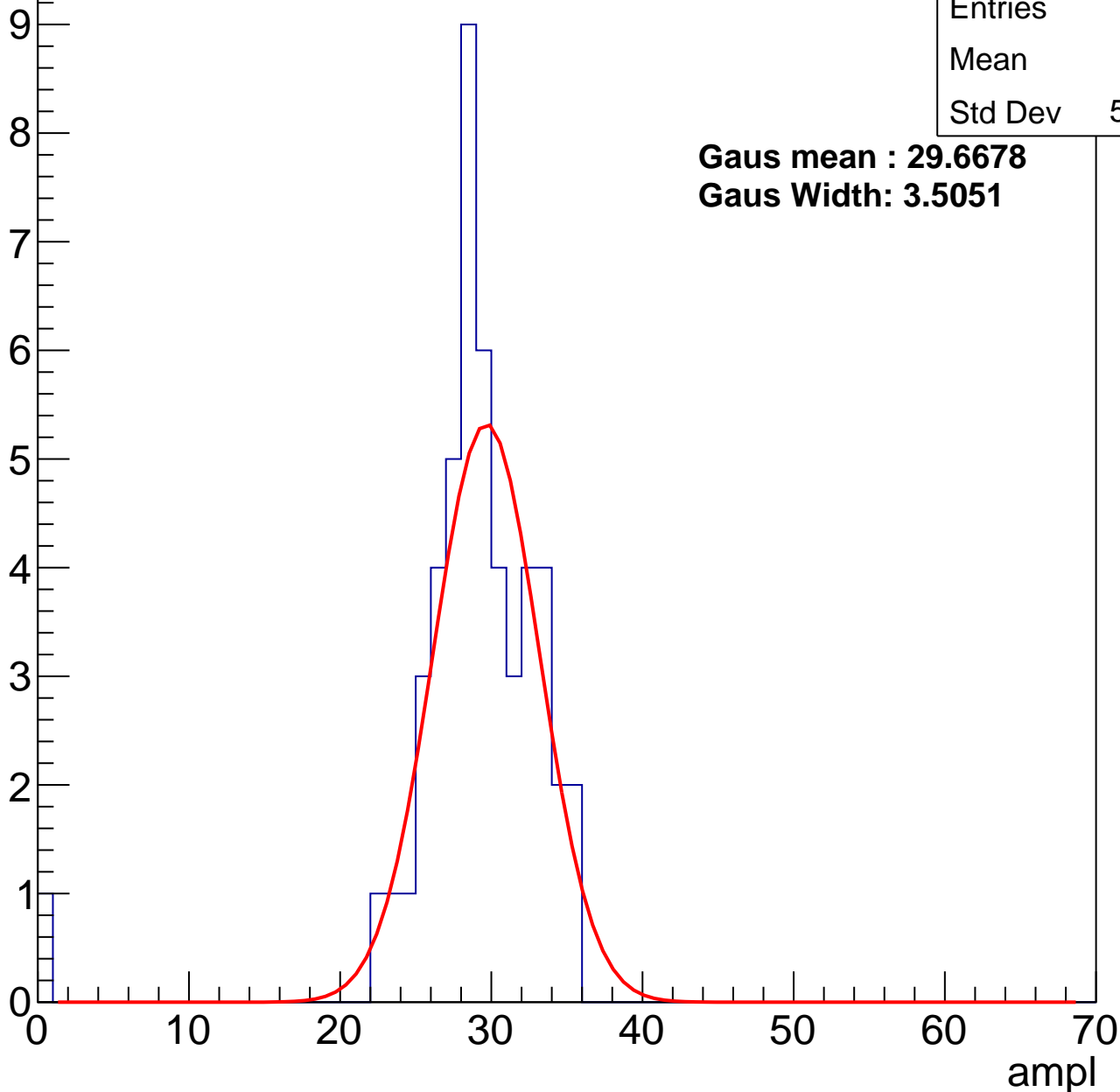
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	28.4
Std Dev	5.068

Gaus mean : 29.6678

Gaus Width: 3.5051



B1L003S, U21-ch24, adc1

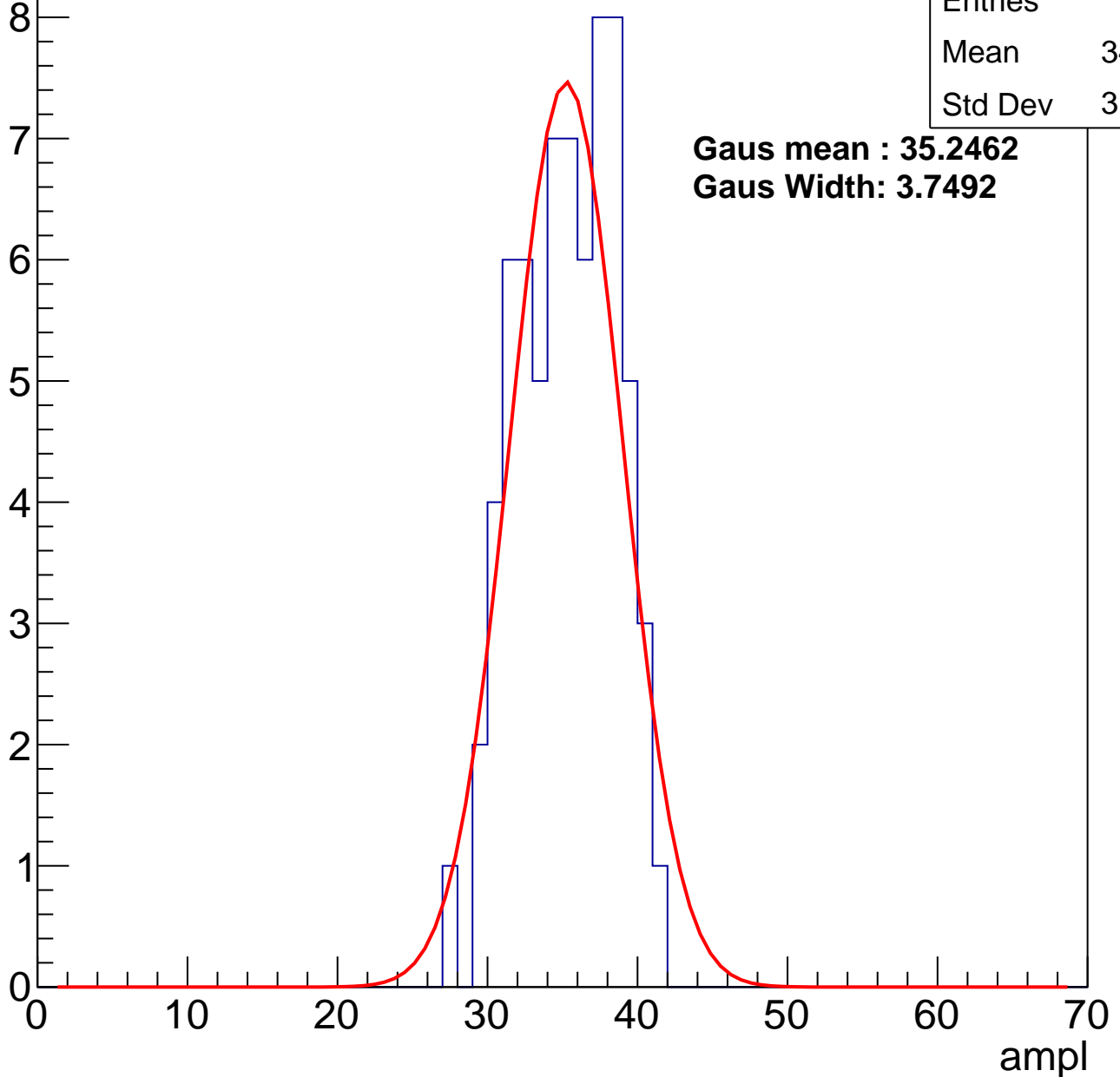
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	34.83
Std Dev	3.212

Gaus mean : 35.2462

Gaus Width: 3.7492



B1L003S, U21-ch24, adc2

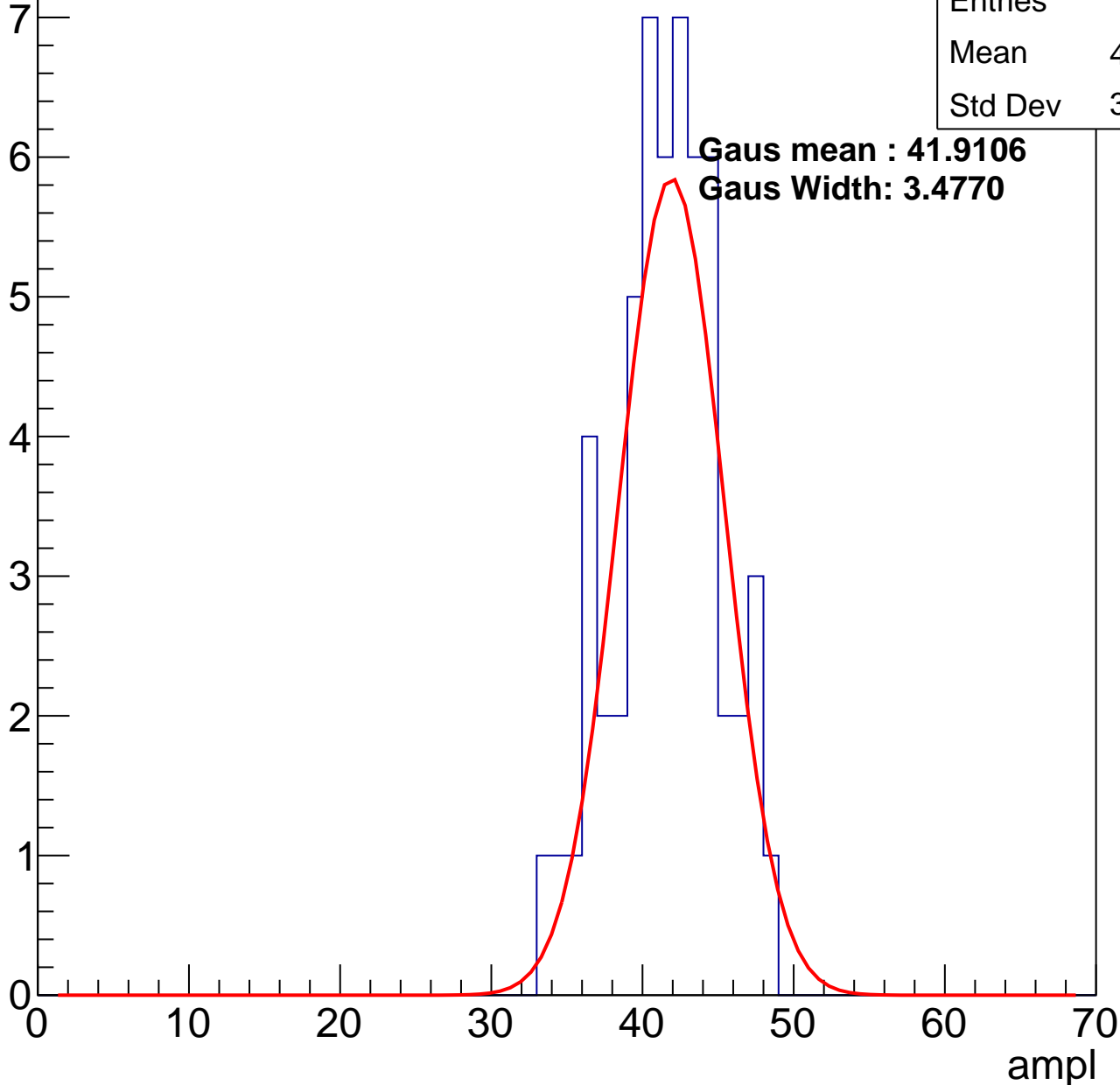
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	41.14
Std Dev	3.404

Gaus mean : 41.9106

Gaus Width: 3.4770



B1L003S, U21-ch24, adc3

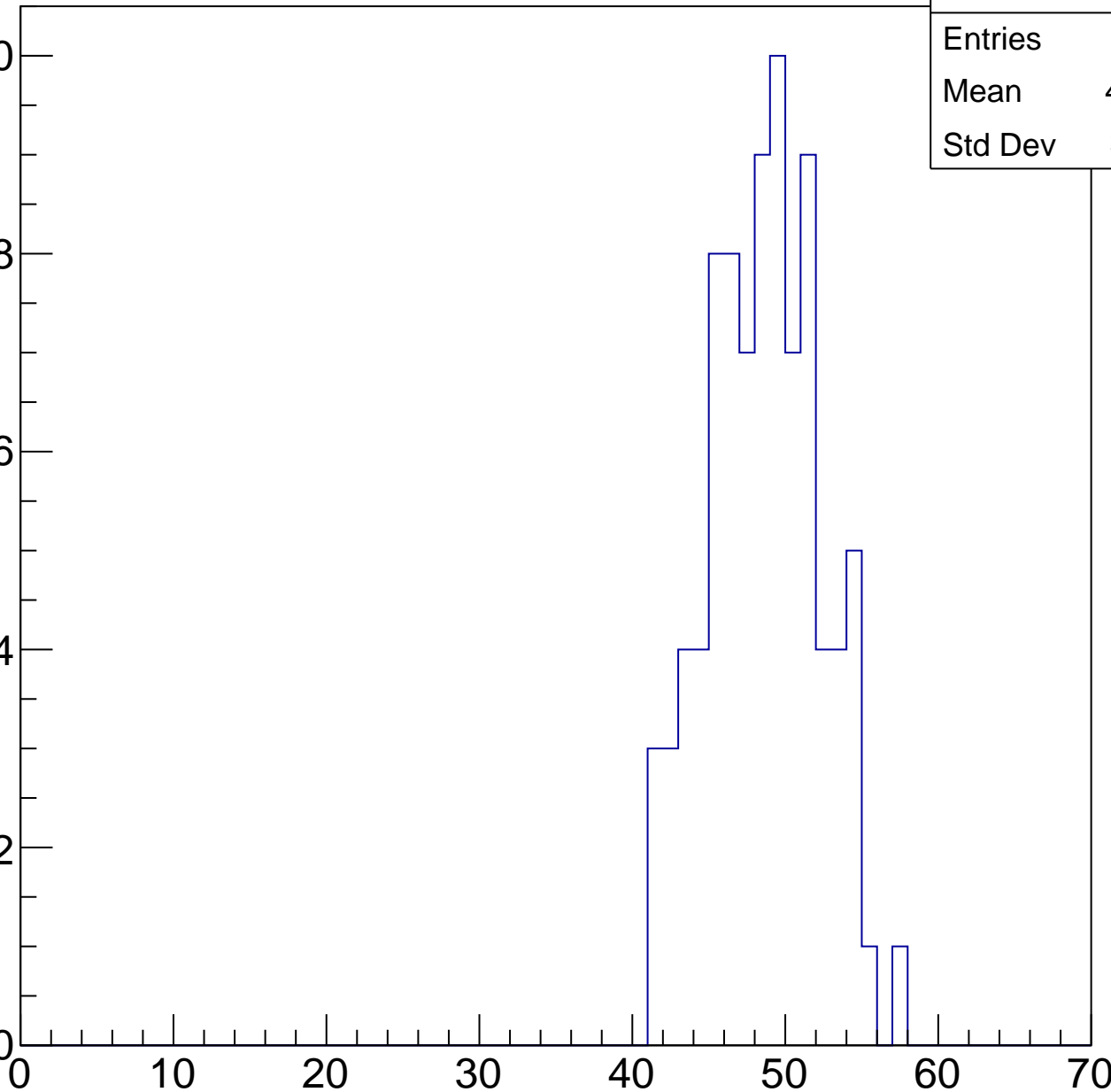
calib_packv5_042523_0143.root, FC#13, port D2

Entry

10
8
6
4
2
0

Entries	87
Mean	48.13
Std Dev	3.591

ampl

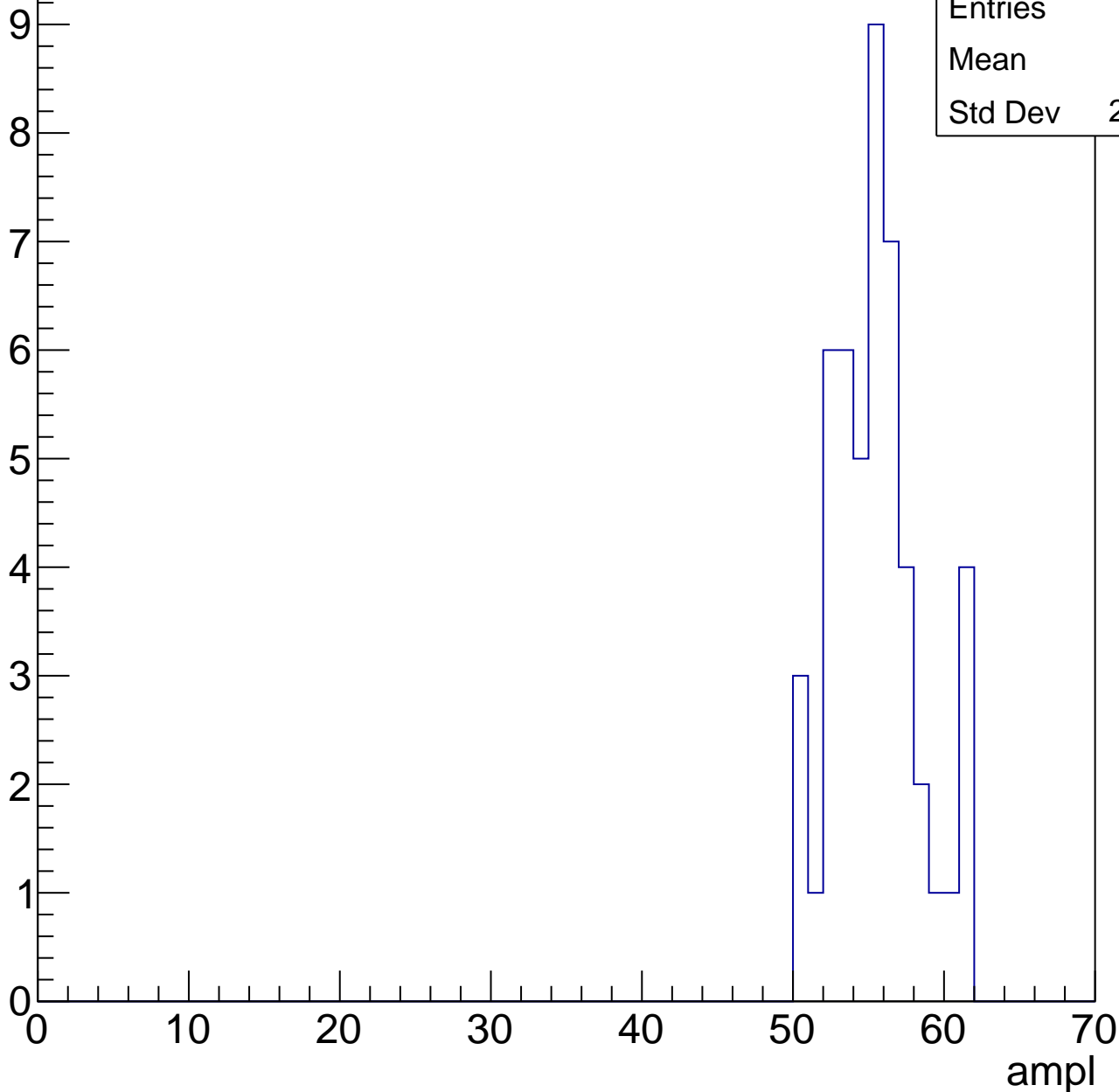


B1L003S, U21-ch24, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	55
Std Dev	2.857

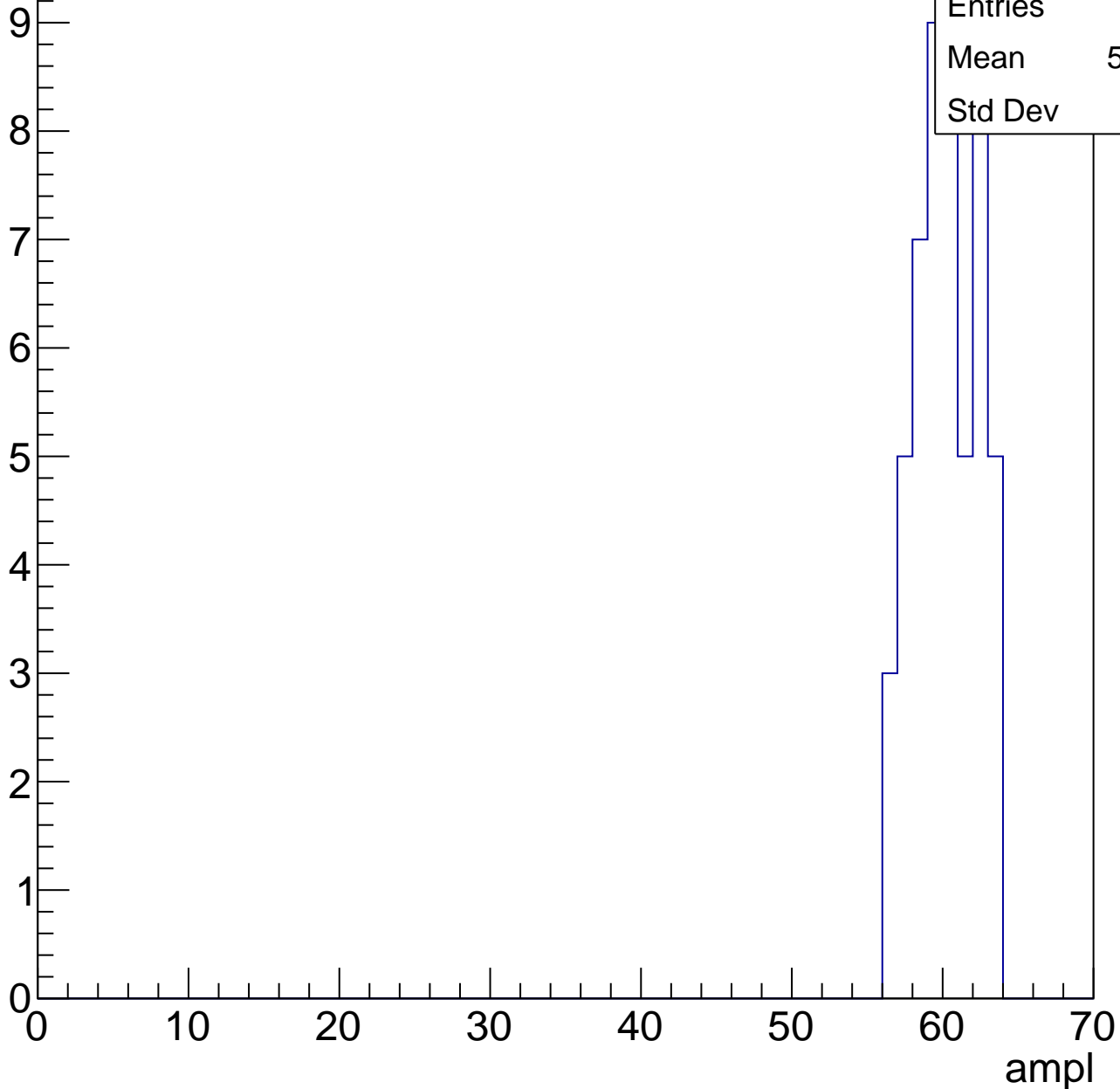


B1L003S, U21-ch24, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	59.72
Std Dev	2.04

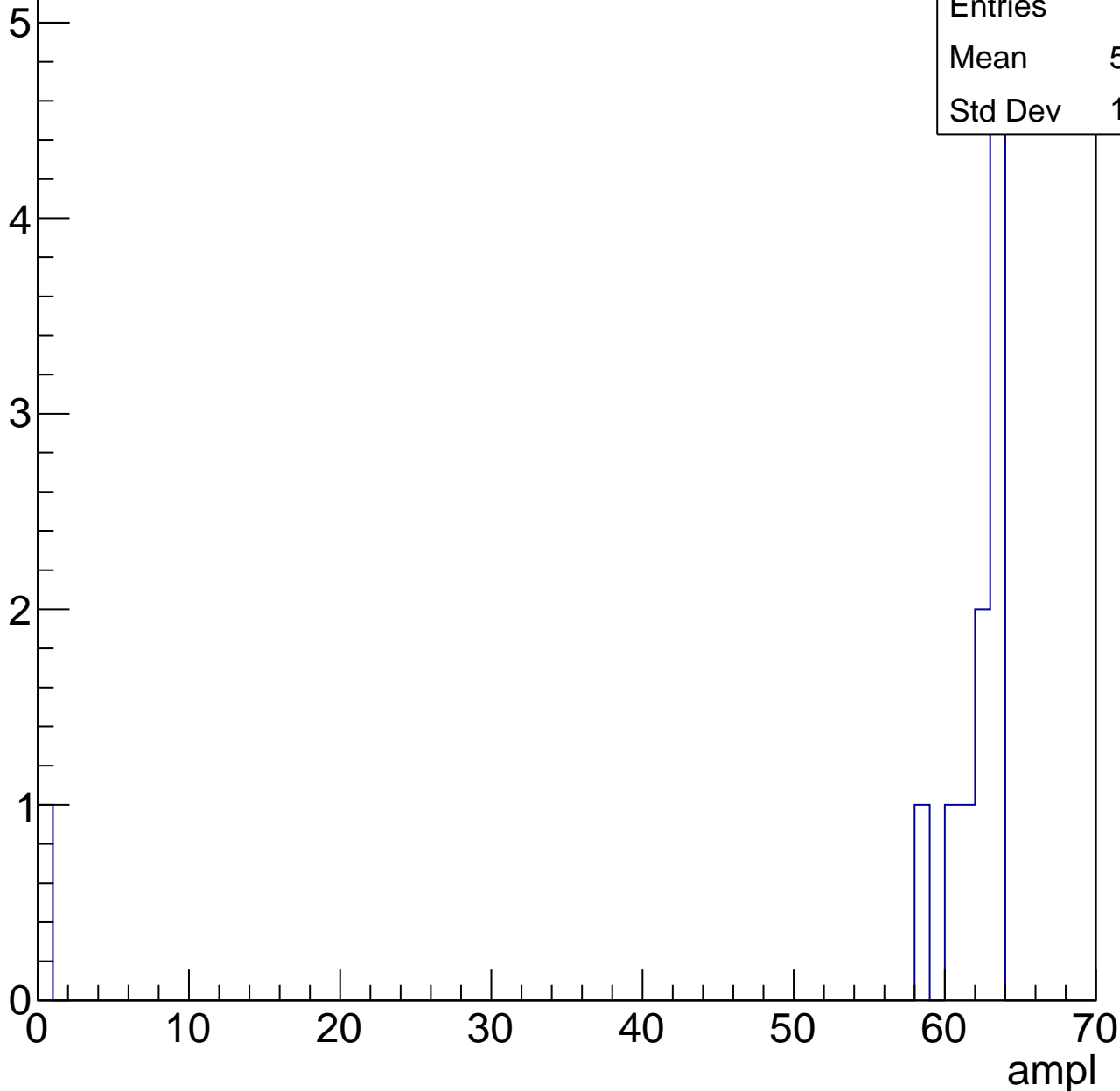


B1L003S, U21-ch24, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.18
Std Dev	17.83



B1L003S, U21-ch24, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch25, adc0

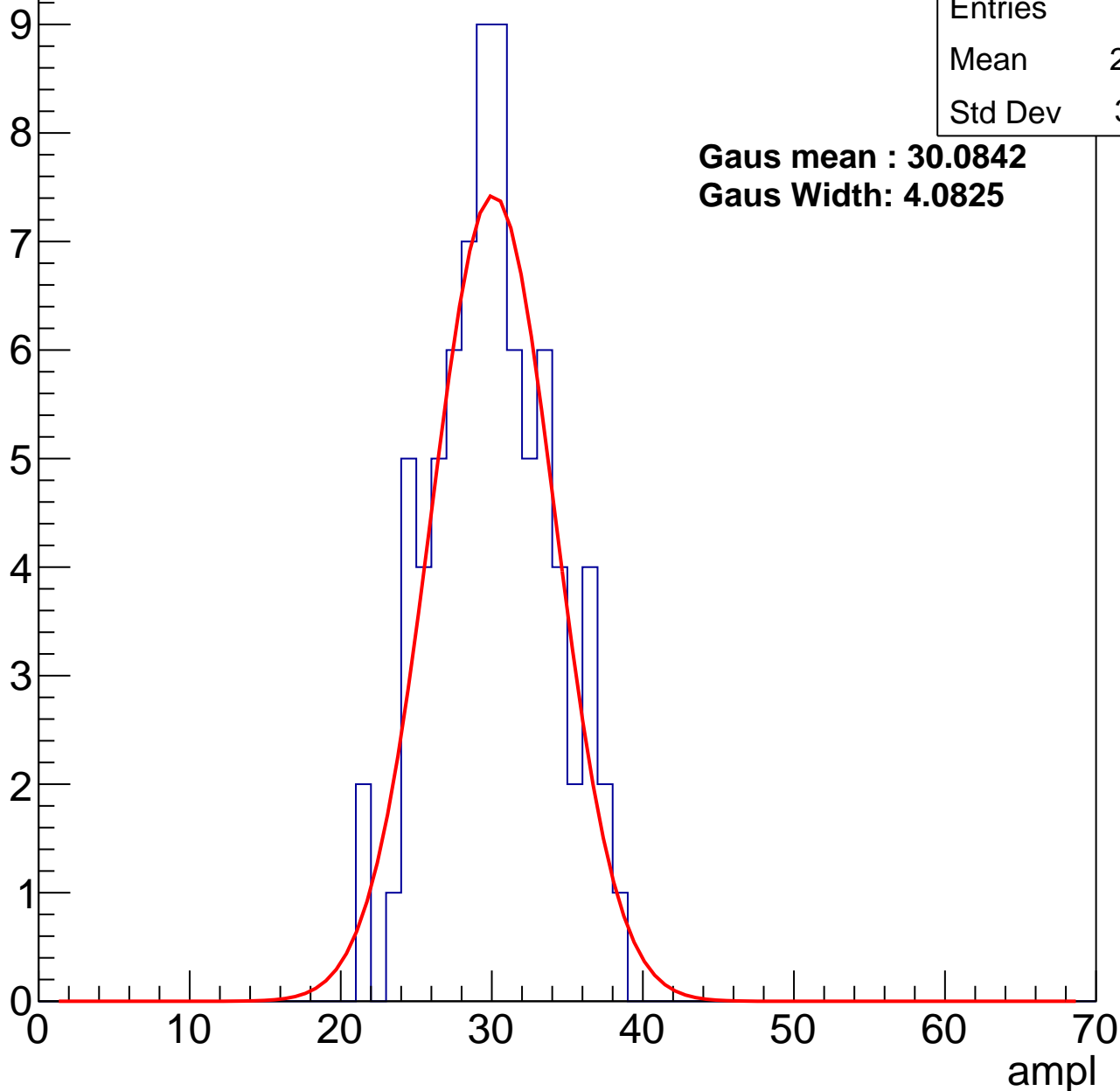
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	29.62
Std Dev	3.841

Gaus mean : 30.0842

Gaus Width: 4.0825



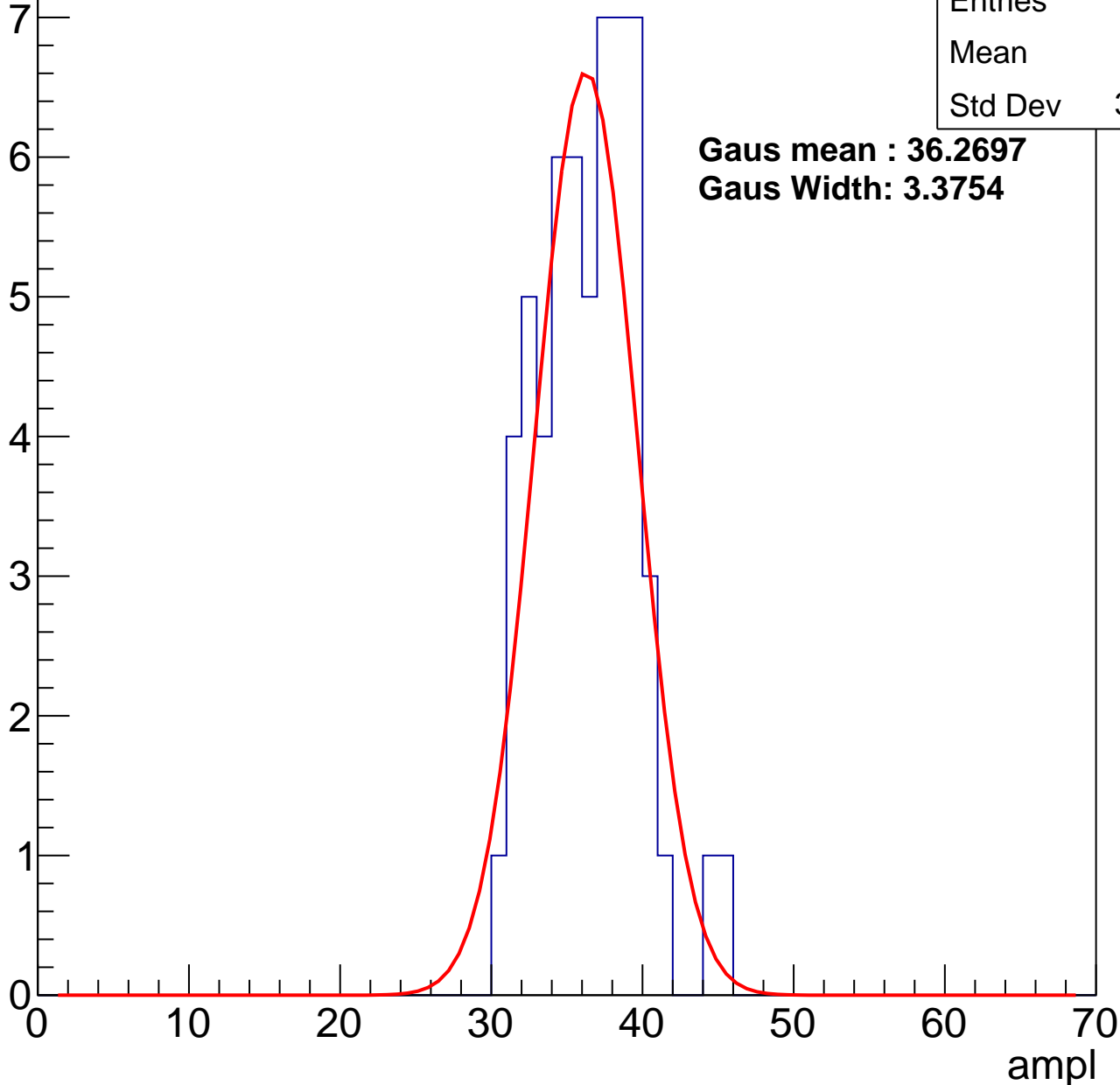
B1L003S, U21-ch25, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	36
Std Dev	3.211

Gaus mean : 36.2697
Gaus Width: 3.3754



B1L003S, U21-ch25, adc2

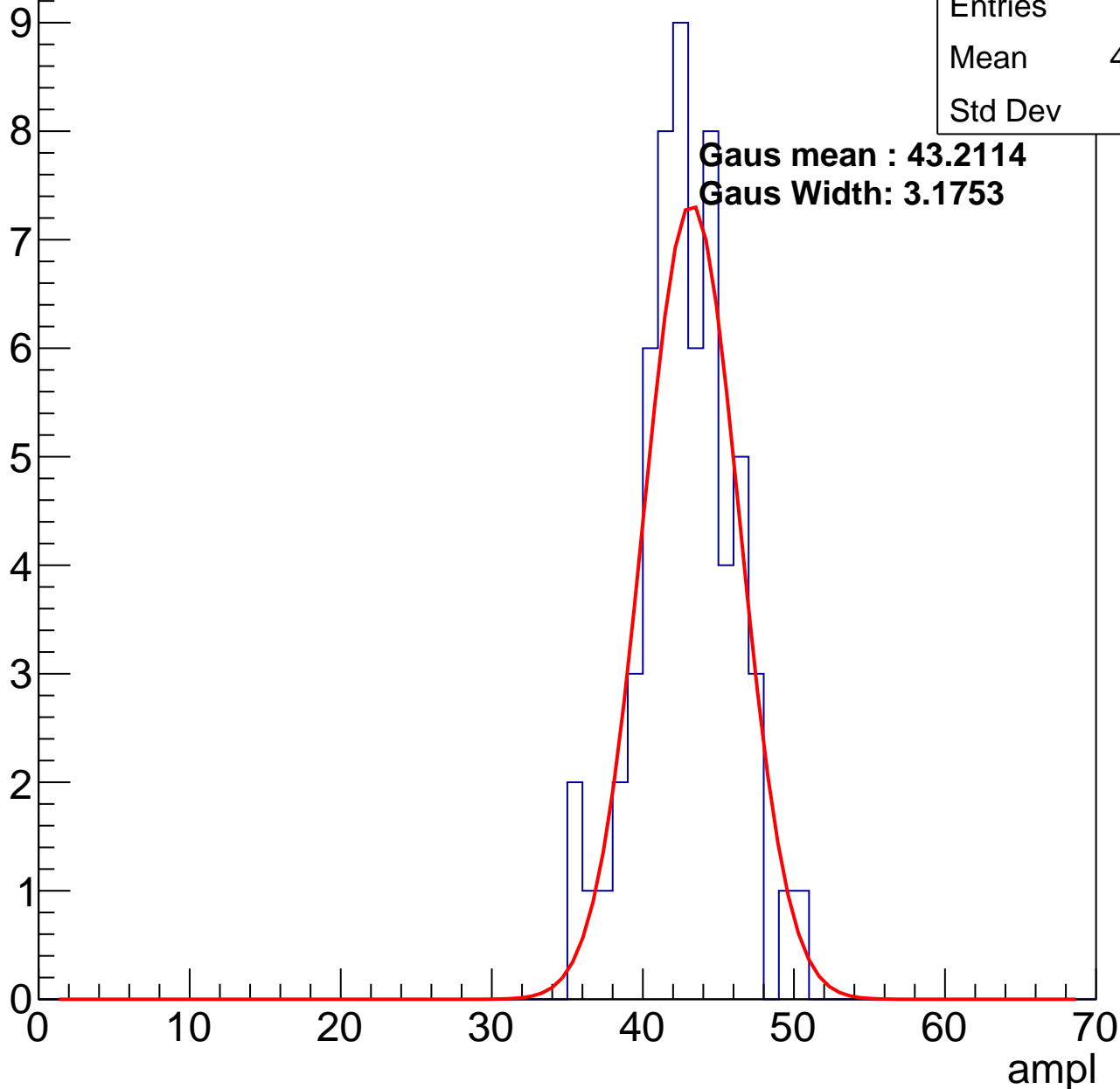
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.37
Std Dev	3.12

Gaus mean : 43.2114

Gaus Width: 3.1753



B1L003S, U21-ch25, adc3

calib_packv5_042523_0143.root, FC#13, port D2

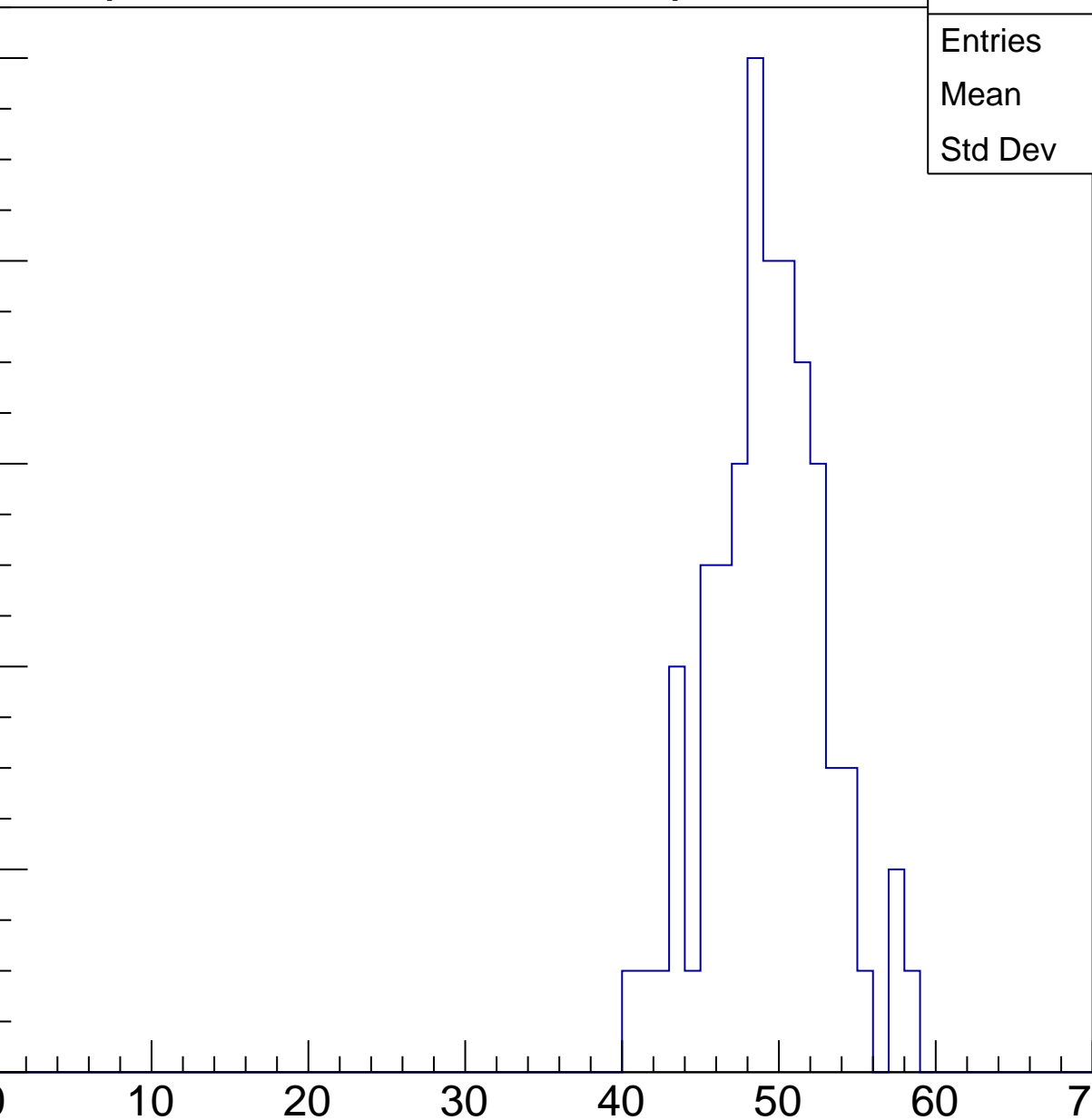
Entries	73
Mean	48.84
Std Dev	3.66

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

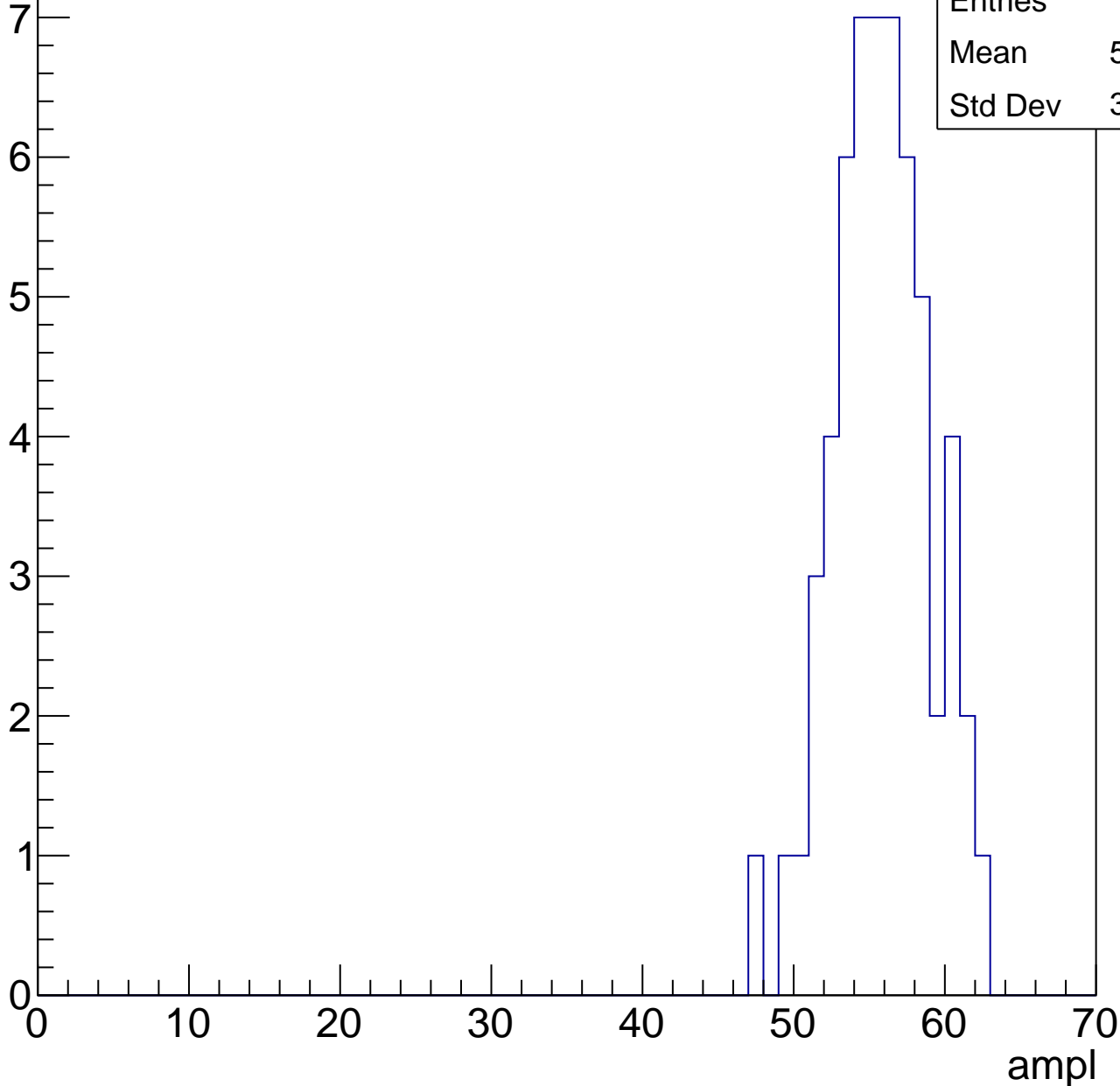


B1L003S, U21-ch25, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

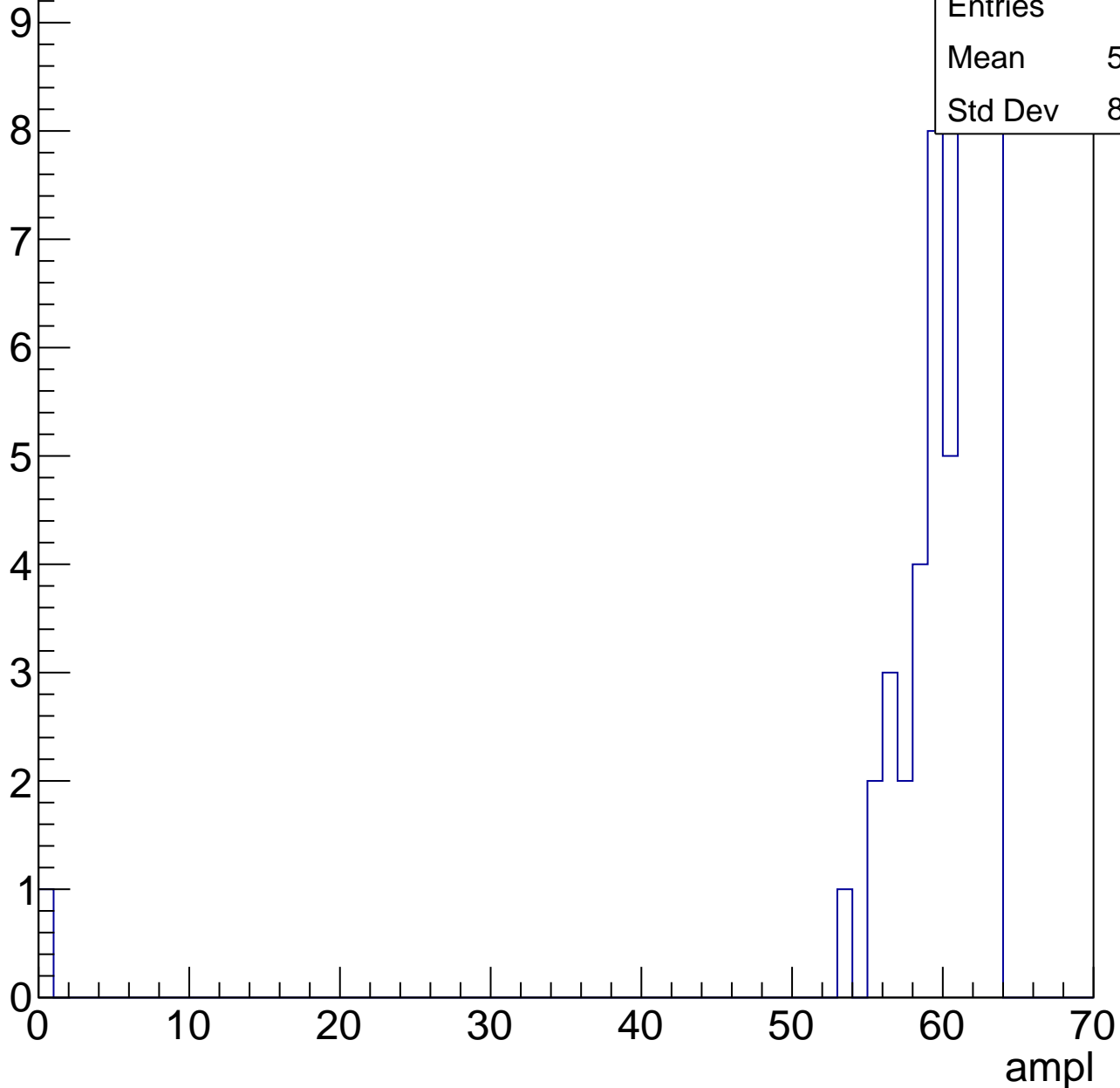
Entries	57
Mean	55.33
Std Dev	3.142



B1L003S, U21-ch25, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch25, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch25, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch26, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	93
Mean	28.73
Std Dev	3.968

Gaus mean : 28.7210

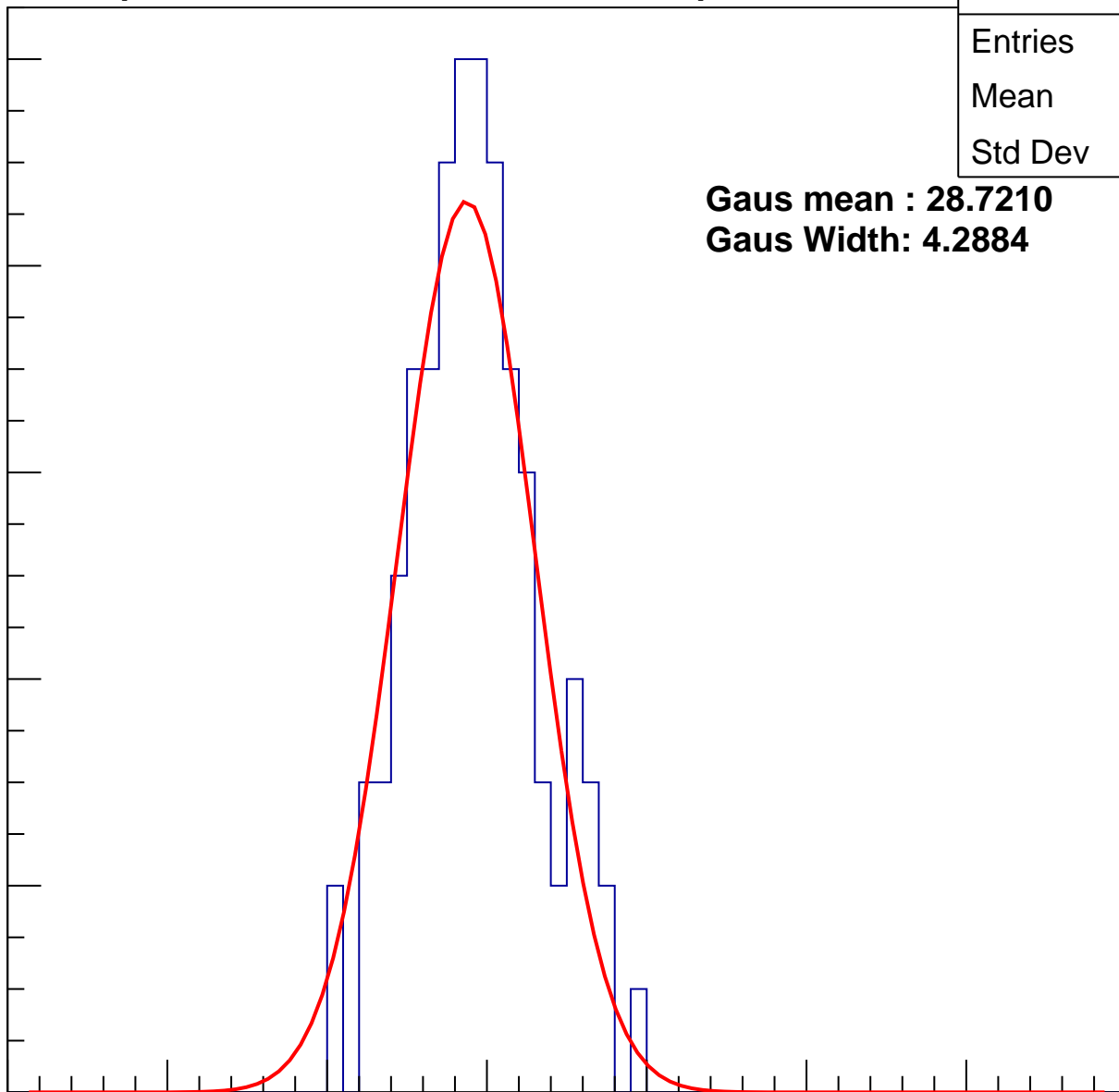
Gaus Width: 4.2884

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch26, adc1

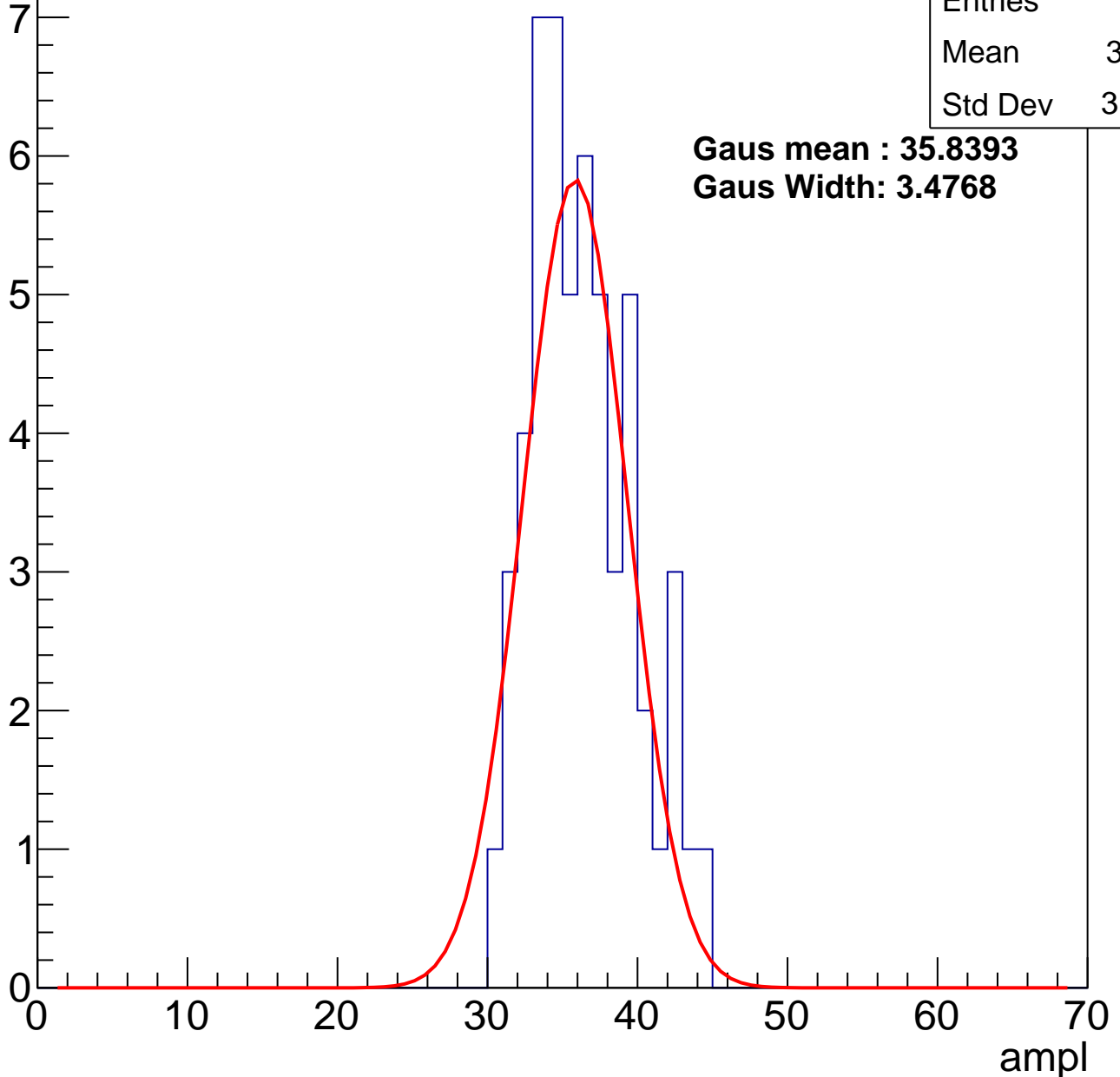
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	35.91
Std Dev	3.362

Gaus mean : 35.8393

Gaus Width: 3.4768



B1L003S, U21-ch26, adc2

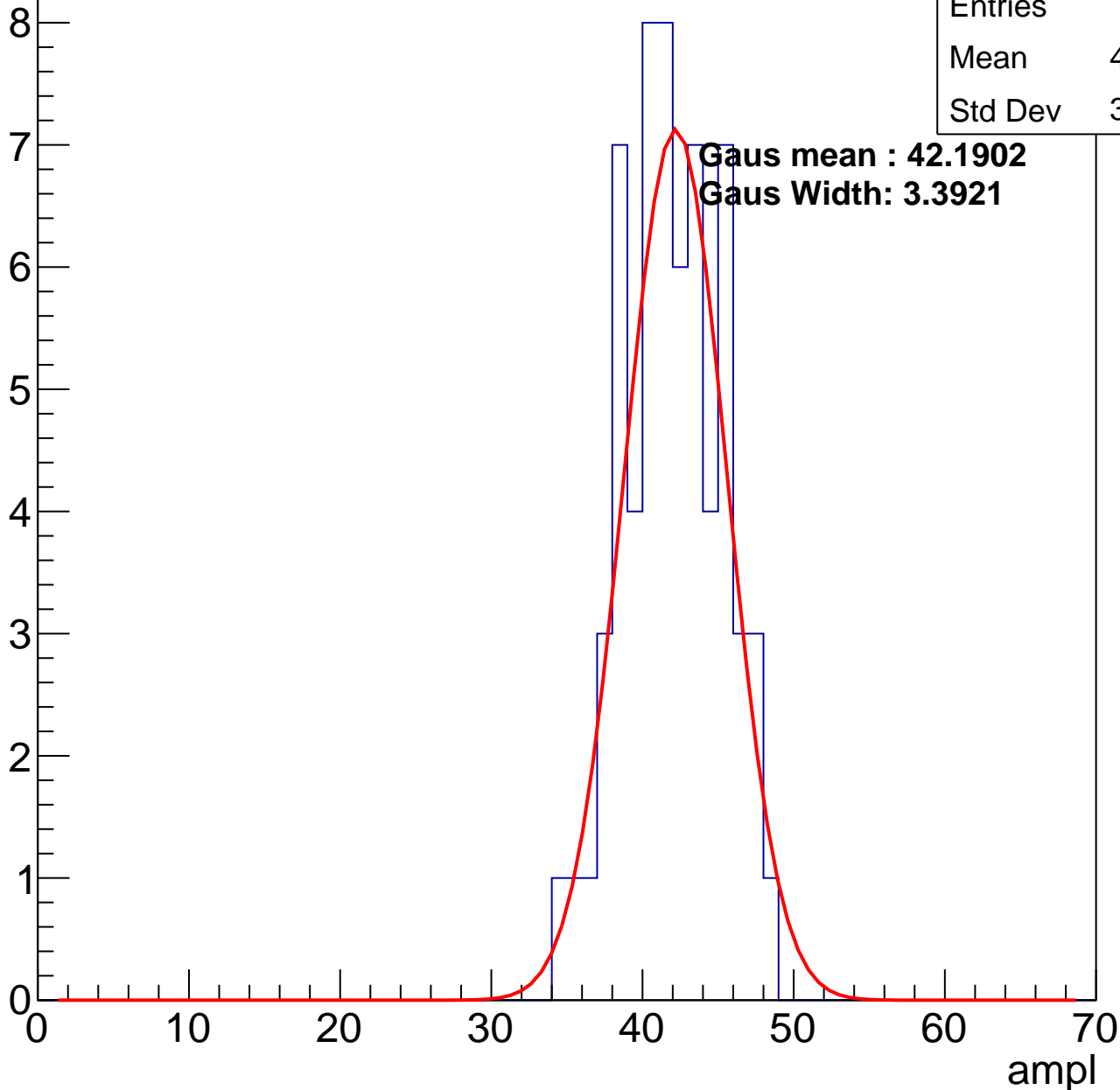
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	41.52
Std Dev	3.167

Gaus mean : 42.1902

Gaus Width: 3.3921

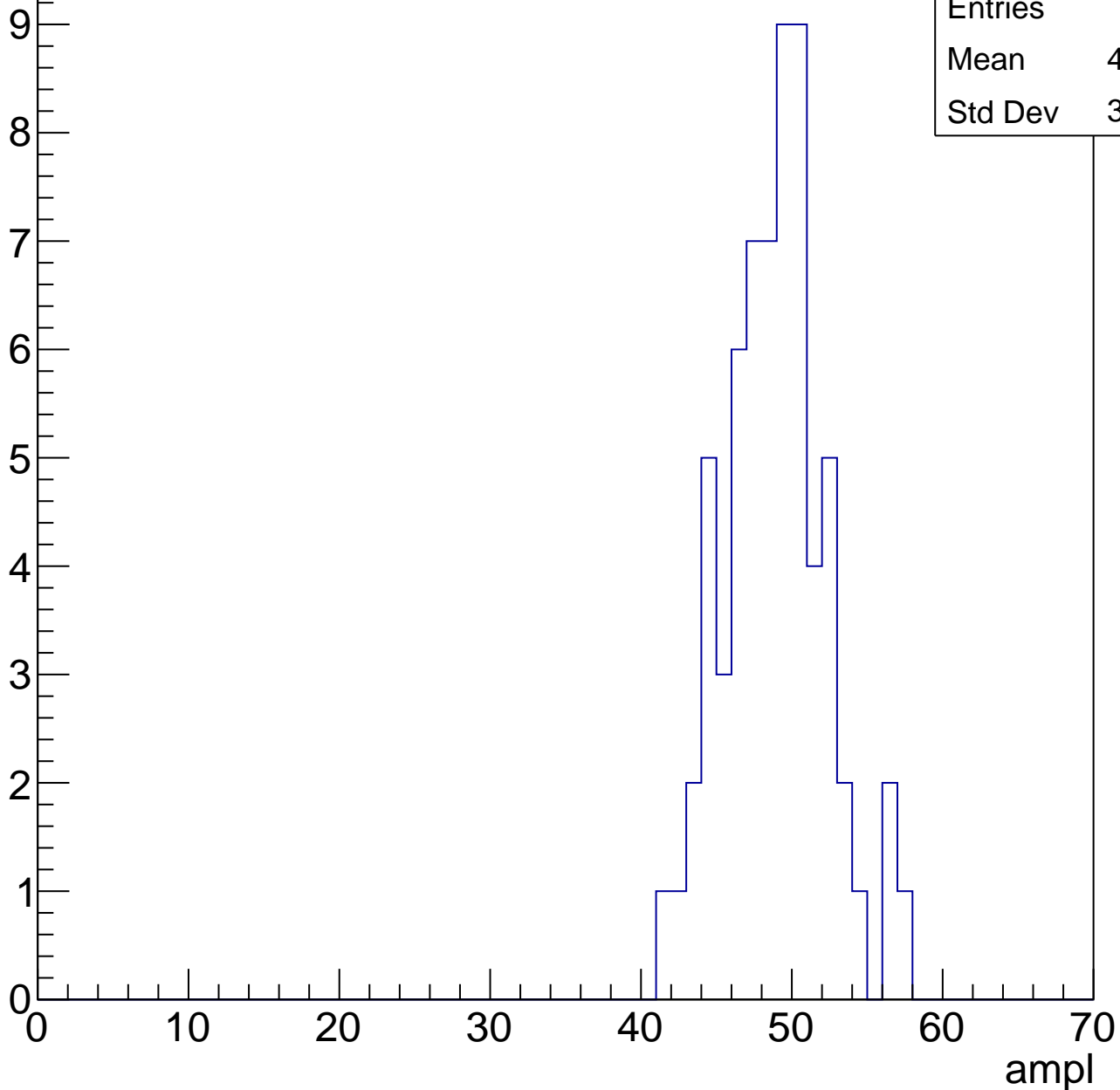


B1L003S, U21-ch26, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	48.45
Std Dev	3.319

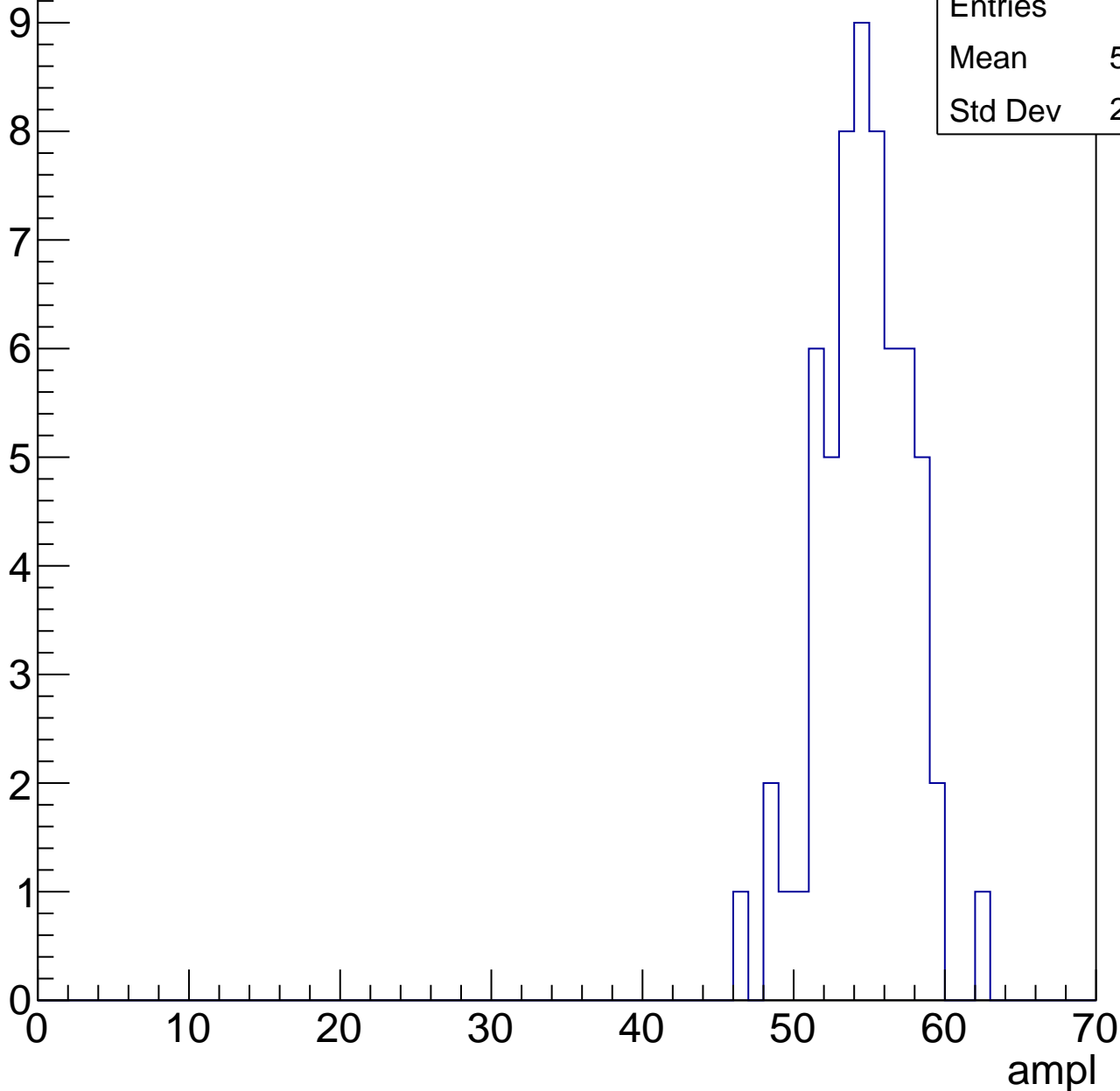


B1L003S, U21-ch26, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	54.18
Std Dev	2.967



B1L003S, U21-ch26, adc5

calib_packv5_042523_0143.root, FC#13, port D2

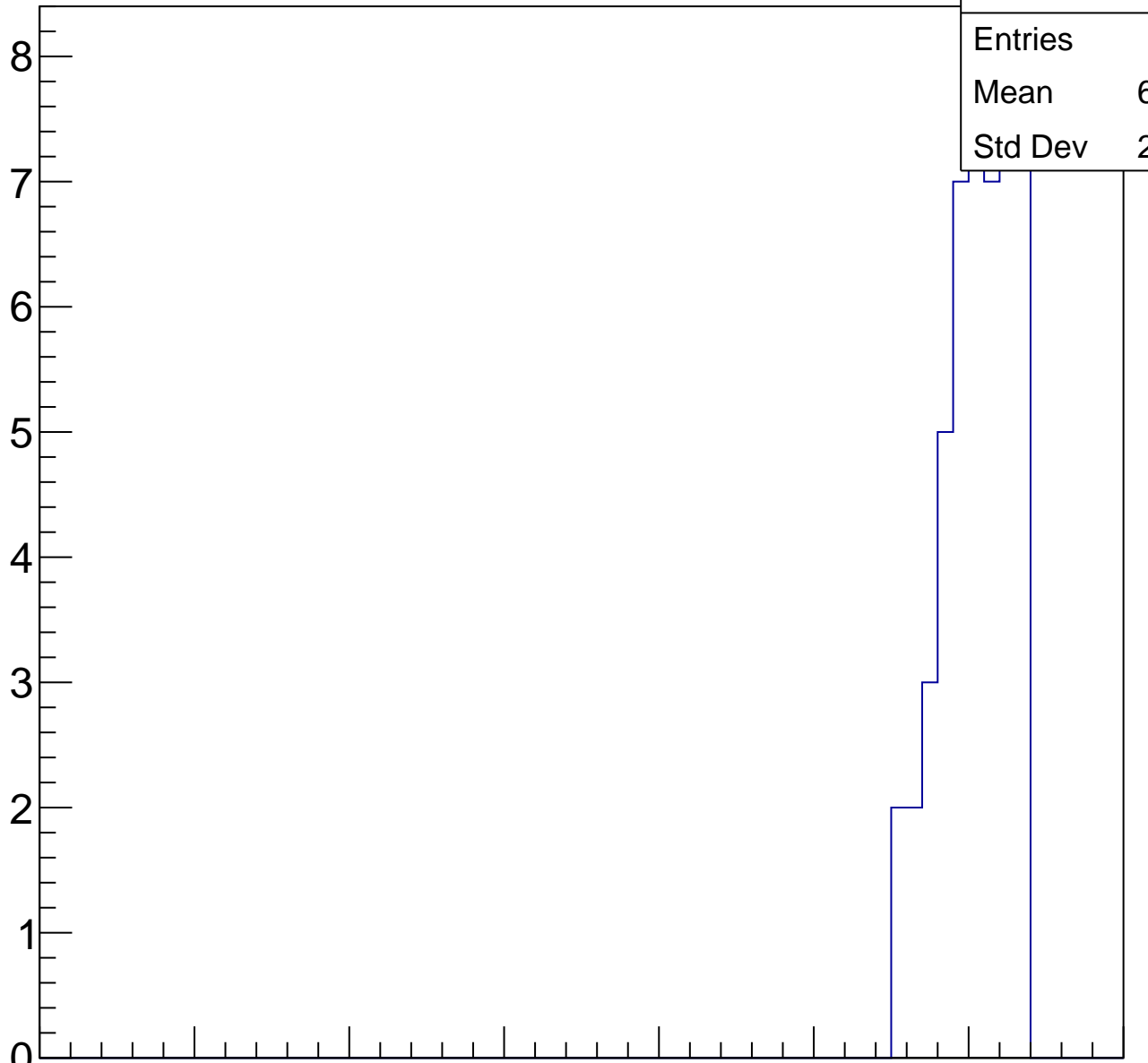
Entry

8
7
6
5
4
3
2
1
0

Entries	50
Mean	60.06
Std Dev	2.222

ampl

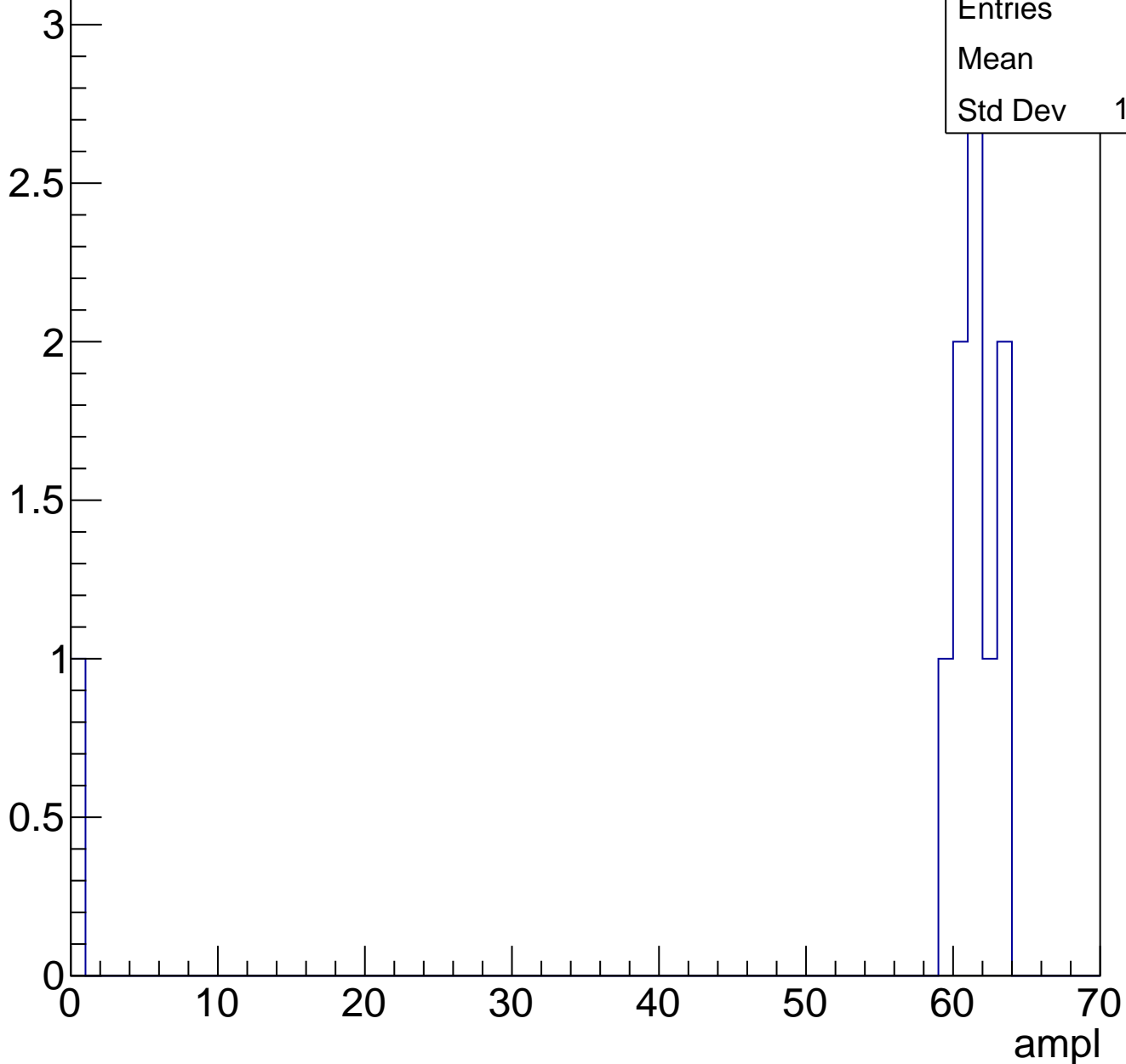
0 10 20 30 40 50 60 70



B1L003S, U21-ch26, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch26, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch27, adc0

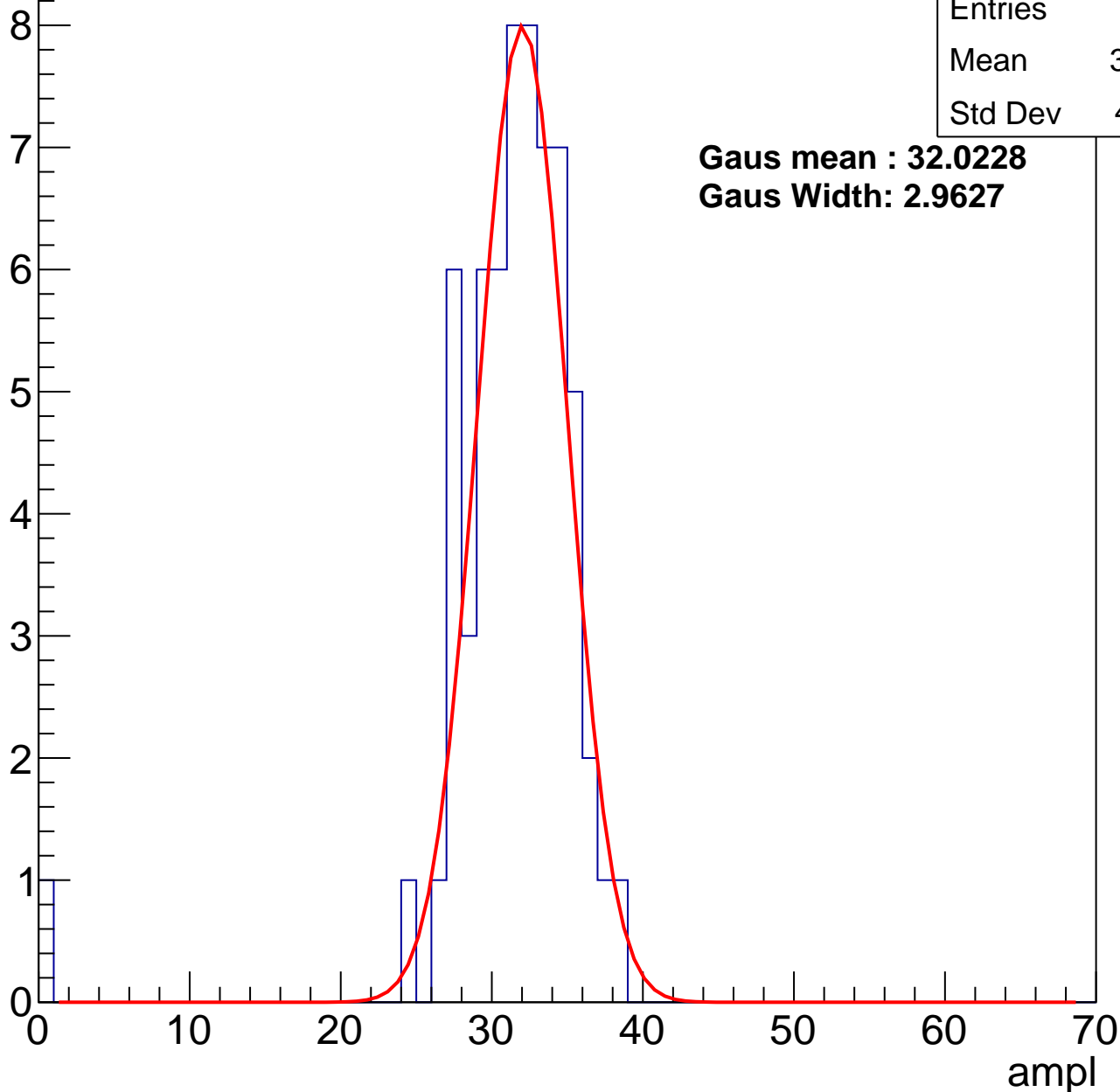
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	30.87
Std Dev	4.881

Gaus mean : 32.0228

Gaus Width: 2.9627



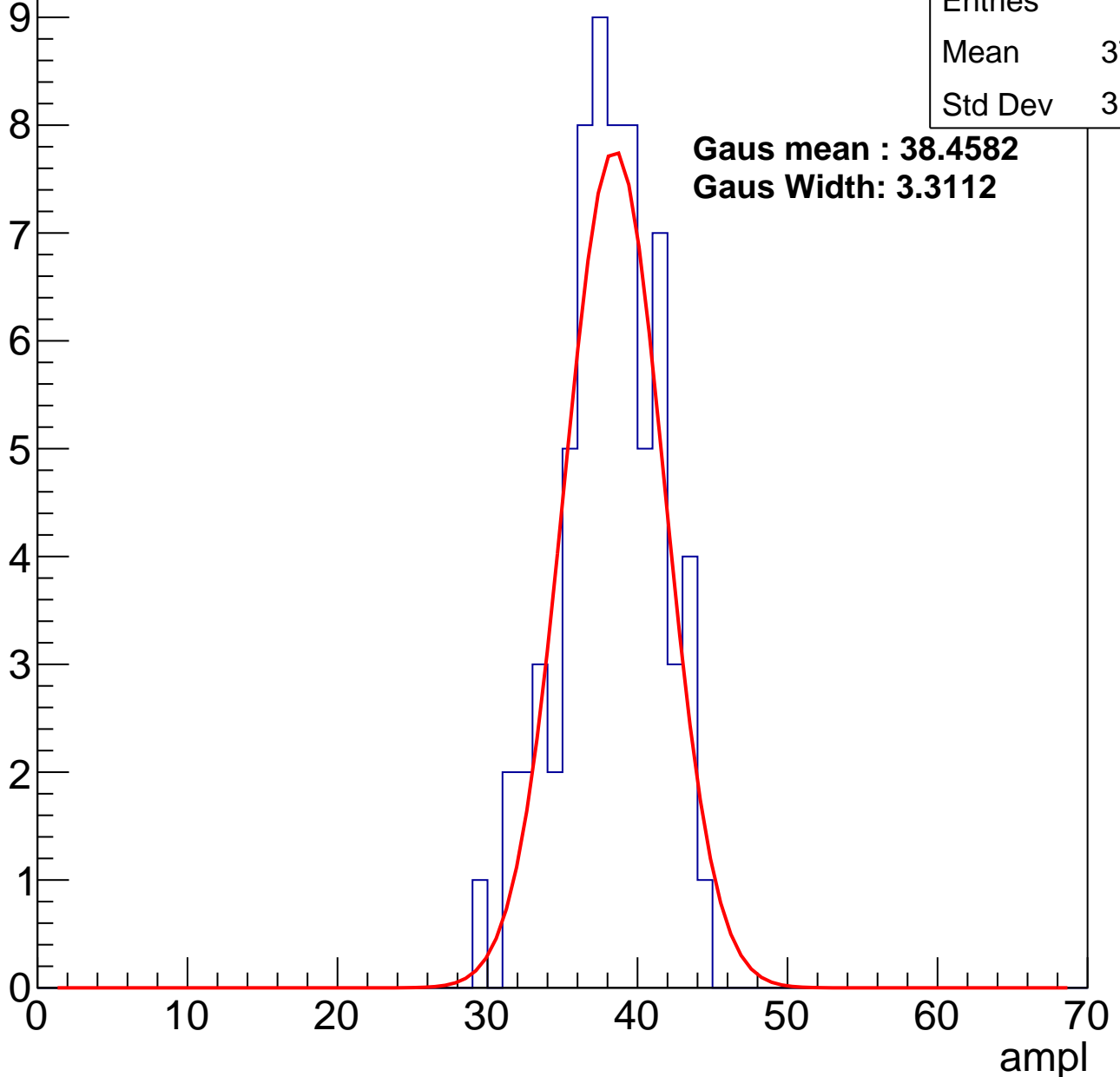
B1L003S, U21-ch27, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.69
Std Dev	3.237

Gaus mean : 38.4582
Gaus Width: 3.3112



B1L003S, U21-ch27, adc2

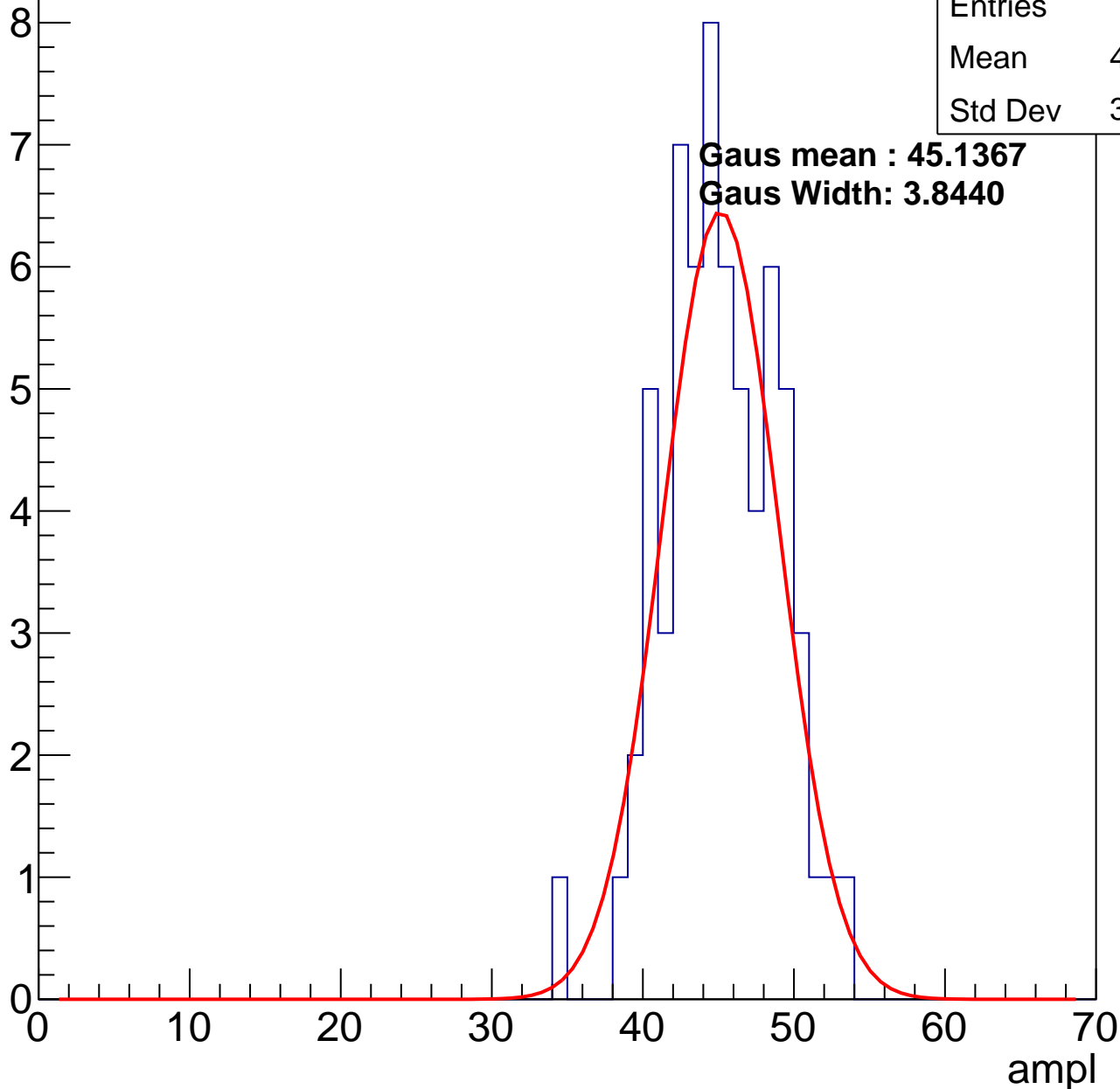
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	44.68
Std Dev	3.684

Gaus mean : 45.1367

Gaus Width: 3.8440

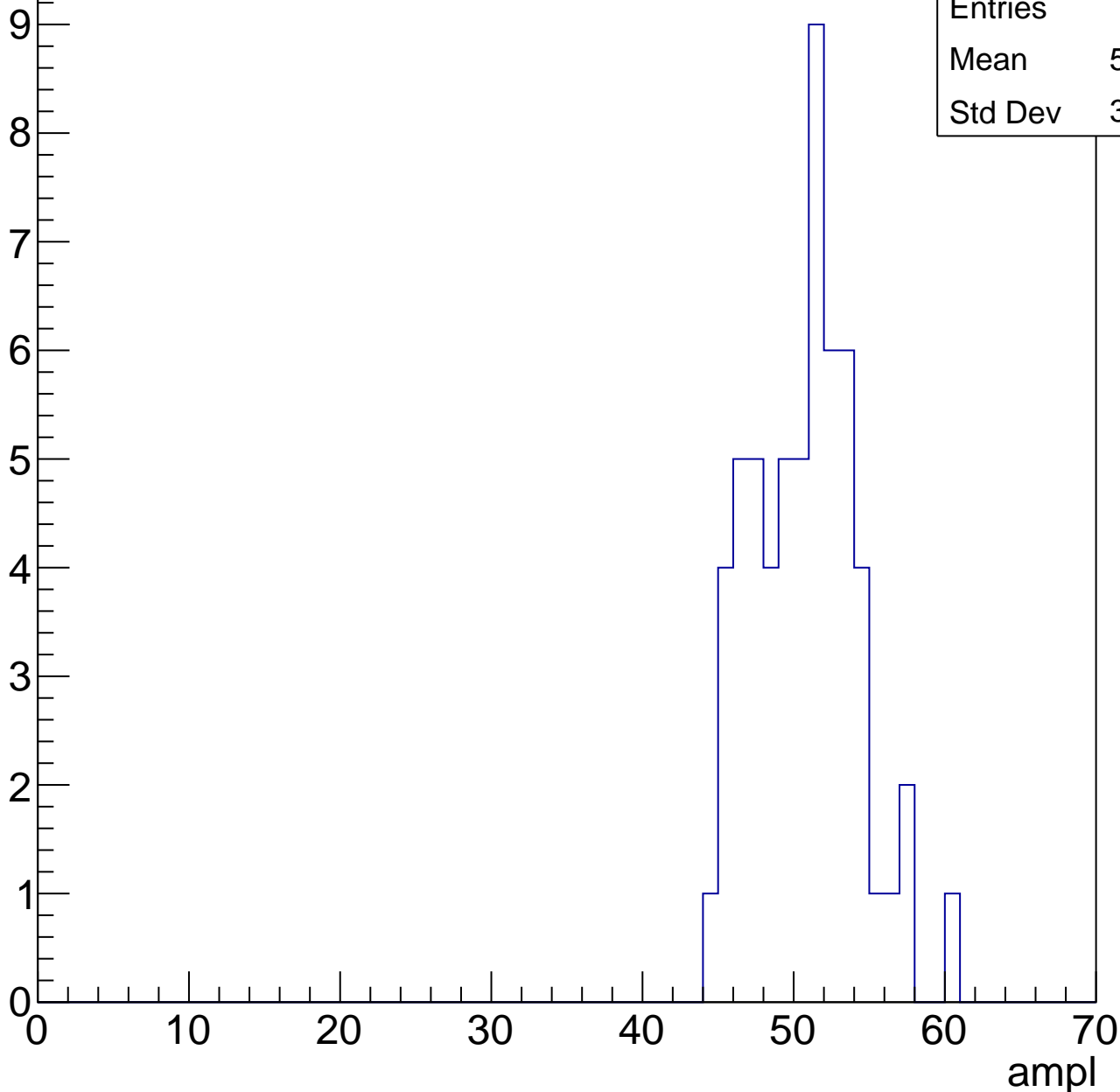


B1L003S, U21-ch27, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	50.27
Std Dev	3.414

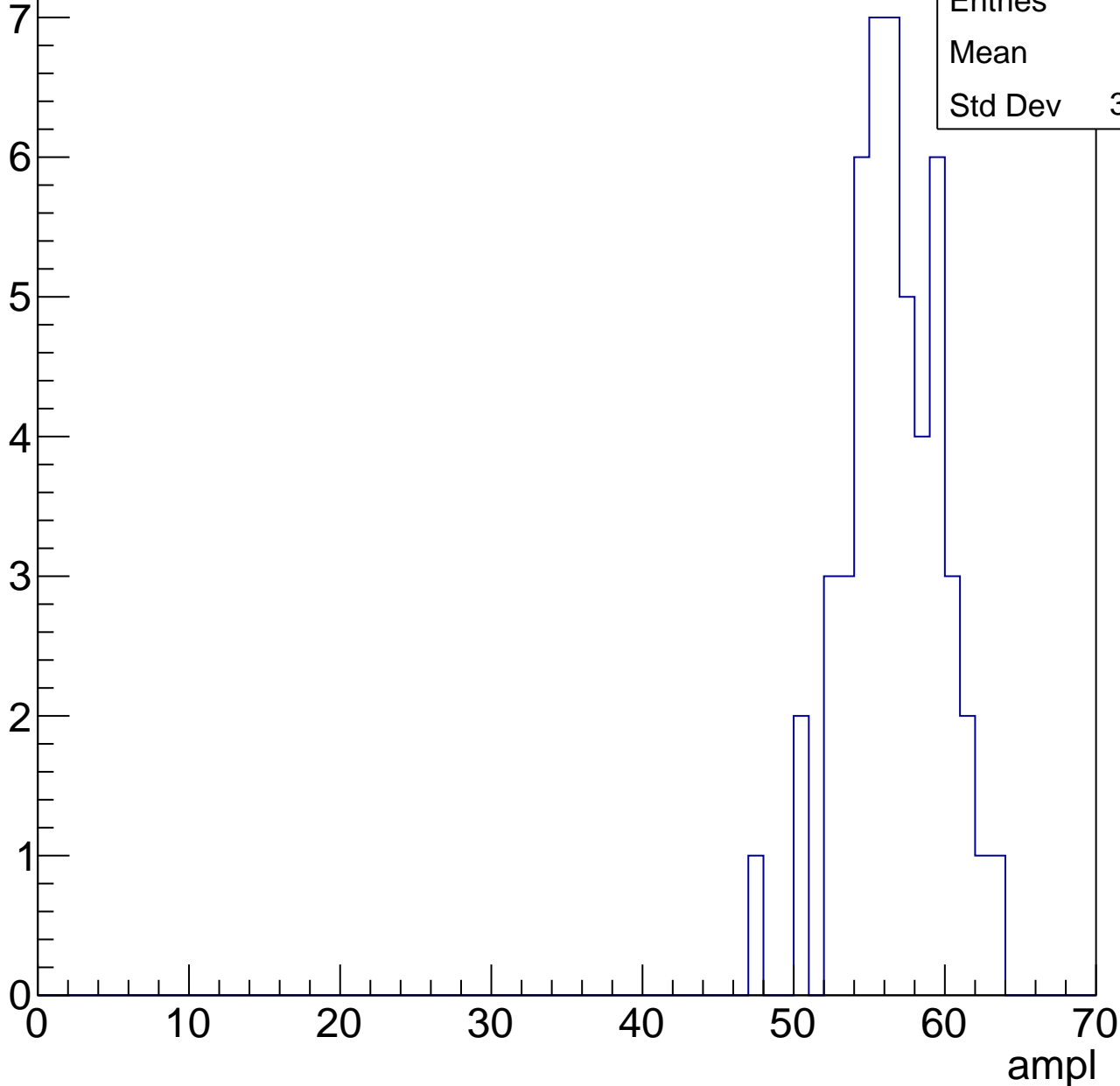


B1L003S, U21-ch27, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.1
Std Dev	3.182

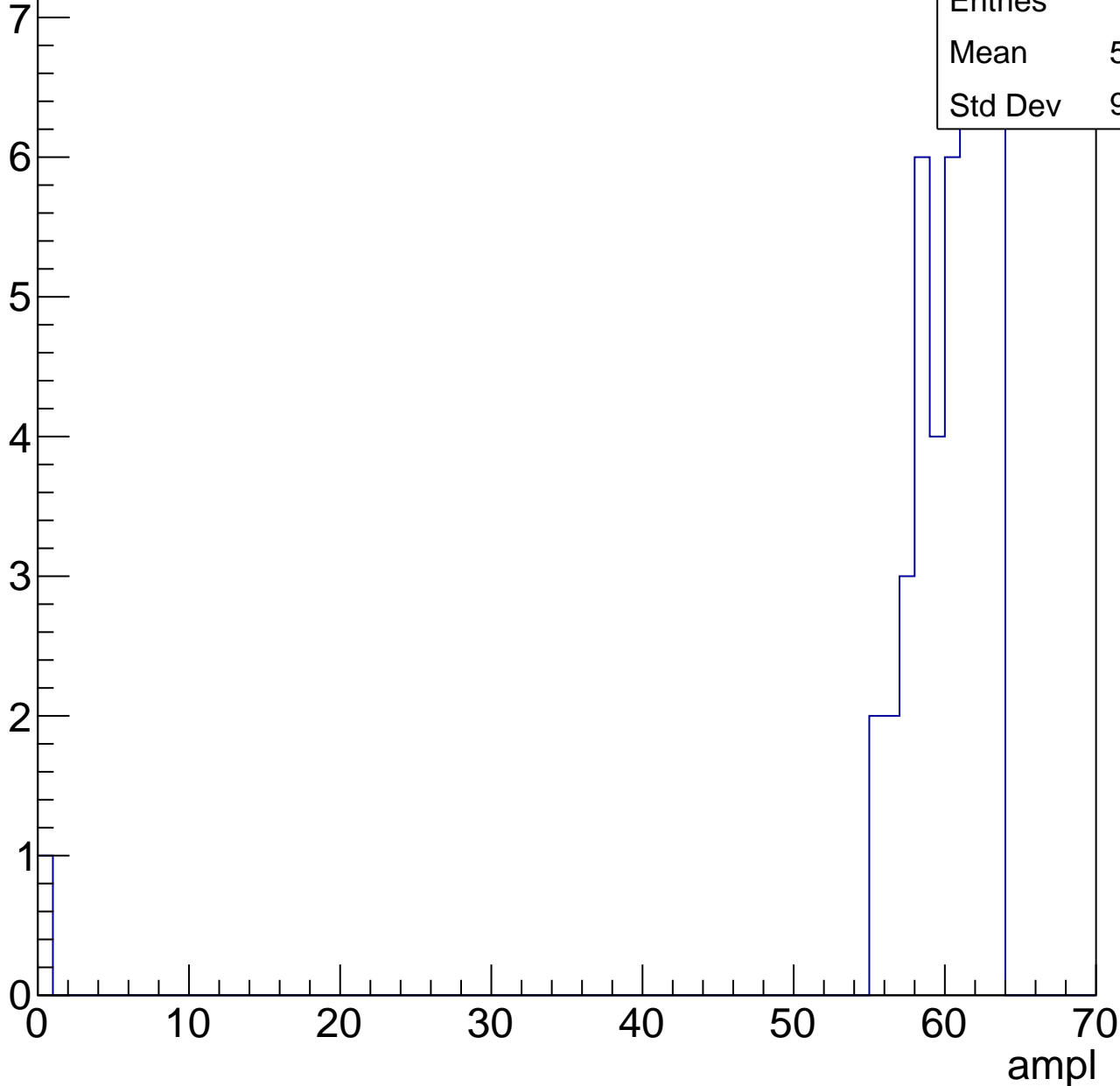


B1L003S, U21-ch27, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	58.64
Std Dev	9.132



B1L003S, U21-ch27, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch27, adc7

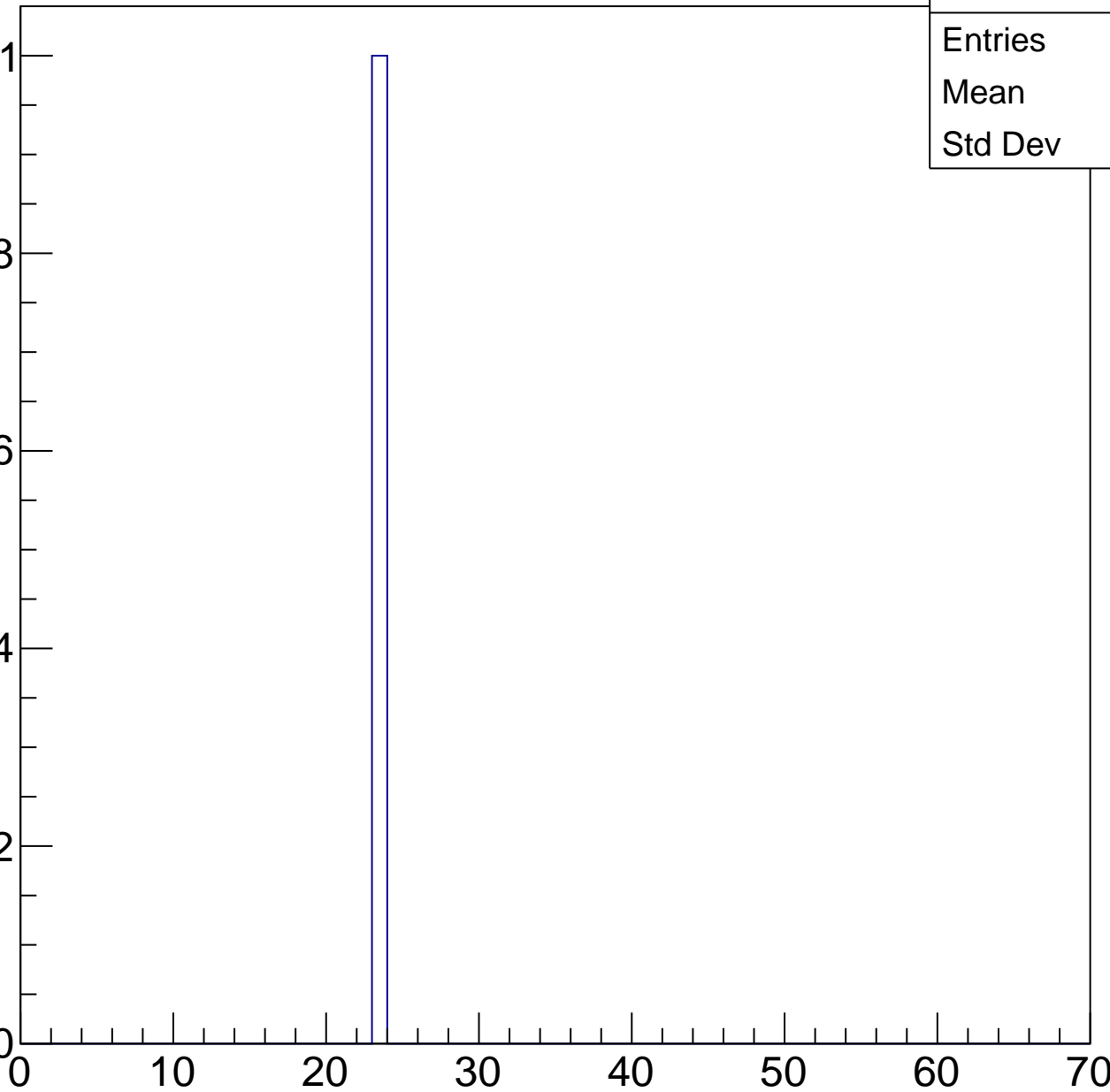
calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl



B1L003S, U21-ch28, adc0

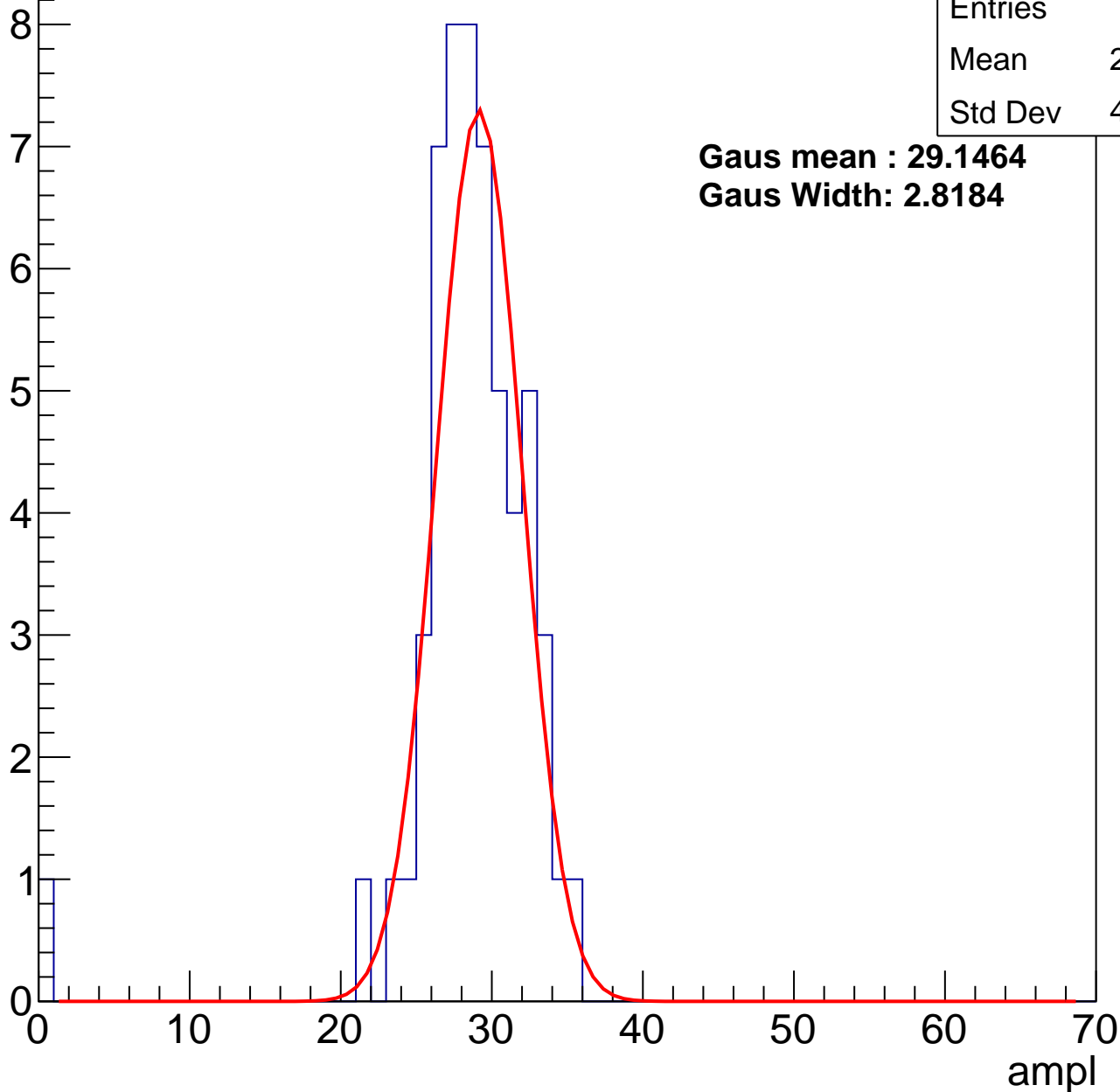
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	28.04
Std Dev	4.705

Gaus mean : 29.1464

Gaus Width: 2.8184



B1L003S, U21-ch28, adc1

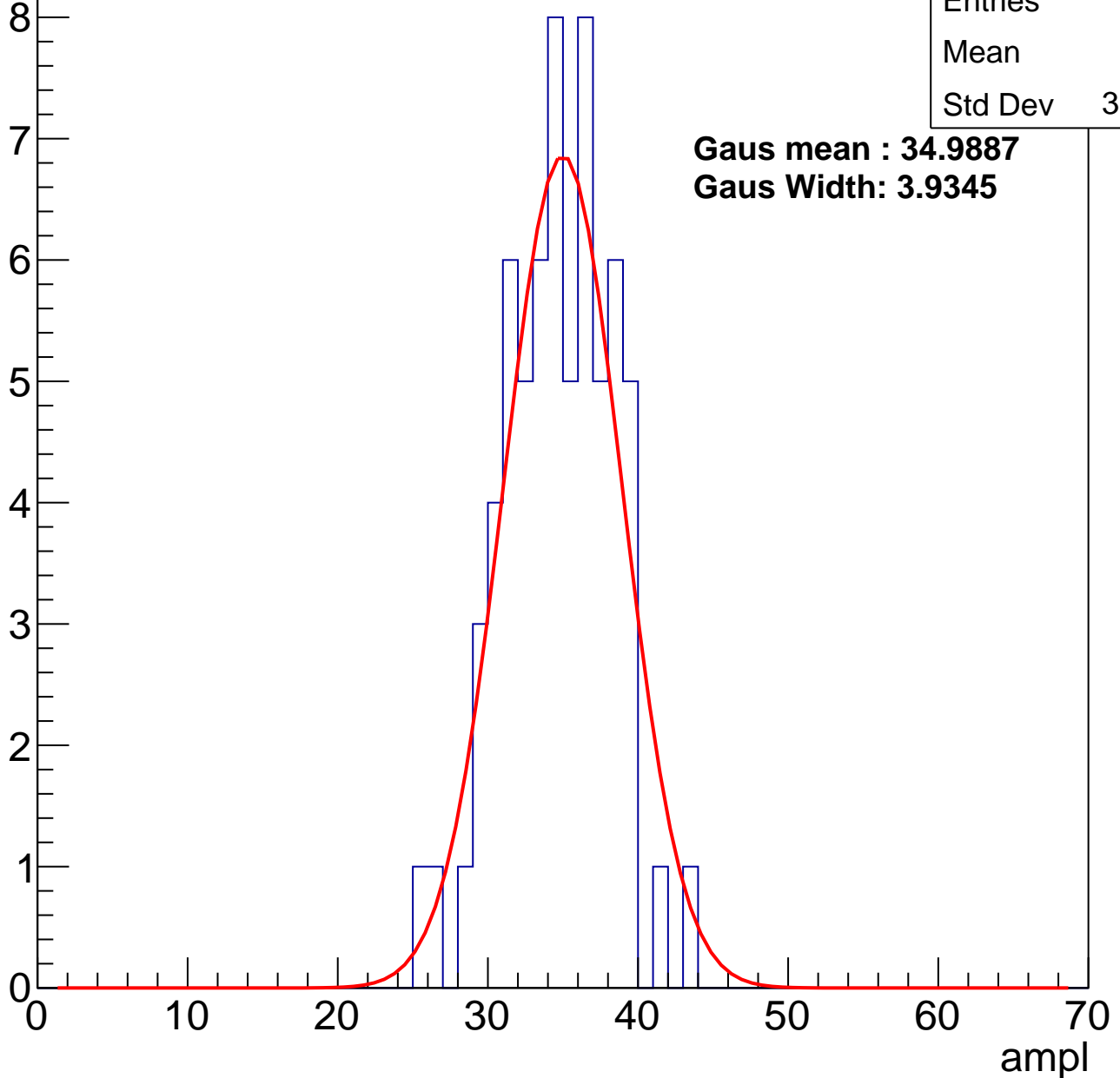
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.2
Std Dev	3.547

Gaus mean : 34.9887

Gaus Width: 3.9345



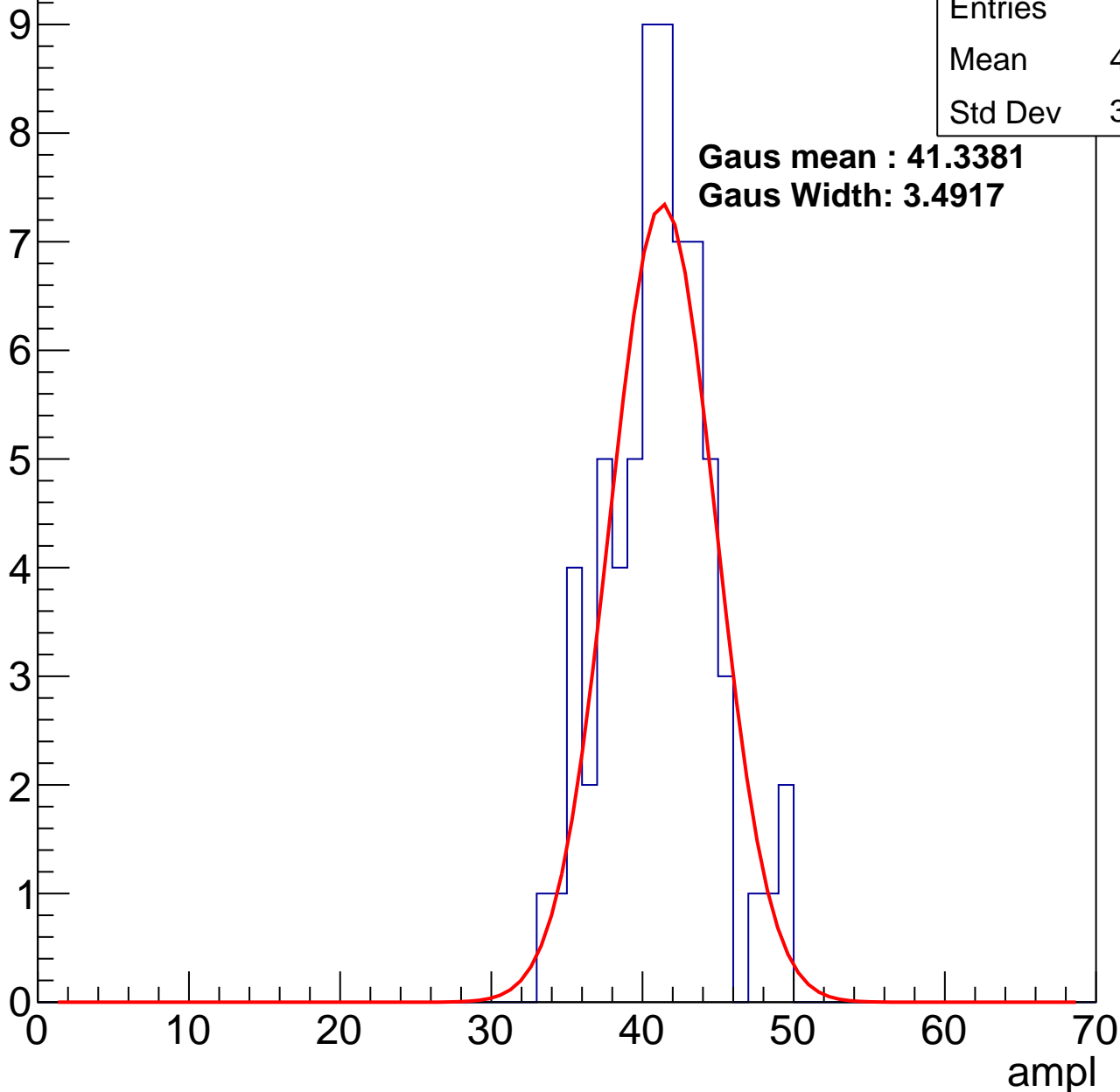
B1L003S, U21-ch28, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	40.65
Std Dev	3.449

Gaus mean : 41.3381
Gaus Width: 3.4917

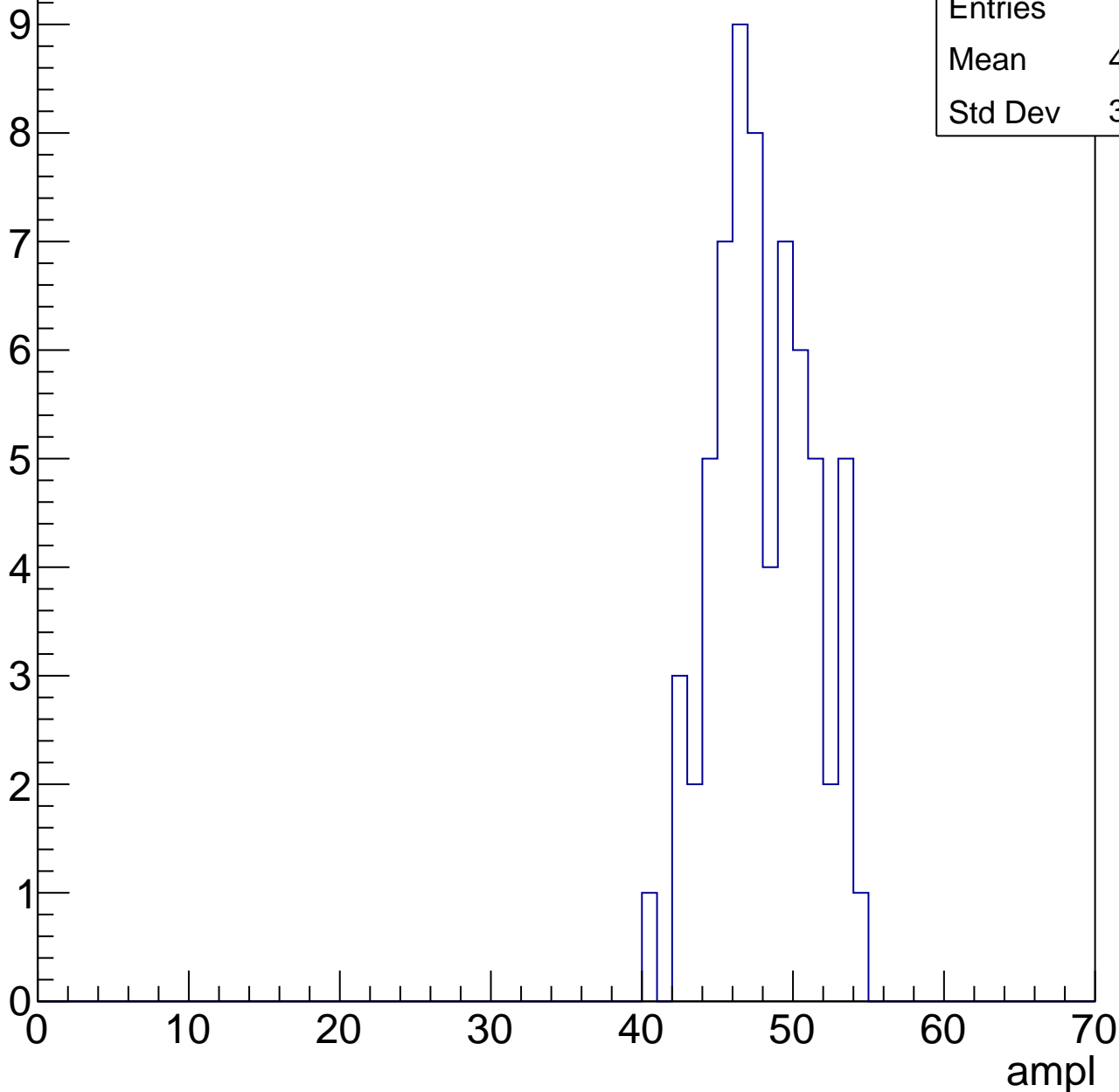


B1L003S, U21-ch28, adc3

calib_packv5_042523_0143.root, FC#13, port D2

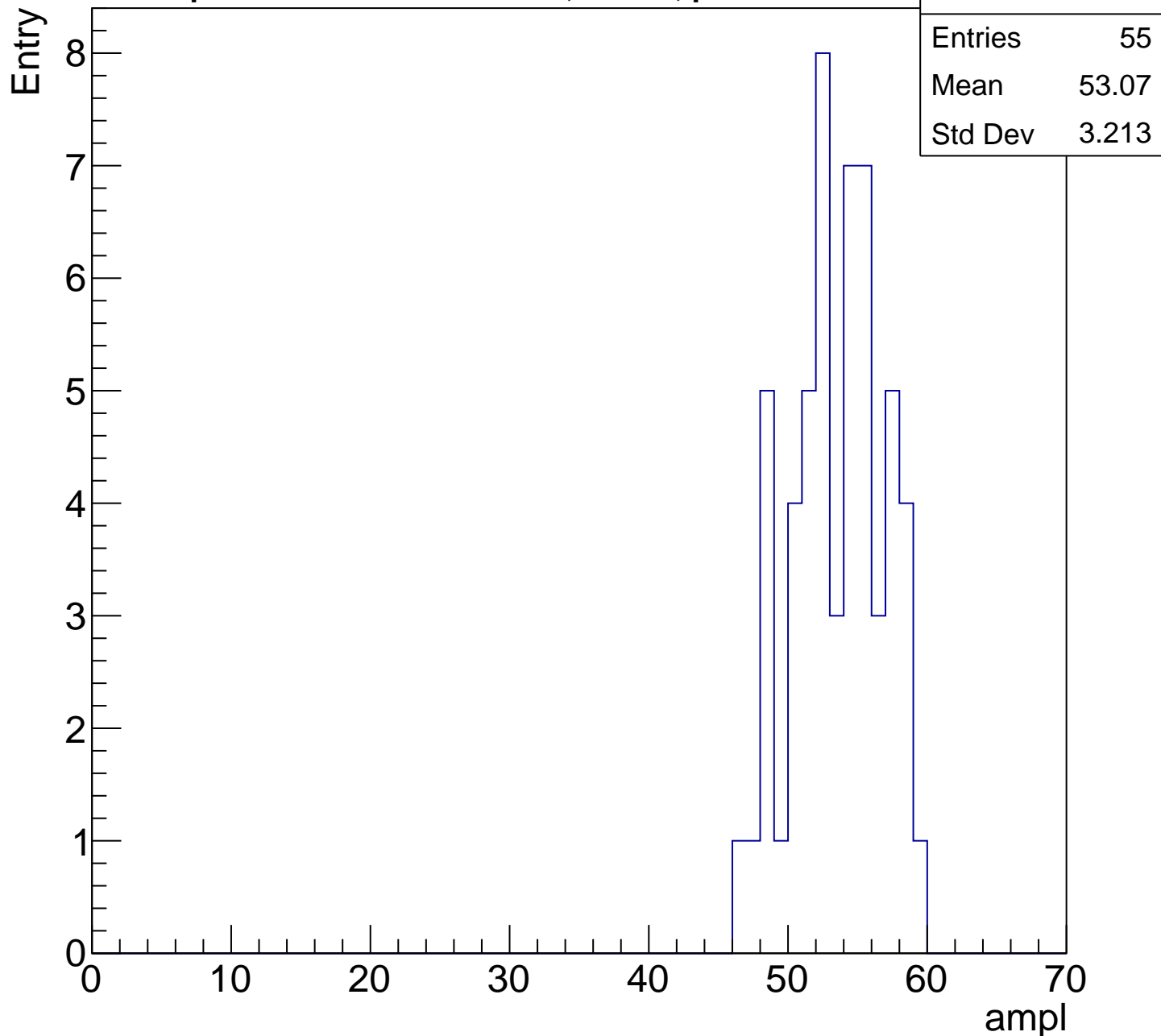
Entry

Entries	65
Mean	47.54
Std Dev	3.197



B1L003S, U21-ch28, adc4

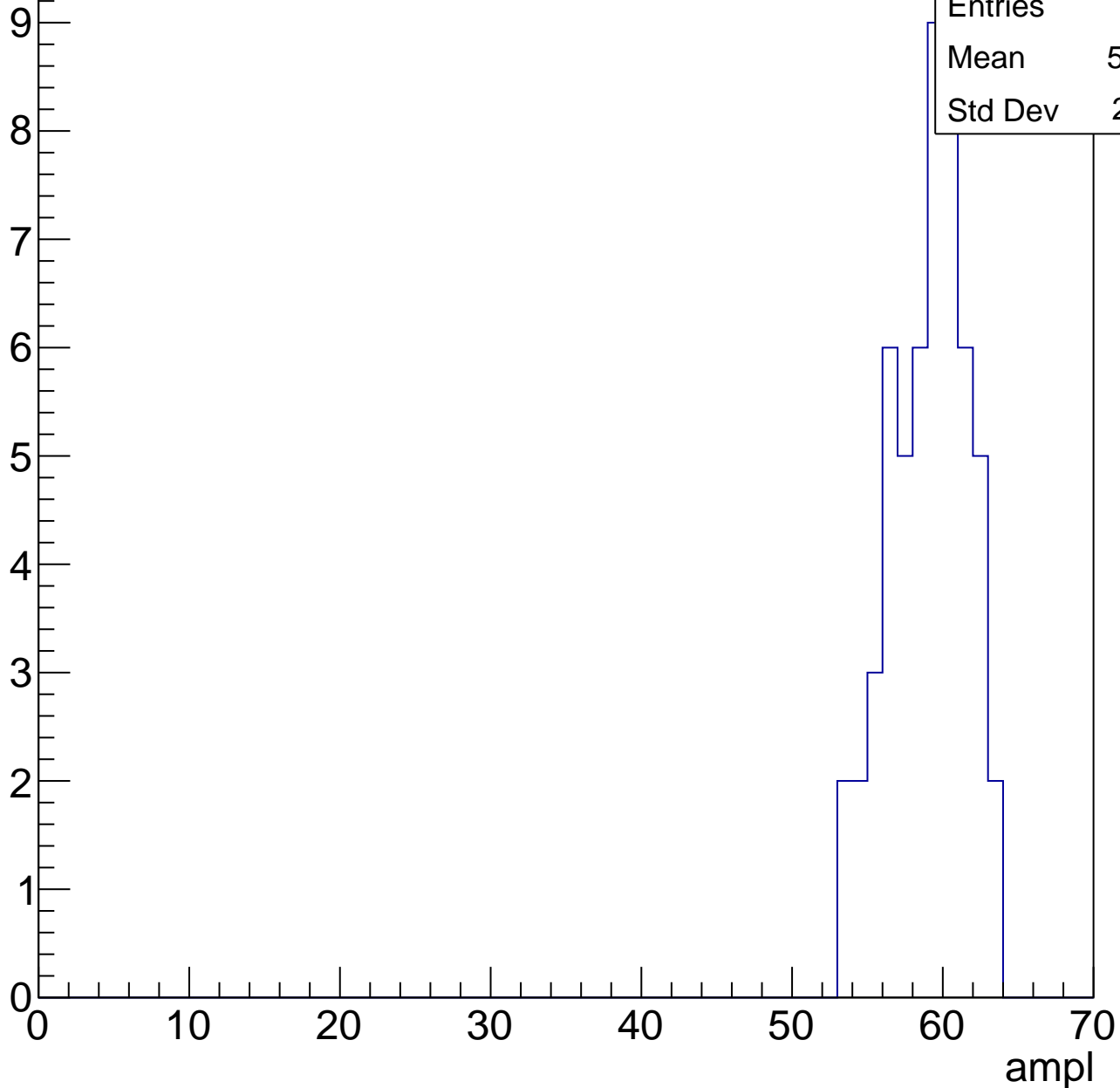
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U21-ch28, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

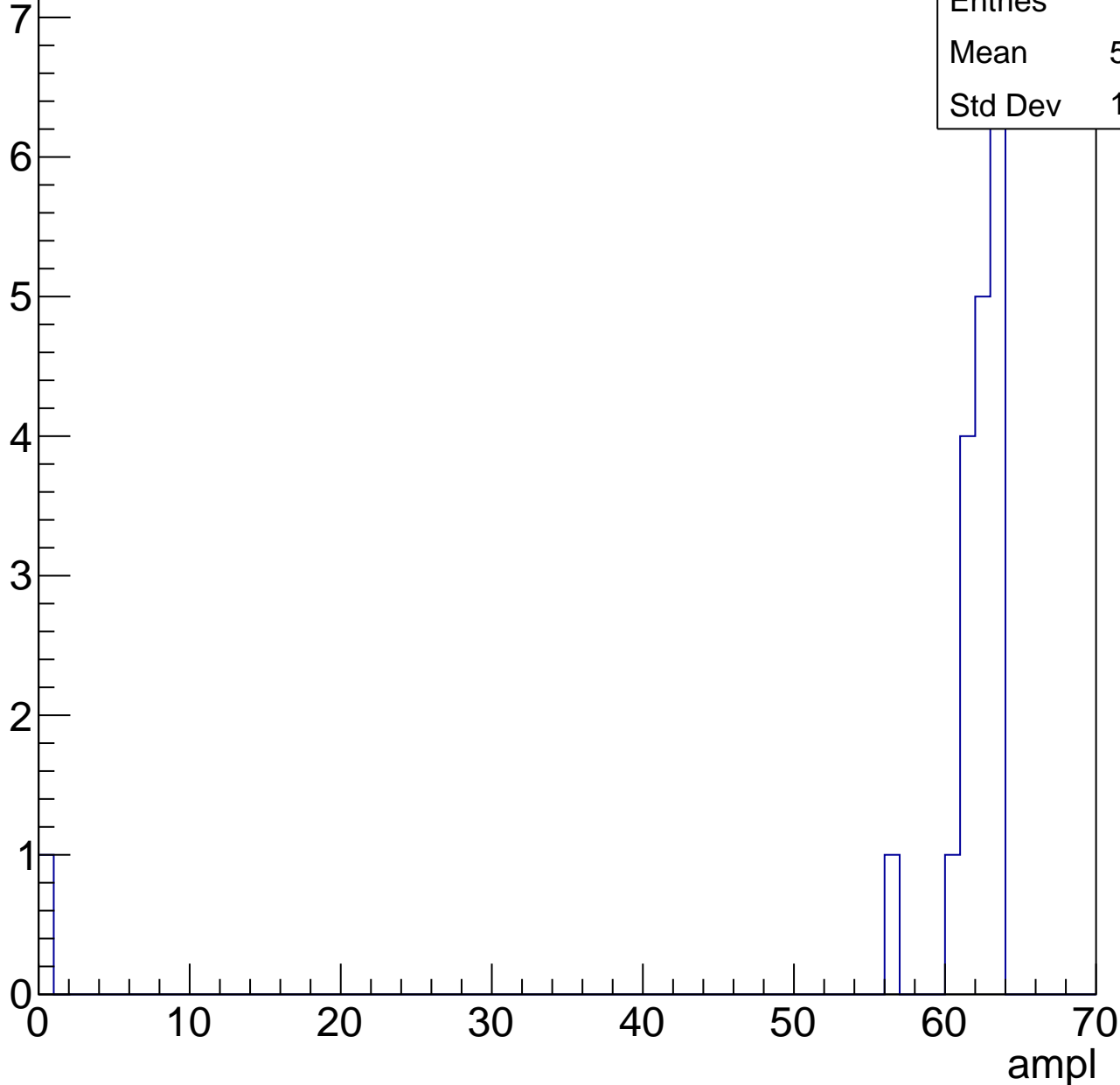


B1L003S, U21-ch28, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	58.47
Std Dev	13.88



B1L003S, U21-ch28, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch29, adc0

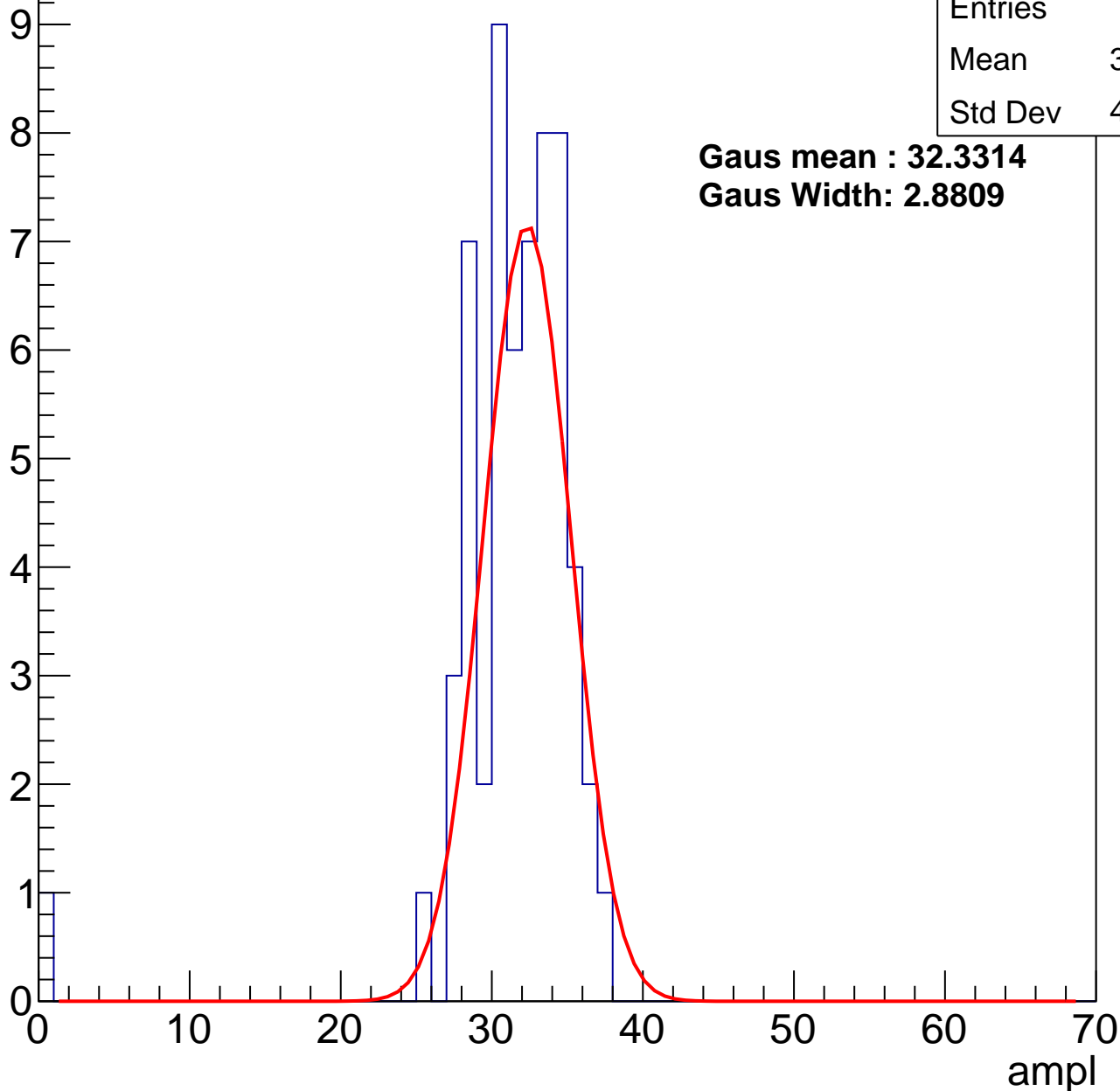
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	30.93
Std Dev	4.846

Gaus mean : 32.3314

Gaus Width: 2.8809



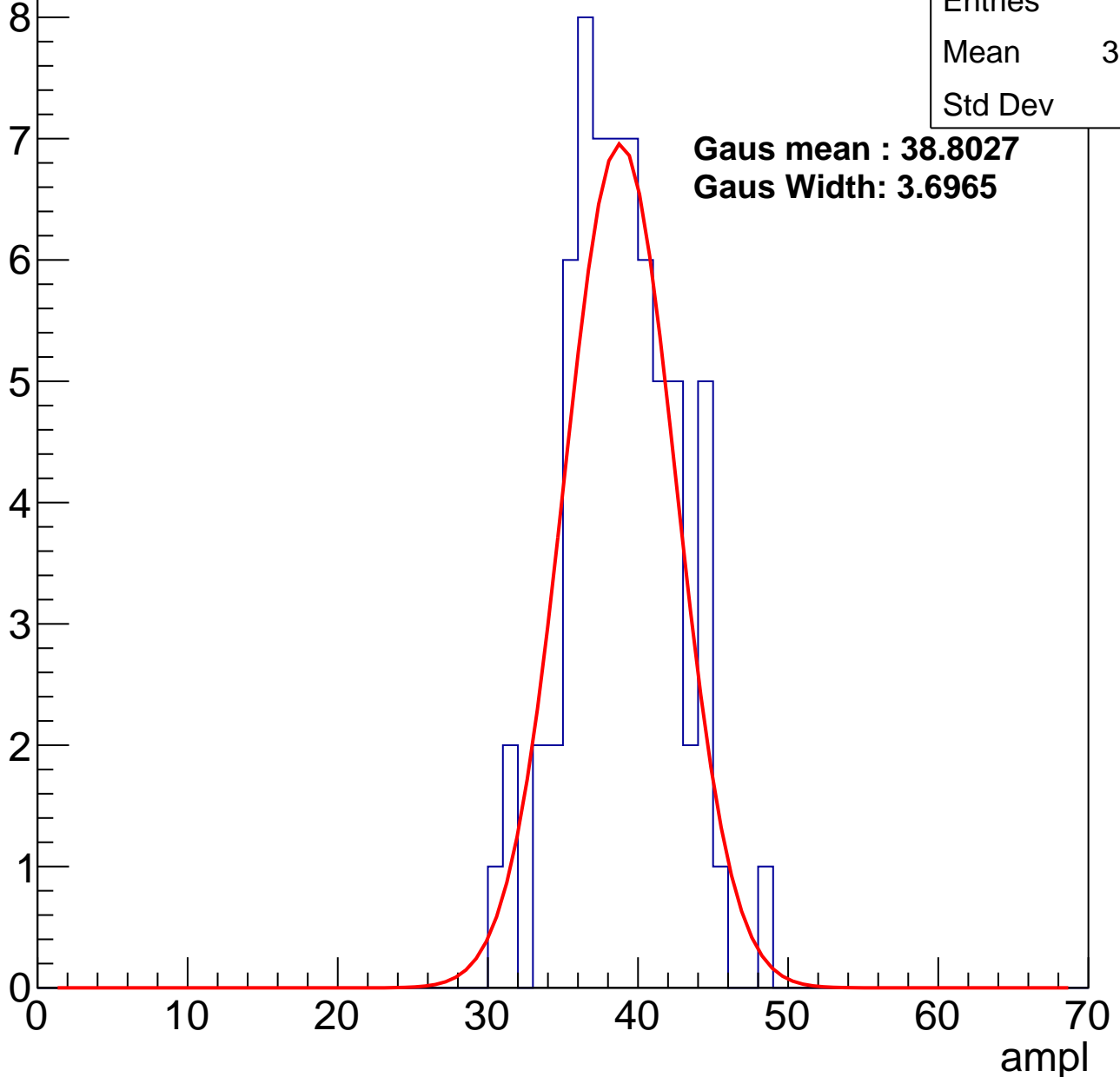
B1L003S, U21-ch29, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	38.45
Std Dev	3.58

Gaus mean : 38.8027
Gaus Width: 3.6965



B1L003S, U21-ch29, adc2

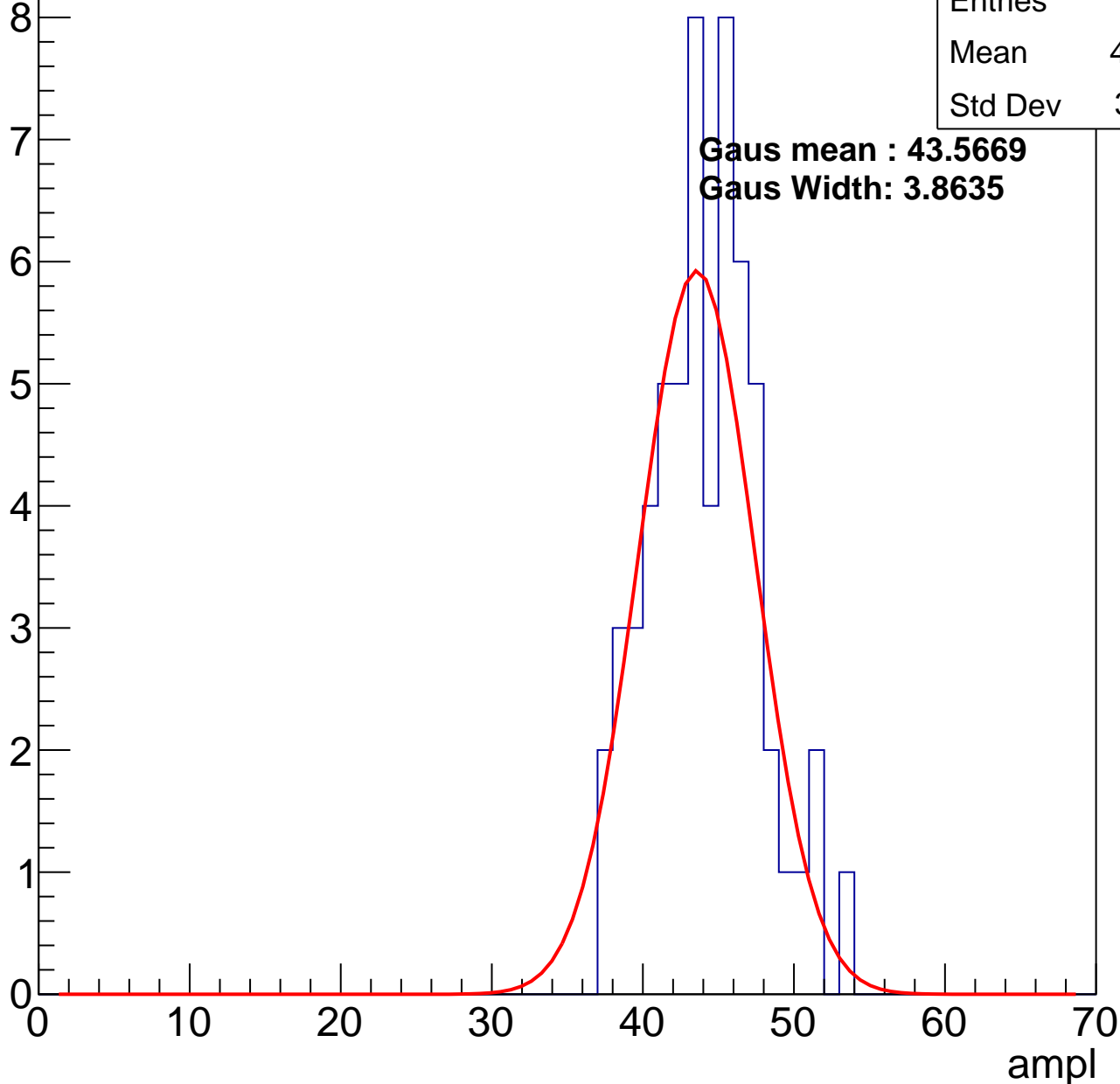
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	43.68
Std Dev	3.561

Gaus mean : 43.5669

Gaus Width: 3.8635

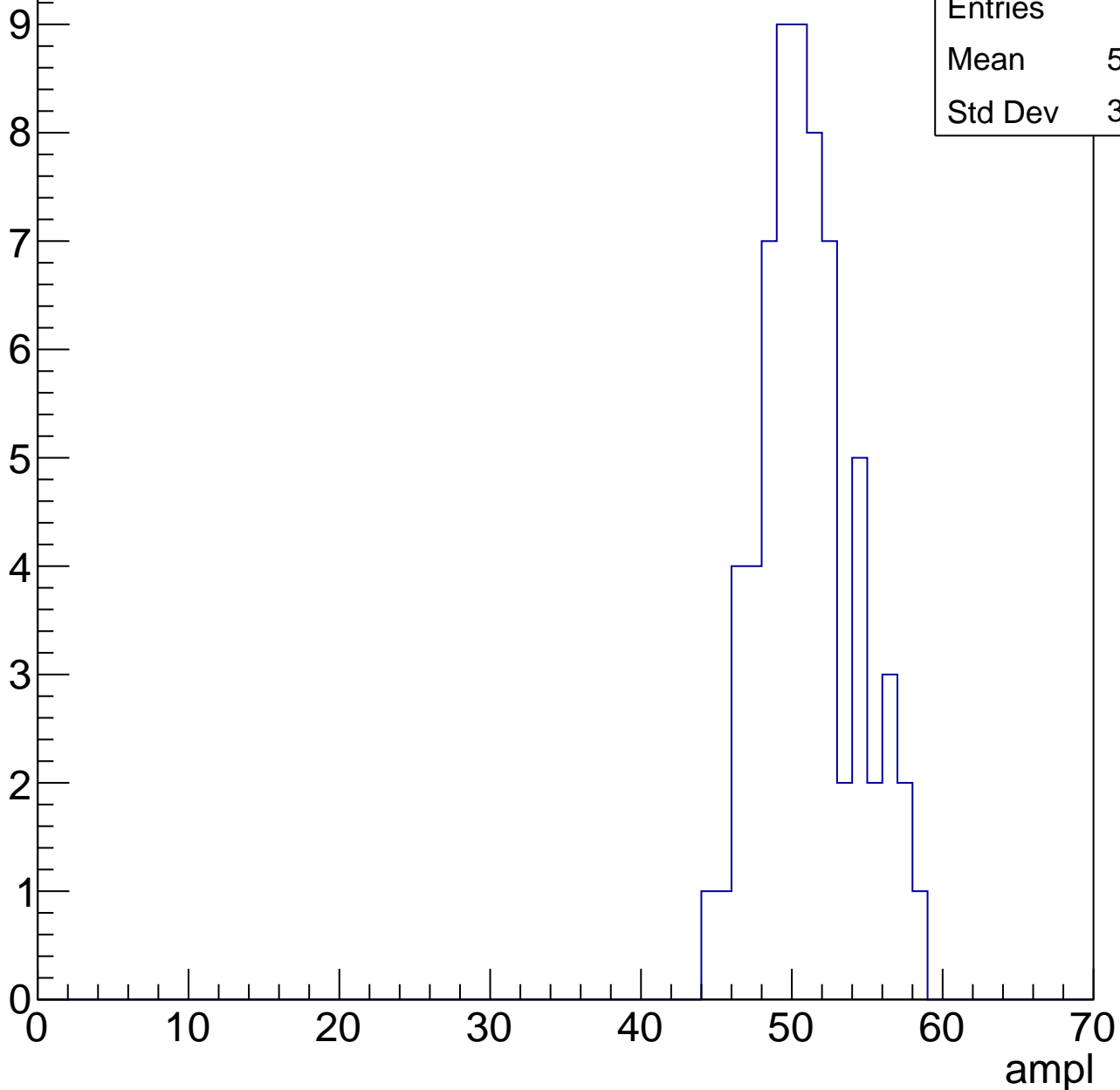


B1L003S, U21-ch29, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	50.55
Std Dev	3.118

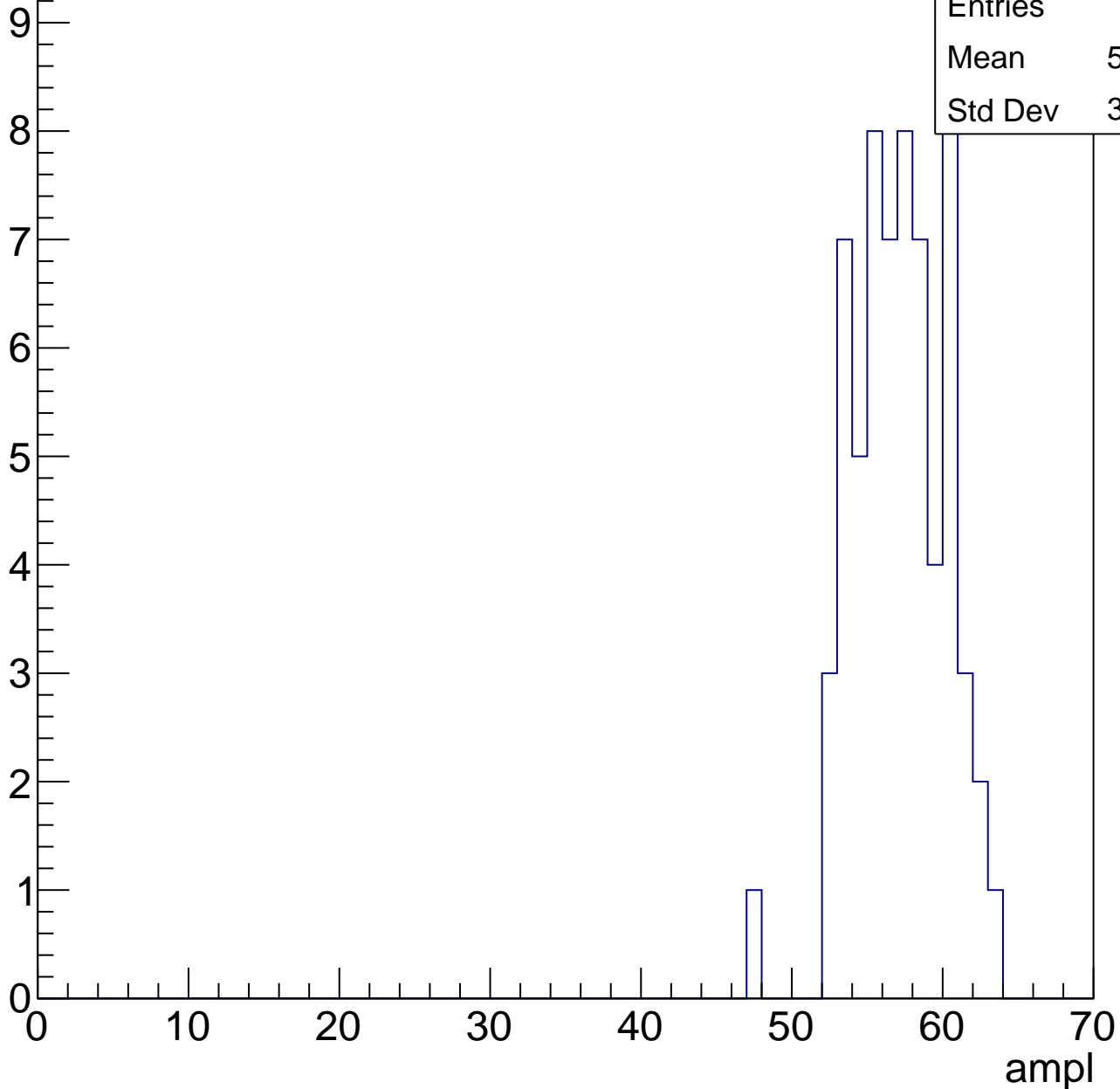


B1L003S, U21-ch29, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	56.68
Std Dev	3.044

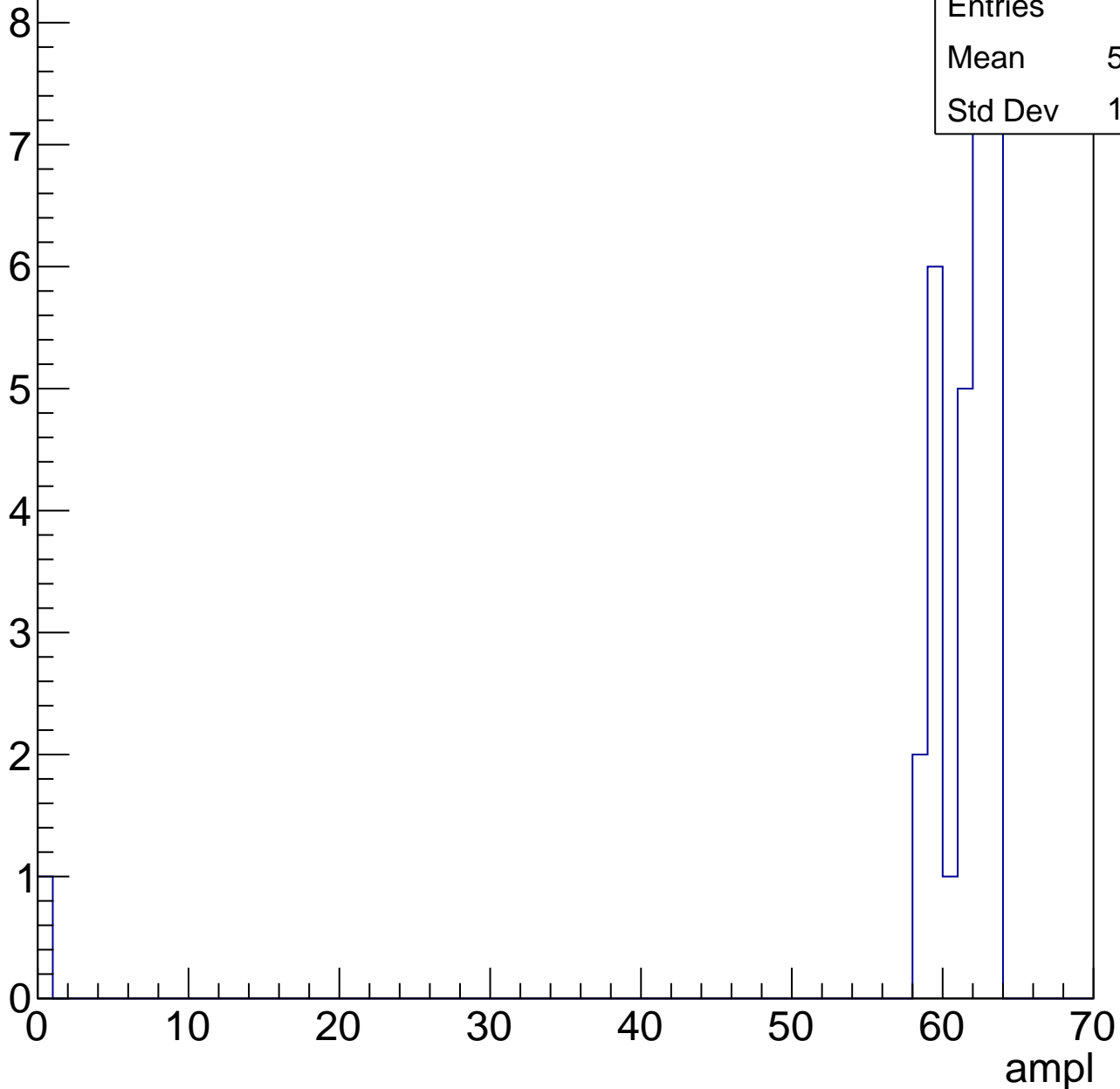


B1L003S, U21-ch29, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	31
Mean	59.19
Std Dev	10.93



B1L003S, U21-ch29, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch29, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch30, adc0

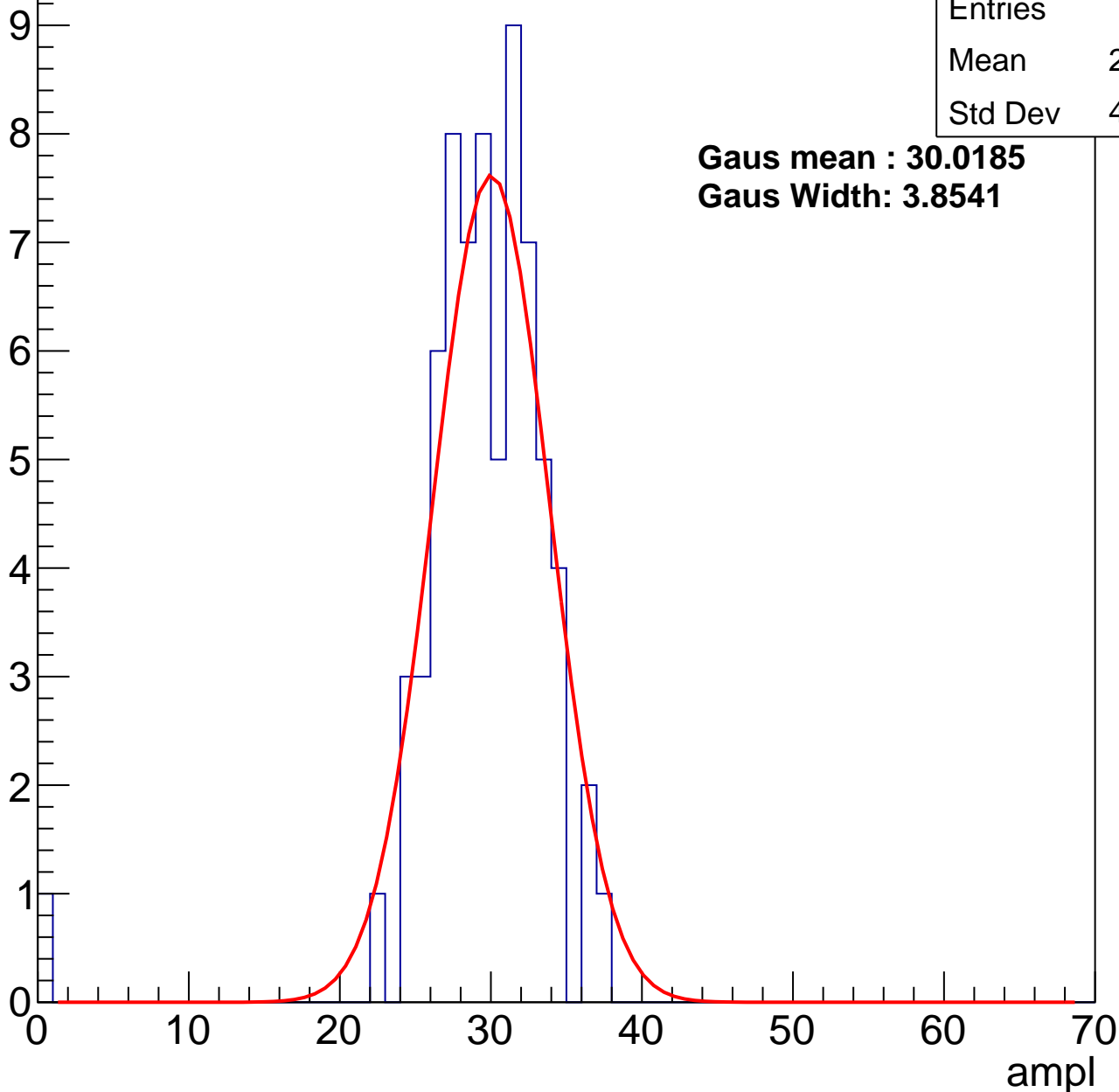
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.03
Std Dev	4.709

Gaus mean : 30.0185

Gaus Width: 3.8541



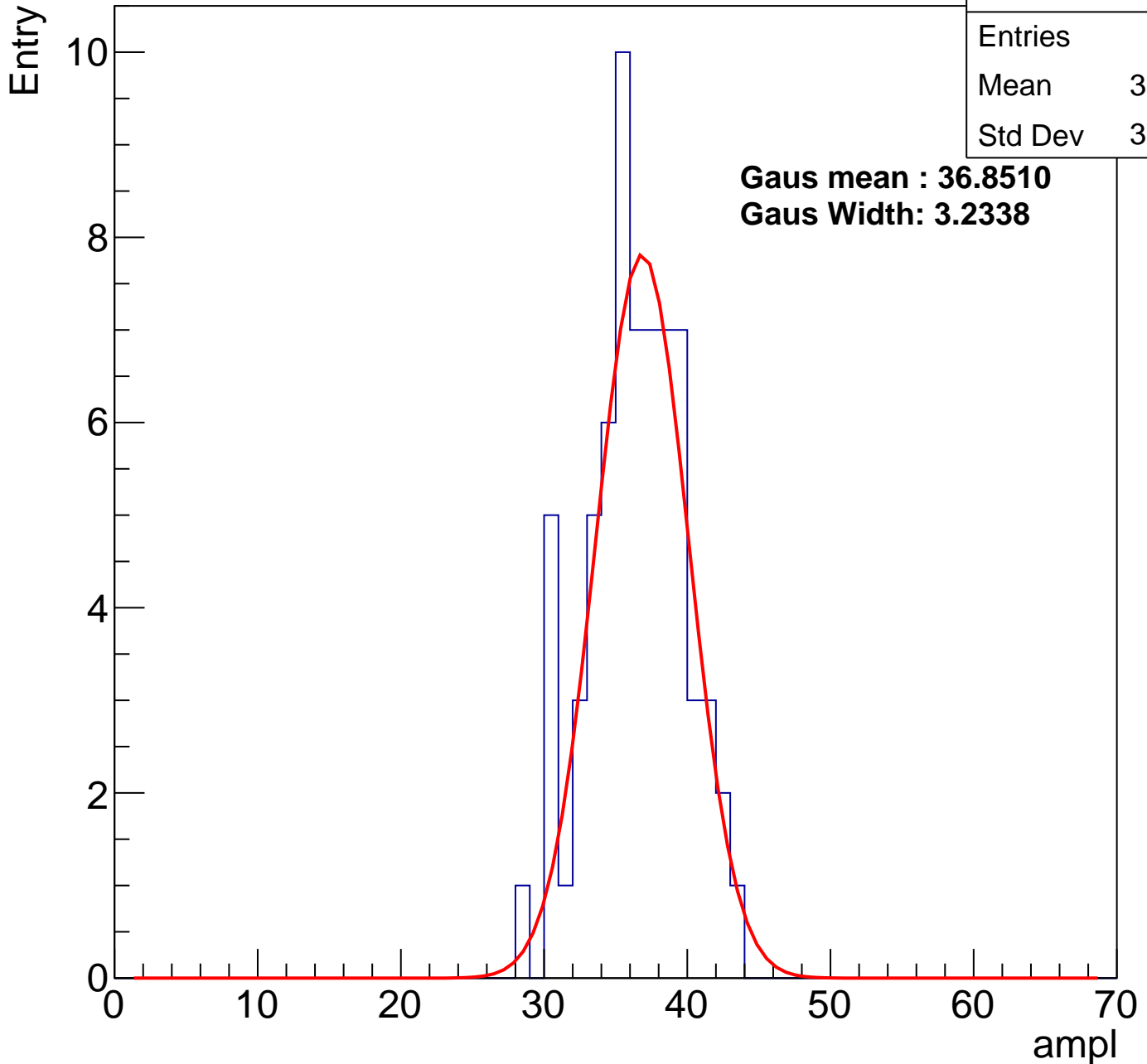
B1L003S, U21-ch30, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	35.94
Std Dev	3.294

Gaus mean : 36.8510

Gaus Width: 3.2338



B1L003S, U21-ch30, adc2

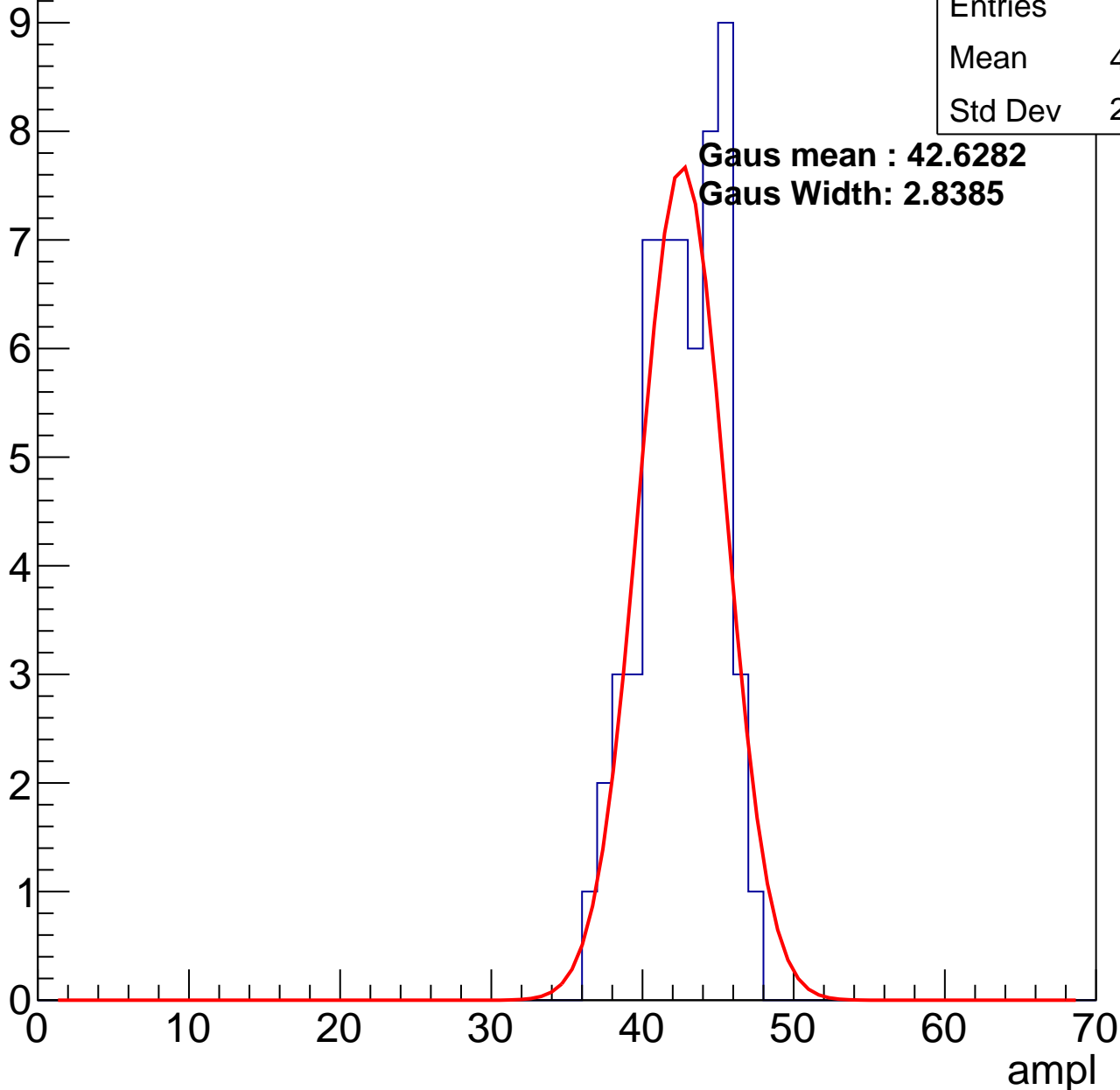
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.14
Std Dev	2.605

Gaus mean : 42.6282

Gaus Width: 2.8385

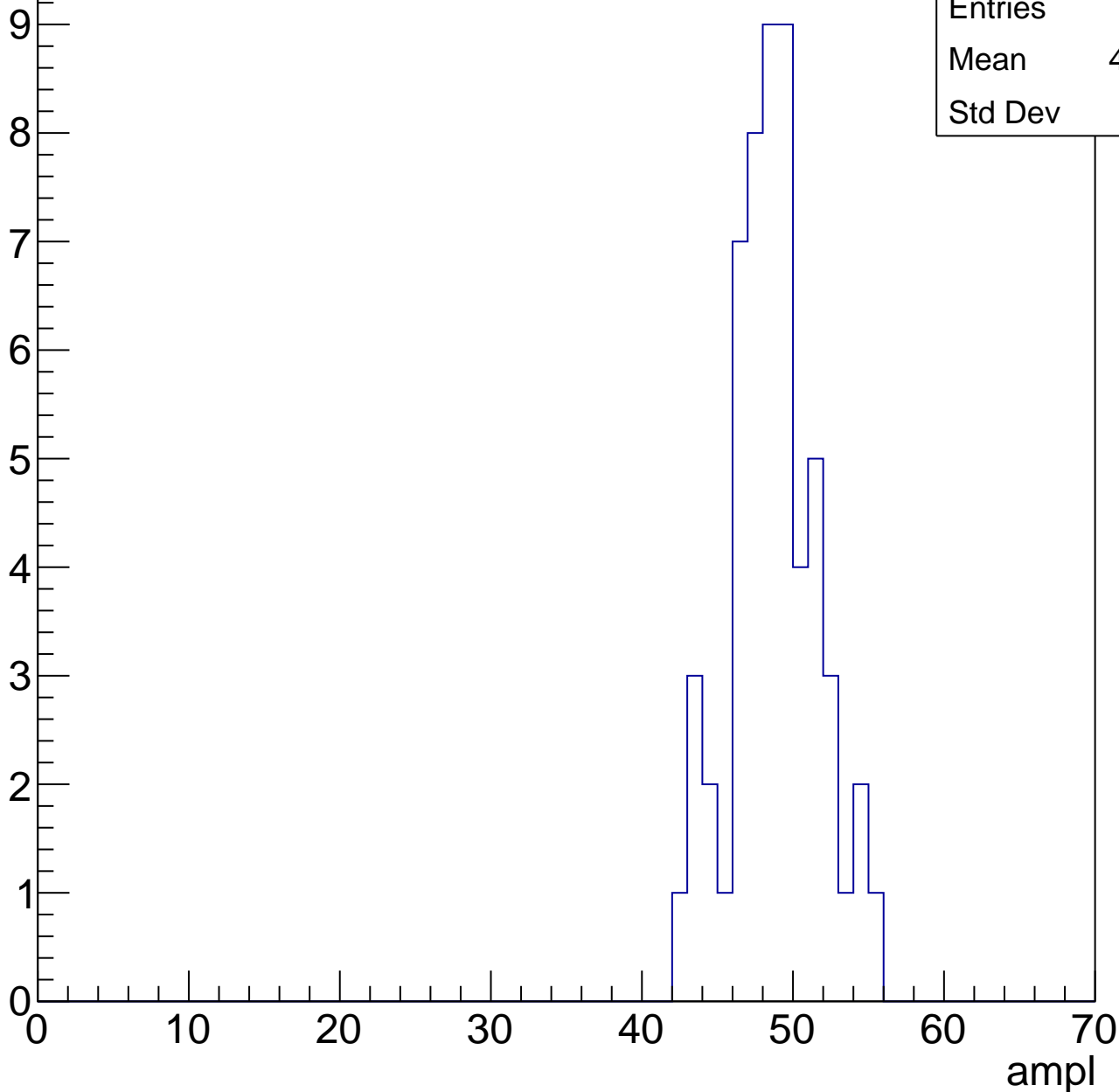


B1L003S, U21-ch30, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	48.25
Std Dev	2.83



B1L003S, U21-ch30, adc4

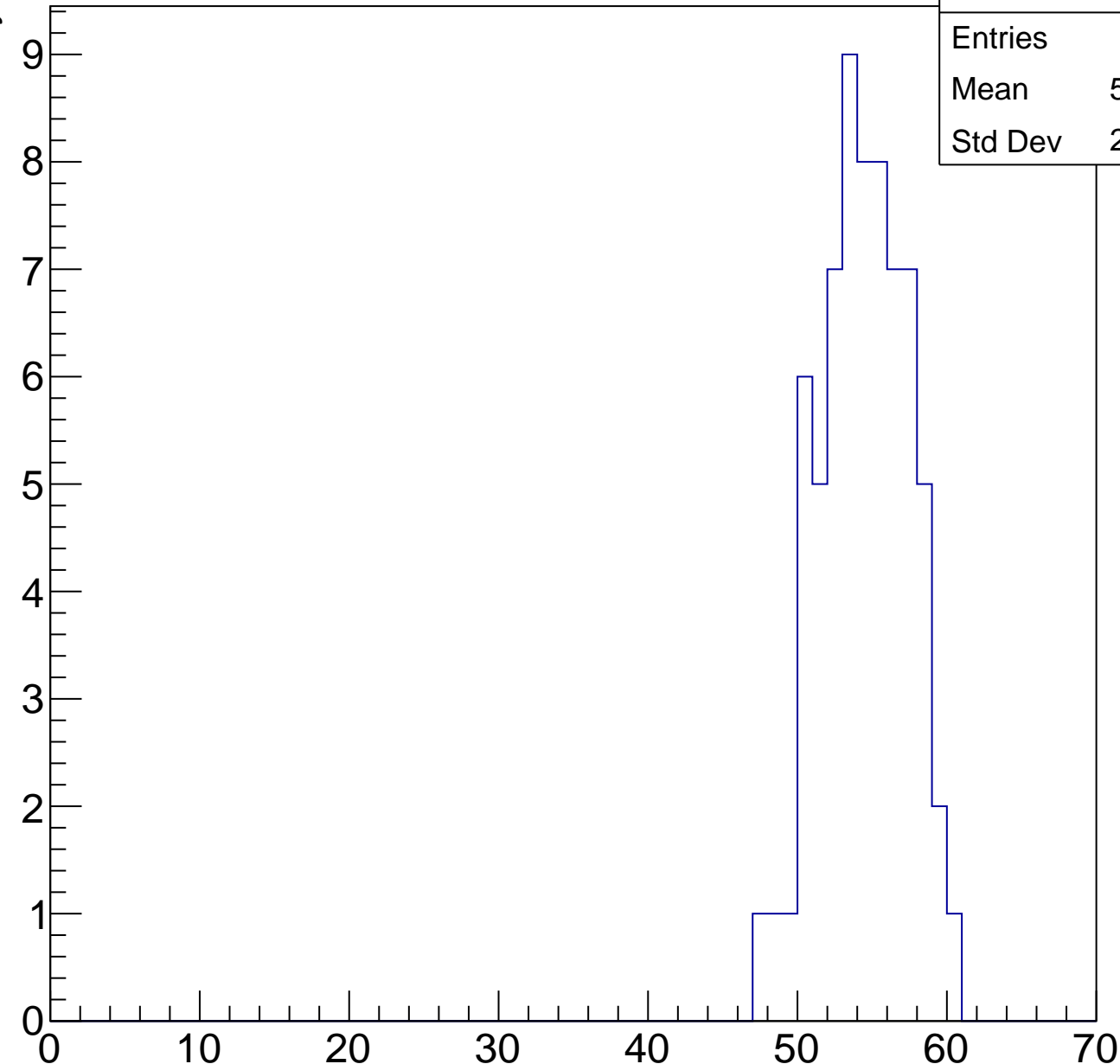
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	68
Mean	53.99
Std Dev	2.852

ampl



B1L003S, U21-ch30, adc5

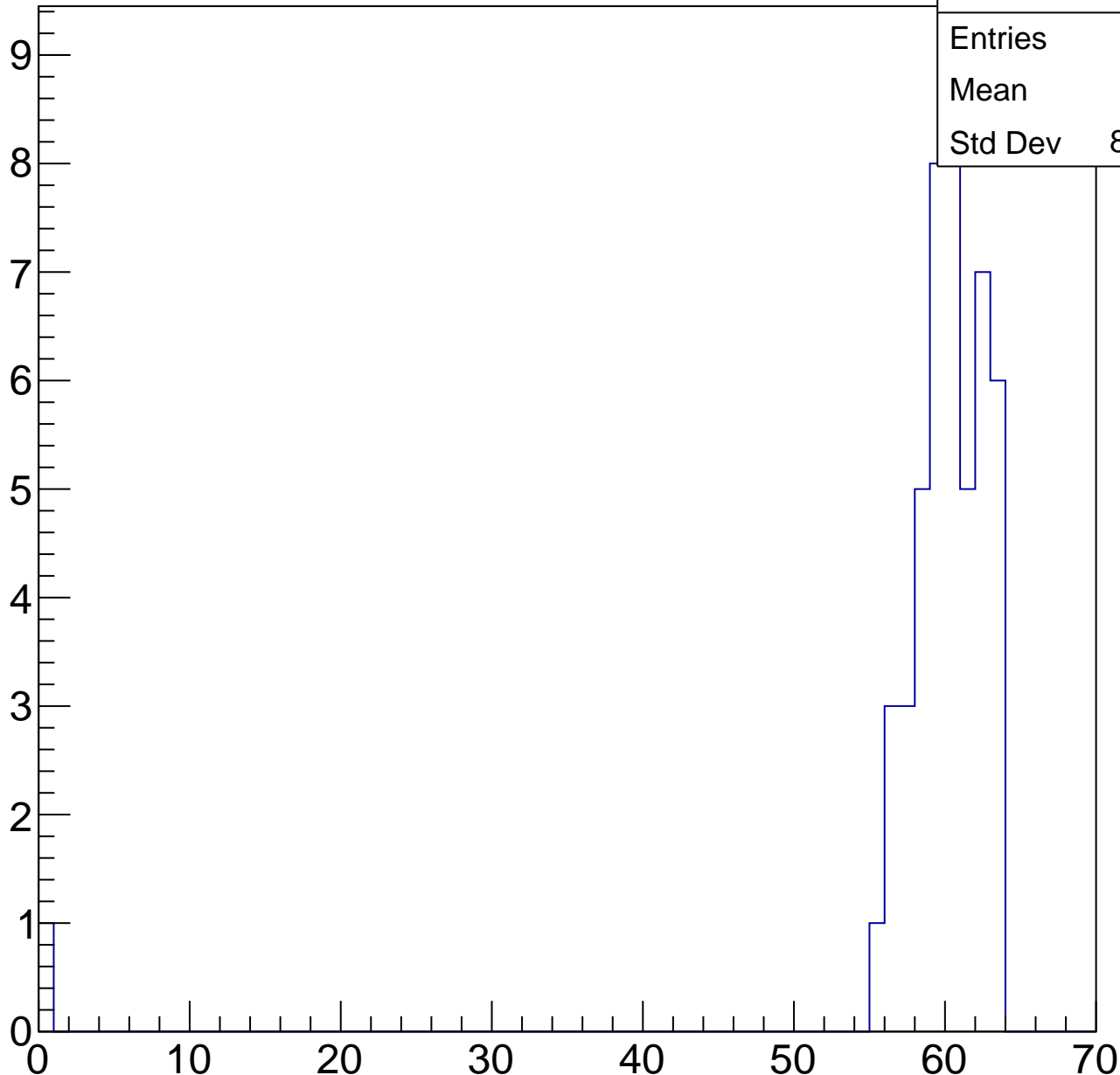
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	48
Mean	58.6
Std Dev	8.805

ampl

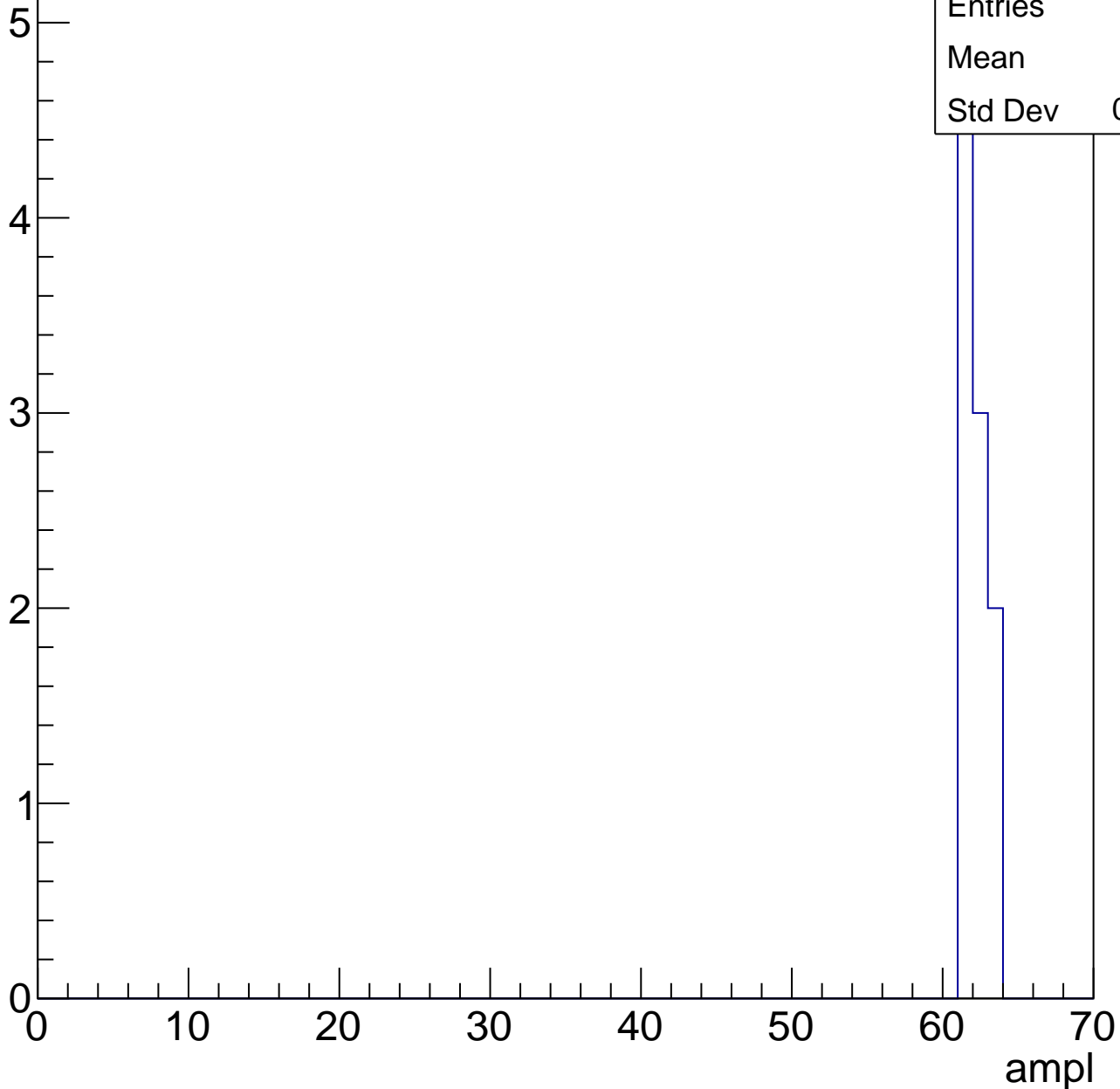


B1L003S, U21-ch30, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.7
Std Dev	0.781



B1L003S, U21-ch30, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	63
Std Dev	0

B1L003S, U21-ch31, adc0

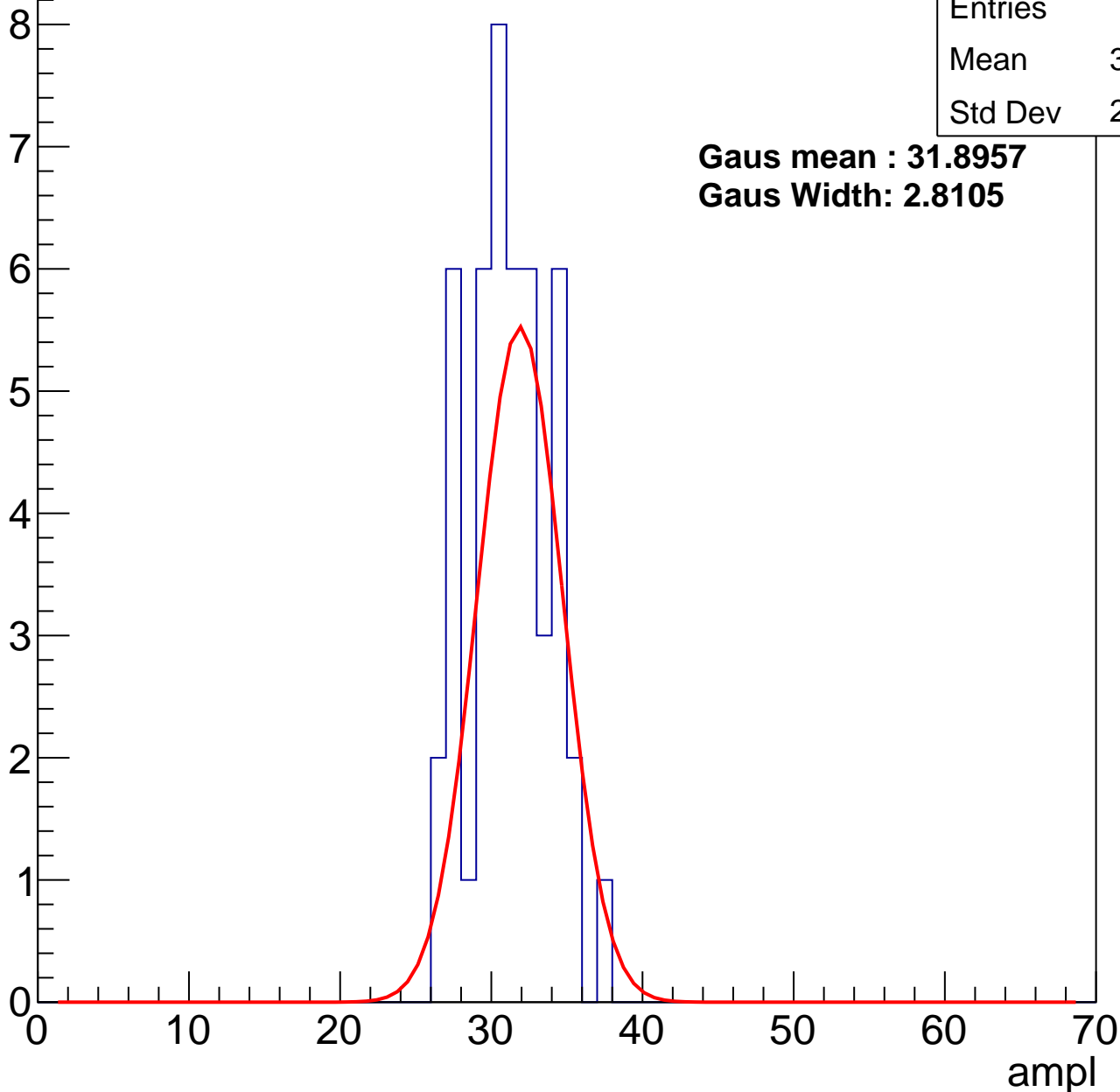
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	30.72
Std Dev	2.623

Gaus mean : 31.8957

Gaus Width: 2.8105

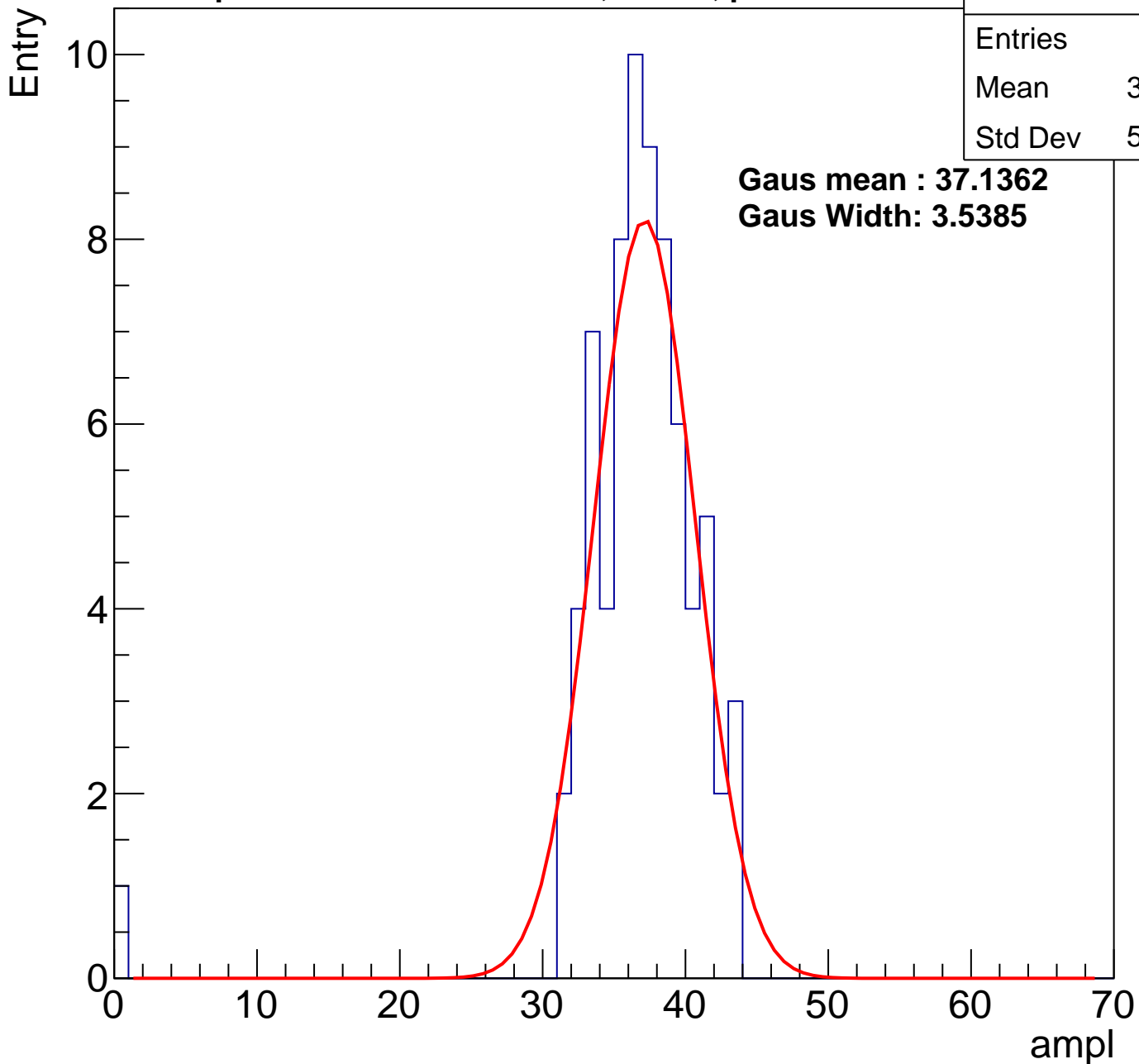


B1L003S, U21-ch31, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	36.25
Std Dev	5.228

Gaus mean : 37.1362
Gaus Width: 3.5385



B1L003S, U21-ch31, adc2

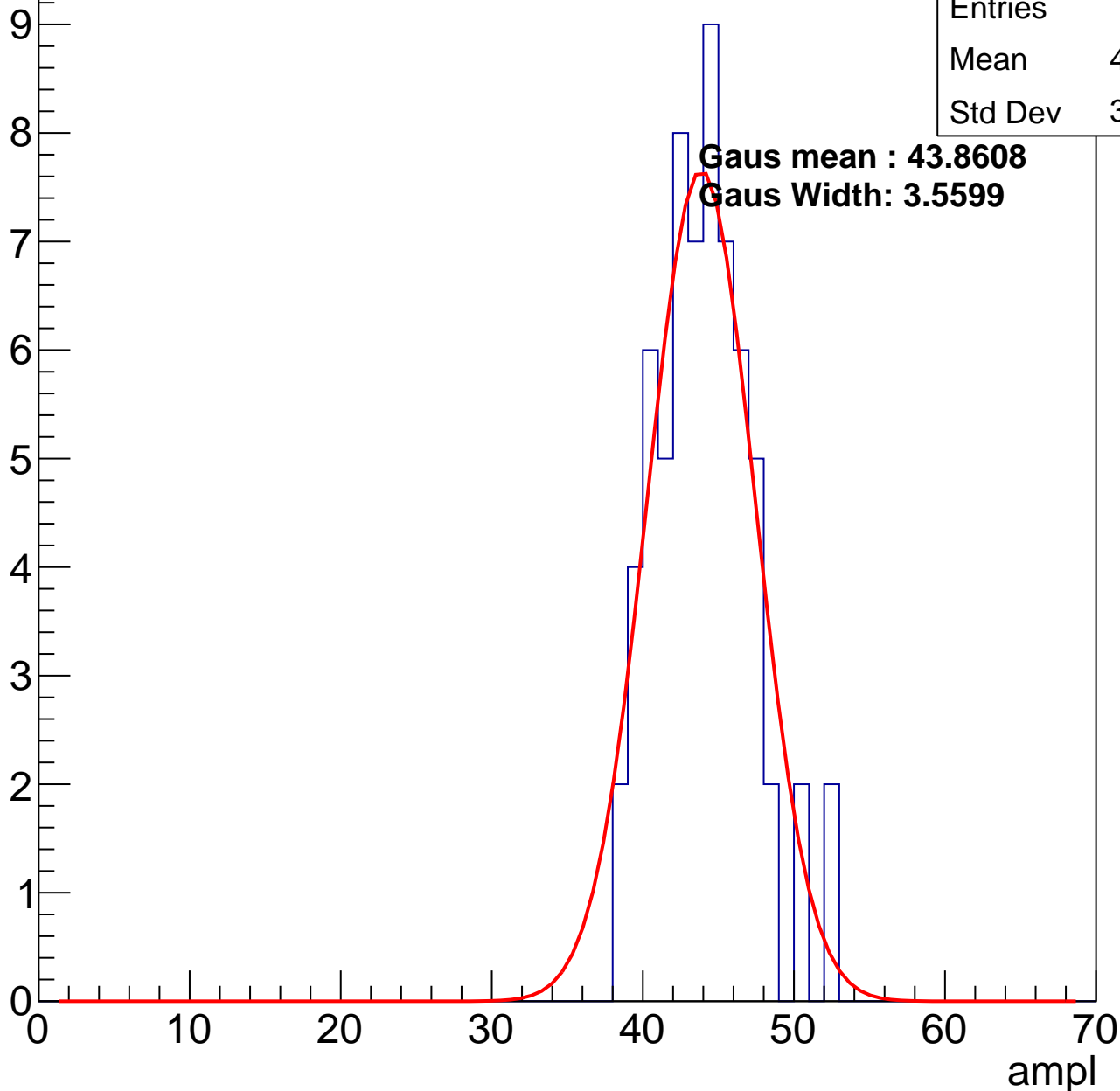
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	43.63
Std Dev	3.165

Gaus mean : 43.8608

Gaus Width: 3.5599

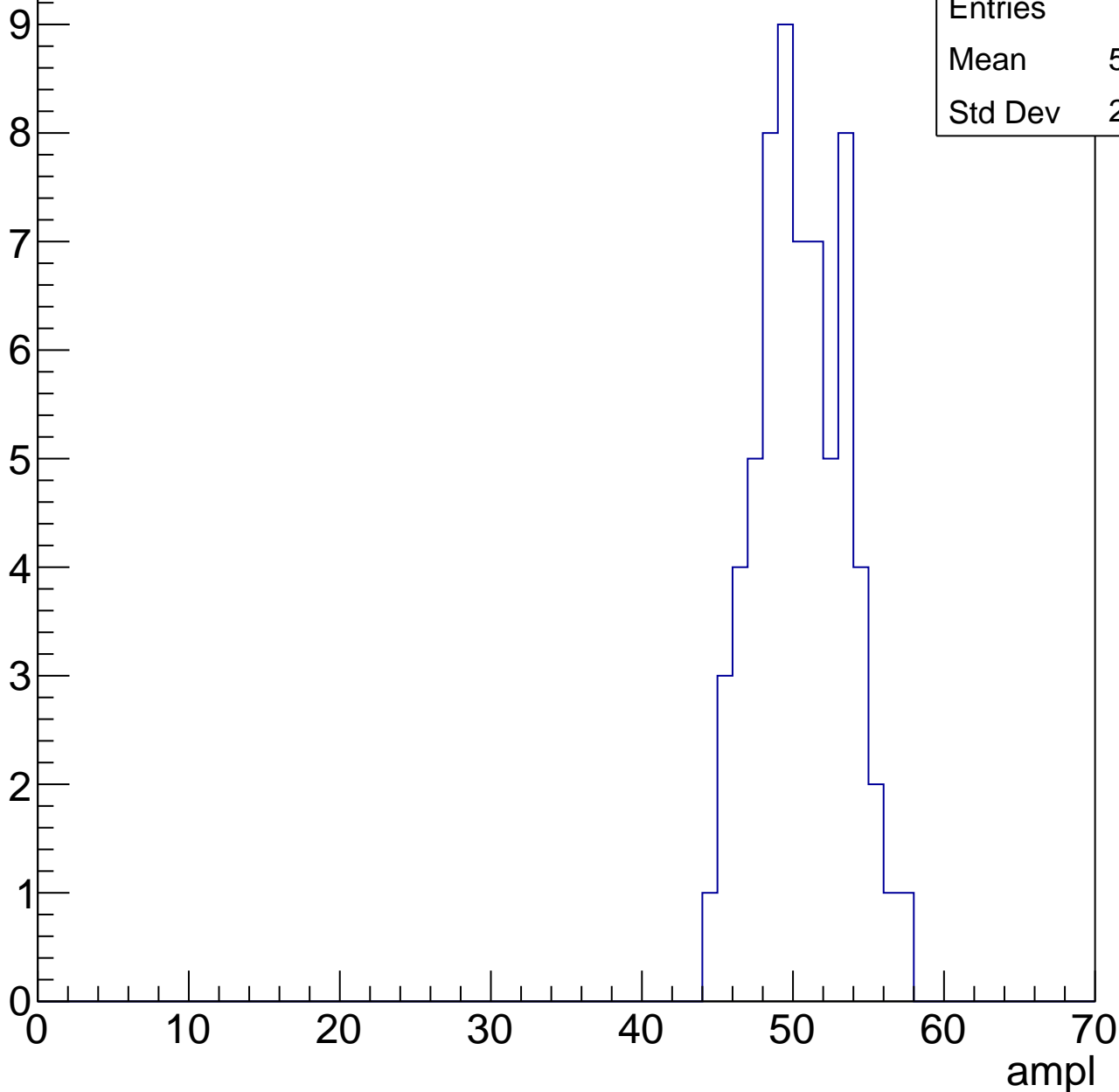


B1L003S, U21-ch31, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

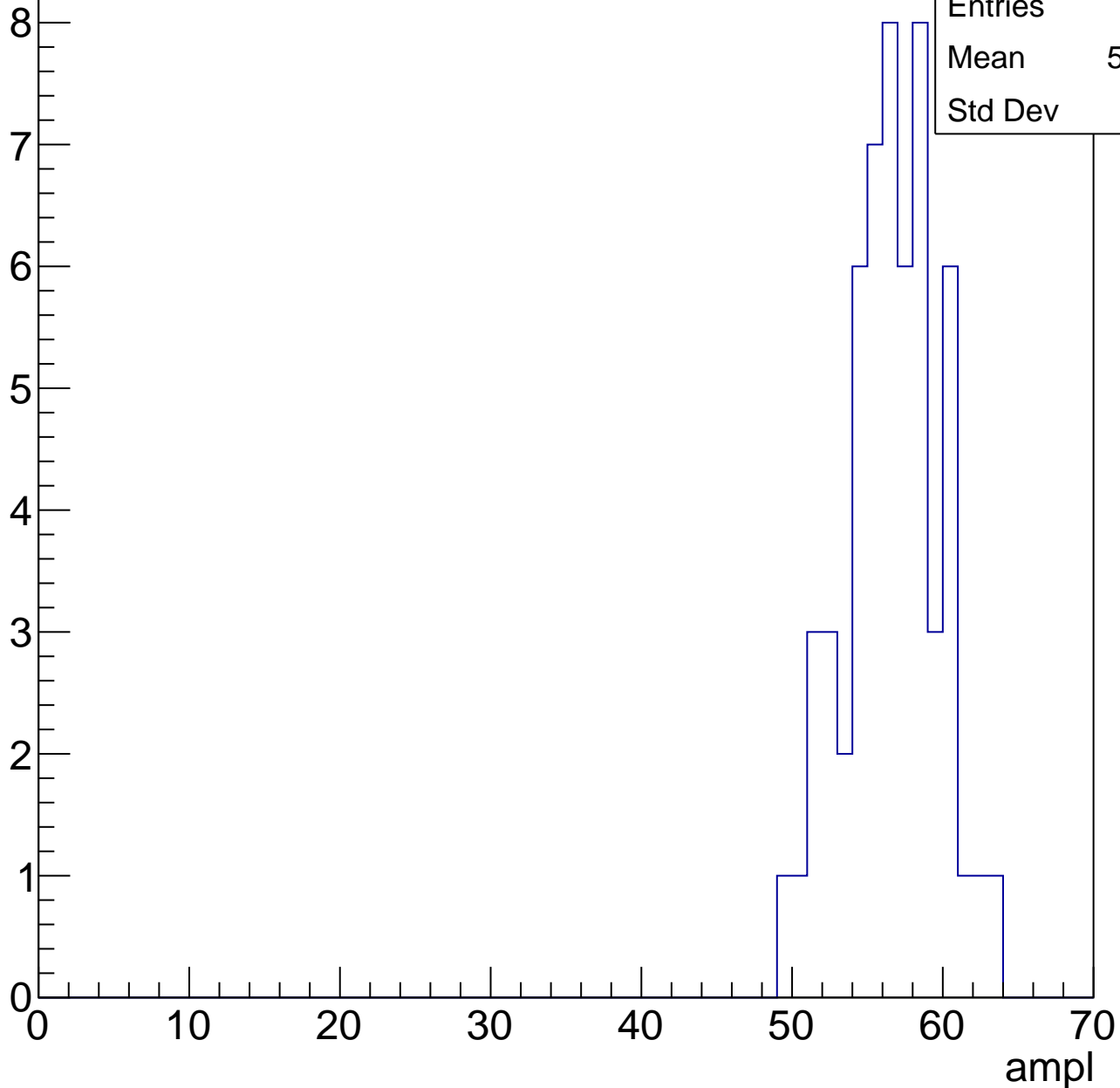
Entries	65
Mean	50.05
Std Dev	2.932



B1L003S, U21-ch31, adc4

calib_packv5_042523_0143.root, FC#13, port D2

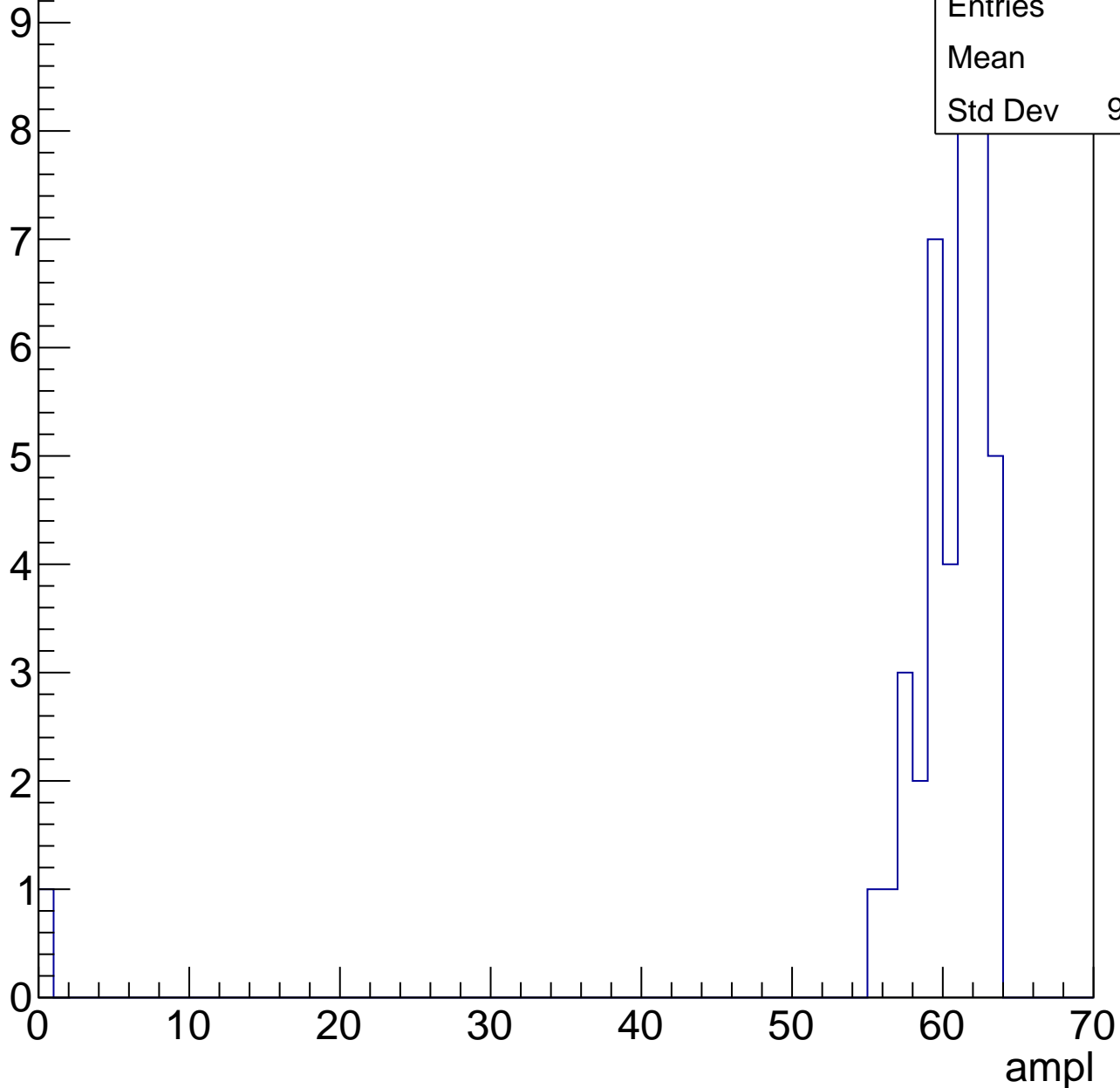
Entry



B1L003S, U21-ch31, adc5

calib_packv5_042523_0143.root, FC#13, port D2

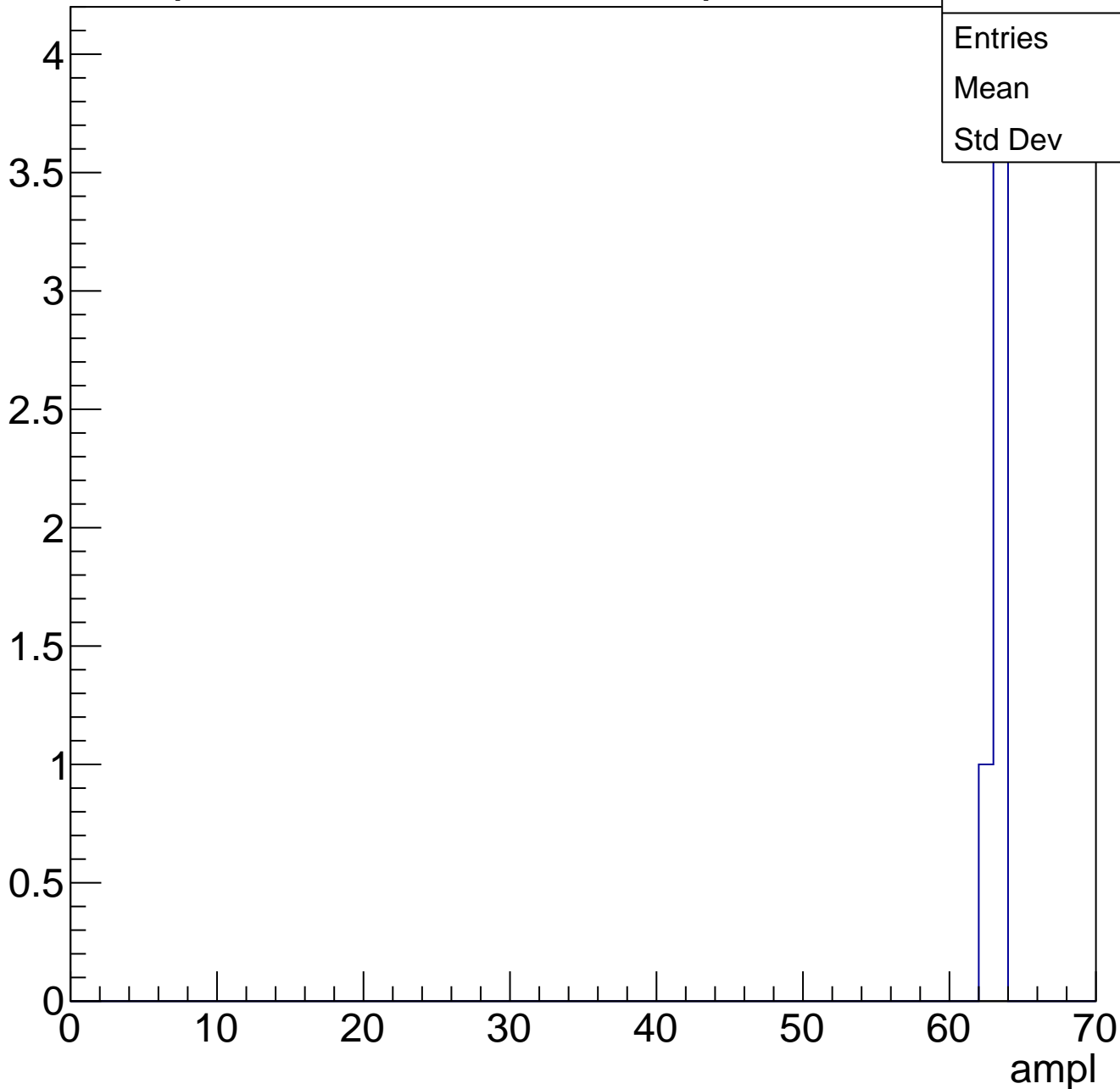
Entry



B1L003S, U21-ch31, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch31, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch32, adc0

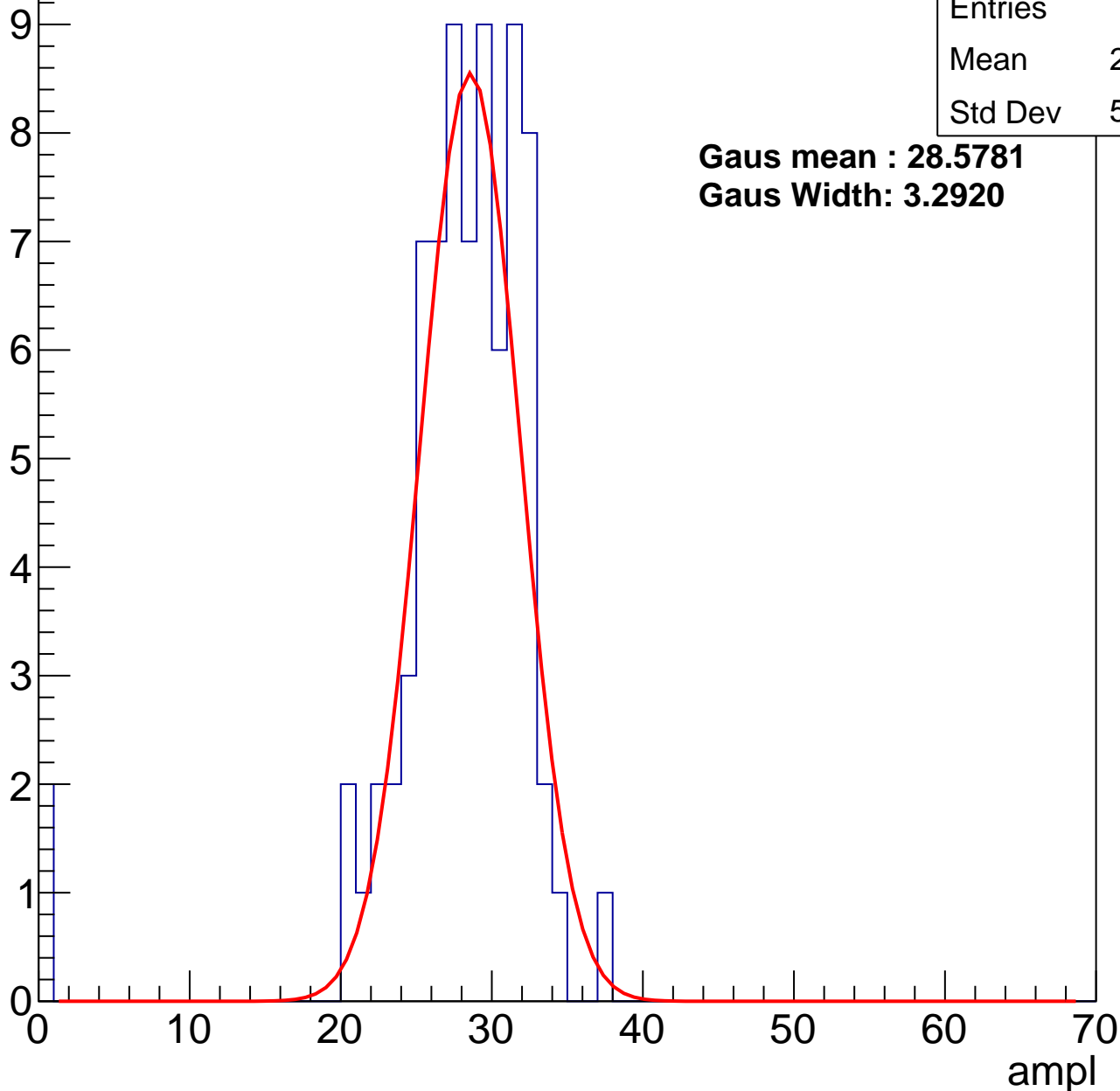
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	27.33
Std Dev	5.535

Gaus mean : 28.5781

Gaus Width: 3.2920



B1L003S, U21-ch32, adc1

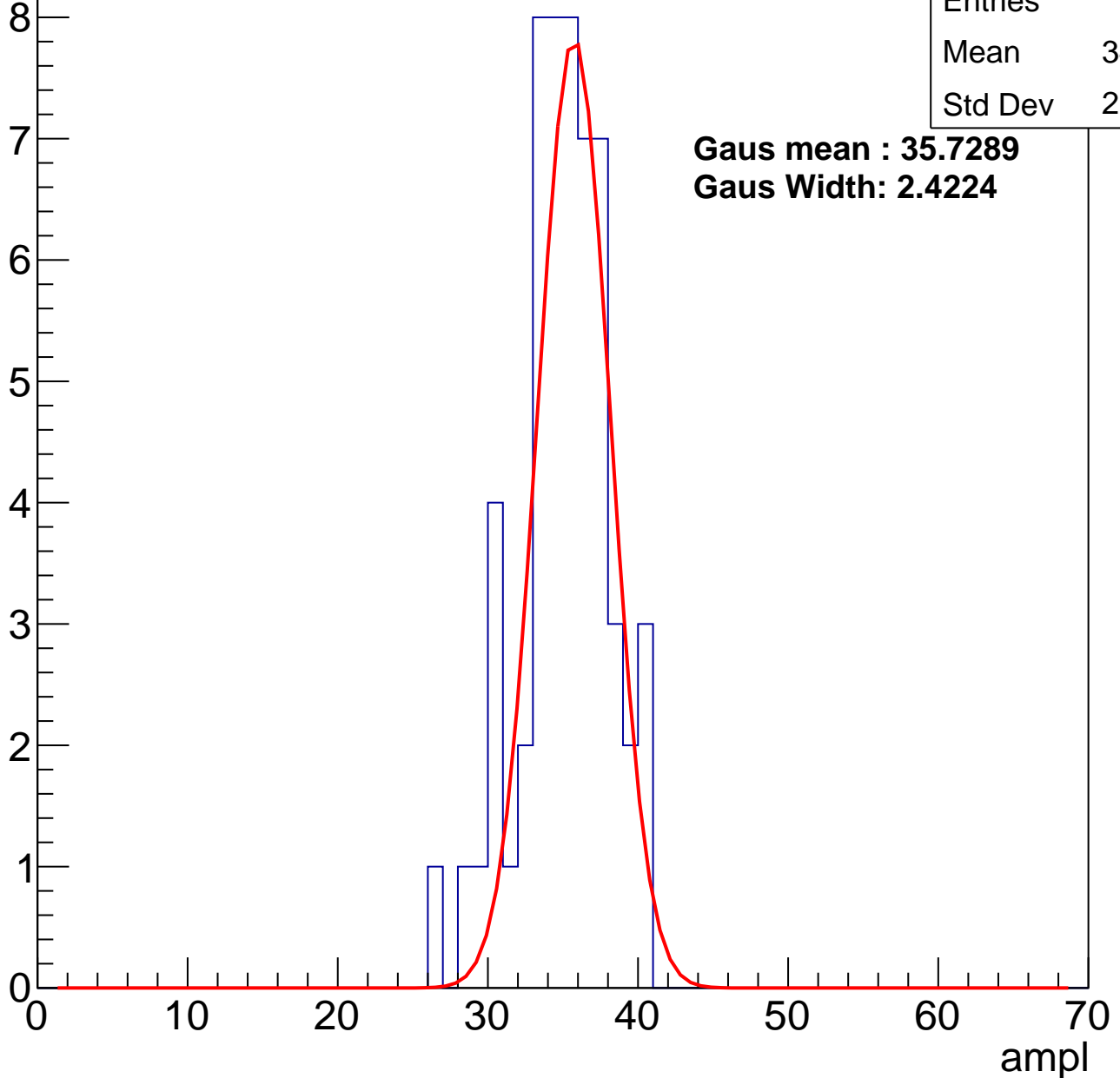
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	34.59
Std Dev	2.987

Gaus mean : 35.7289

Gaus Width: 2.4224



B1L003S, U21-ch32, adc2

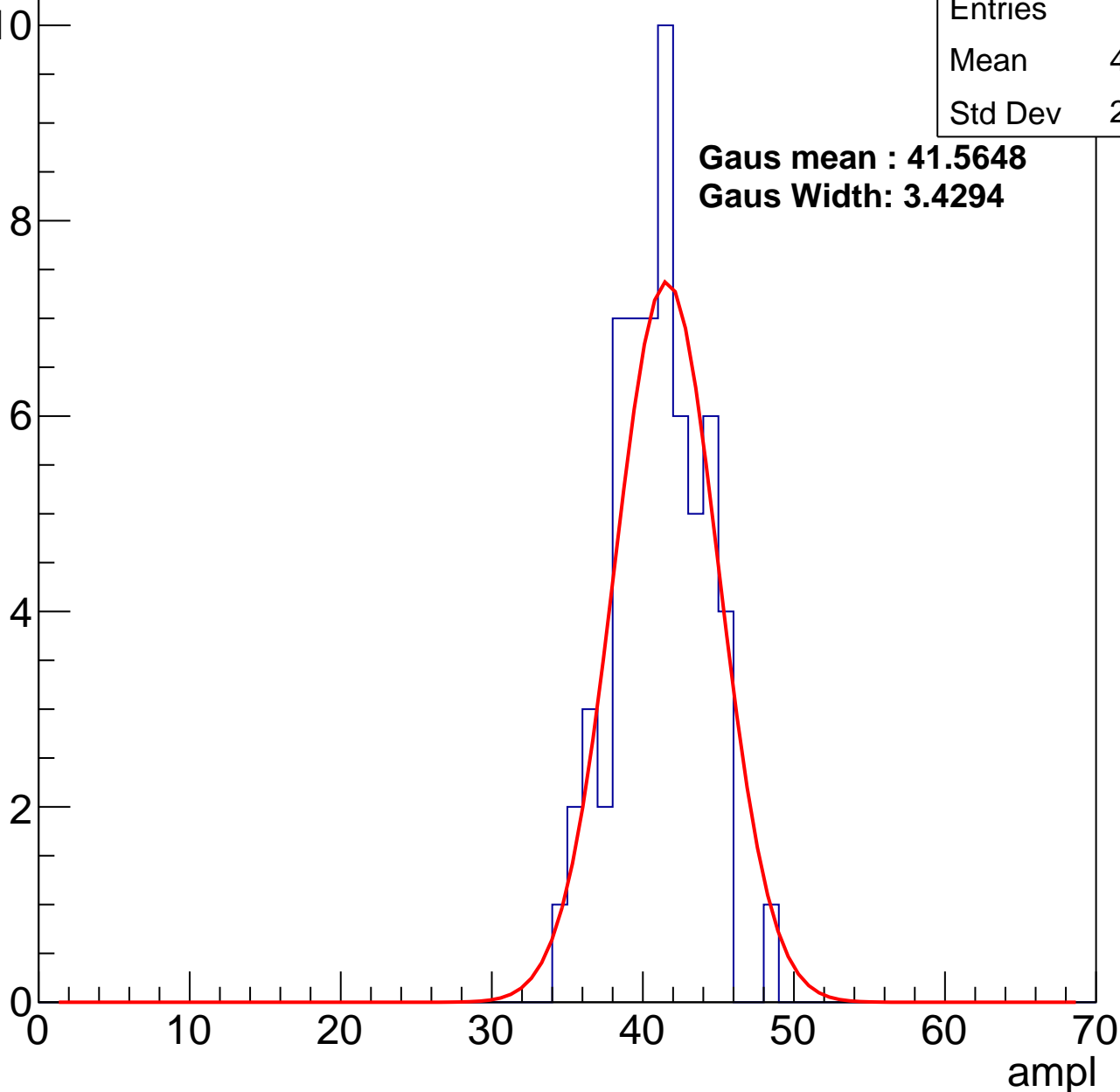
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	40.56
Std Dev	2.883

Gaus mean : 41.5648

Gaus Width: 3.4294

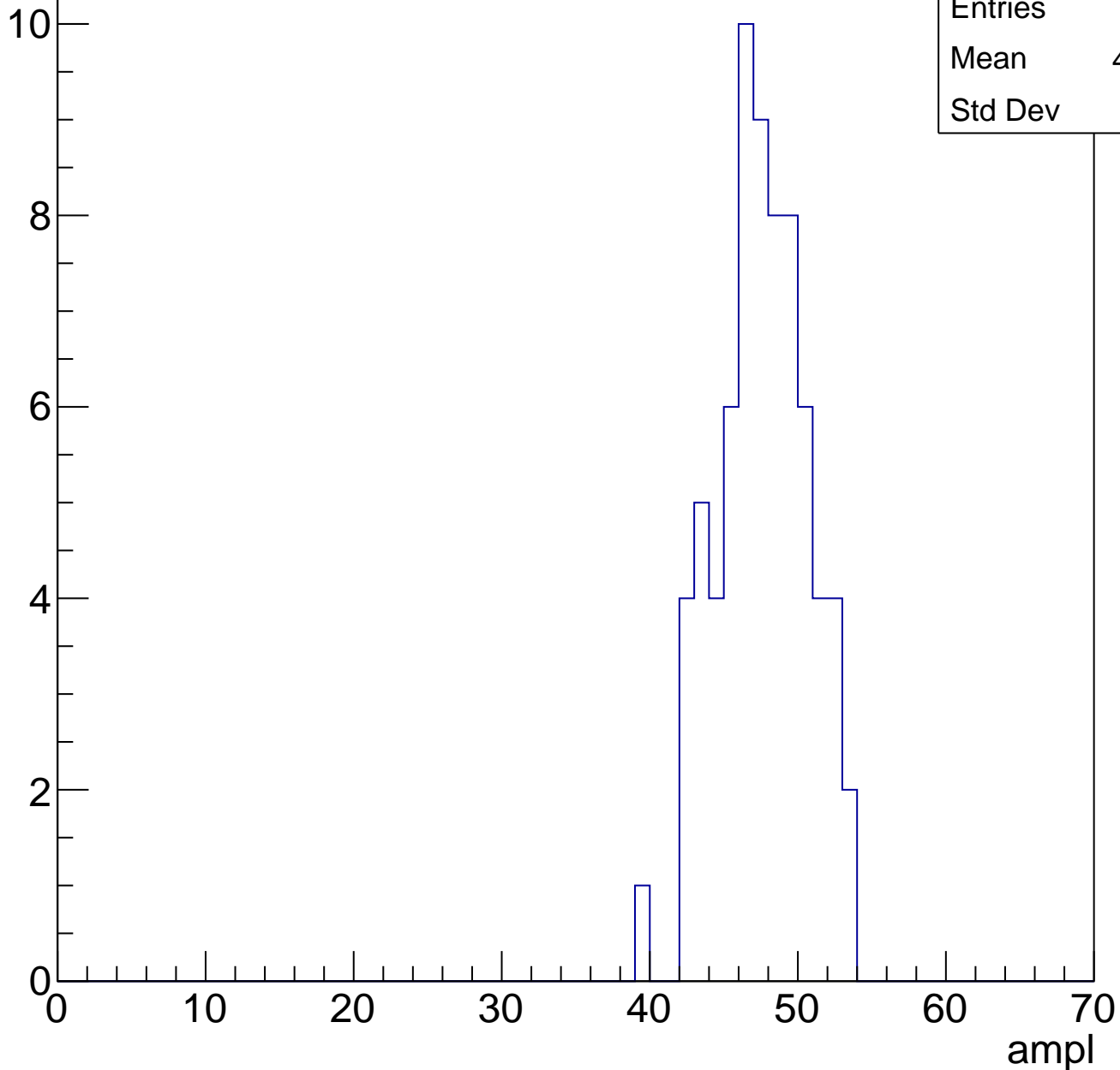


B1L003S, U21-ch32, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	47.11
Std Dev	3.01

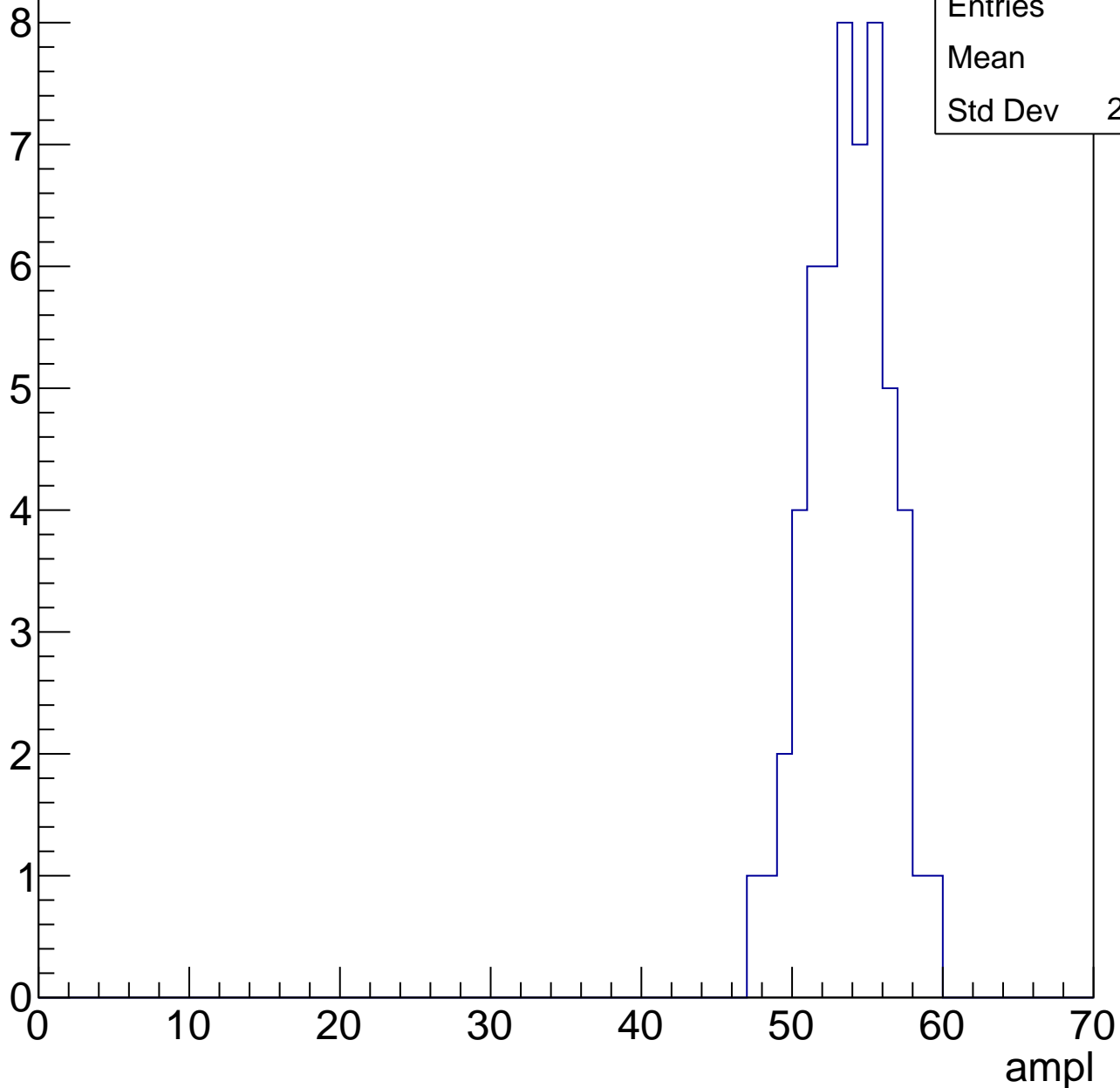
Entry



B1L003S, U21-ch32, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

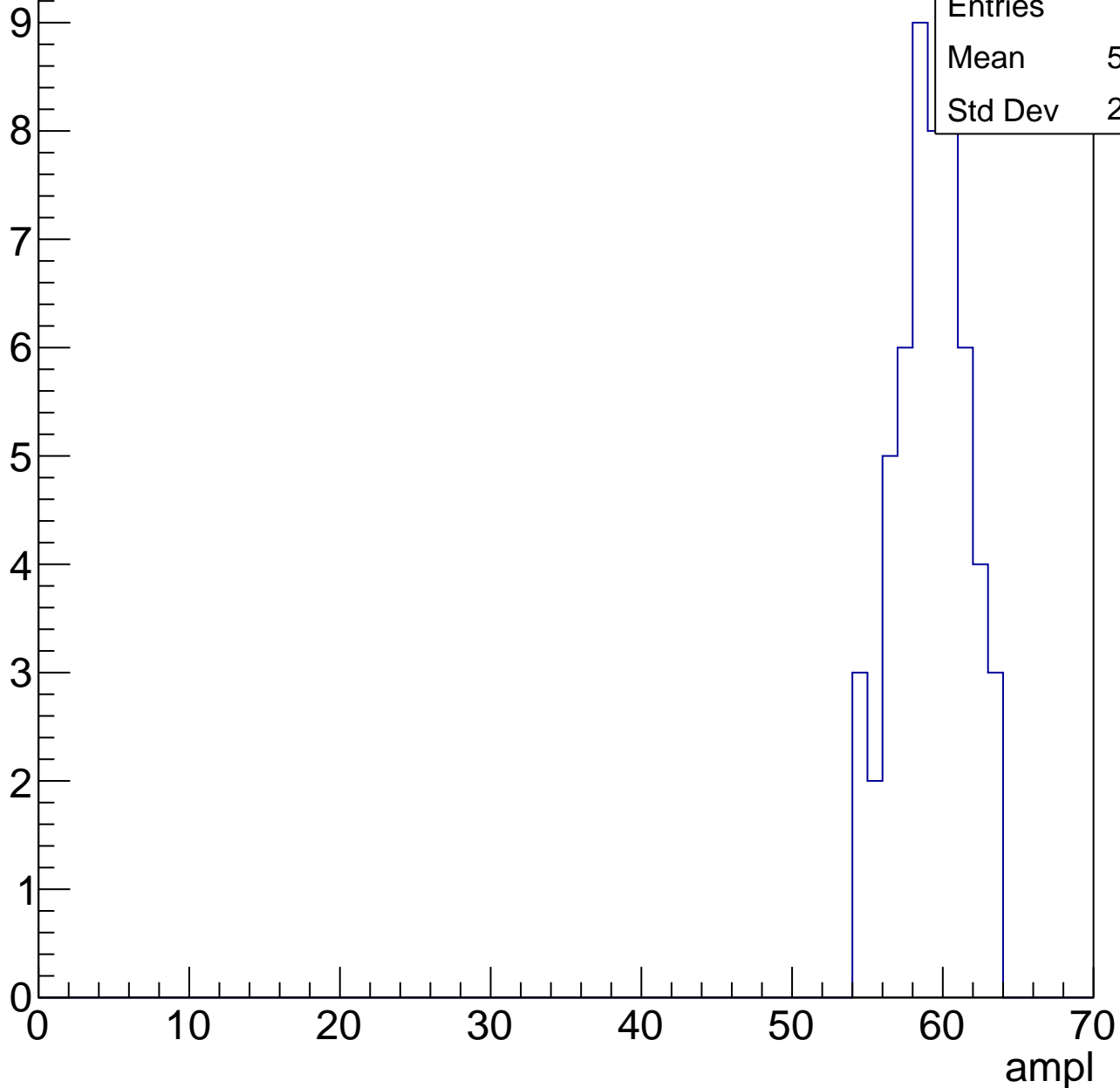


Entries	54
Mean	53.3
Std Dev	2.594

B1L003S, U21-ch32, adc5

calib_packv5_042523_0143.root, FC#13, port D2

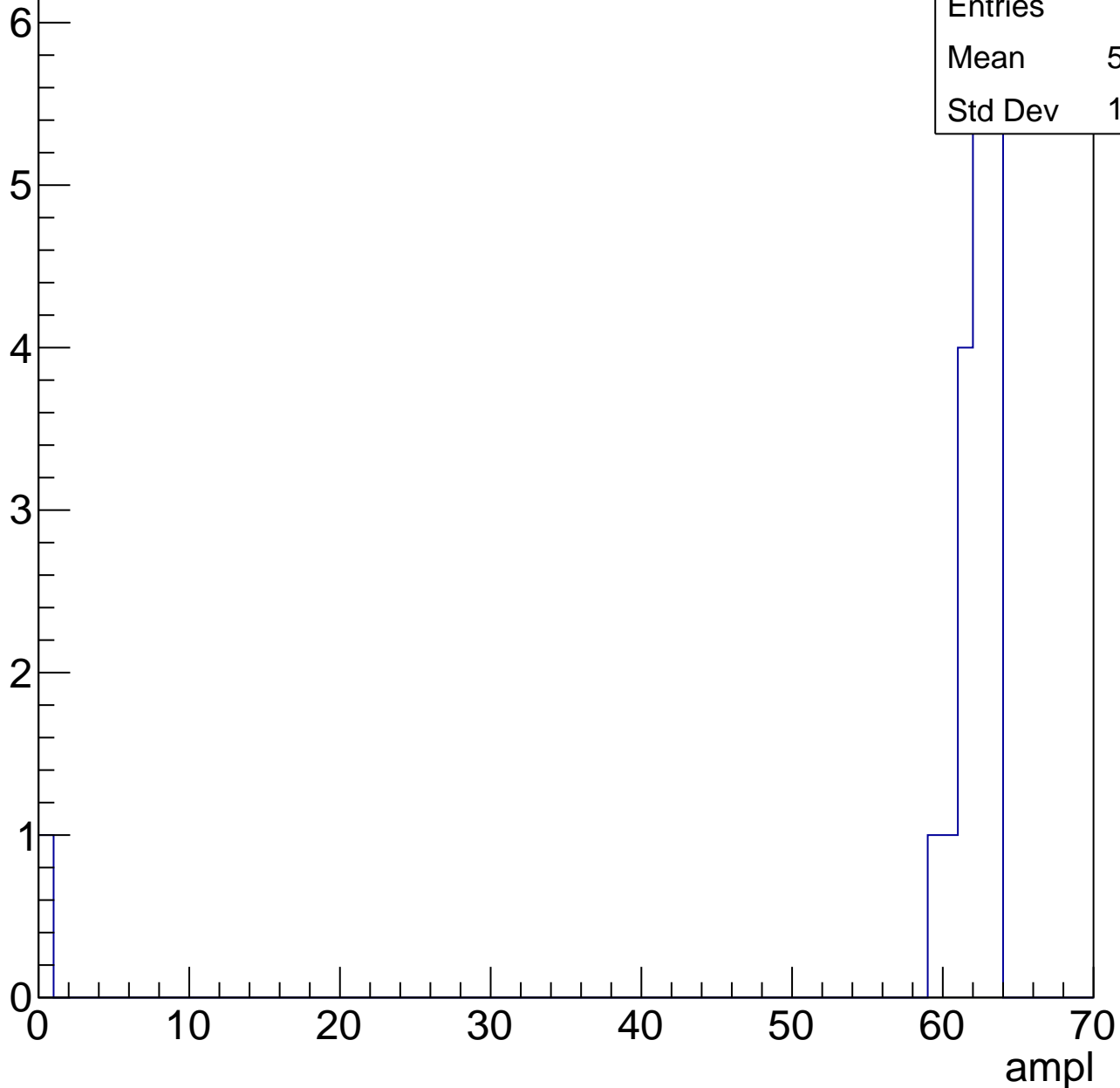
Entry



B1L003S, U21-ch32, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch32, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch33, adc0

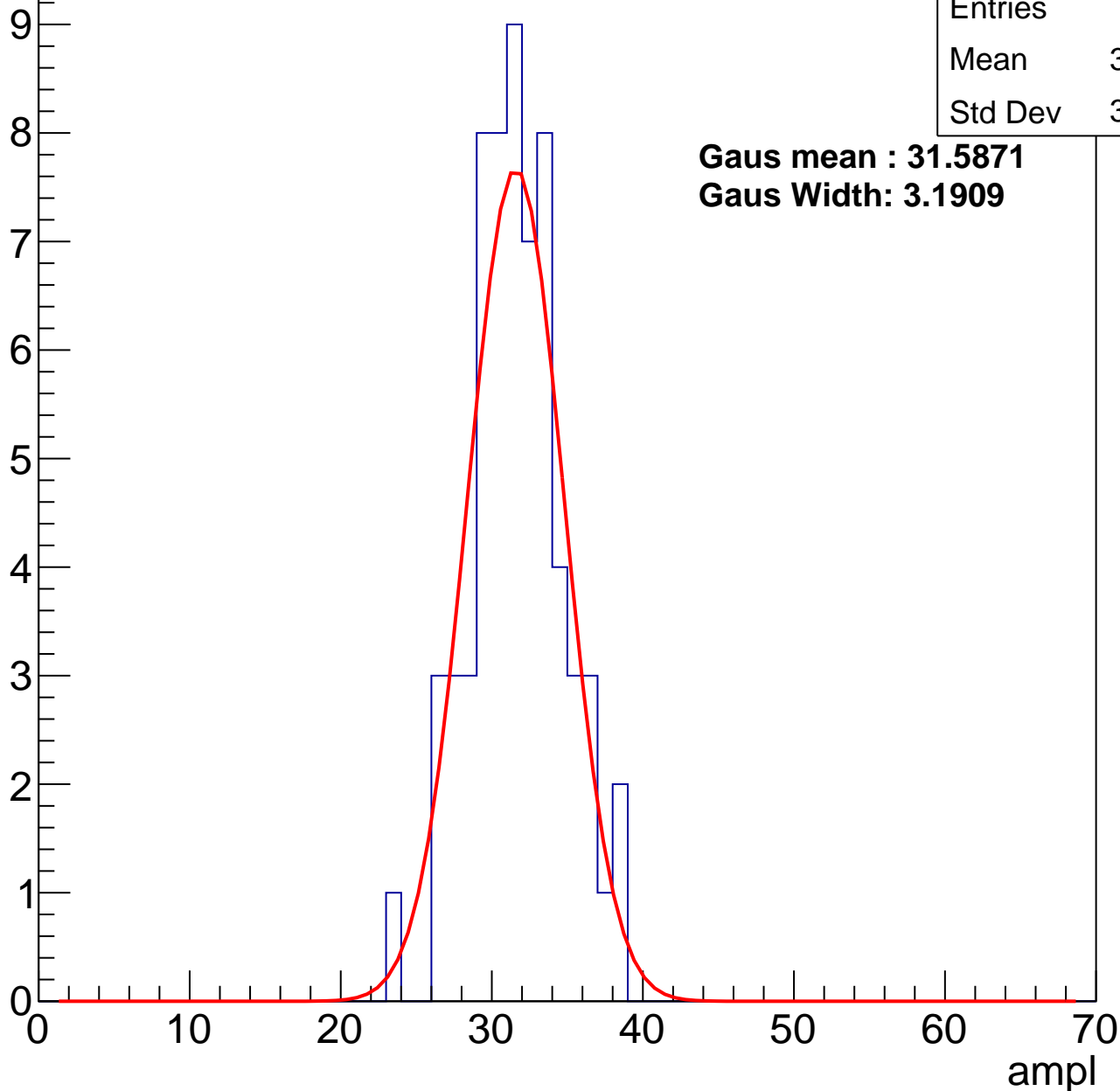
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	31.22
Std Dev	3.042

Gaus mean : 31.5871

Gaus Width: 3.1909



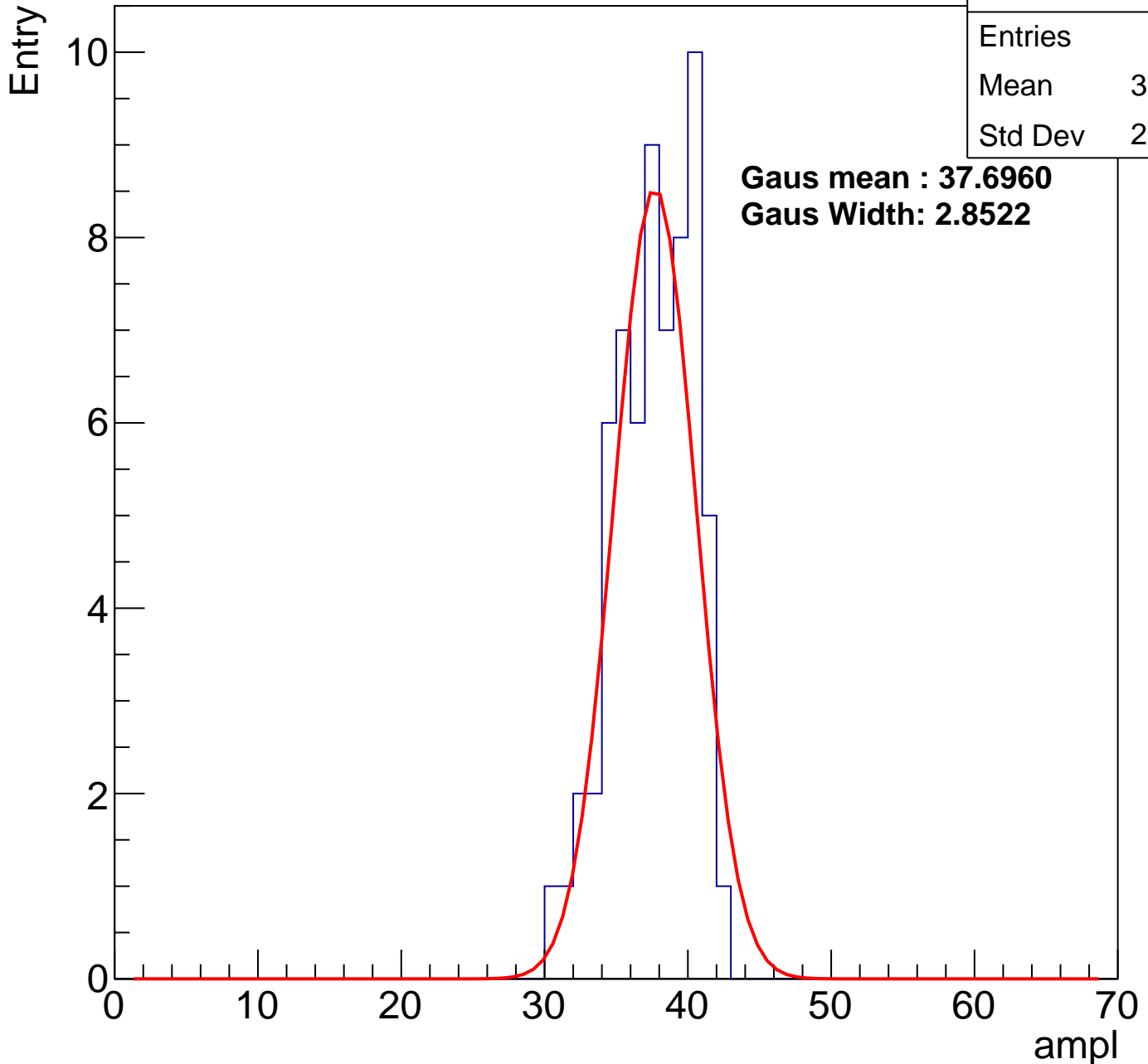
B1L003S, U21-ch33, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	37.14
Std Dev	2.739

Gaus mean : 37.6960

Gaus Width: 2.8522



B1L003S, U21-ch33, adc2

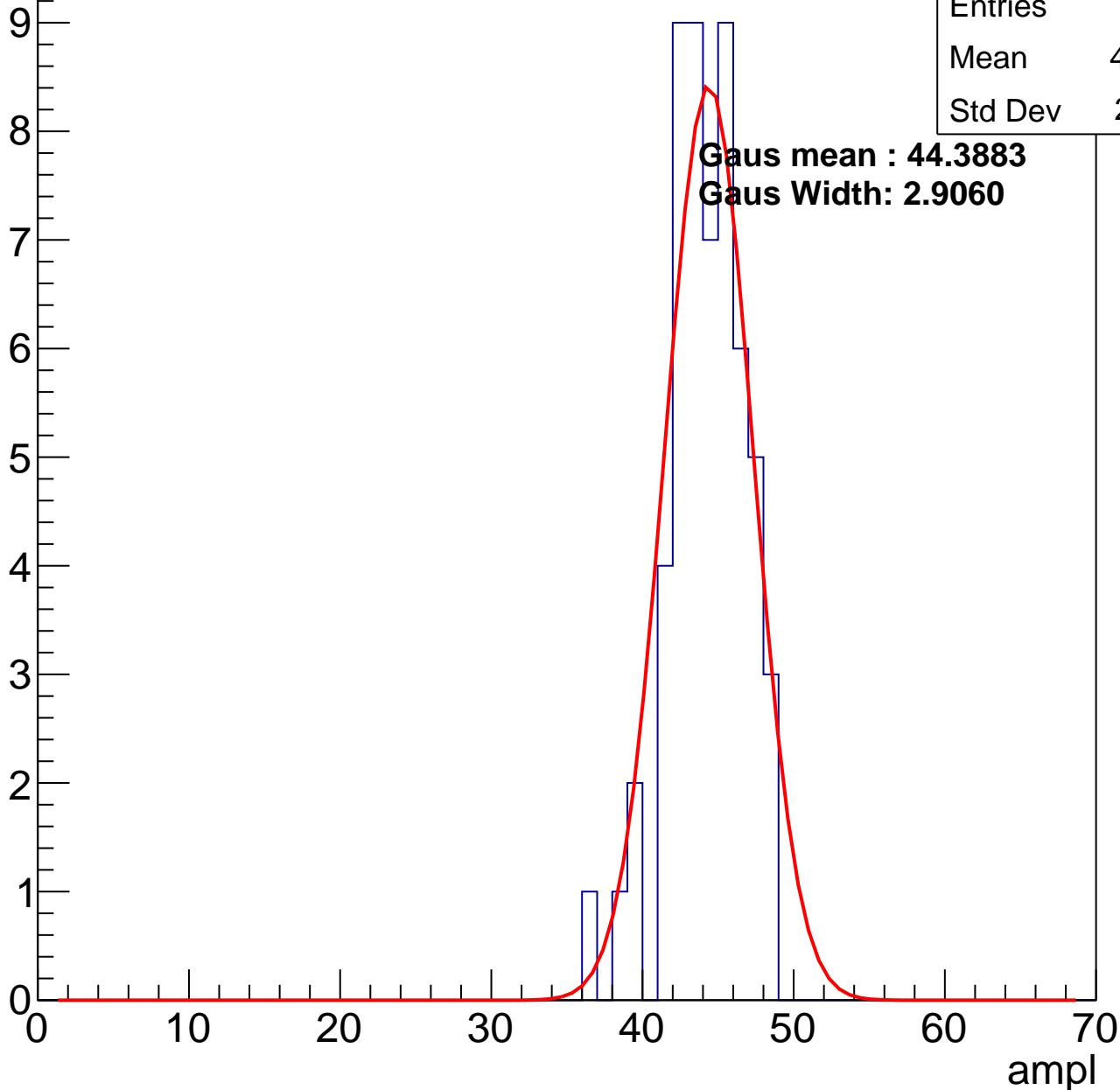
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.73
Std Dev	2.511

Gaus mean : 44.3883

Gaus Width: 2.9060



B1L003S, U21-ch33, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	50.05
Std Dev	3.237

Entry

10

8

6

4

2

0

0

10

20

30

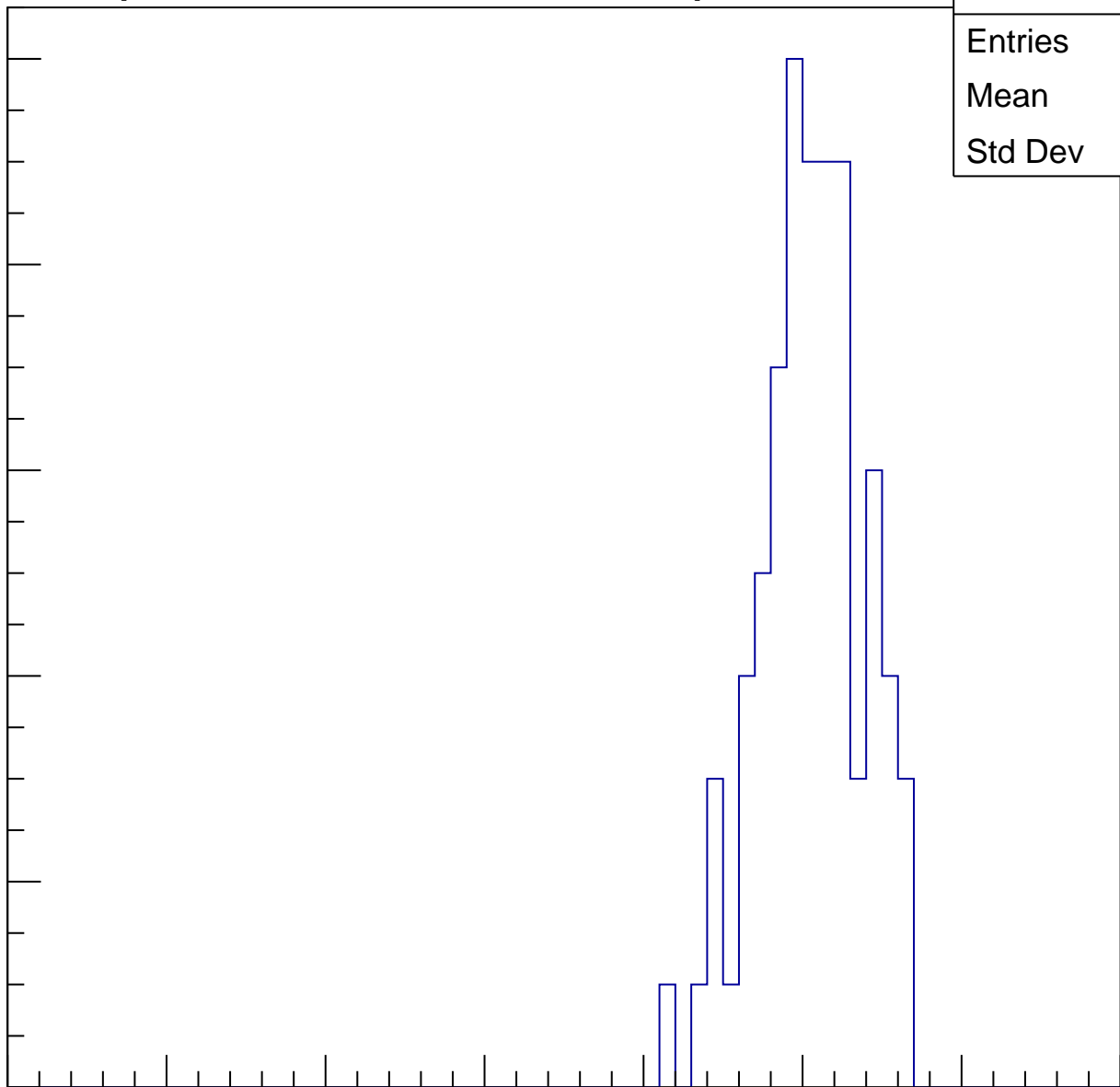
40

50

60

70

ampl

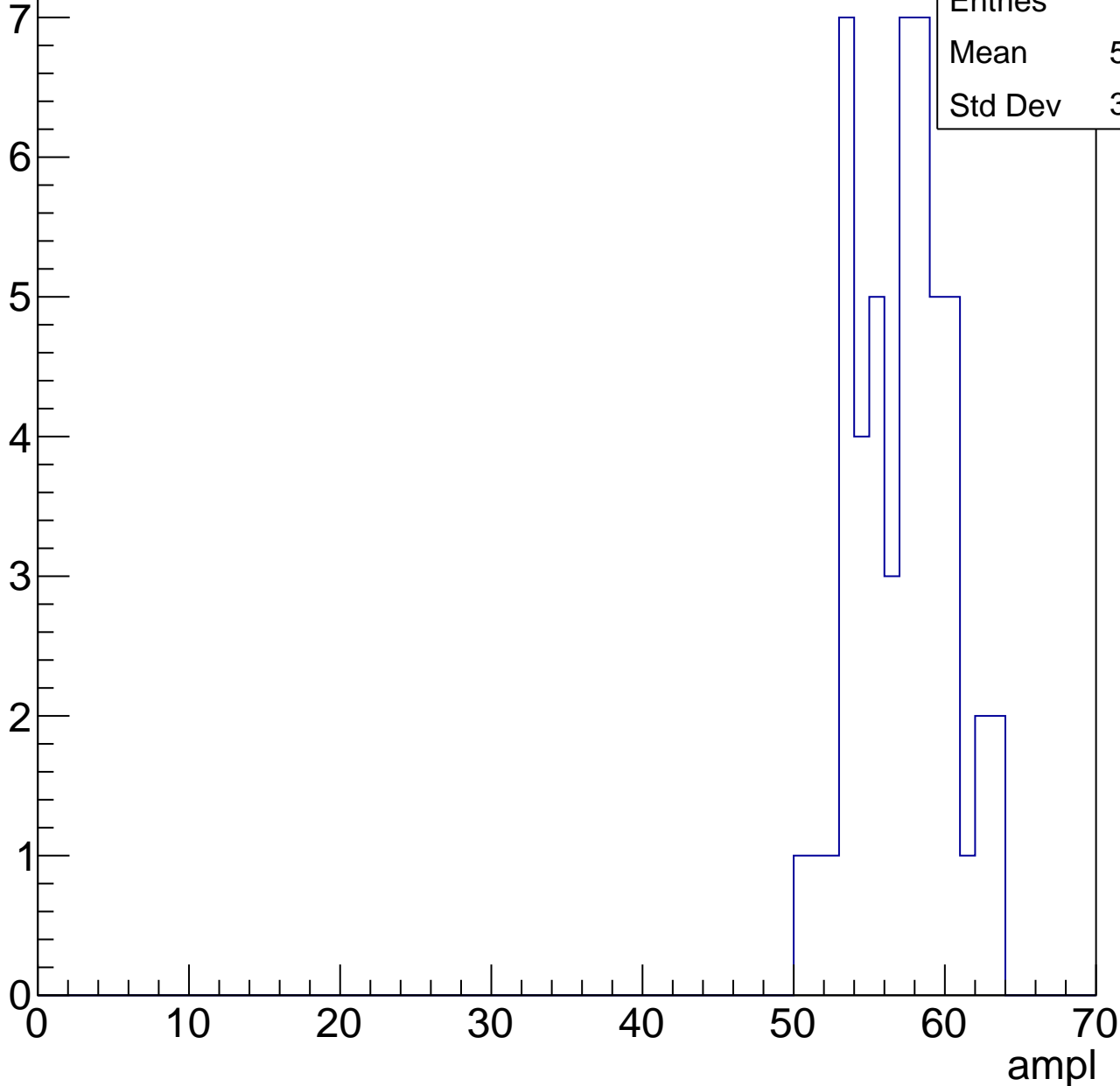


B1L003S, U21-ch33, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.75
Std Dev	3.092

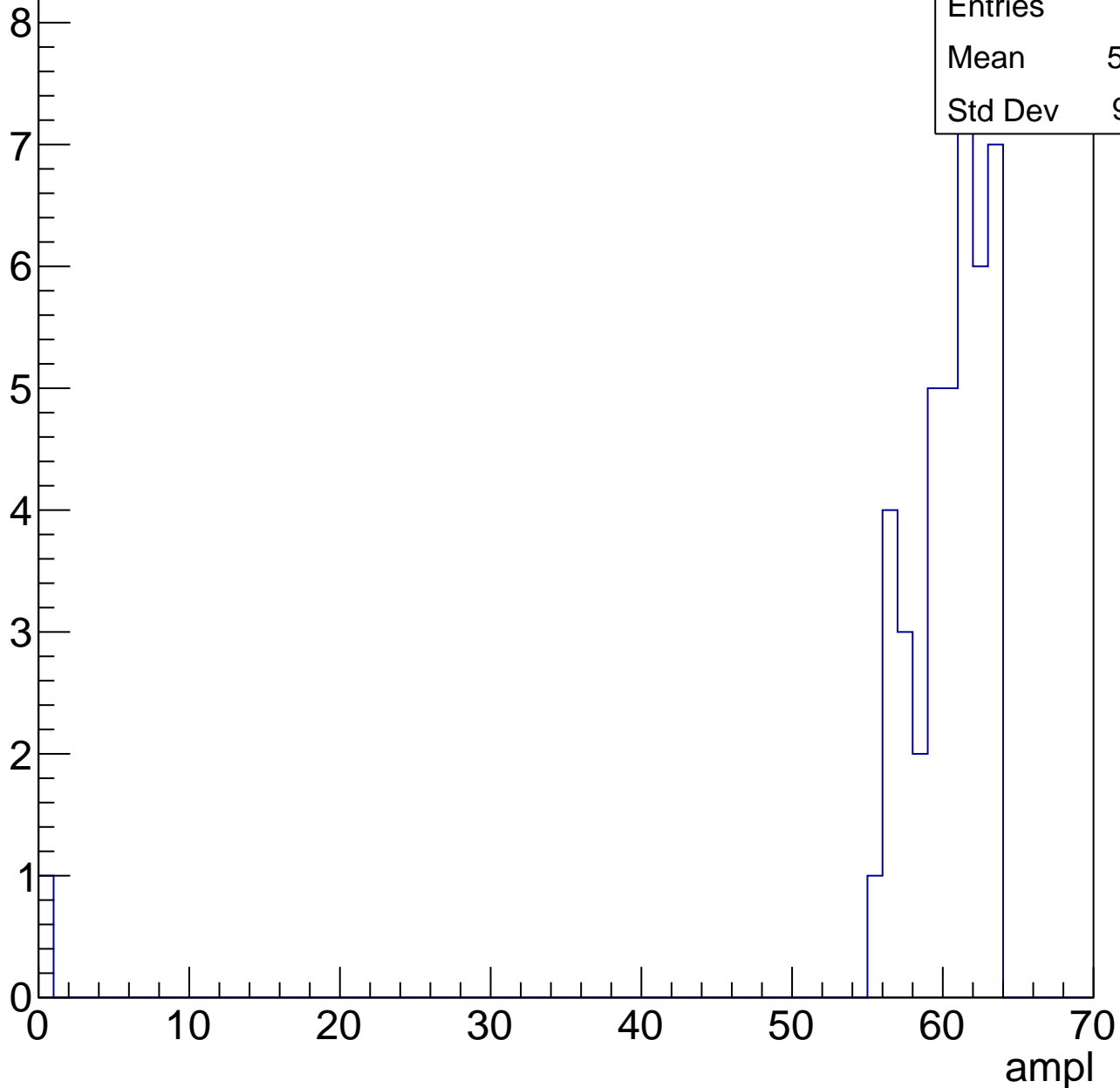


B1L003S, U21-ch33, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

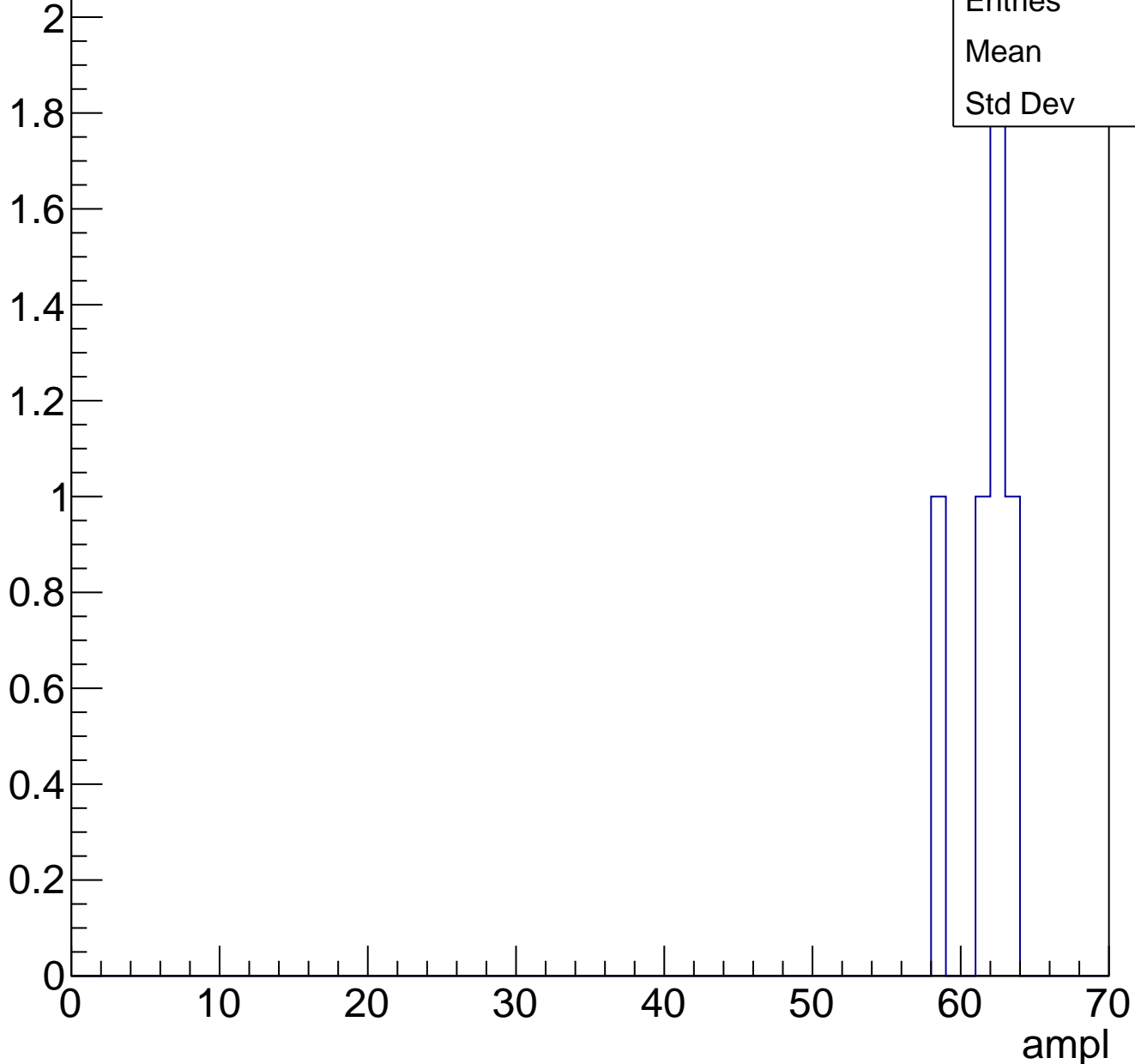
Entries	42
Mean	58.62
Std Dev	9.441



B1L003S, U21-ch33, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	5
Mean	61.2
Std Dev	1.72

B1L003S, U21-ch33, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch34, adc0

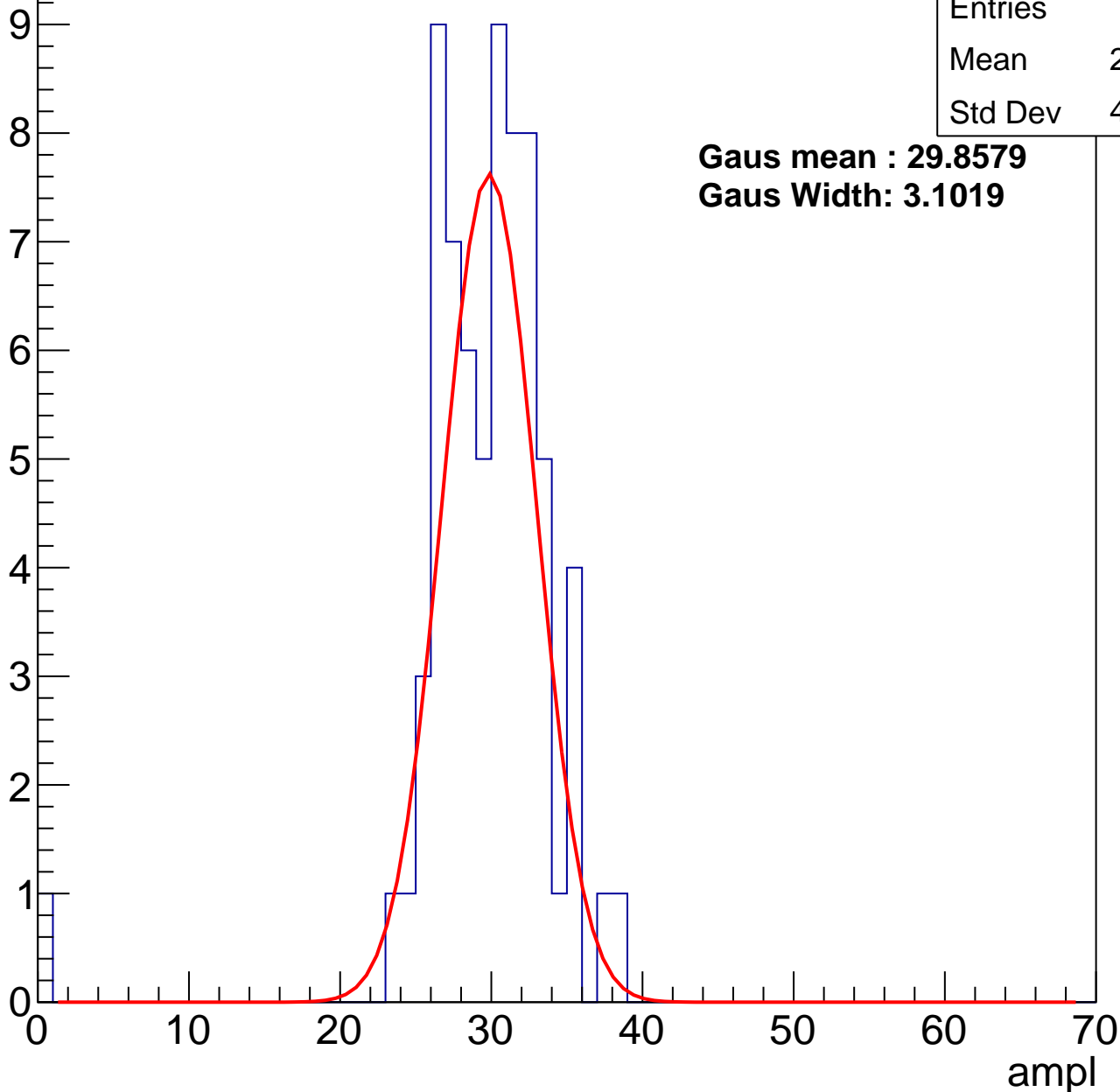
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.23
Std Dev	4.733

Gaus mean : 29.8579

Gaus Width: 3.1019



B1L003S, U21-ch34, adc1

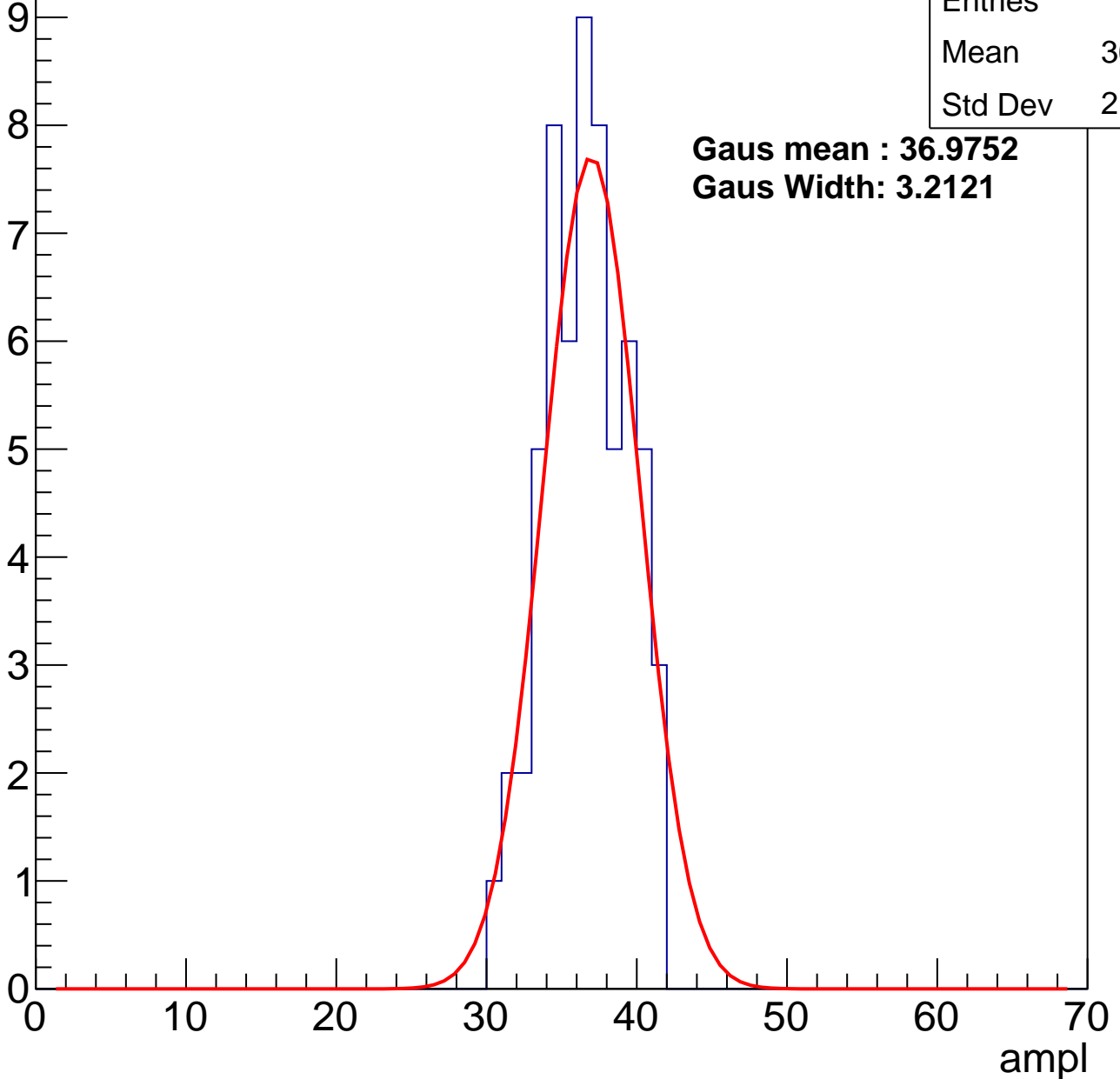
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.17
Std Dev	2.697

Gaus mean : 36.9752

Gaus Width: 3.2121



B1L003S, U21-ch34, adc2

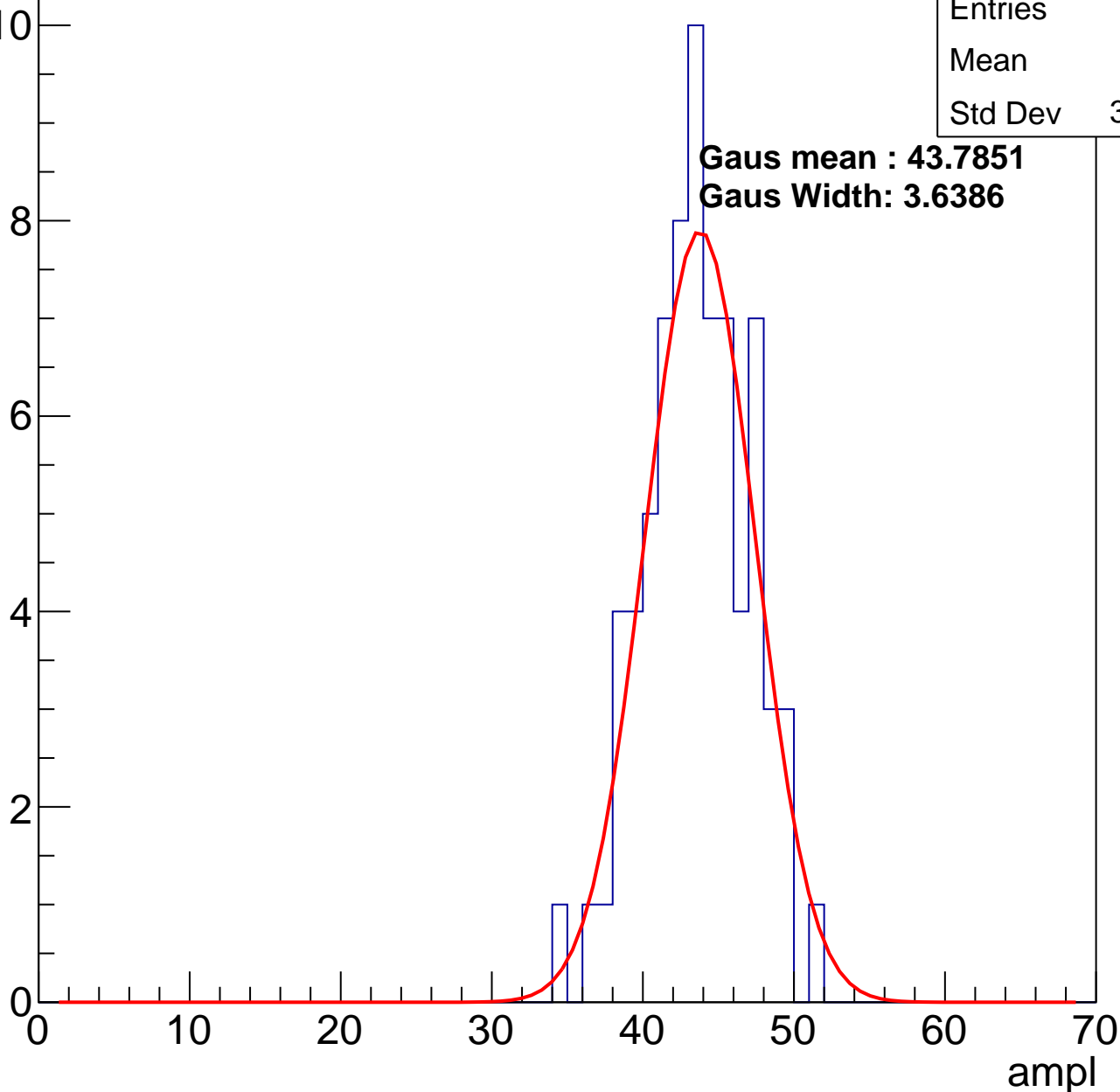
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	43.1
Std Dev	3.393

Gaus mean : 43.7851

Gaus Width: 3.6386

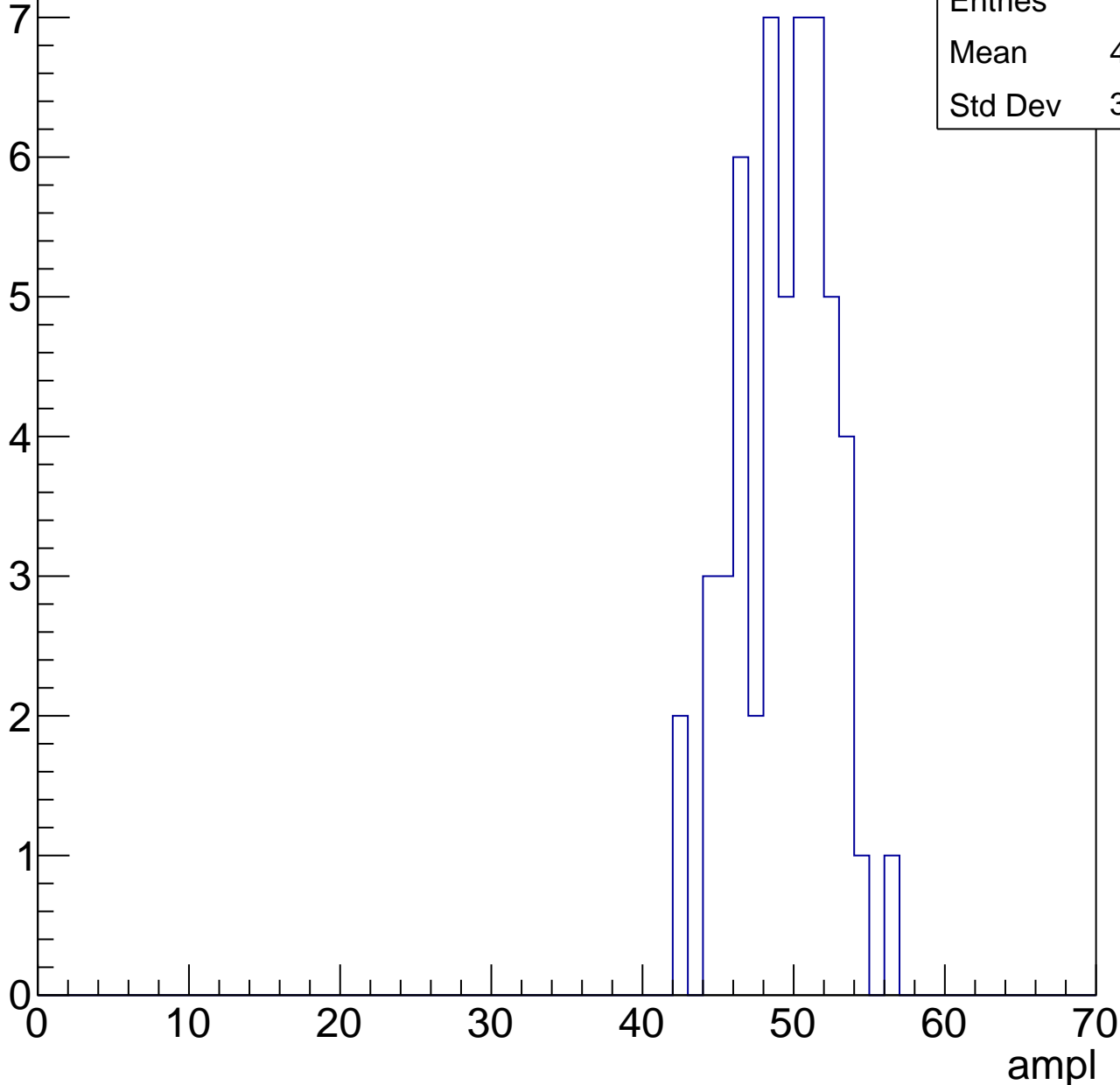


B1L003S, U21-ch34, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	48.89
Std Dev	3.094



B1L003S, U21-ch34, adc4

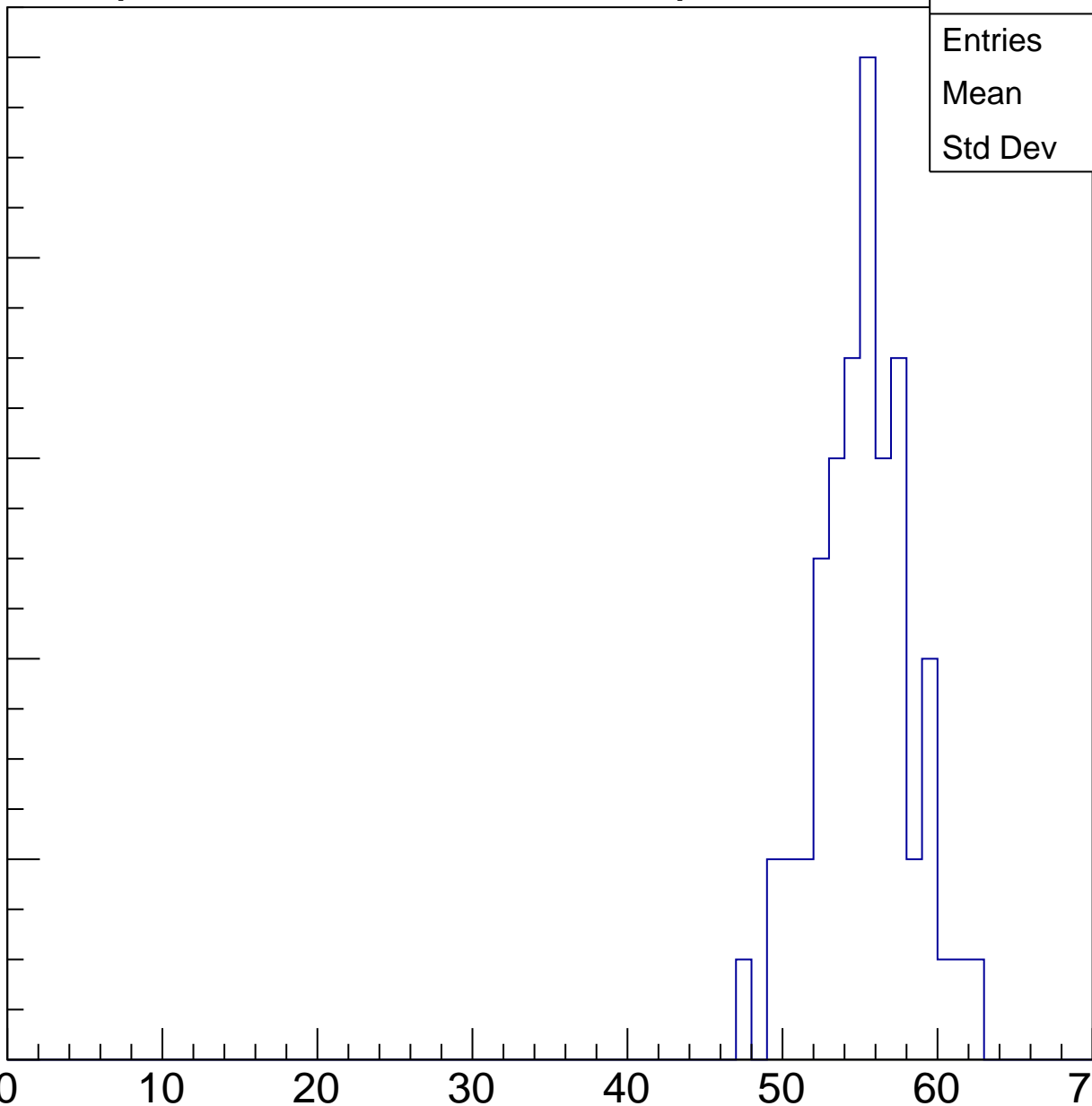
calib_packv5_042523_0143.root, FC#13, port D2

Entry

10
8
6
4
2
0

Entries	57
Mean	54.79
Std Dev	3.013

ampl

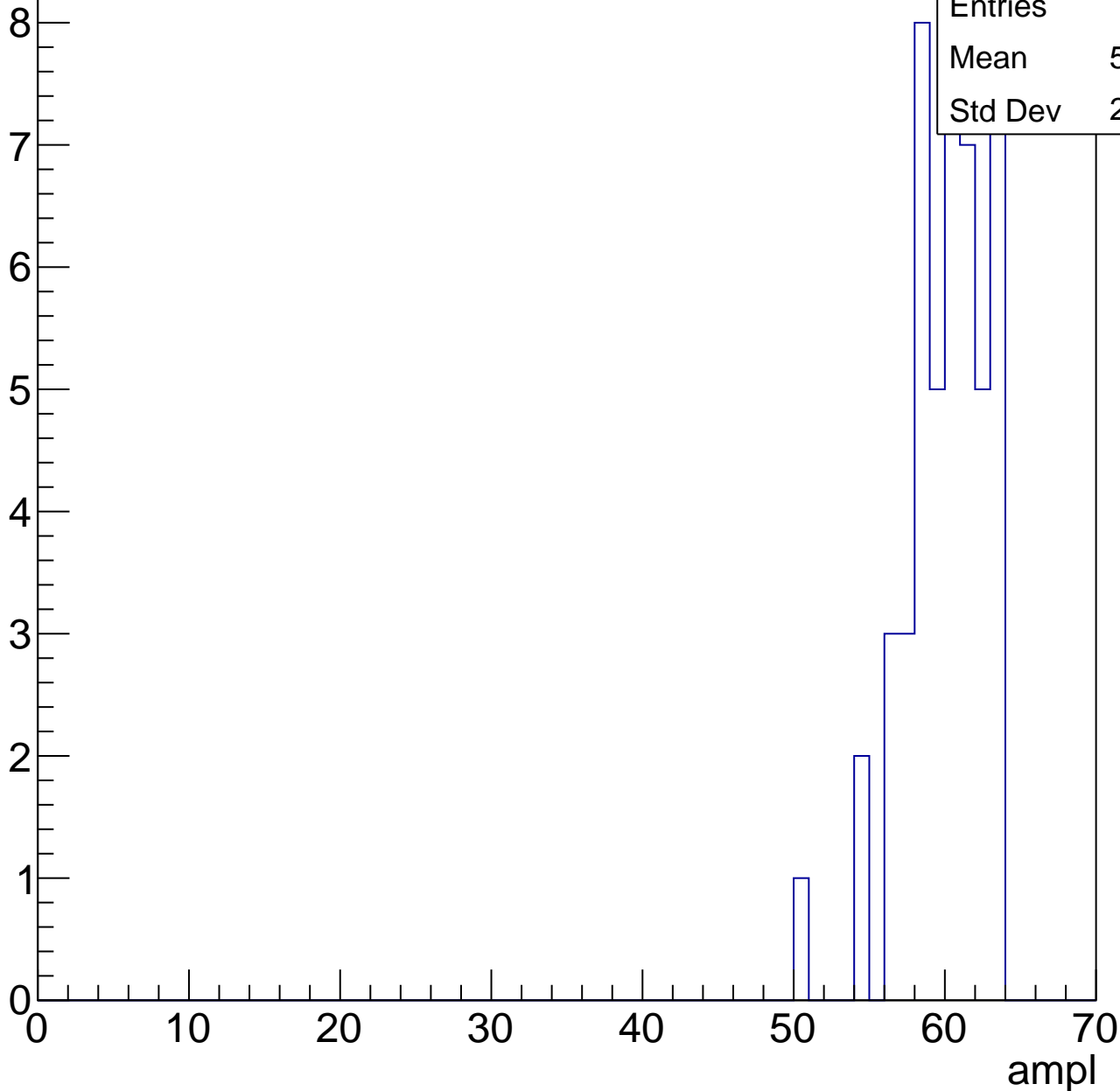


B1L003S, U21-ch34, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

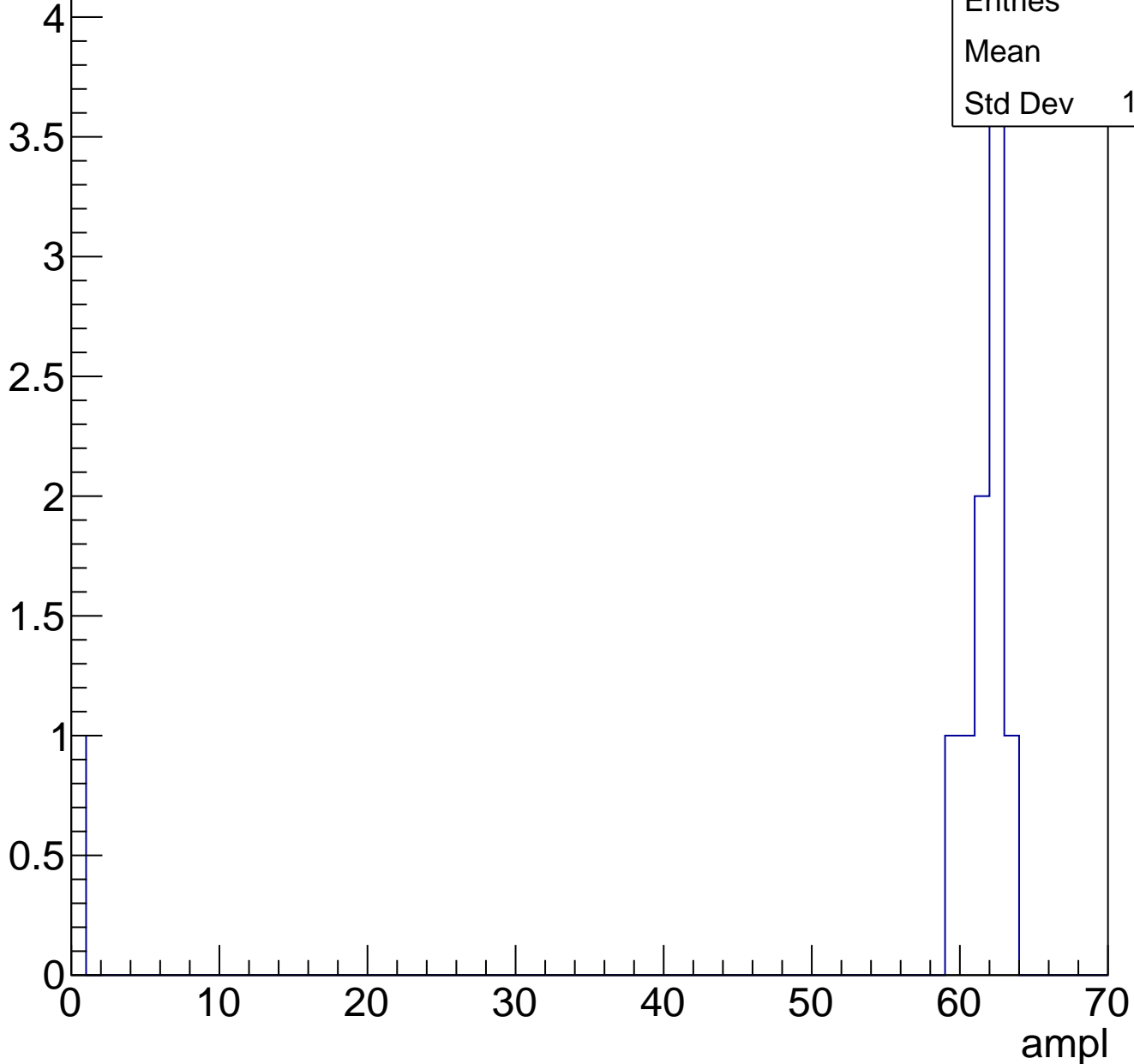
Entries	50
Mean	59.54
Std Dev	2.729



B1L003S, U21-ch34, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch34, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	31.5
Std Dev	31.5

B1L003S, U21-ch35, adc0

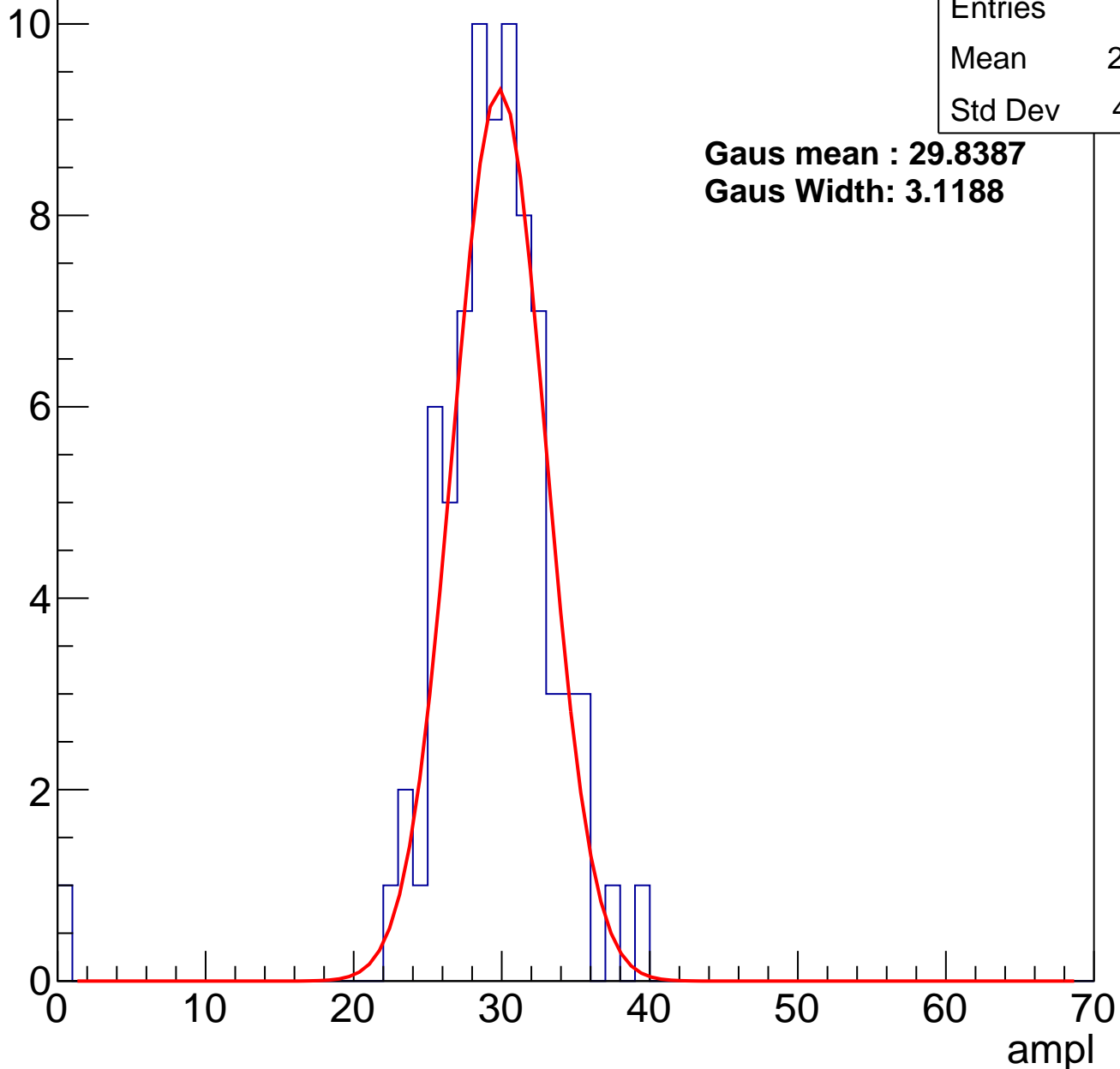
calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	28.92
Std Dev	4.621

Gaus mean : 29.8387

Gaus Width: 3.1188

Entry



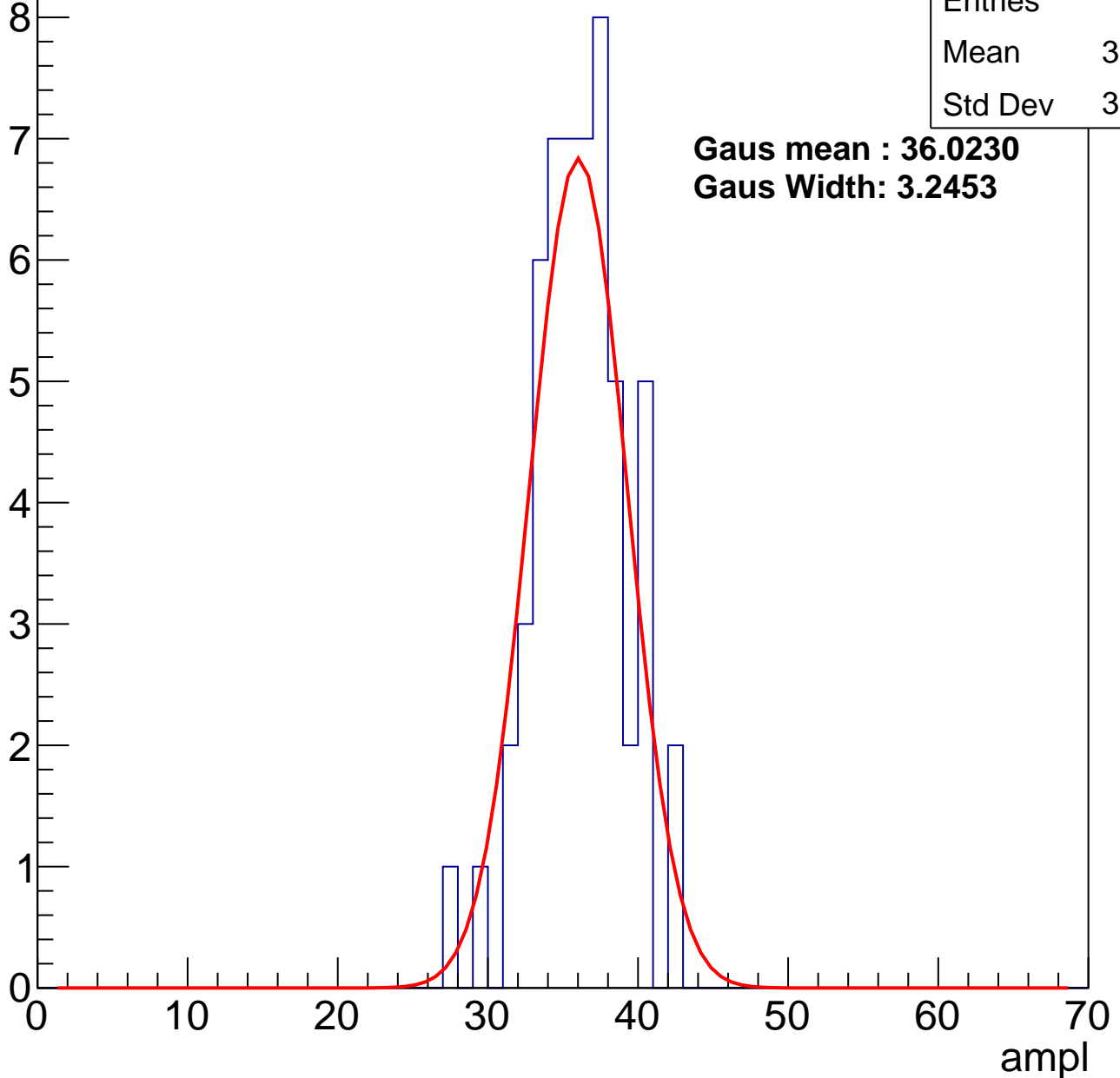
B1L003S, U21-ch35, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35.62
Std Dev	3.009

Gaus mean : 36.0230
Gaus Width: 3.2453



B1L003S, U21-ch35, adc2

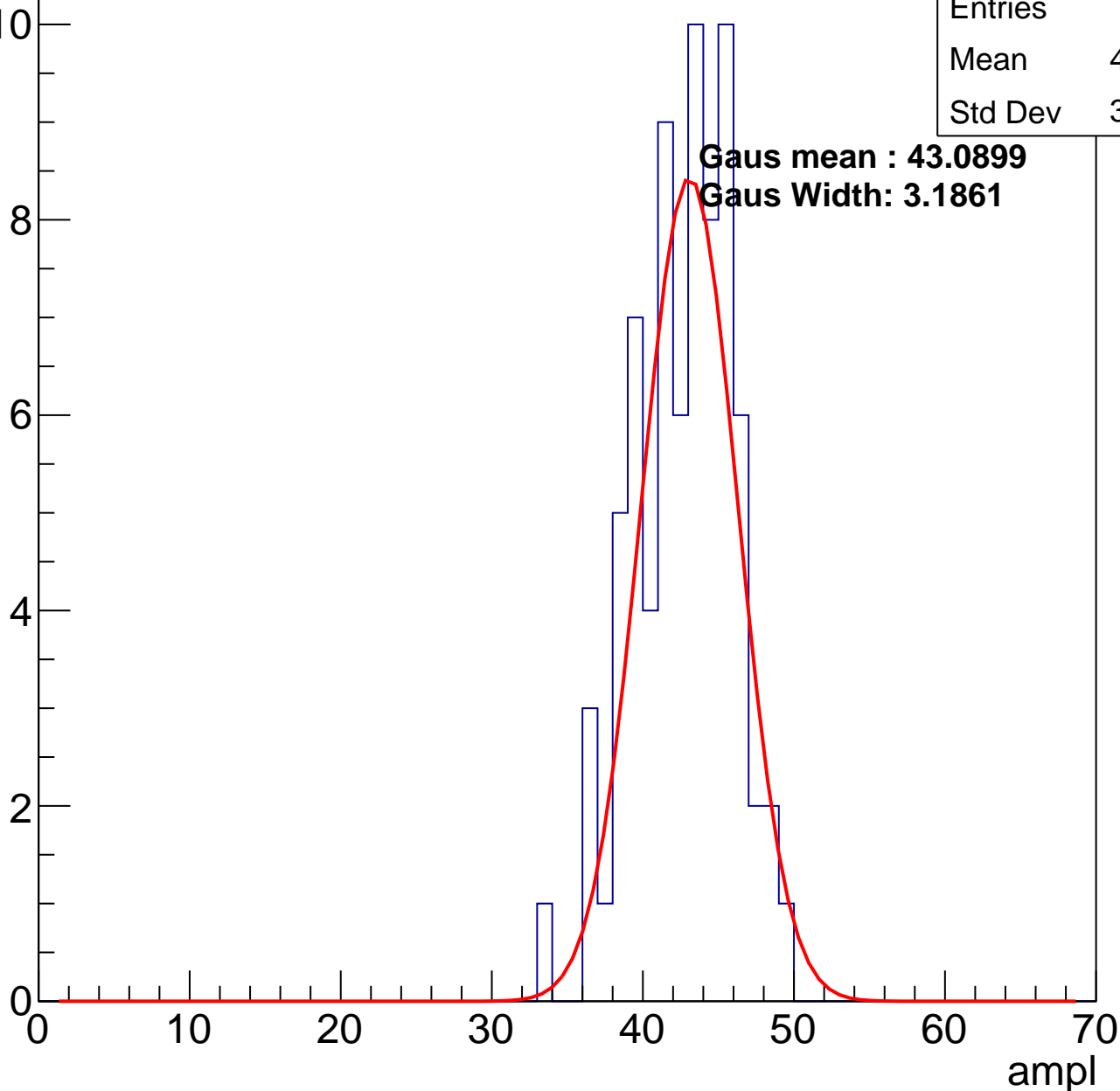
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	42.25
Std Dev	3.217

Gaus mean : 43.0899

Gaus Width: 3.1861

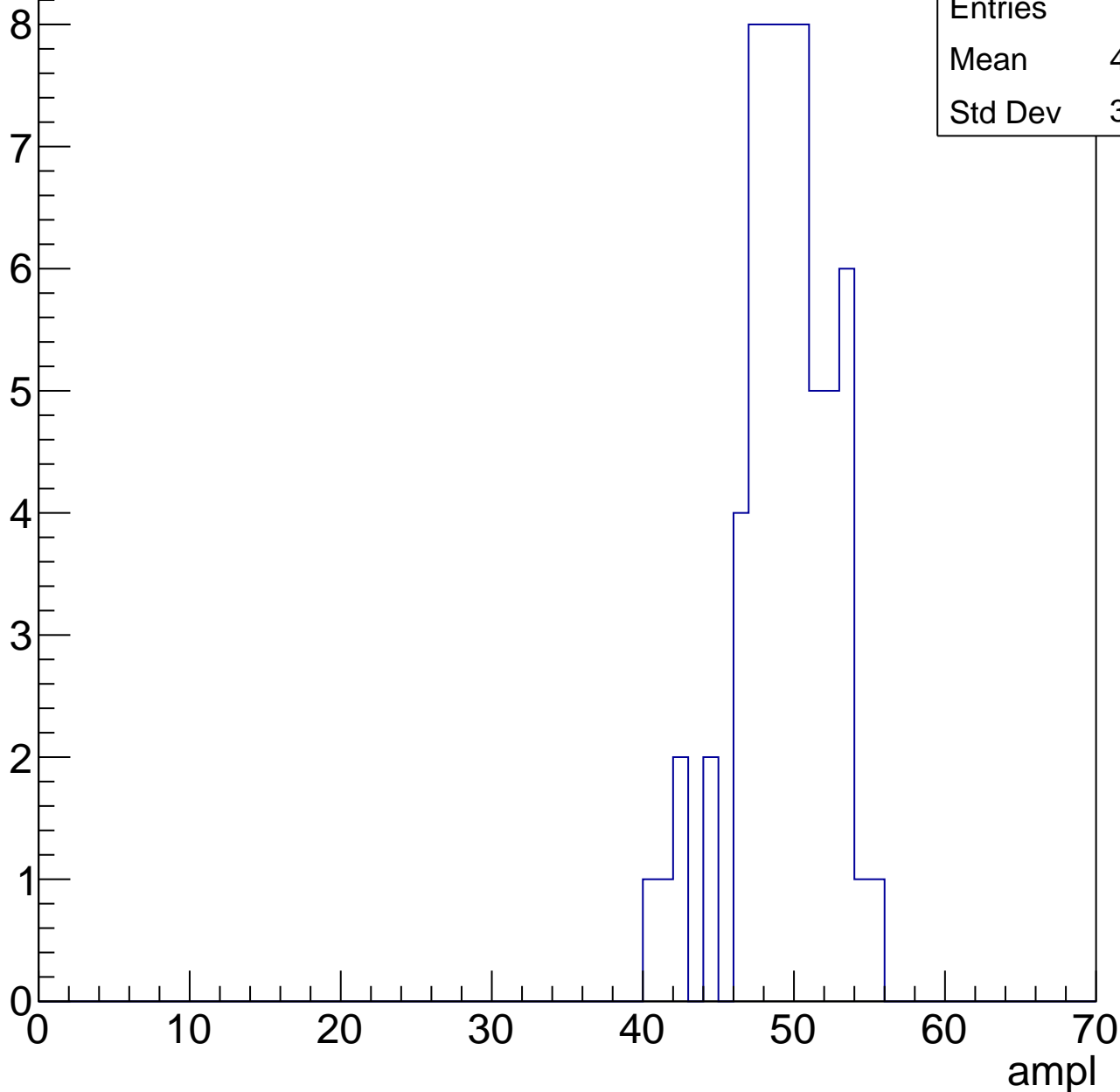


B1L003S, U21-ch35, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

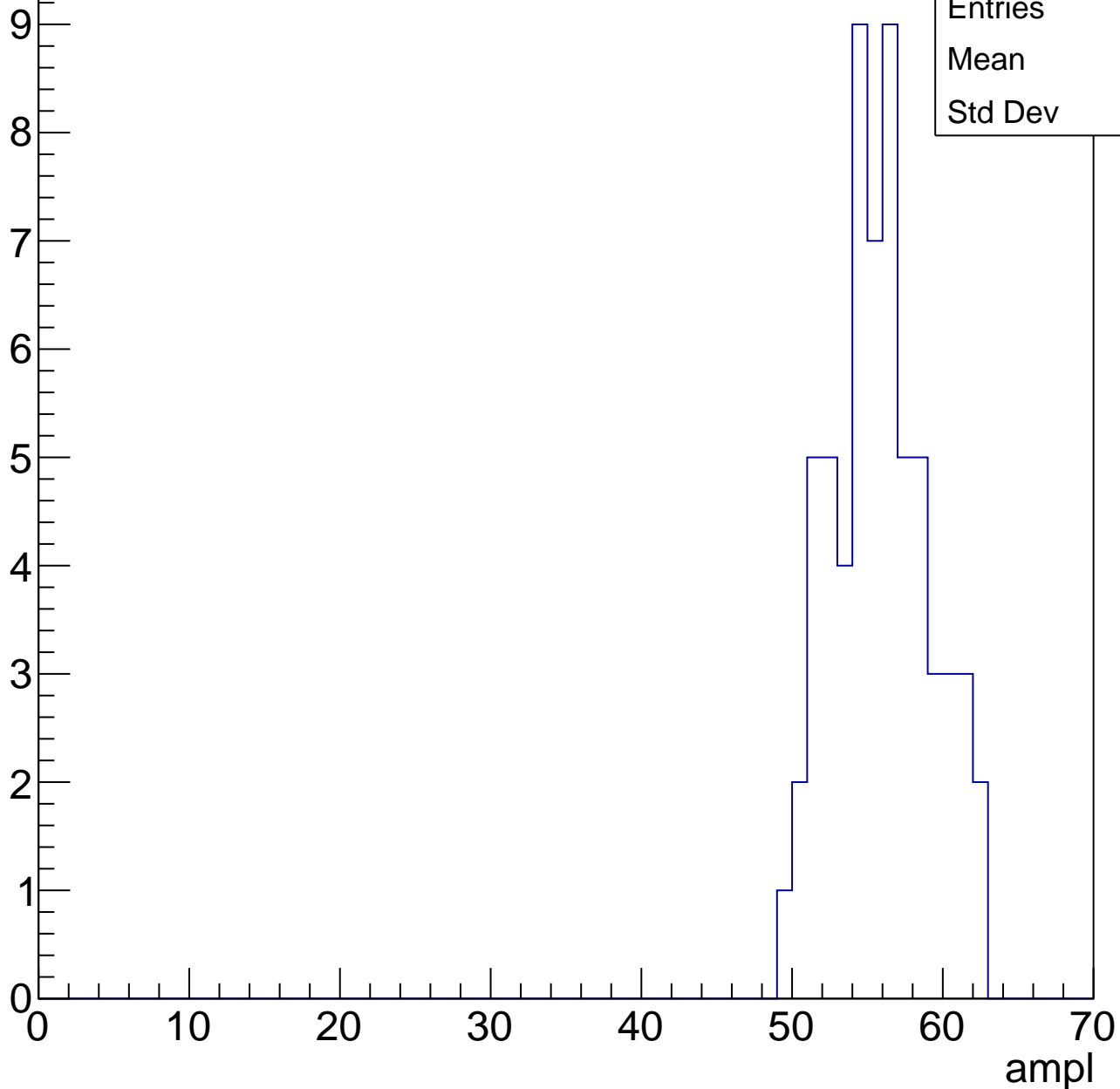
Entries	60
Mean	48.85
Std Dev	3.156



B1L003S, U21-ch35, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



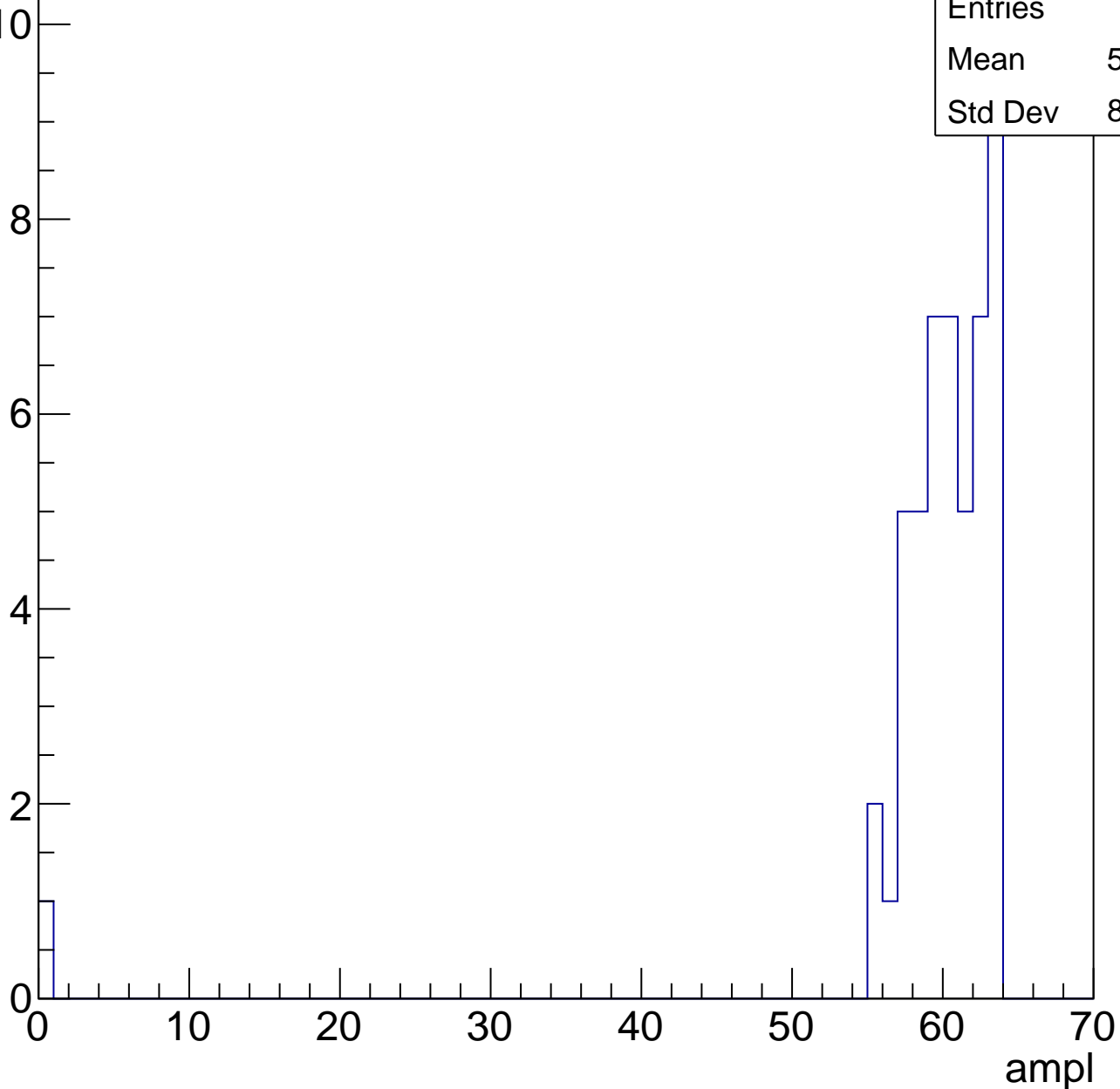
Entries	63
Mean	55.4
Std Dev	3.16

B1L003S, U21-ch35, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

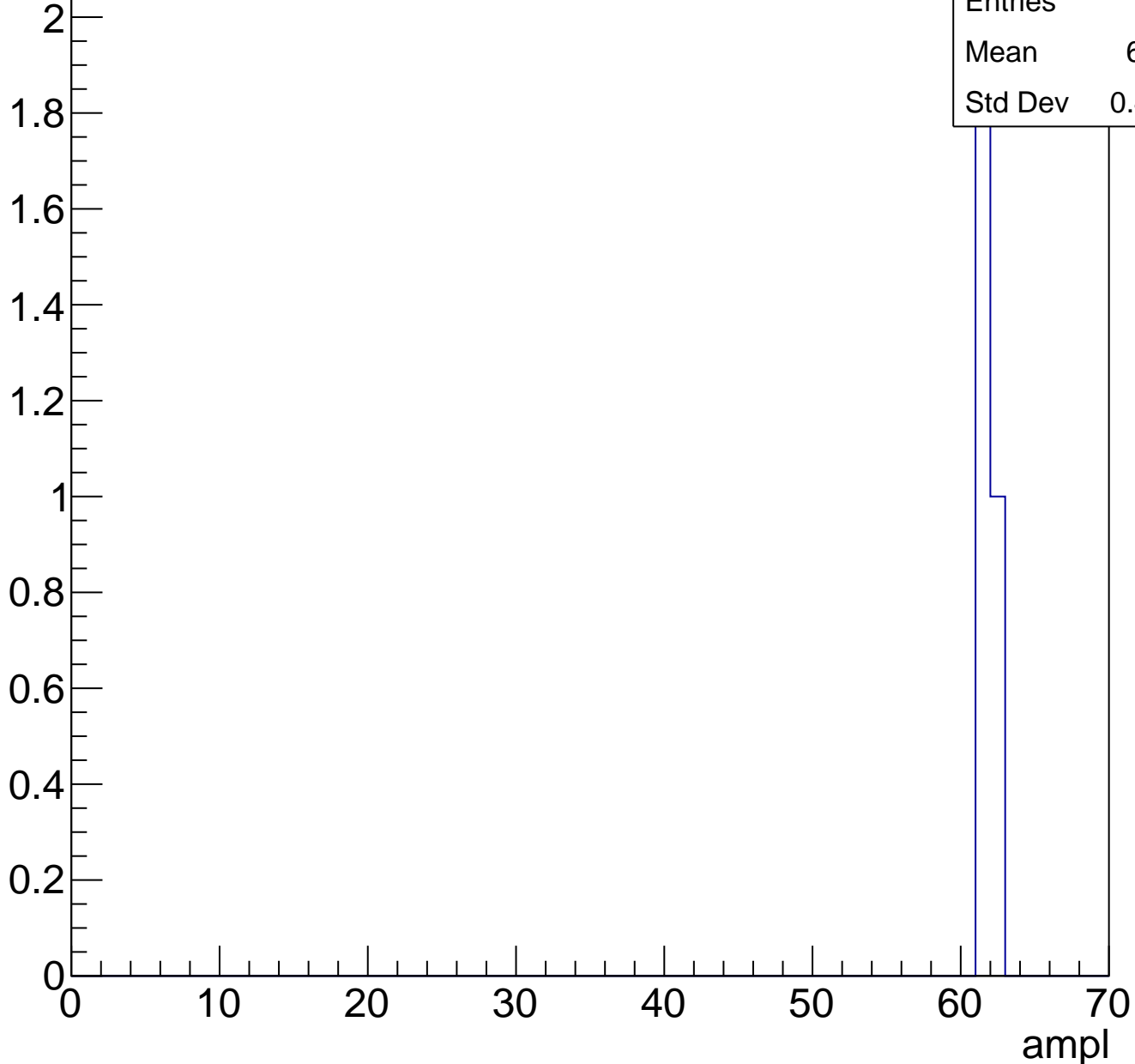
Entries	50
Mean	58.86
Std Dev	8.713



B1L003S, U21-ch35, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch35, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L003S, U21-ch36, adc0

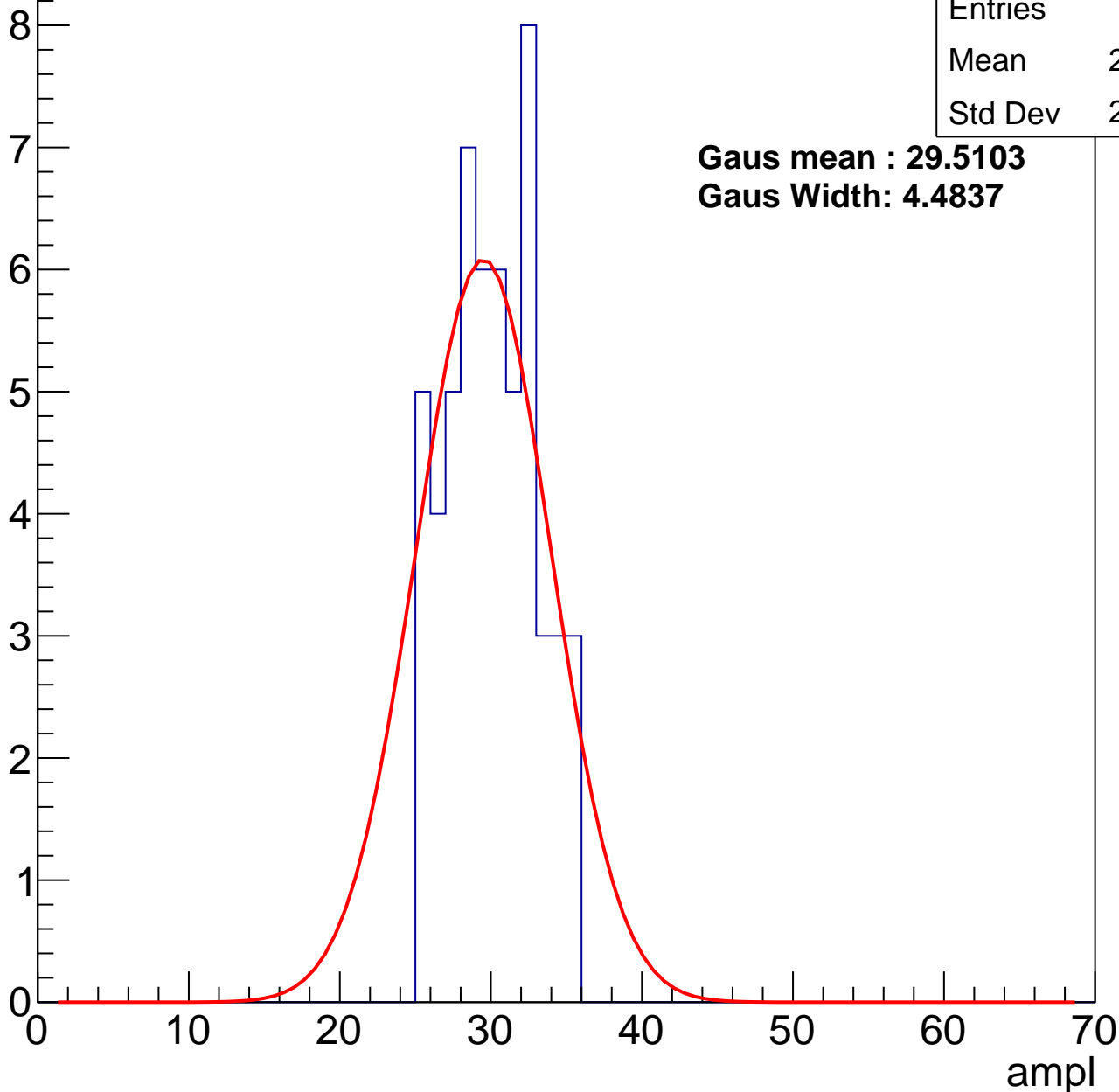
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	29.65
Std Dev	2.855

Gaus mean : 29.5103

Gaus Width: 4.4837



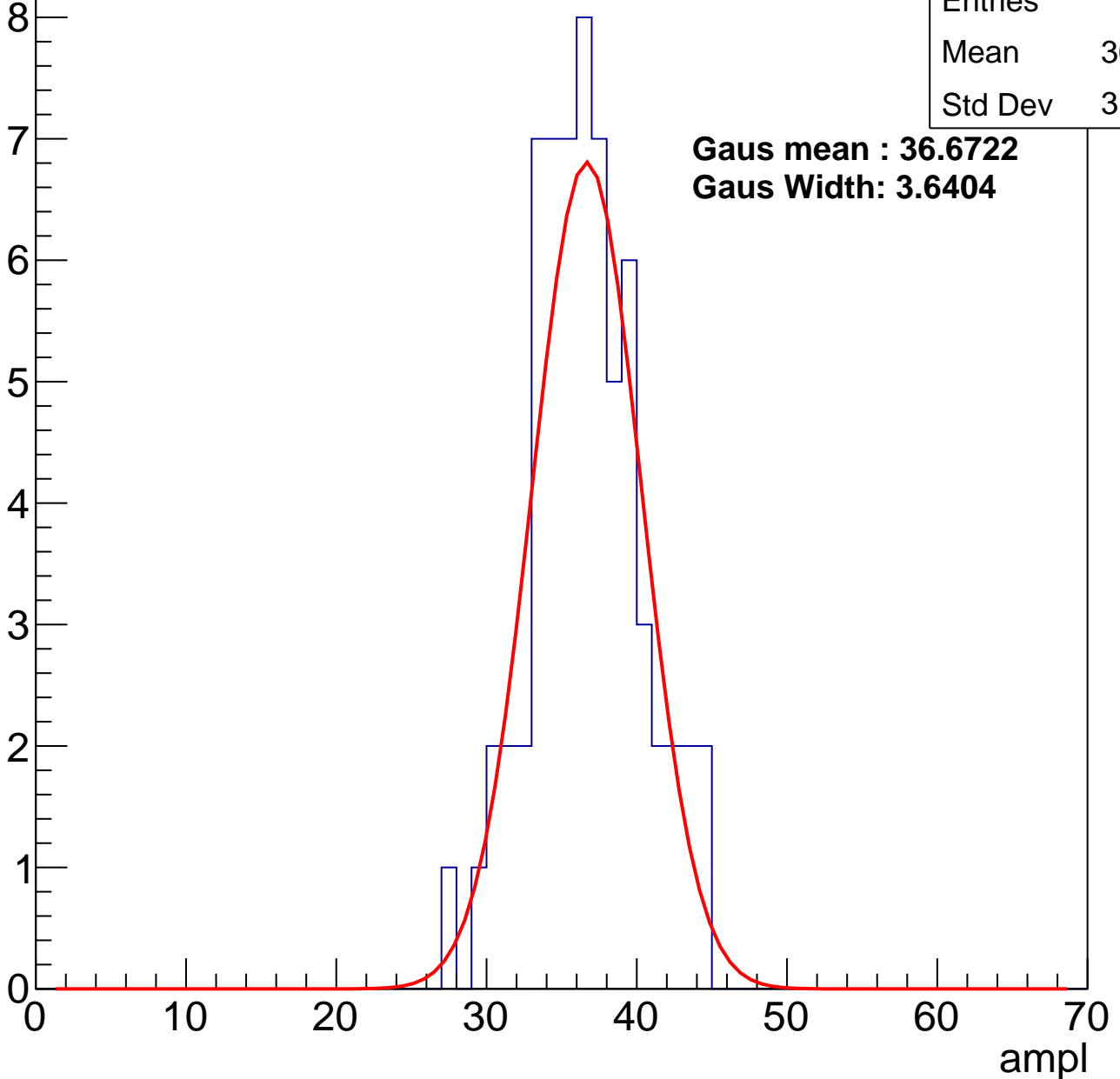
B1L003S, U21-ch36, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.17
Std Dev	3.604

Gaus mean : 36.6722
Gaus Width: 3.6404



B1L003S, U21-ch36, adc2

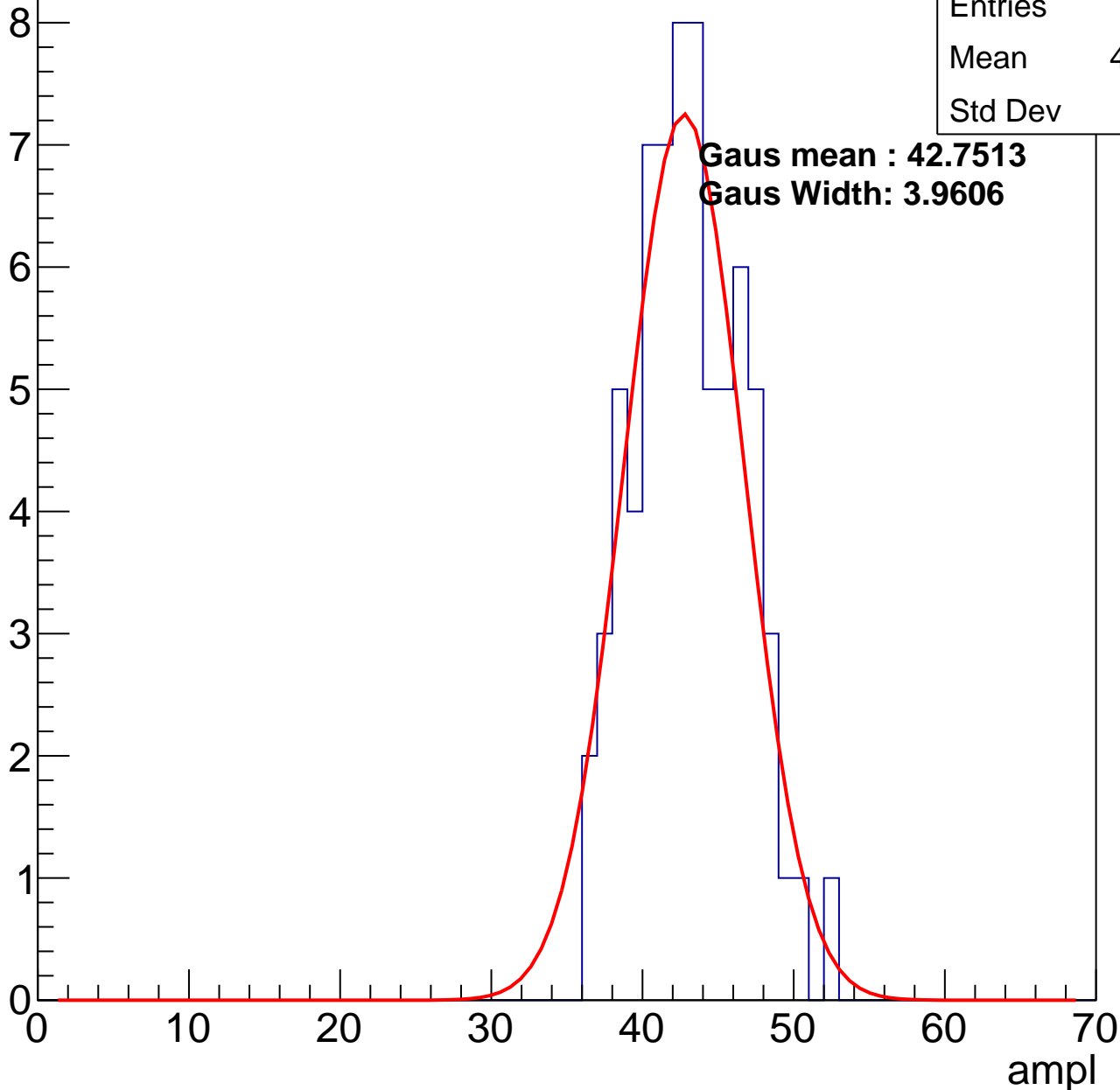
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42.63
Std Dev	3.53

Gaus mean : 42.7513

Gaus Width: 3.9606

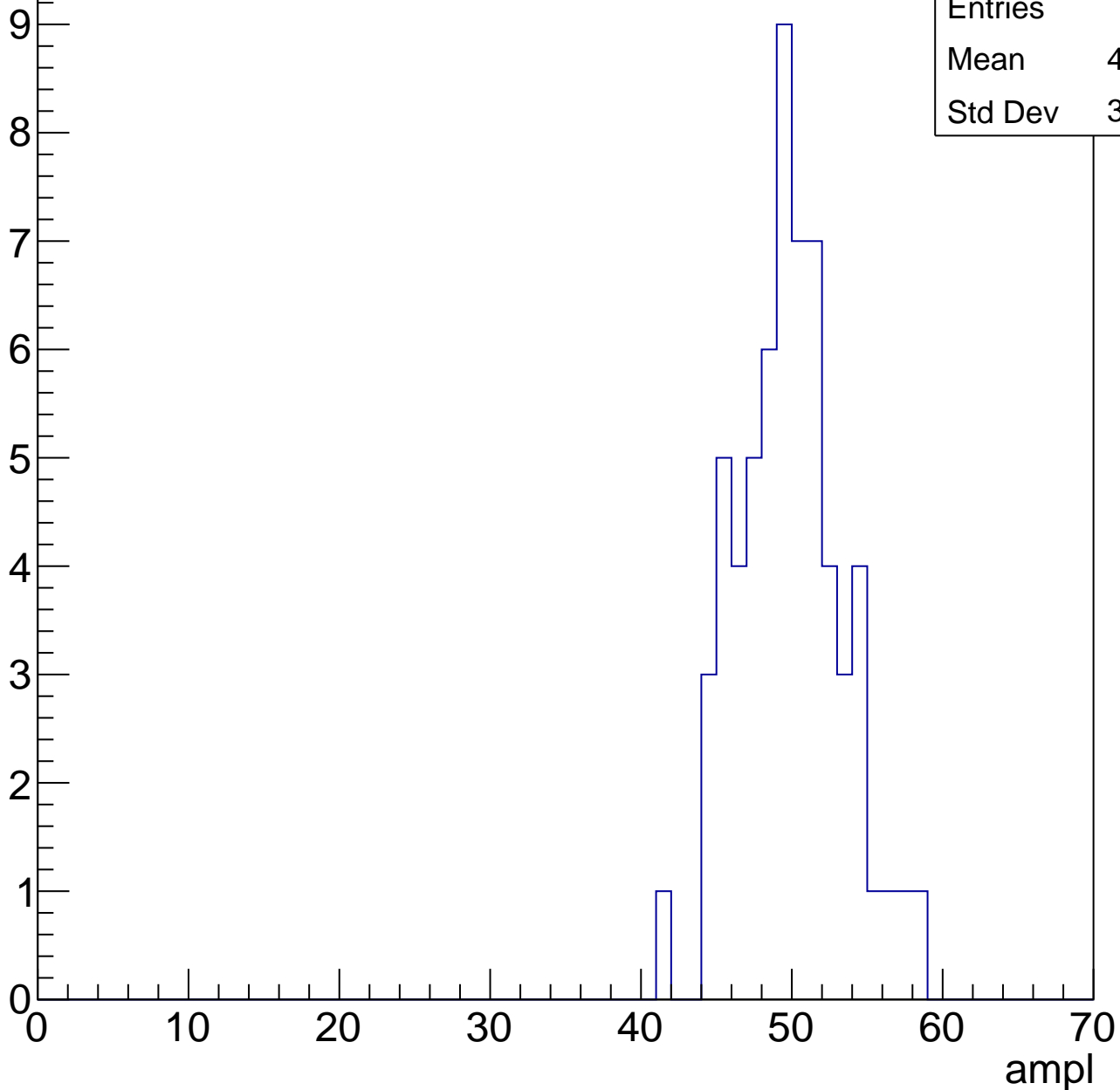


B1L003S, U21-ch36, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.39
Std Dev	3.409

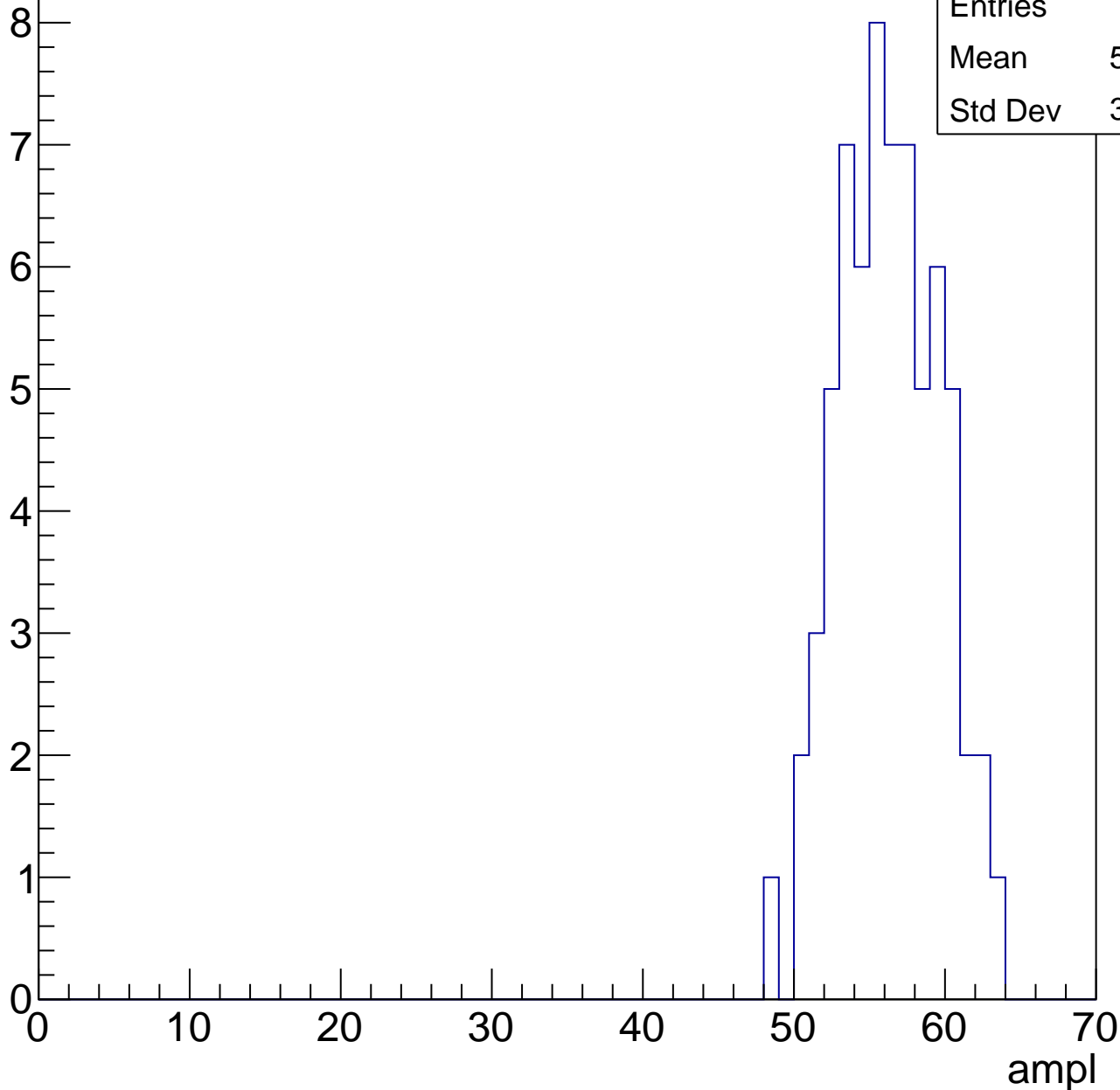


B1L003S, U21-ch36, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	55.82
Std Dev	3.269

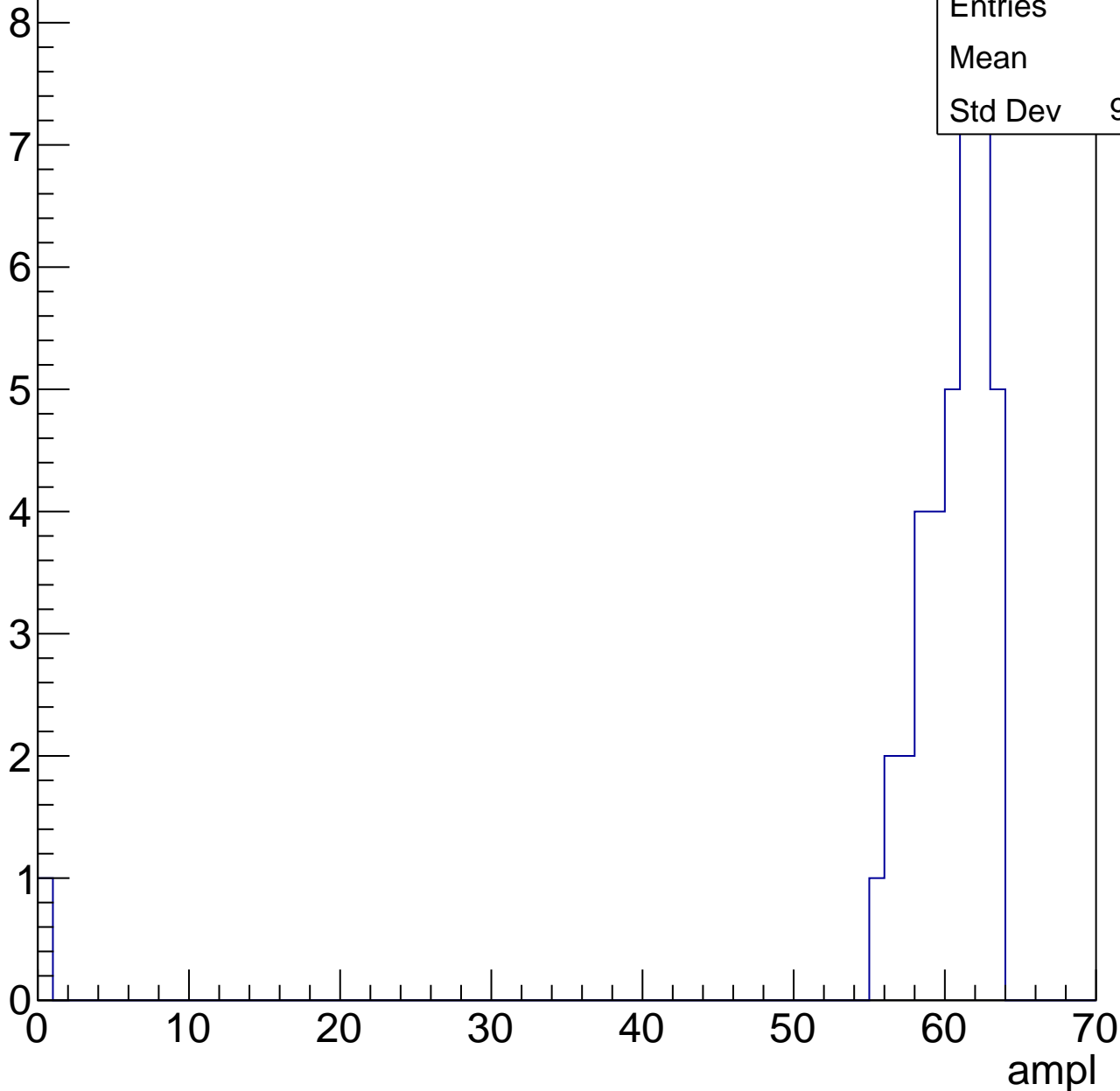


B1L003S, U21-ch36, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	58.7
Std Dev	9.634



B1L003S, U21-ch36, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch36, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch37, adc0

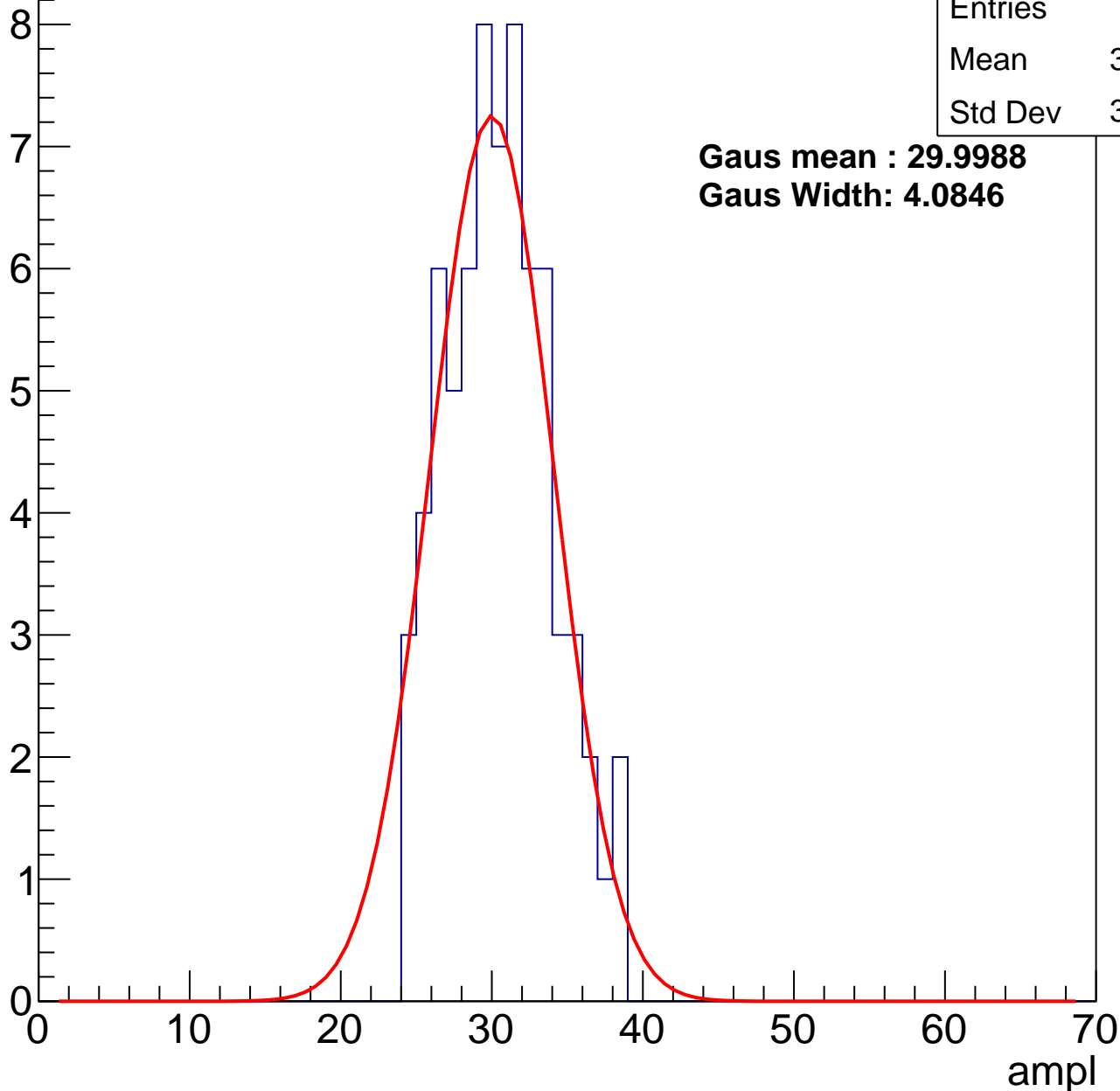
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.04
Std Dev	3.462

Gaus mean : 29.9988

Gaus Width: 4.0846



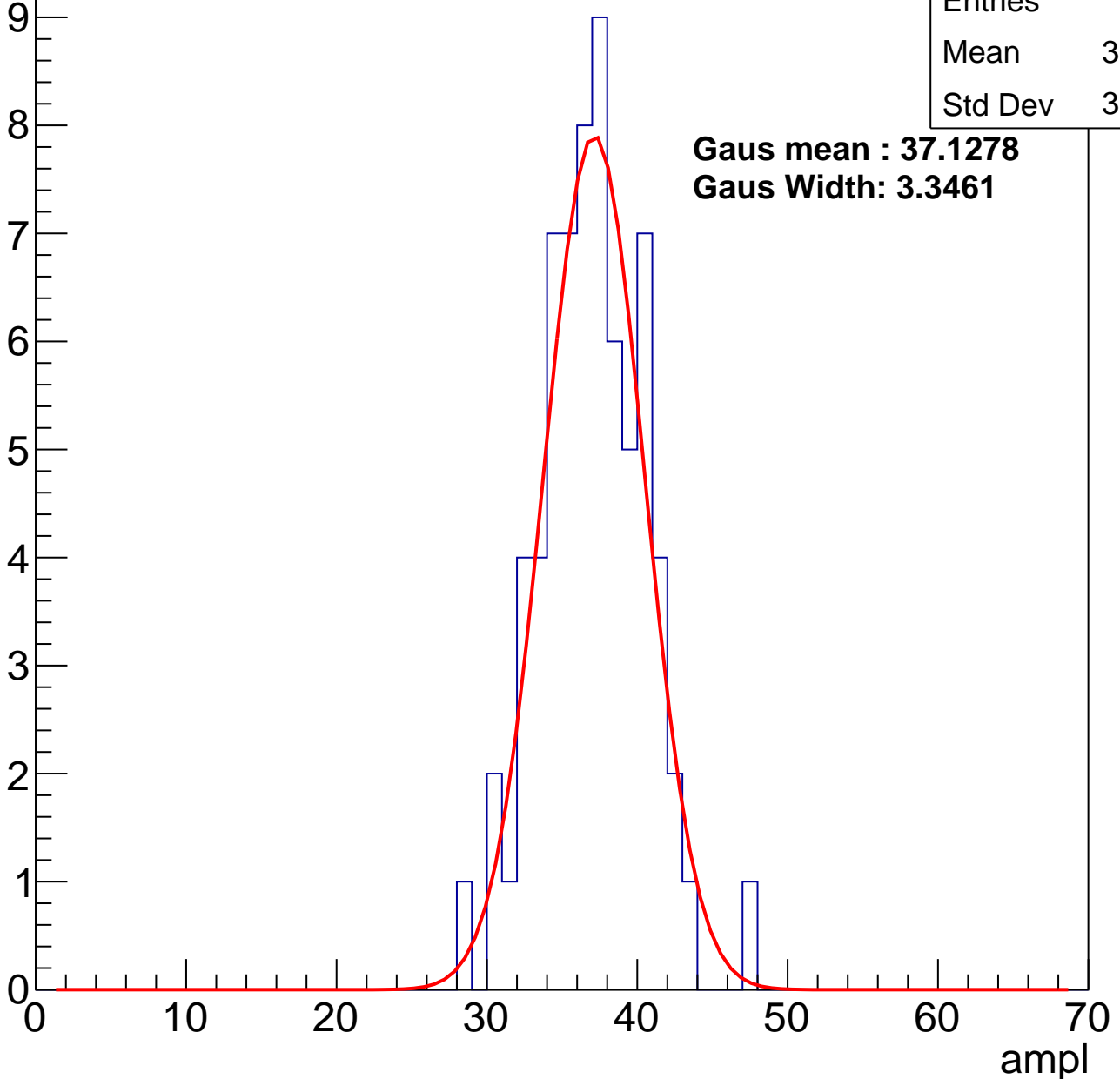
B1L003S, U21-ch37, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	36.58
Std Dev	3.415

Gaus mean : 37.1278
Gaus Width: 3.3461



B1L003S, U21-ch37, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	44.22
Std Dev	3.457

Gaus mean : 44.6941

Gaus Width: 4.2671

Entry

10

8

6

4

2

0

0

10

20

30

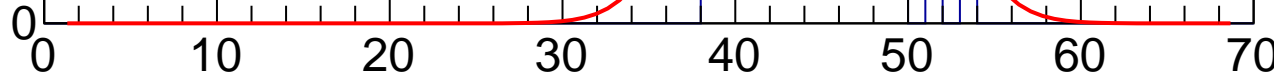
40

50

60

70

ampl

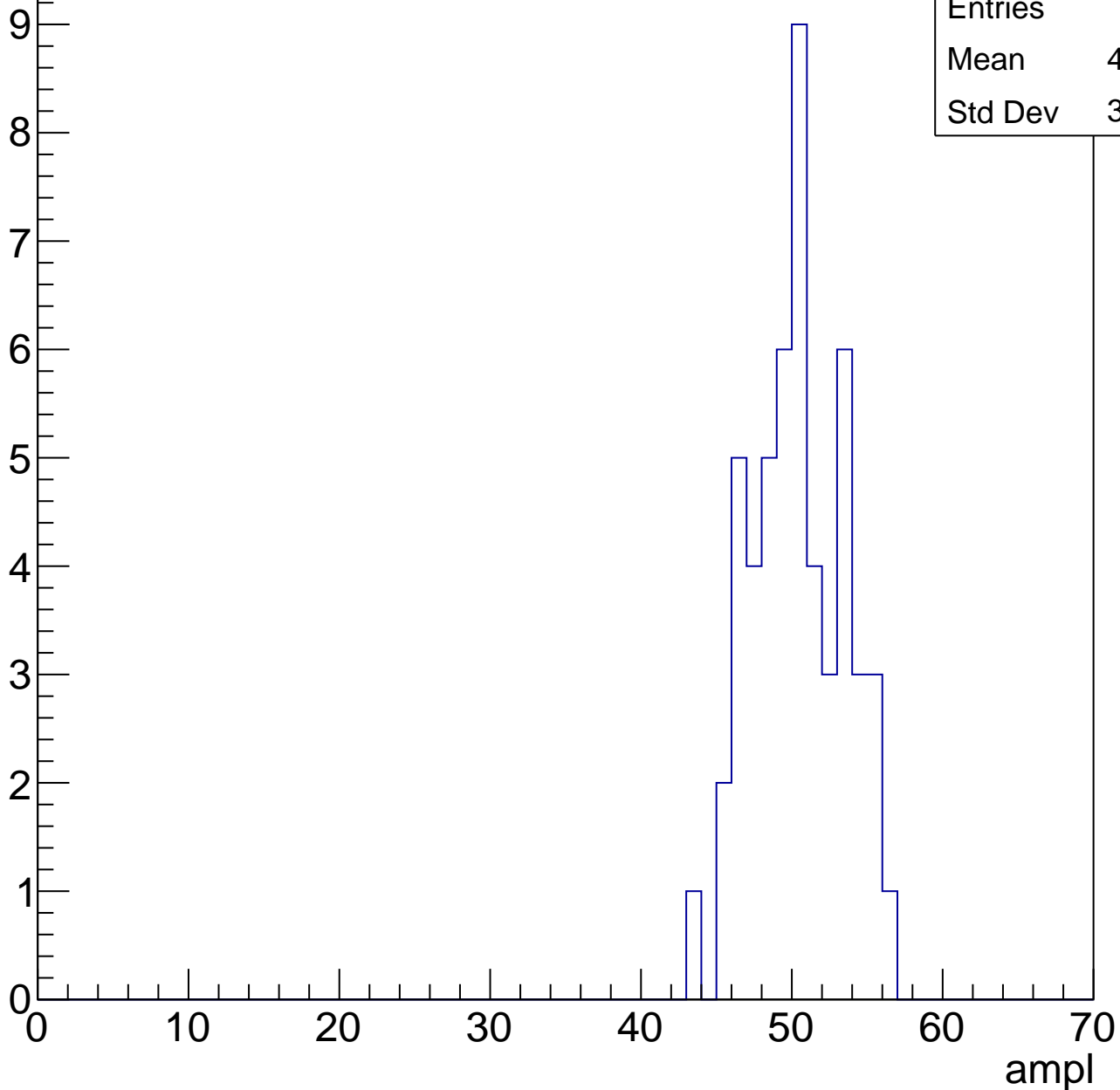


B1L003S, U21-ch37, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	49.92
Std Dev	3.005

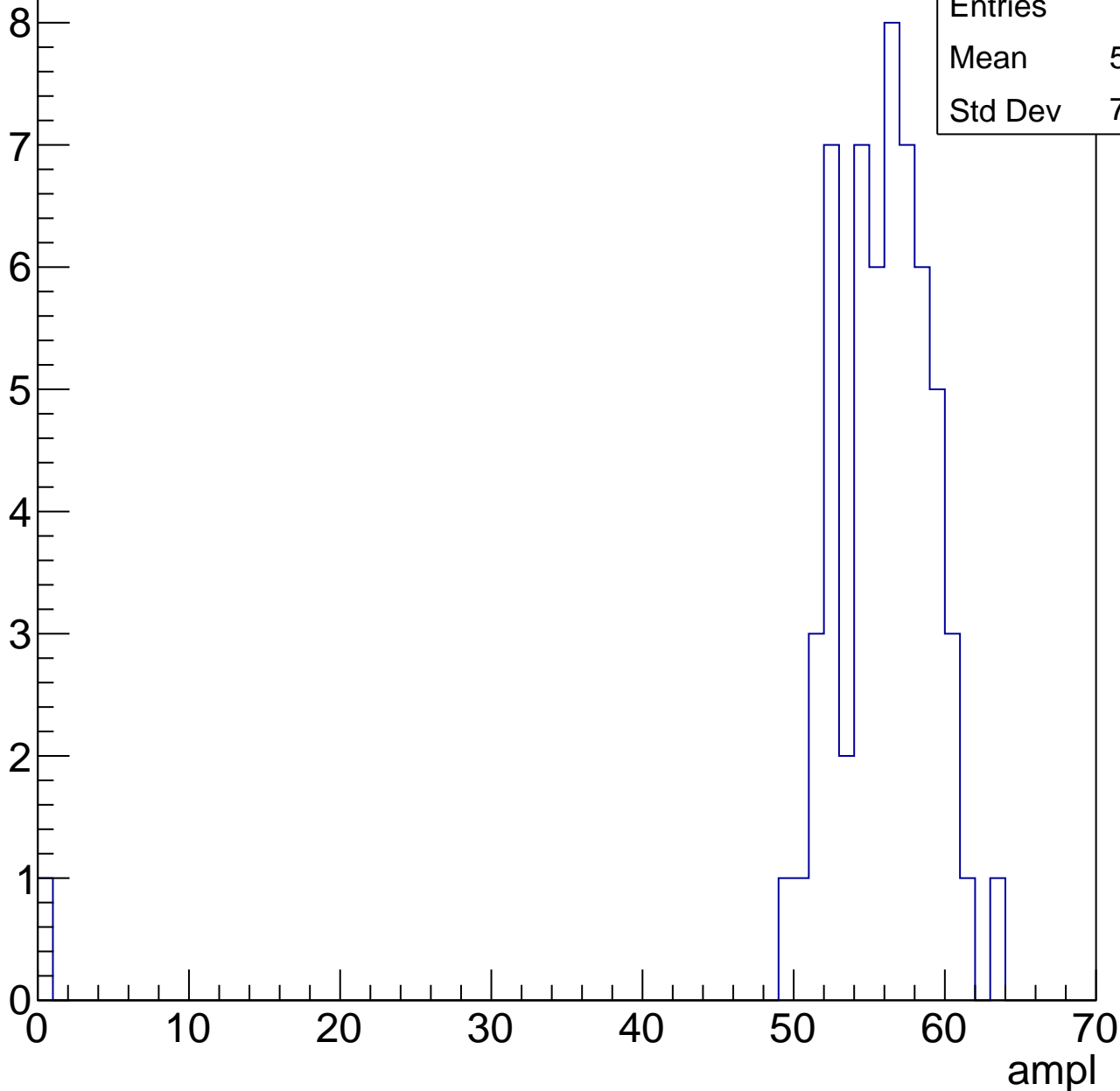


B1L003S, U21-ch37, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	54.64
Std Dev	7.756

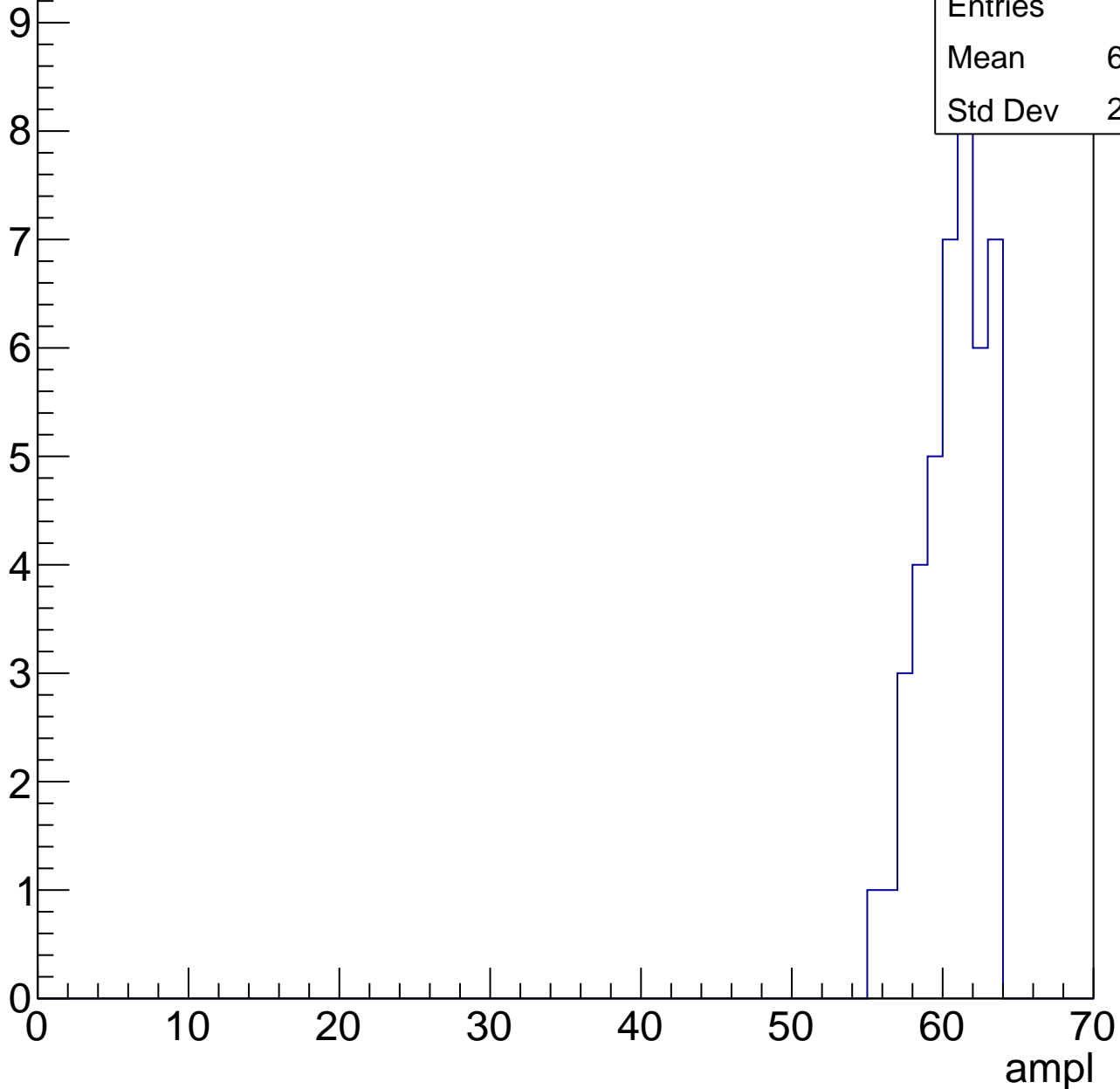


B1L003S, U21-ch37, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

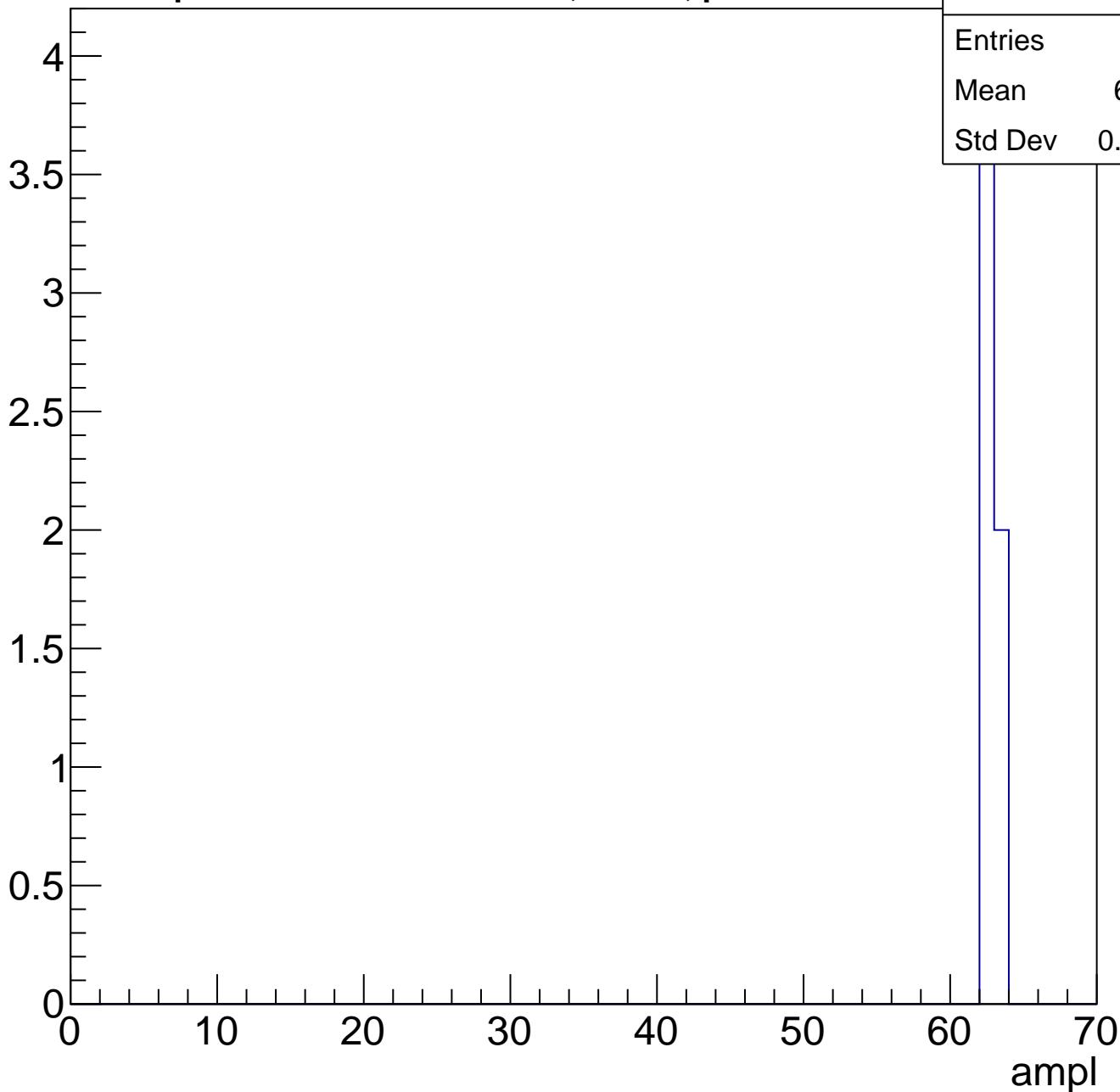
Entries	43
Mean	60.26
Std Dev	2.058



B1L003S, U21-ch37, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch37, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch38, adc0

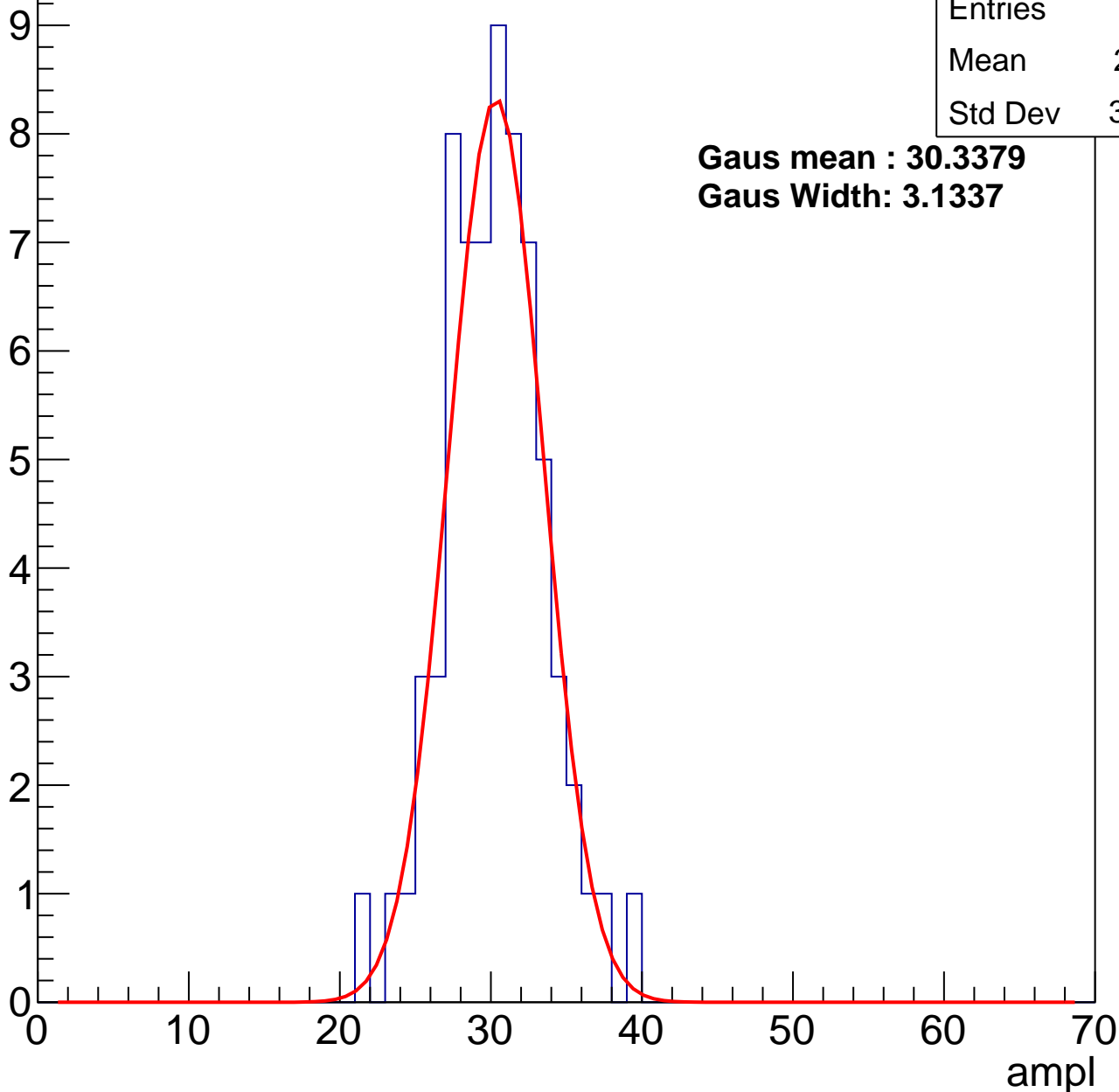
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.81
Std Dev	3.296

Gaus mean : 30.3379

Gaus Width: 3.1337



B1L003S, U21-ch38, adc1

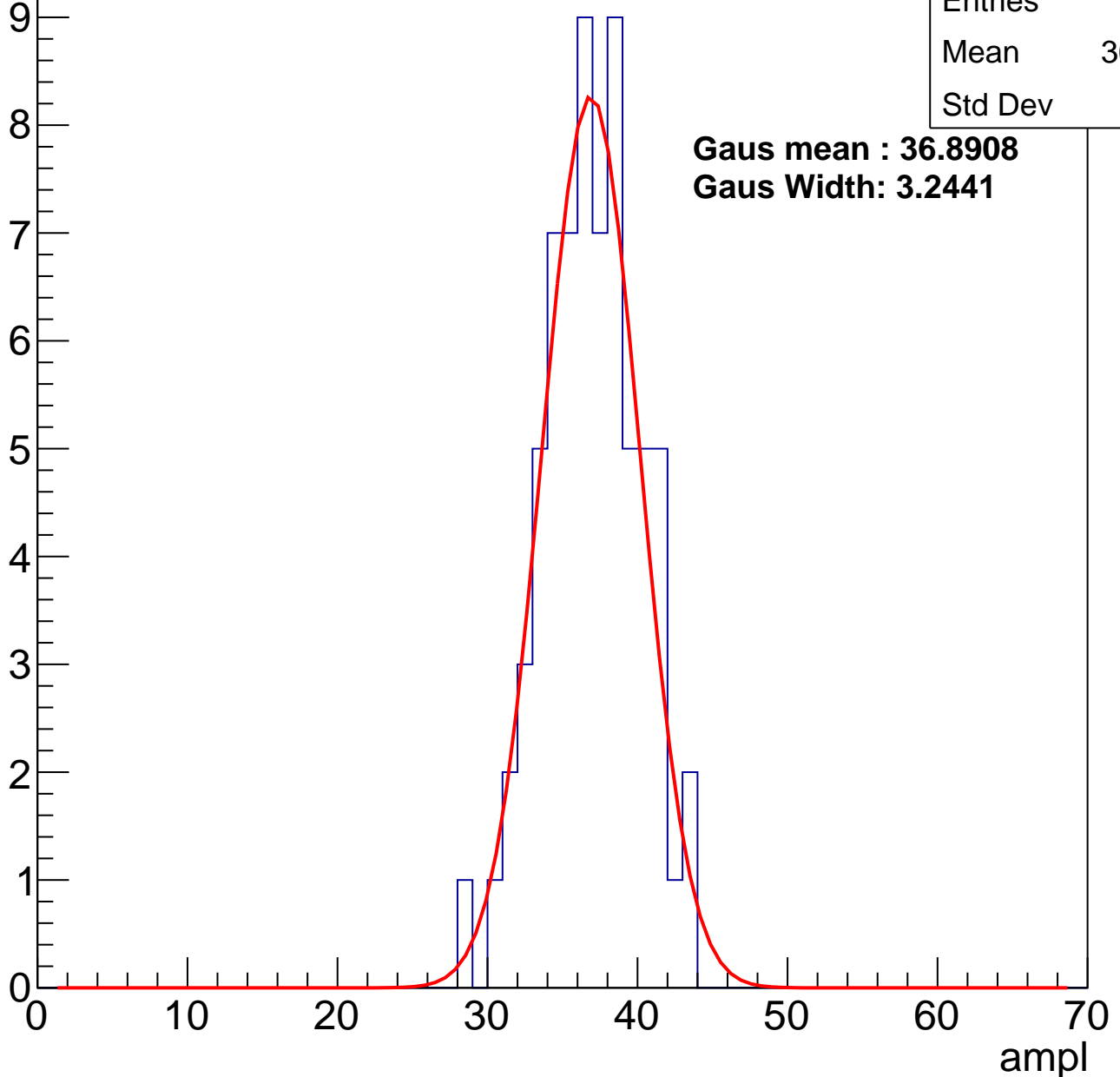
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	36.48
Std Dev	3.16

Gaus mean : 36.8908

Gaus Width: 3.2441



B1L003S, U21-ch38, adc2

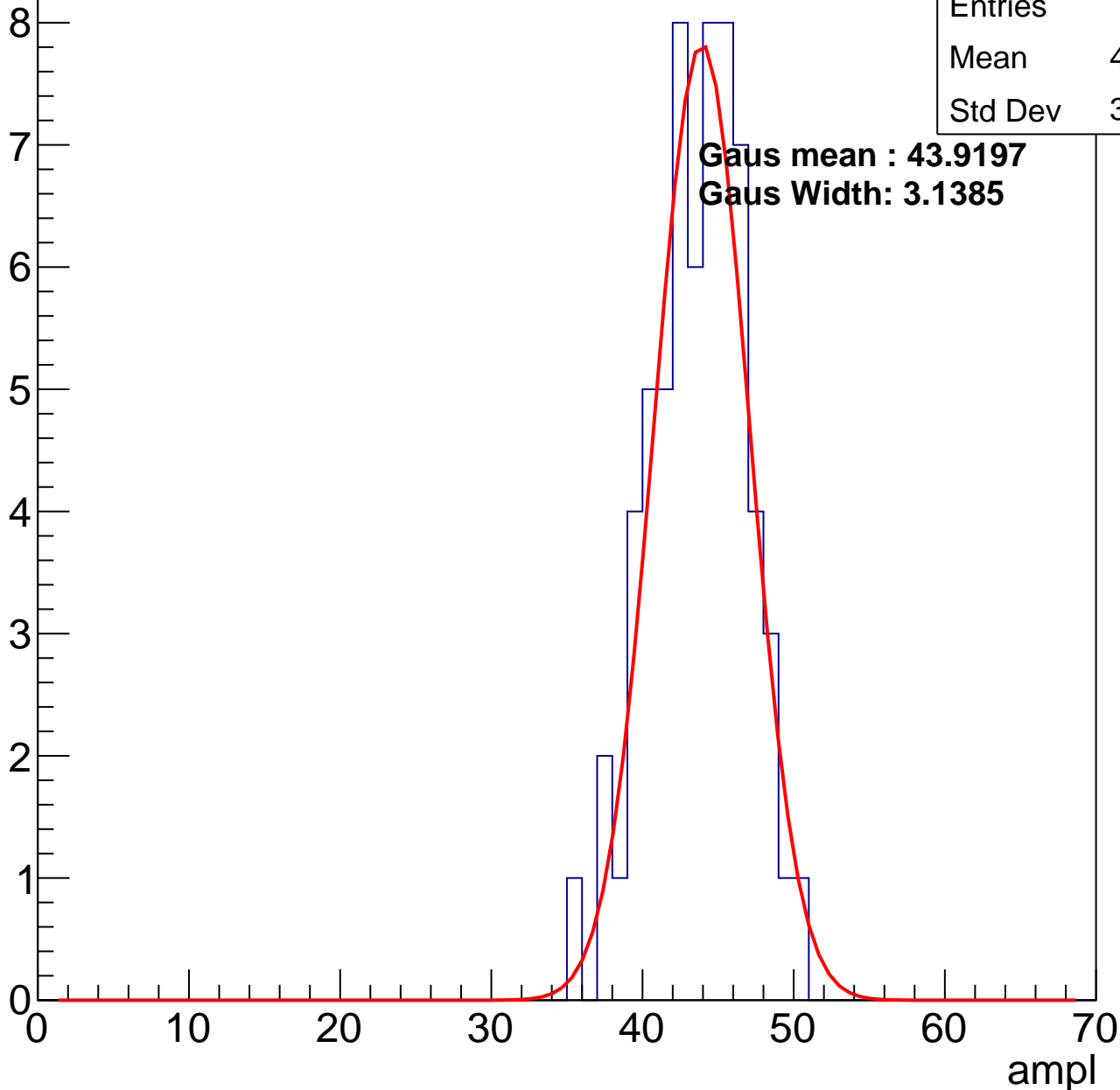
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.23
Std Dev	3.116

Gaus mean : 43.9197

Gaus Width: 3.1385

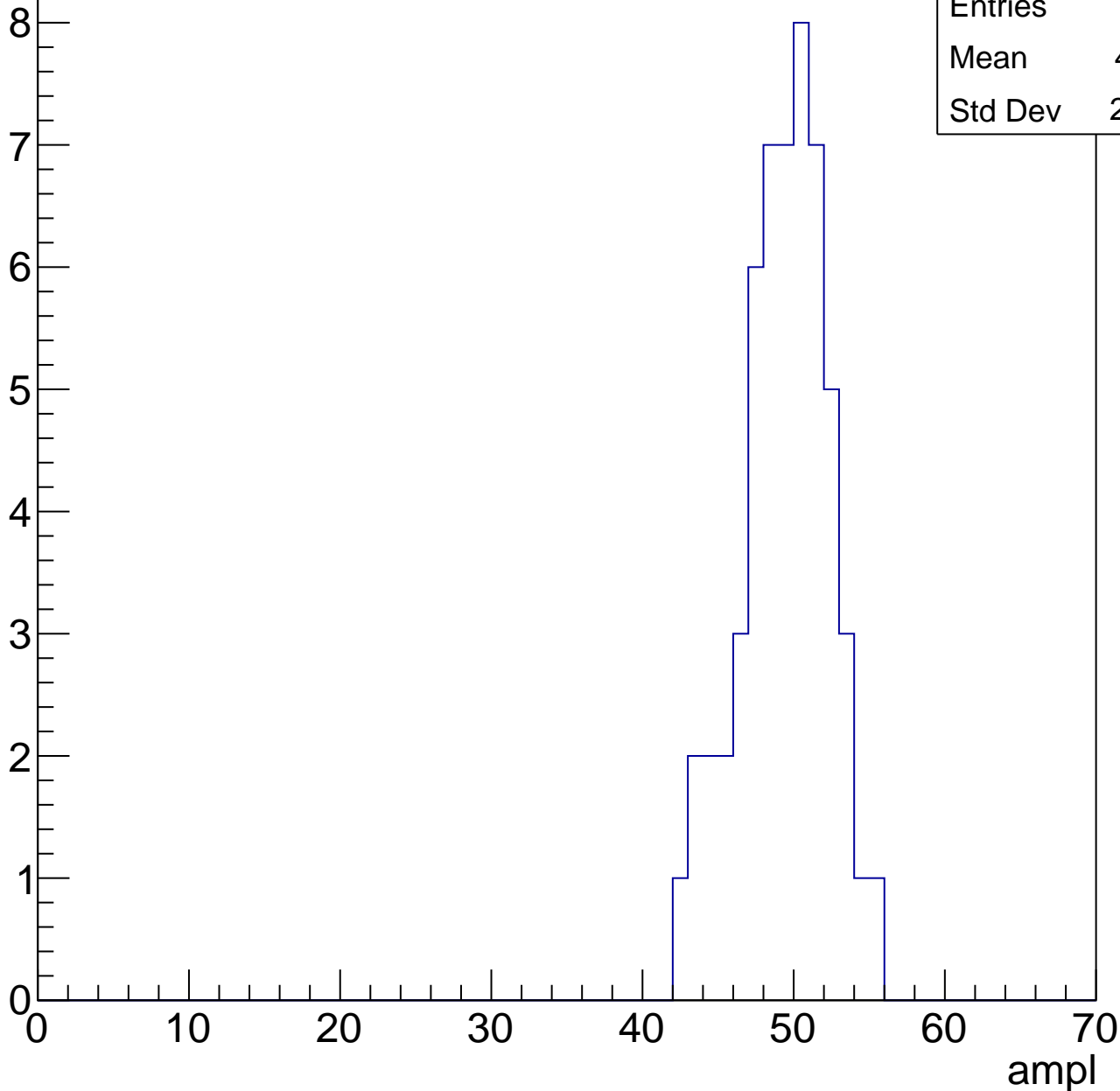


B1L003S, U21-ch38, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	48.91
Std Dev	2.862



B1L003S, U21-ch38, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	55.34
Std Dev	2.958

Entry

10

8

6

4

2

0

0

10

20

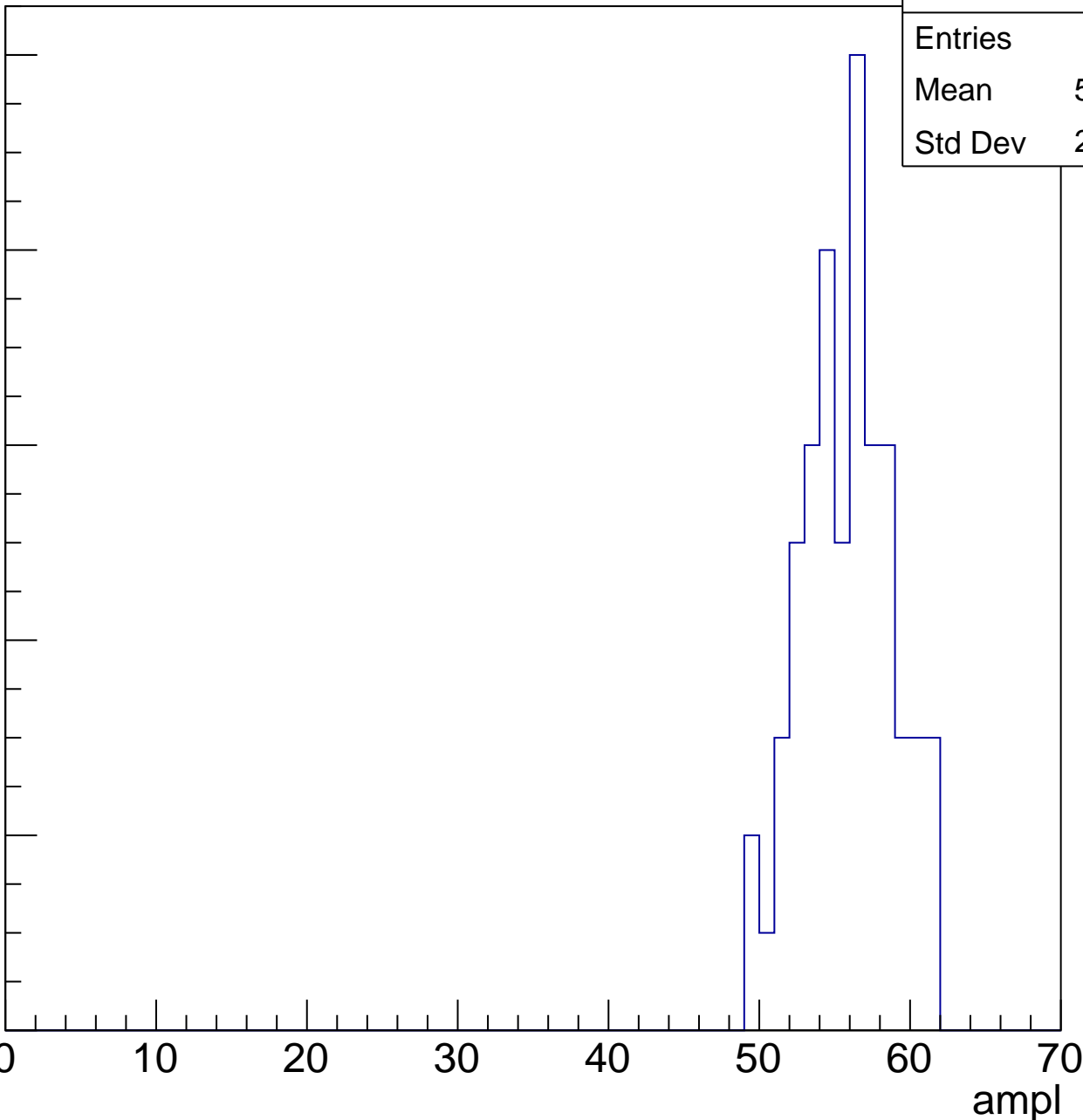
30

40

50

60

ampl



B1L003S, U21-ch38, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7
6
5
4
3
2
1
0

Entries	50
Mean	58.44
Std Dev	8.741

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

B1L003S, U21-ch38, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch38, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch39, adc0

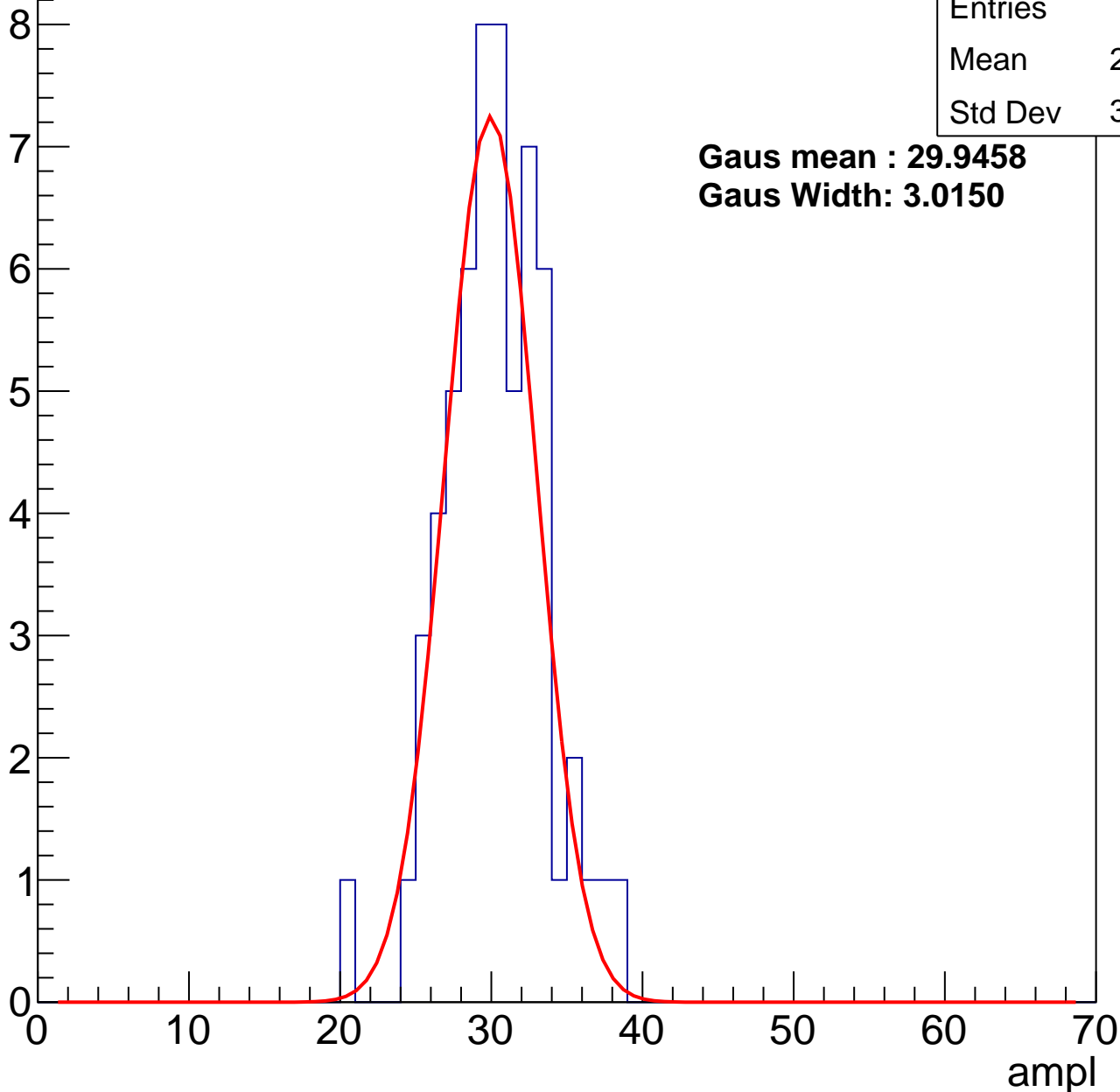
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.83
Std Dev	3.297

Gaus mean : 29.9458

Gaus Width: 3.0150



B1L003S, U21-ch39, adc1

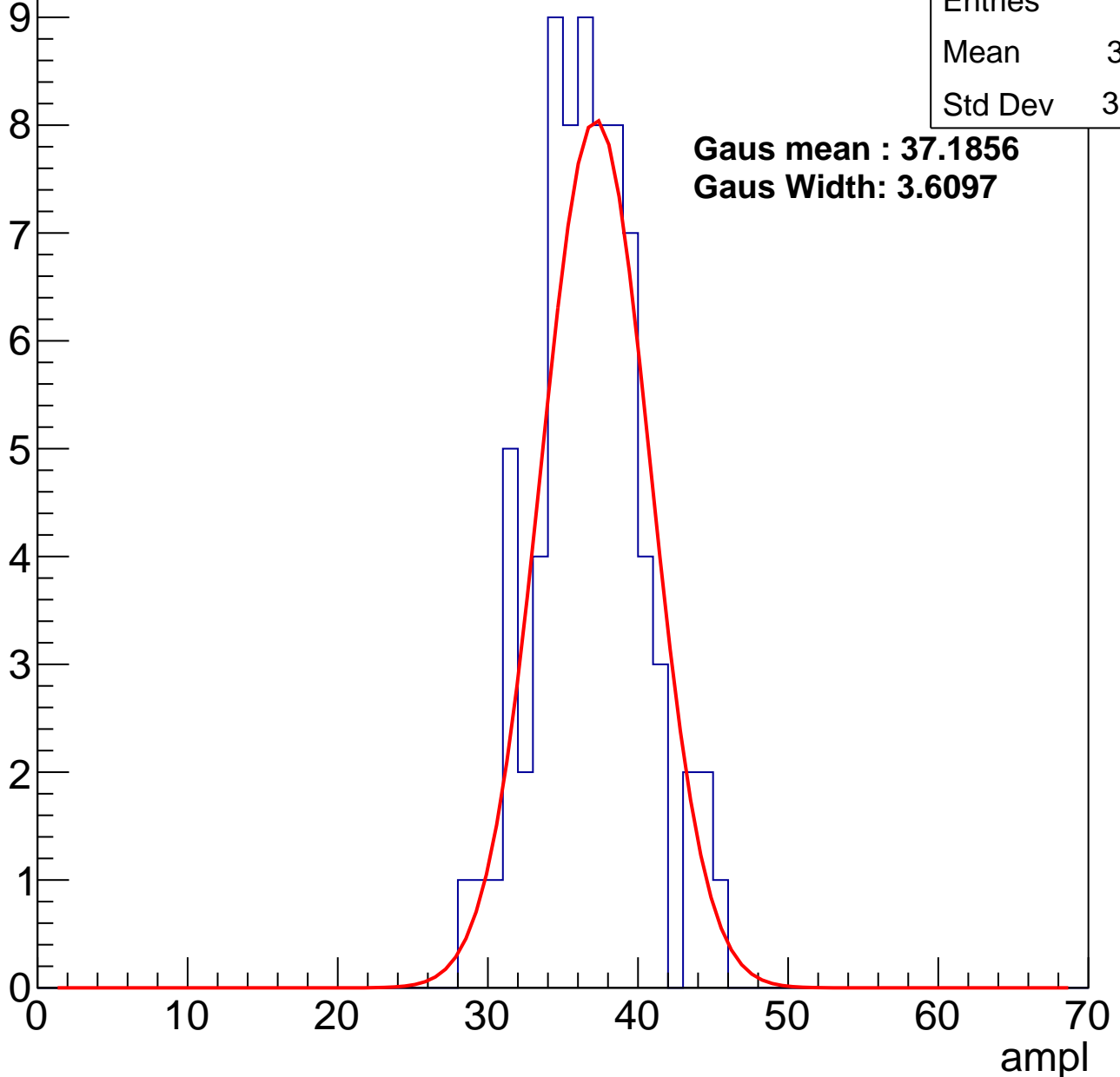
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	36.31
Std Dev	3.517

Gaus mean : 37.1856

Gaus Width: 3.6097



B1L003S, U21-ch39, adc2

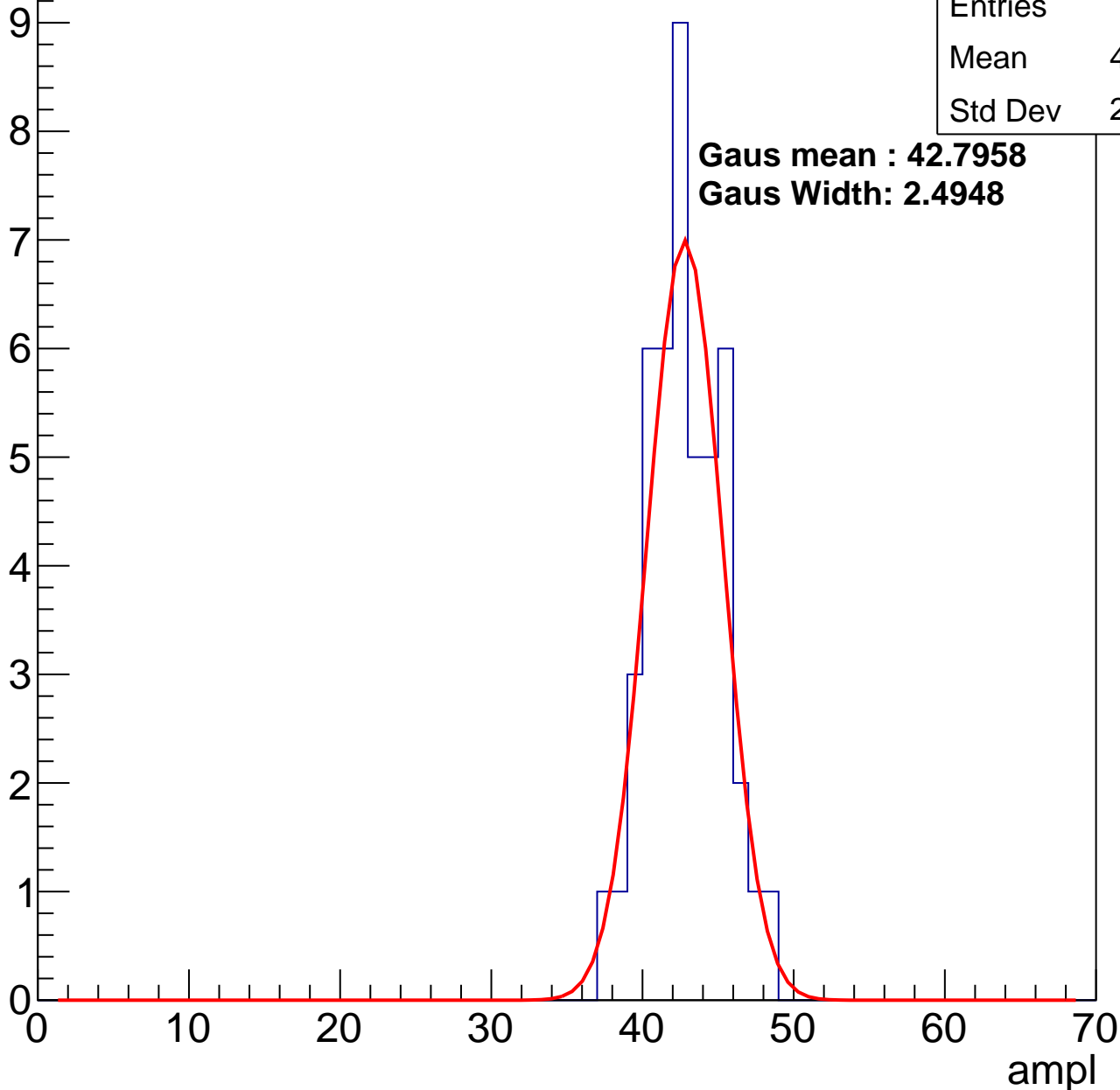
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	42.35
Std Dev	2.398

Gaus mean : 42.7958

Gaus Width: 2.4948



B1L003S, U21-ch39, adc3

calib_packv5_042523_0143.root, FC#13, port D2

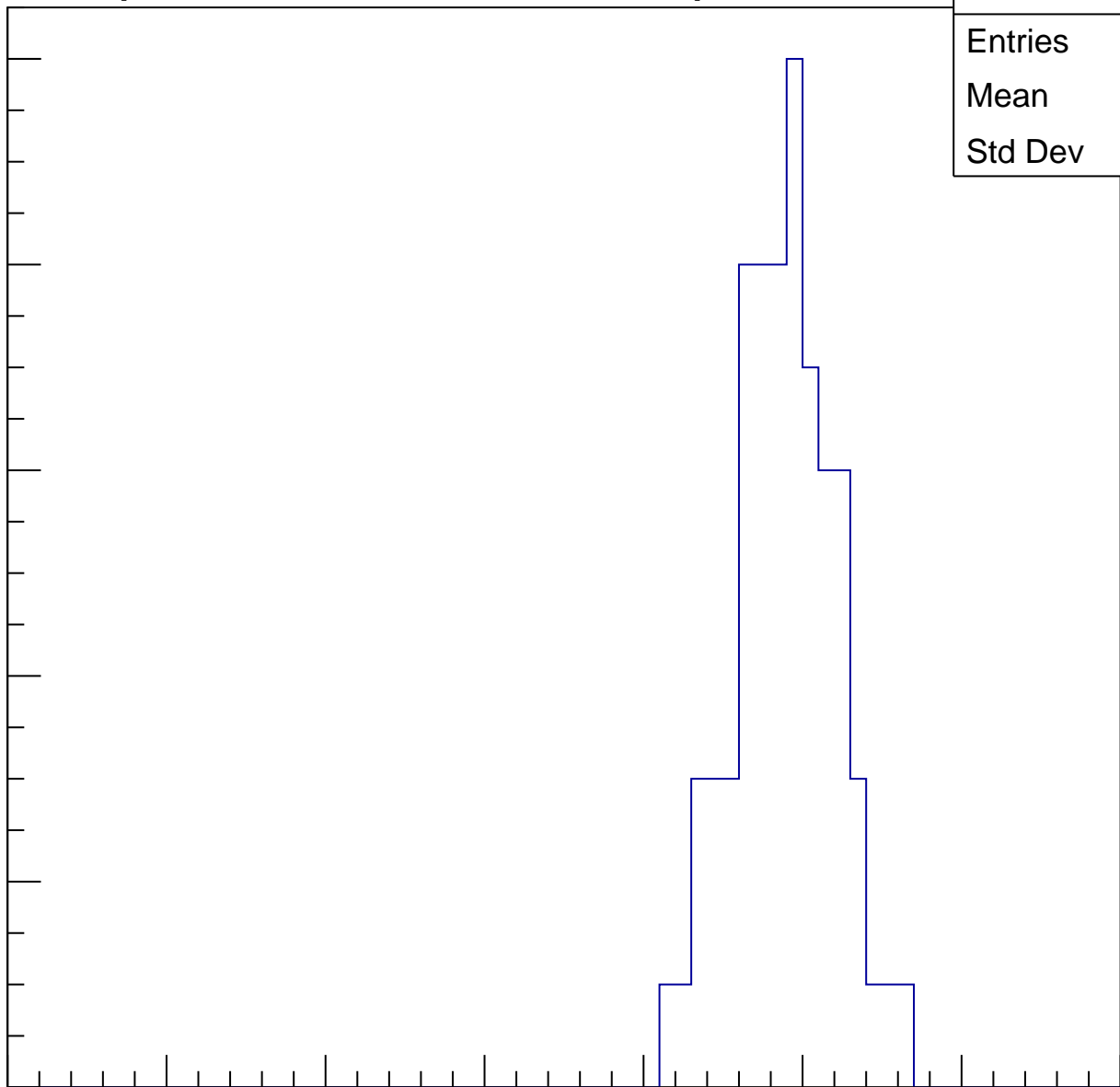
Entries	70
Mean	48.41
Std Dev	3.105

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

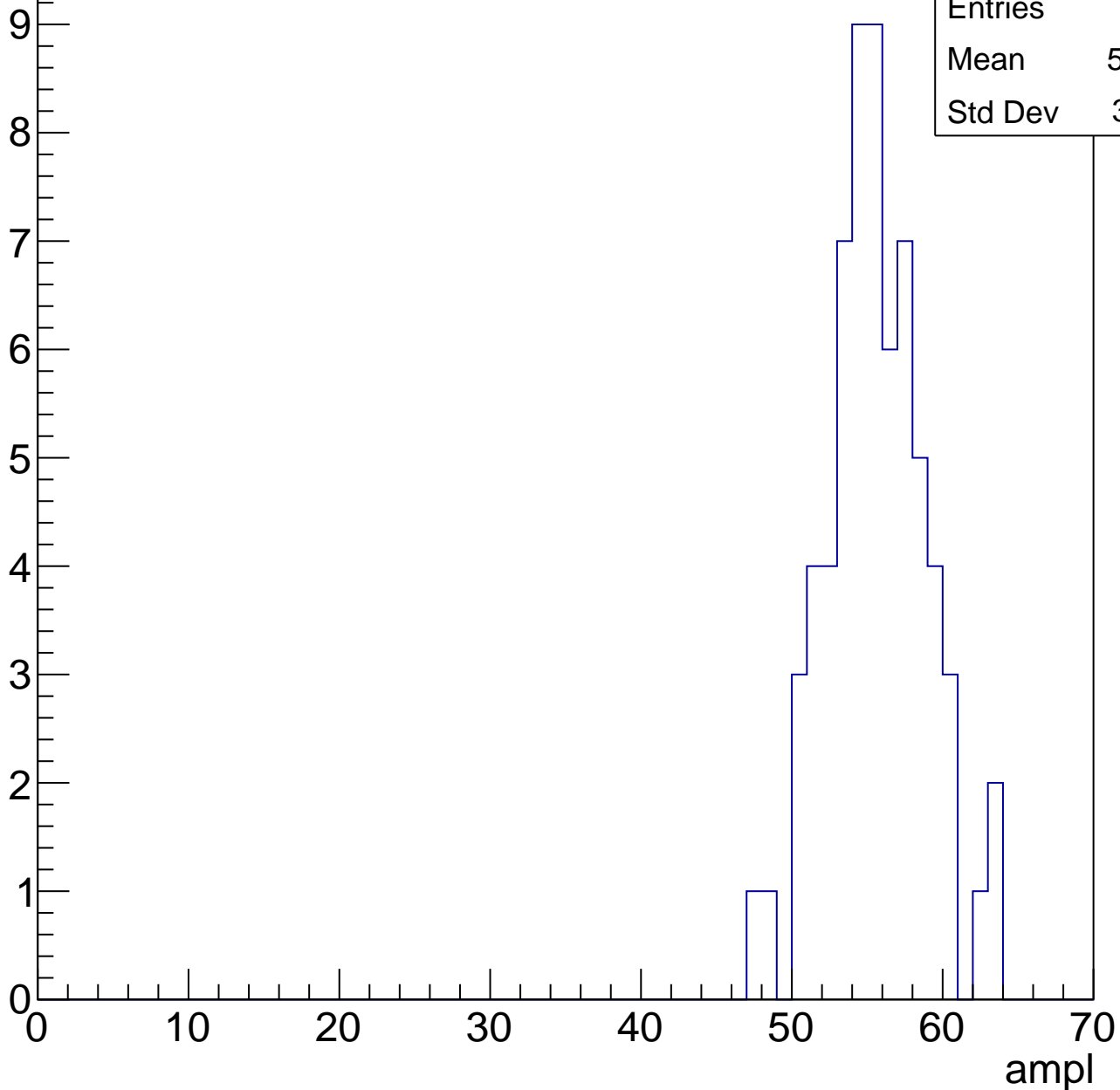


B1L003S, U21-ch39, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	55.12
Std Dev	3.301

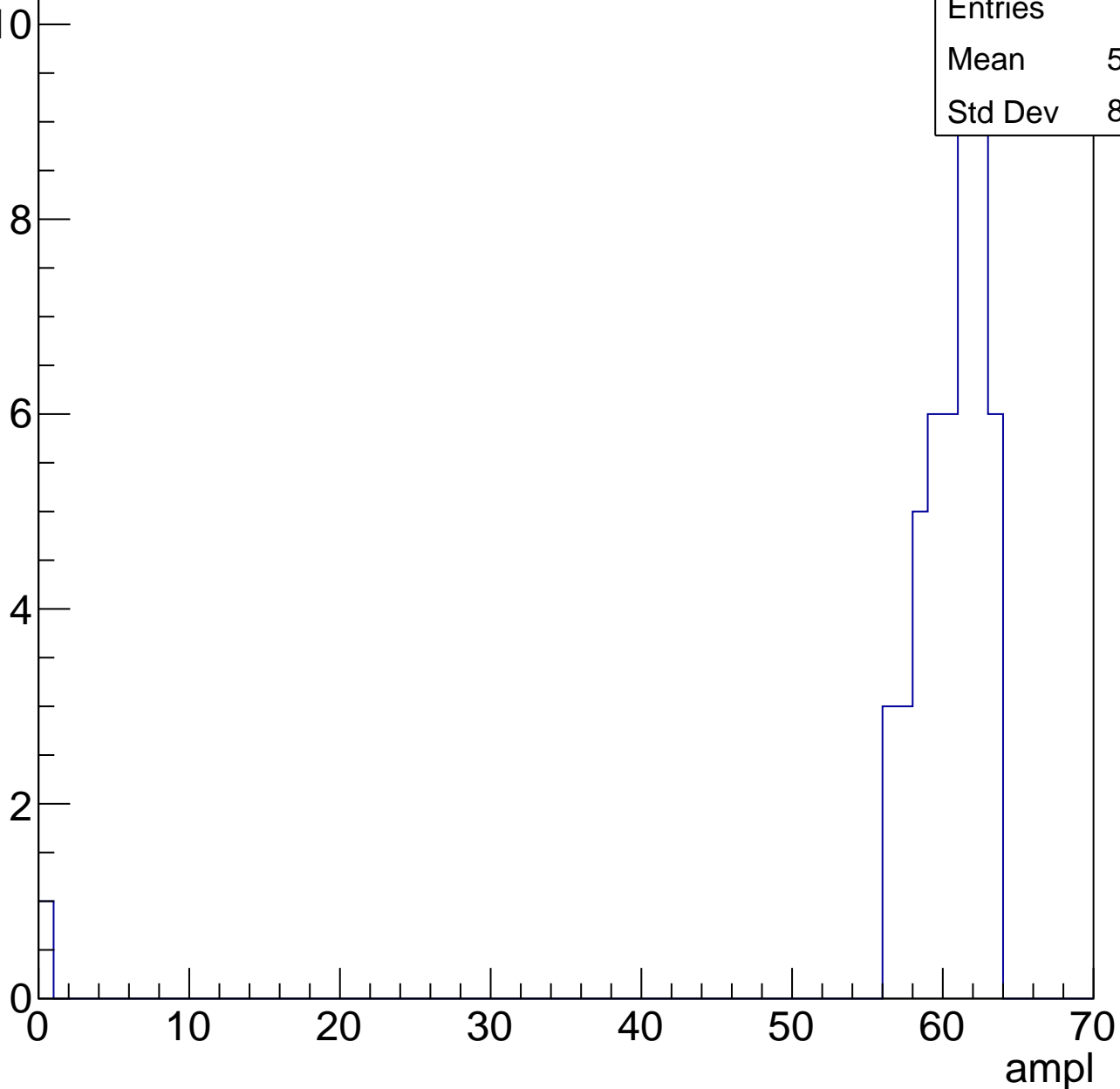


B1L003S, U21-ch39, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.96
Std Dev	8.746



B1L003S, U21-ch39, adc6

calib_packv5_042523_0143.root, FC#13, port D2

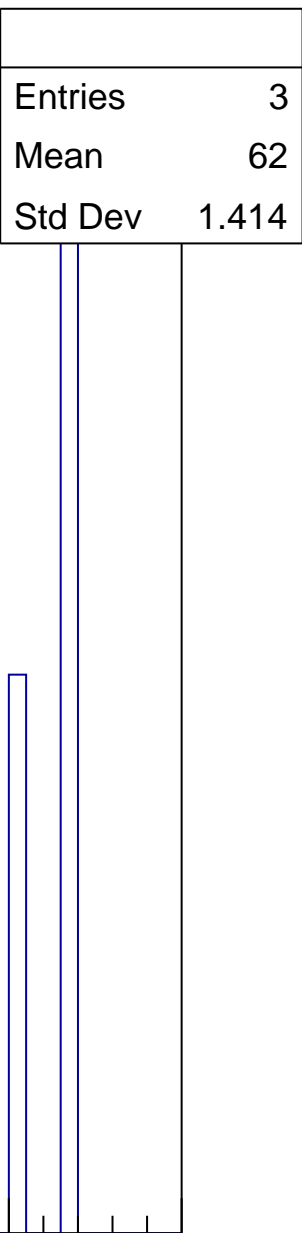
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62
Std Dev	1.414

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch39, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch40, adc0

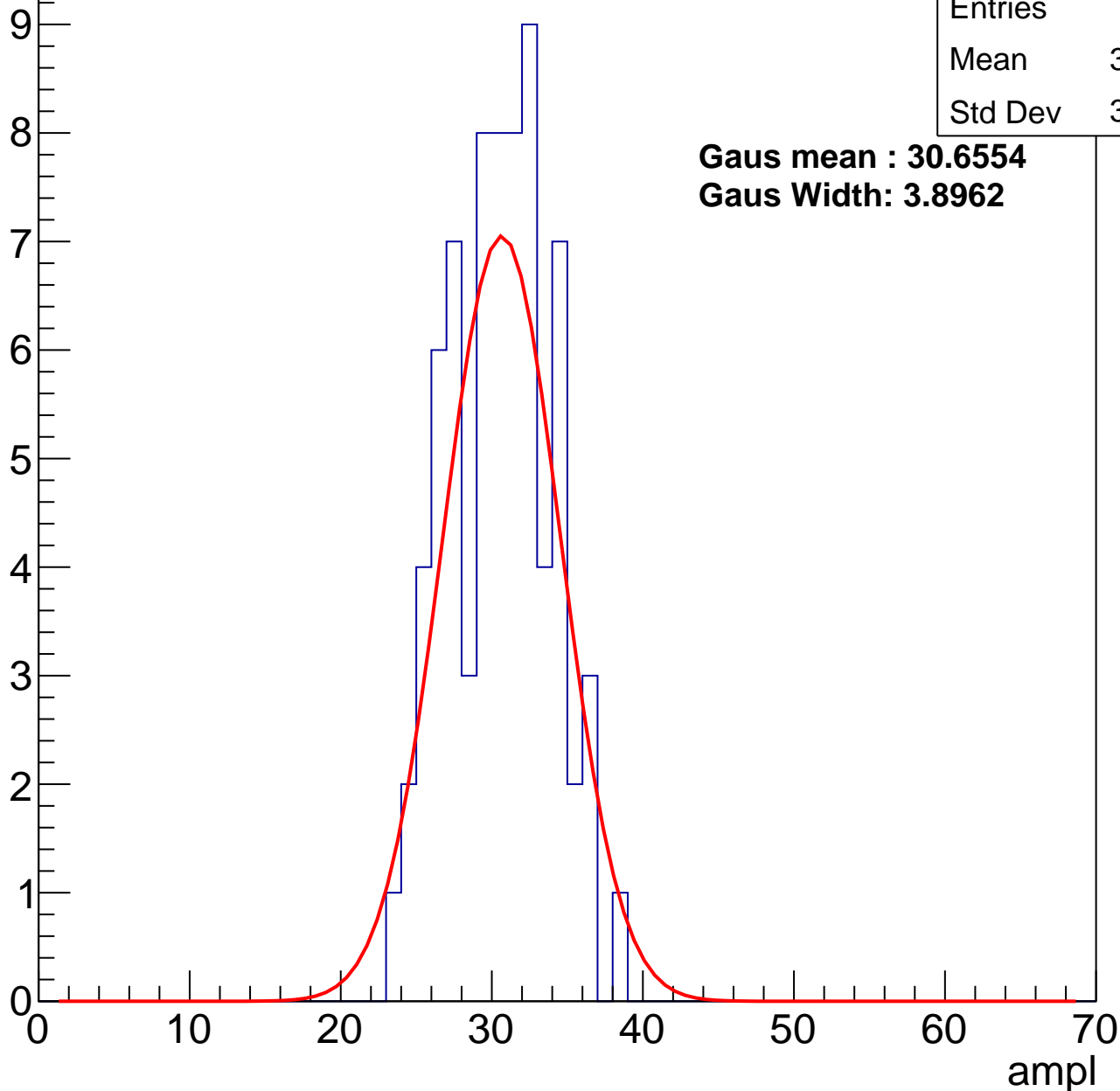
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	30.05
Std Dev	3.339

Gaus mean : 30.6554

Gaus Width: 3.8962



B1L003S, U21-ch40, adc1

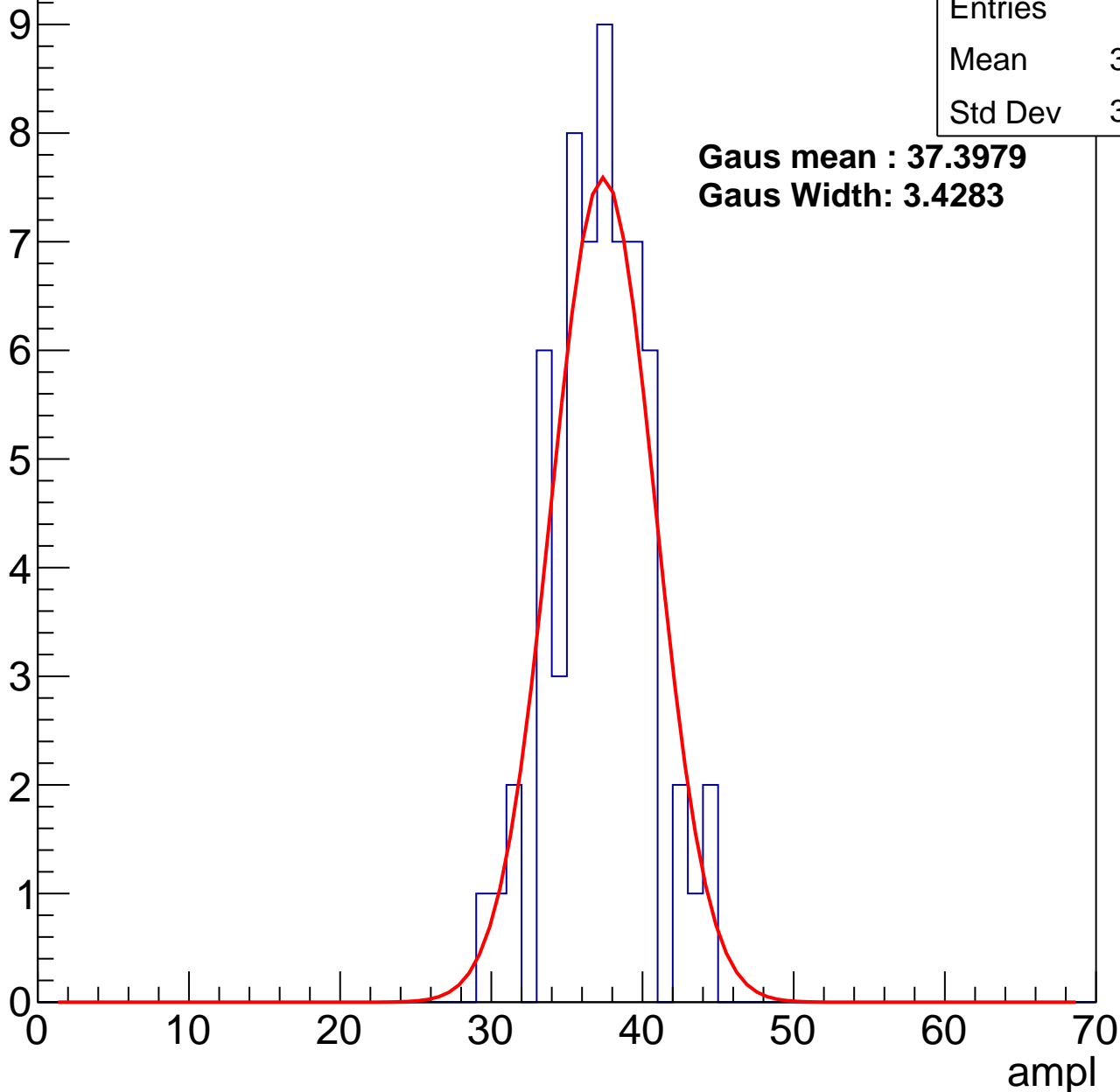
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.77
Std Dev	3.154

Gaus mean : 37.3979

Gaus Width: 3.4283



B1L003S, U21-ch40, adc2

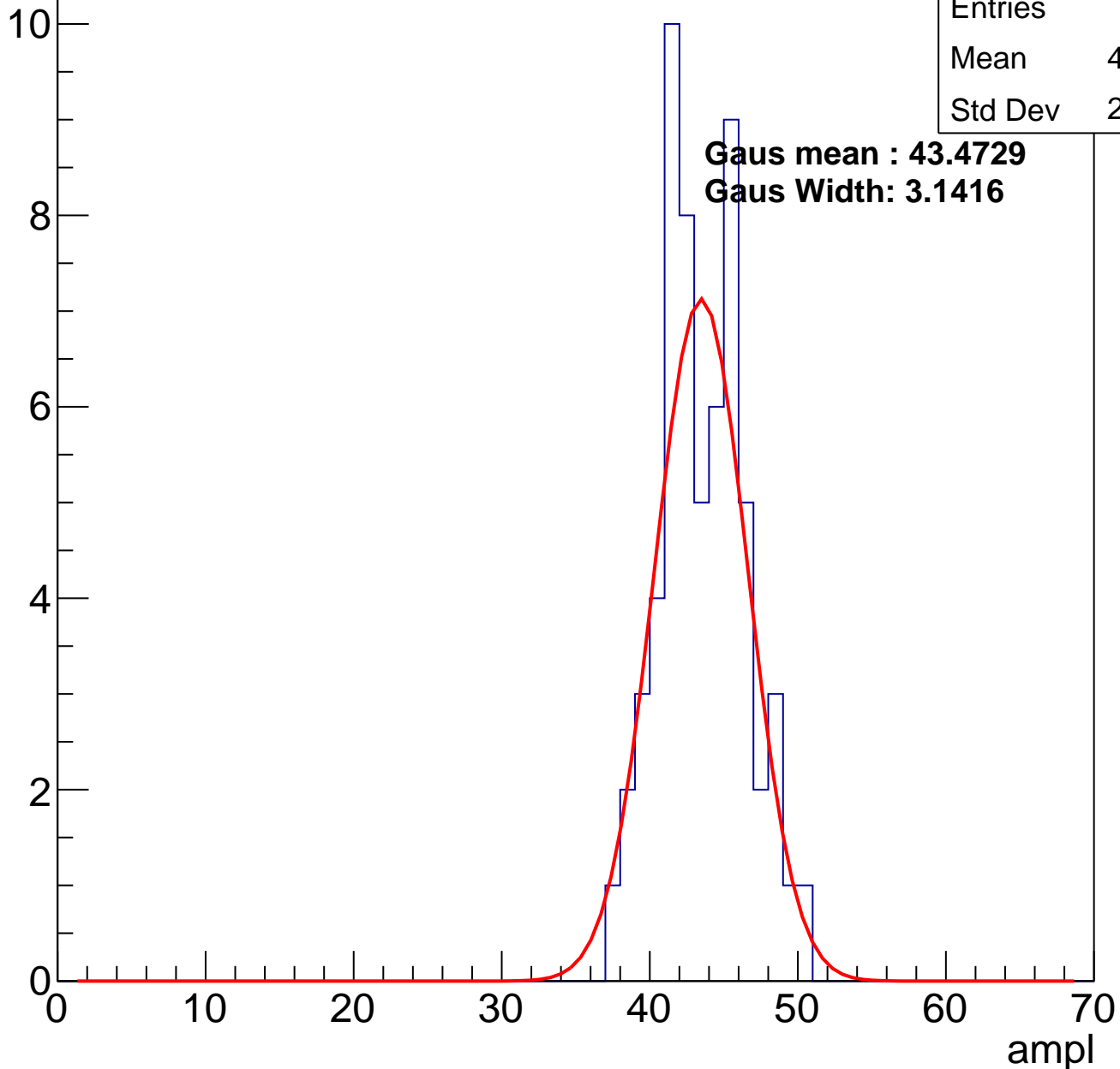
calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	43.12
Std Dev	2.876

Gaus mean : 43.4729

Gaus Width: 3.1416

Entry

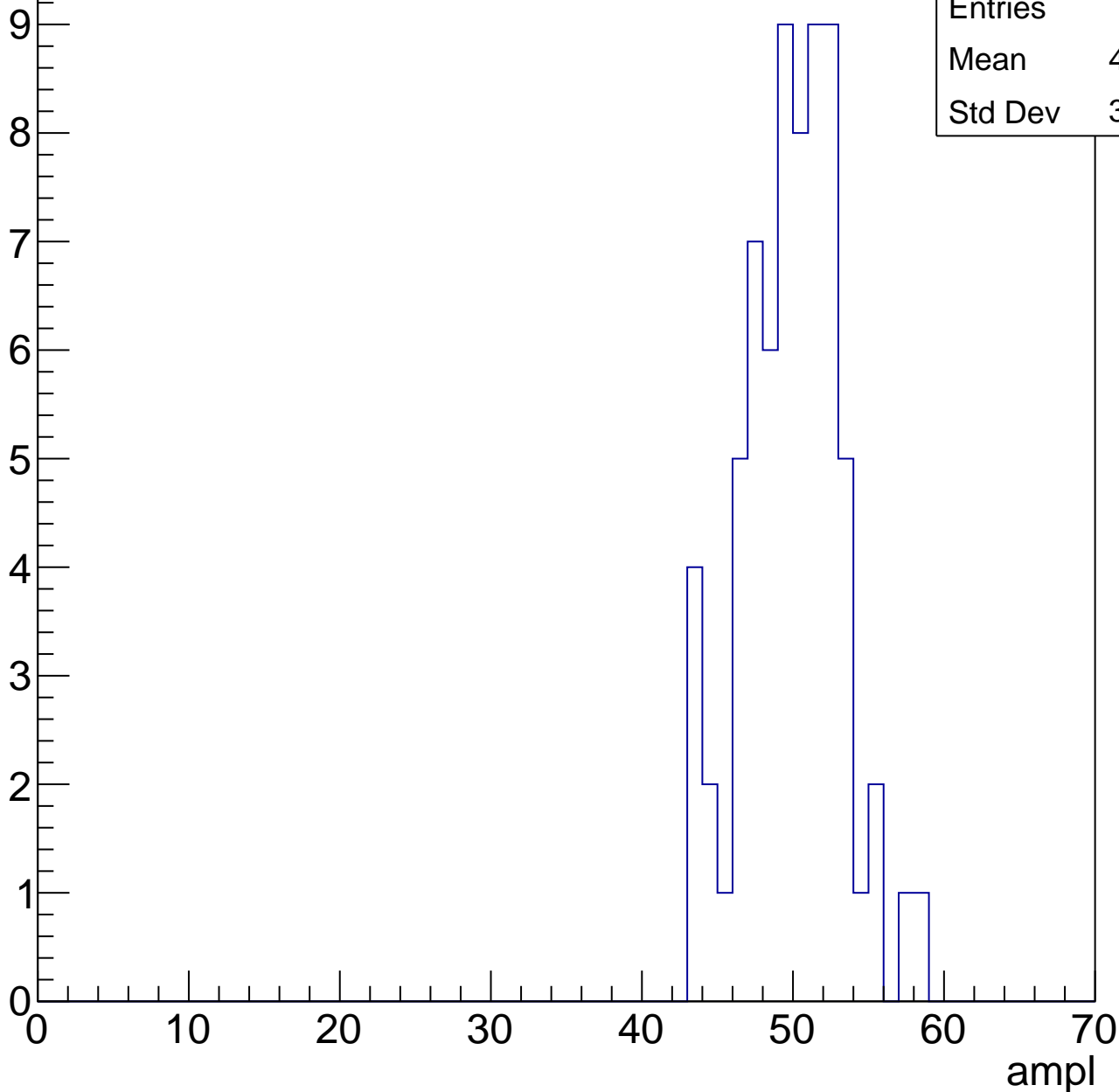


B1L003S, U21-ch40, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	49.49
Std Dev	3.206

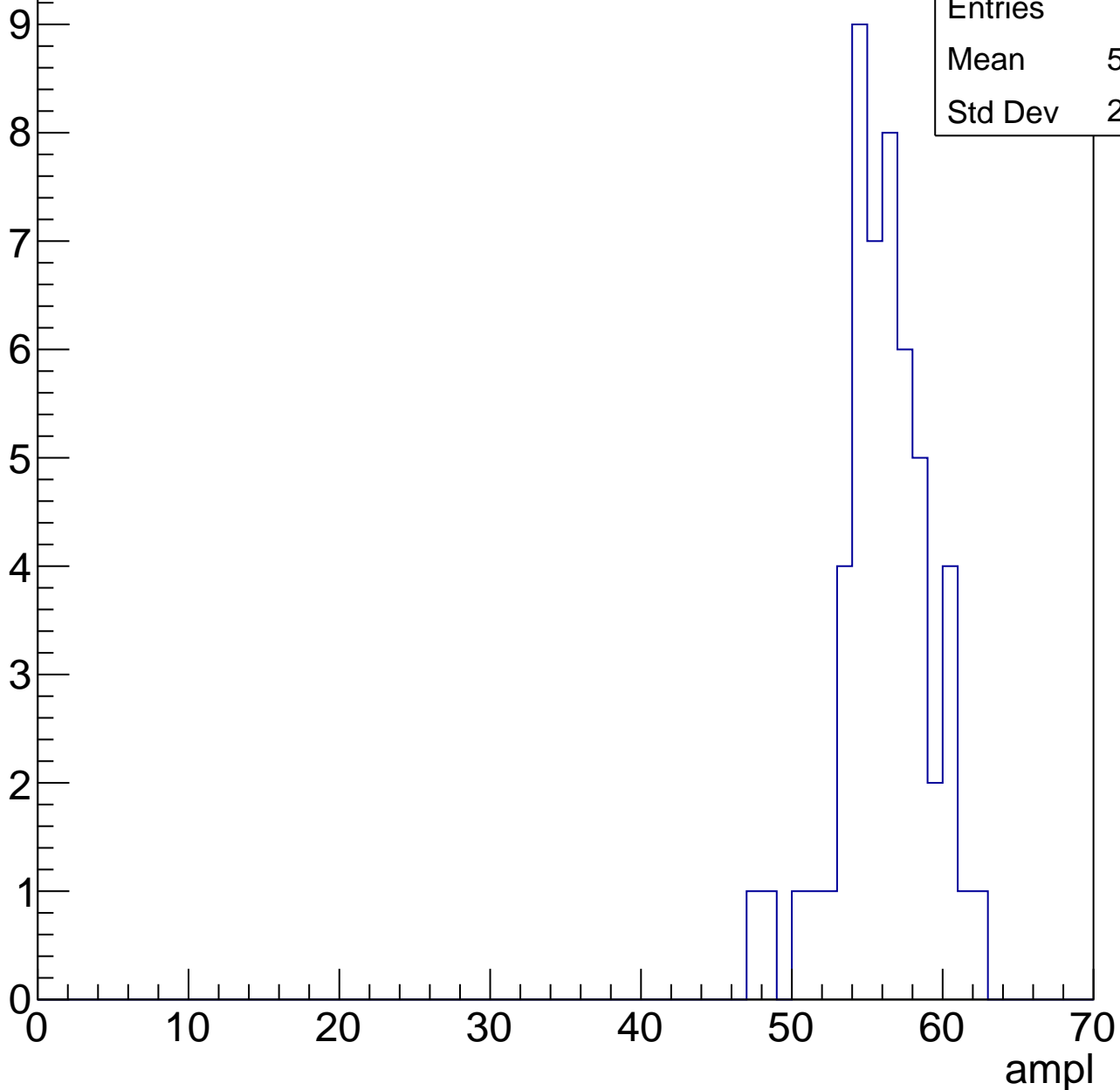


B1L003S, U21-ch40, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.62
Std Dev	2.982



B1L003S, U21-ch40, adc5

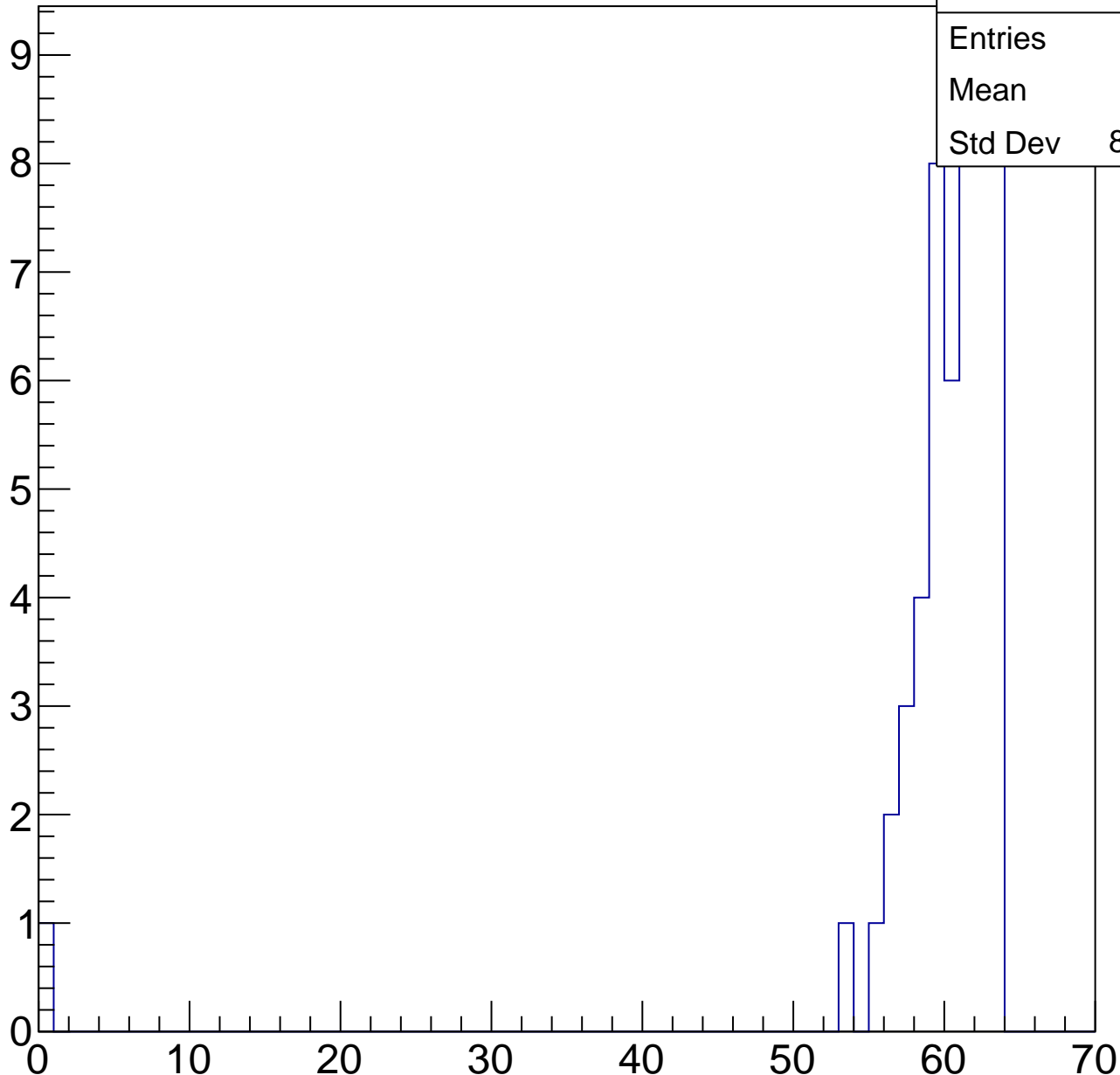
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.9
Std Dev	8.642

ampl



B1L003S, U21-ch40, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch40, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch41, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	29.68
Std Dev	3.096

Gaus mean : 29.8709

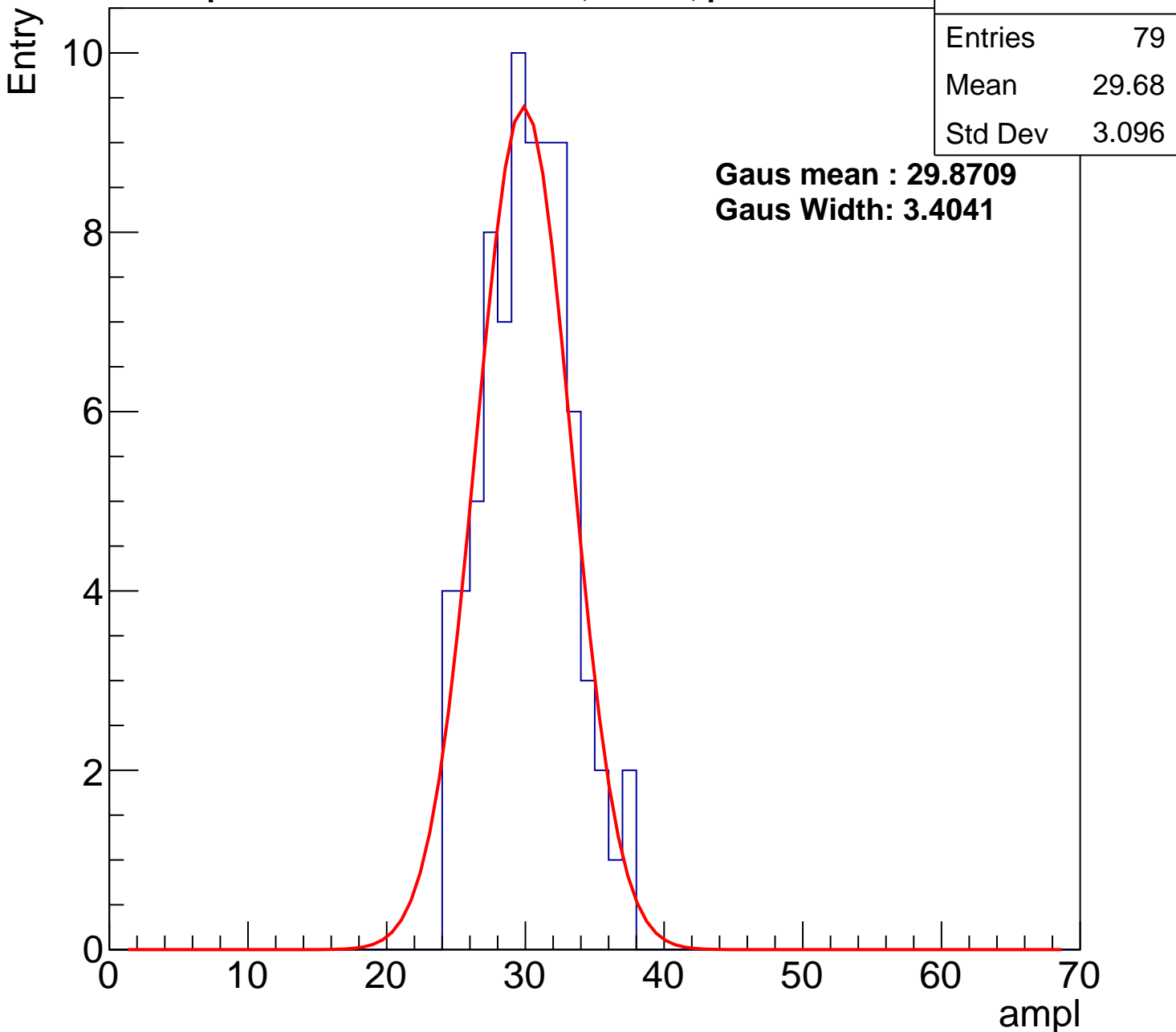
Gaus Width: 3.4041

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch41, adc1

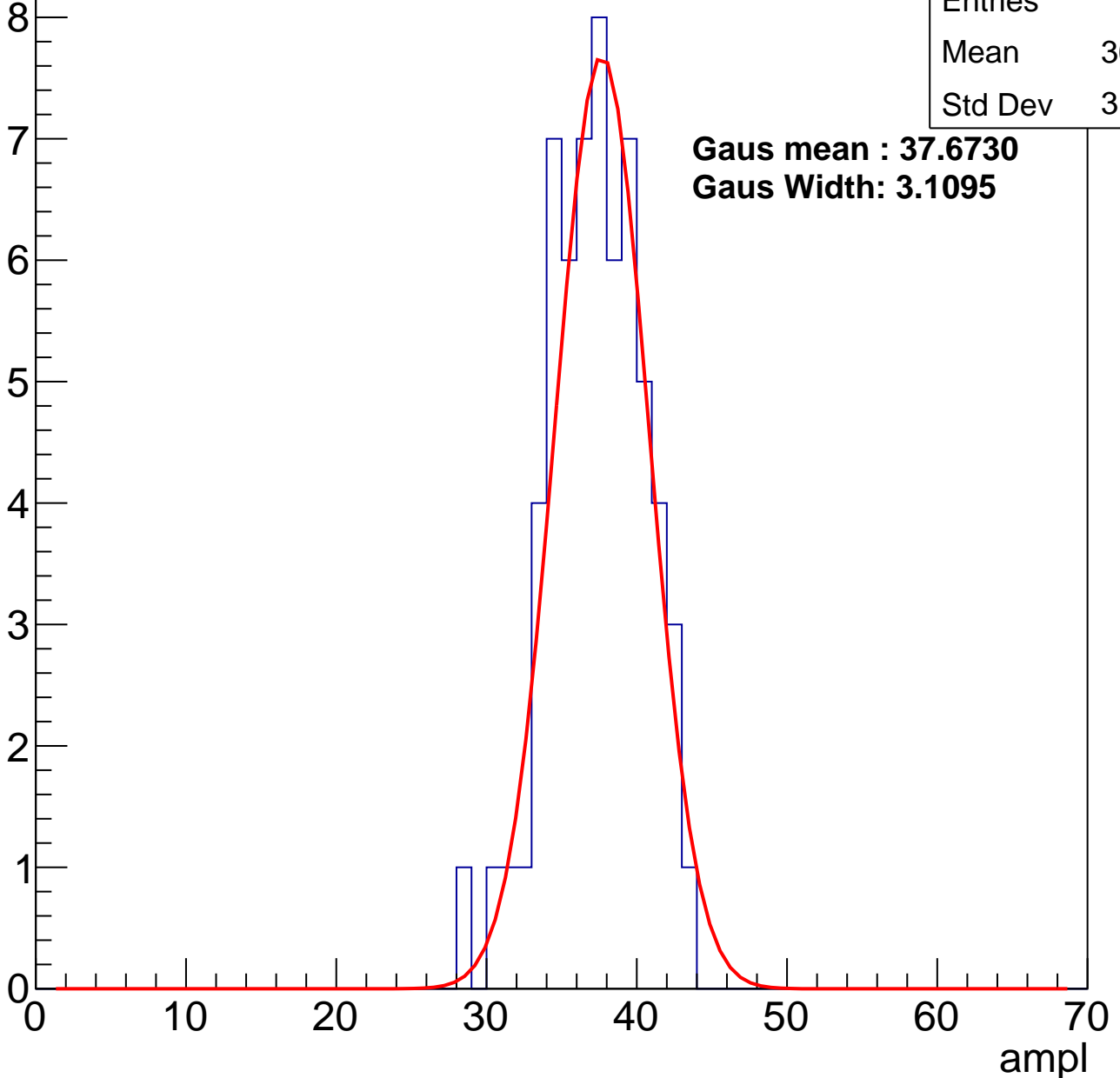
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.82
Std Dev	3.114

Gaus mean : 37.6730

Gaus Width: 3.1095



B1L003S, U21-ch41, adc2

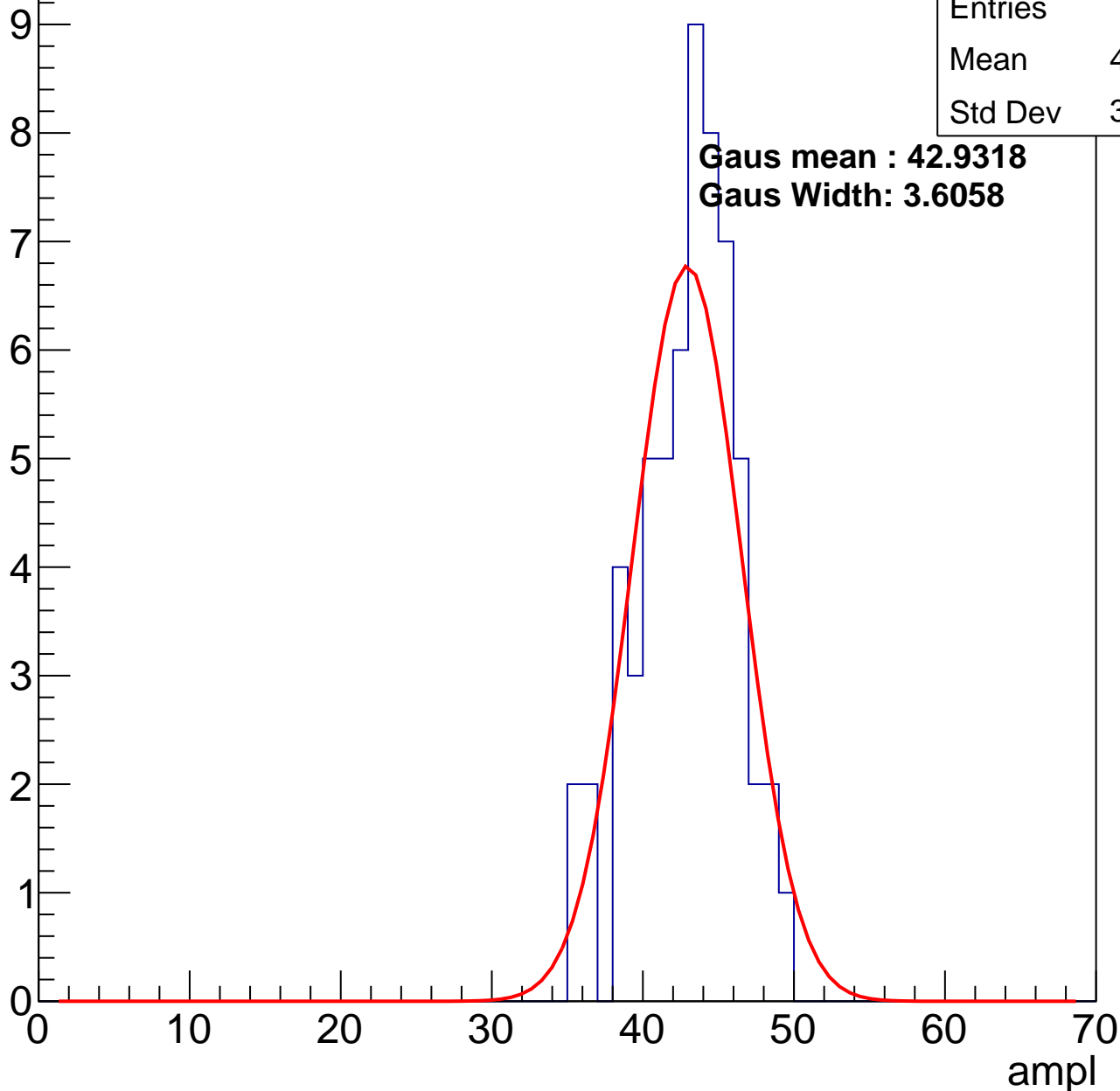
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.48
Std Dev	3.207

Gaus mean : 42.9318

Gaus Width: 3.6058

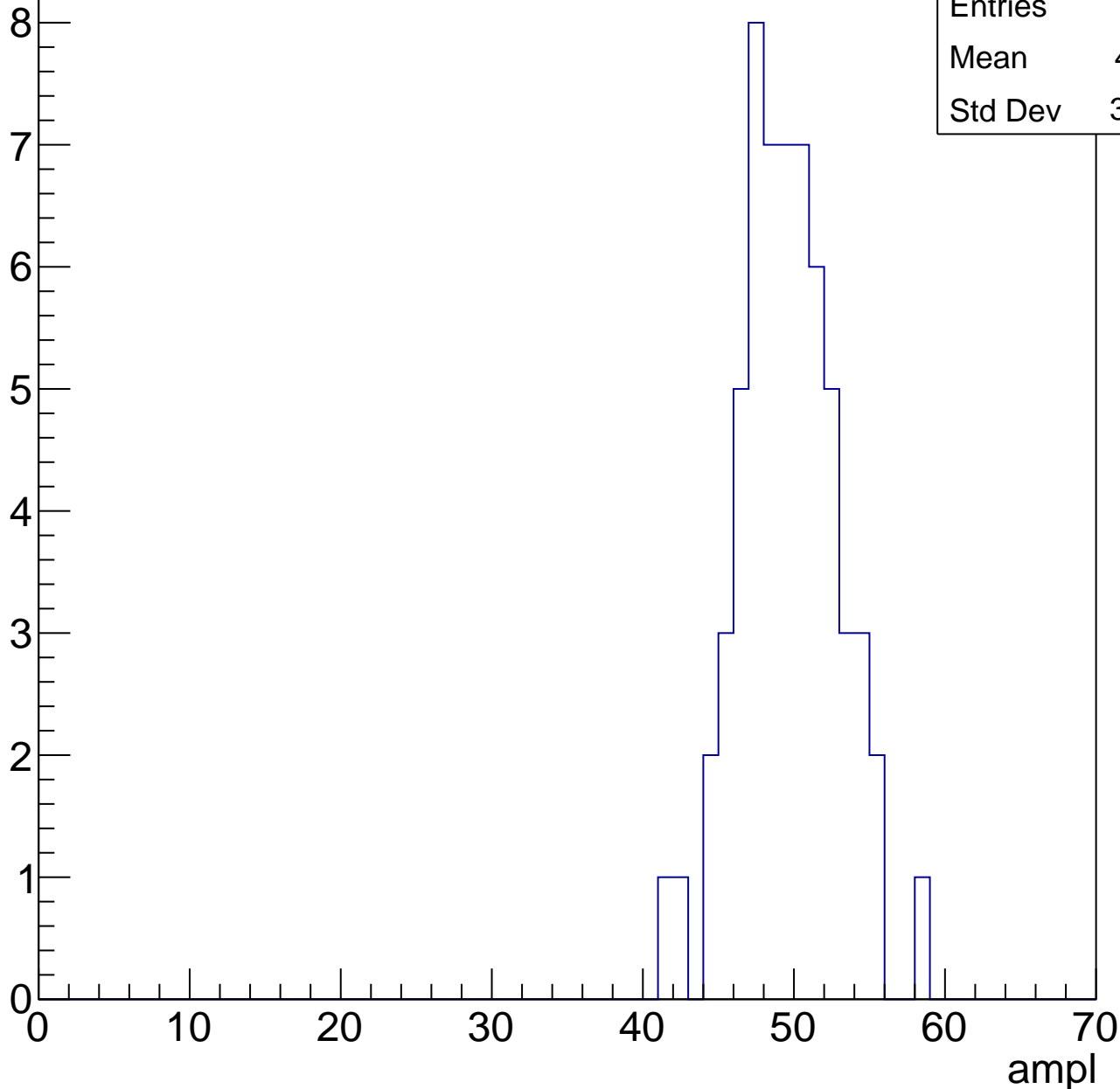


B1L003S, U21-ch41, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	49.11
Std Dev	3.255

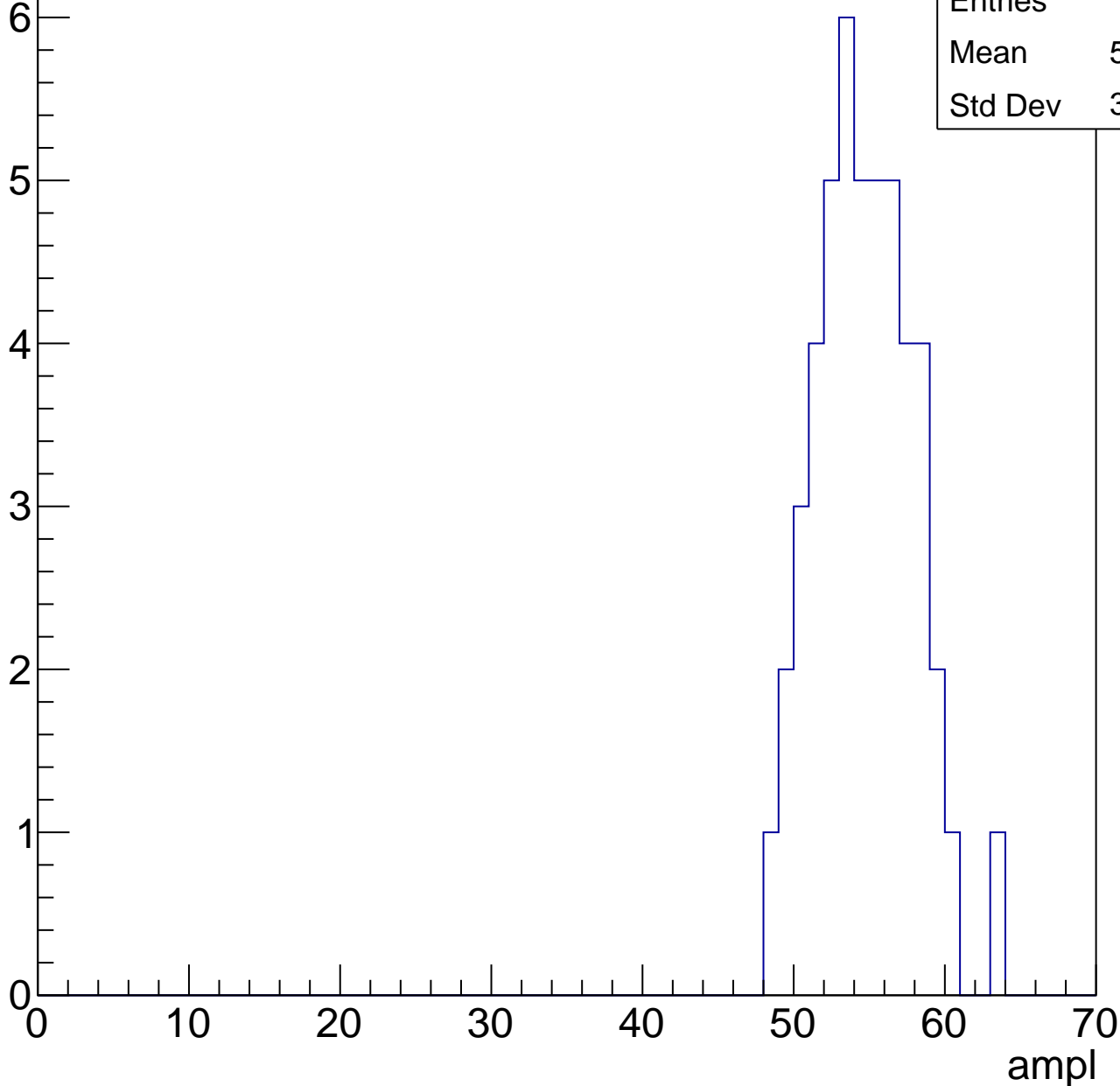


B1L003S, U21-ch41, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	54.25
Std Dev	3.179

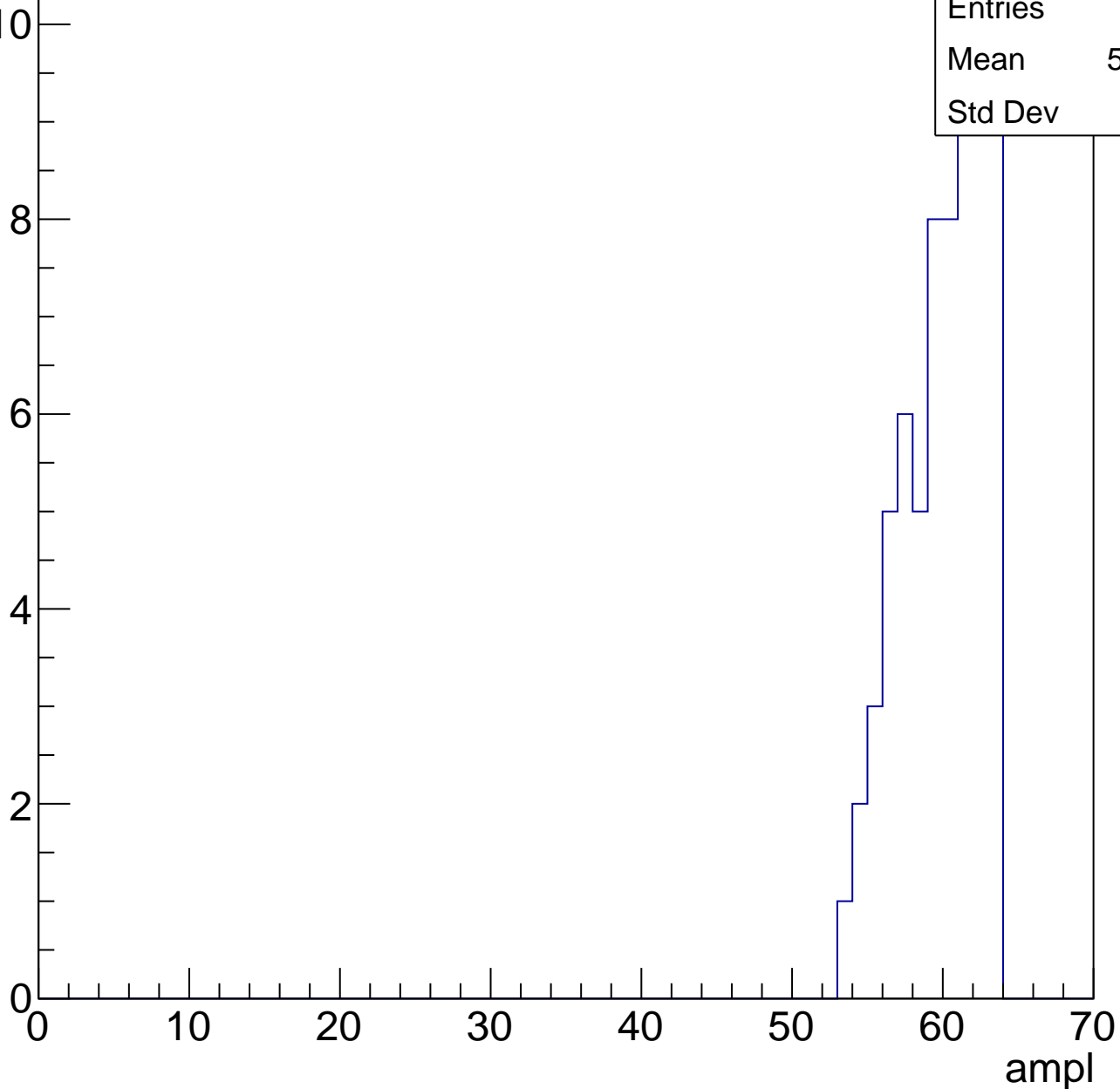


B1L003S, U21-ch41, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

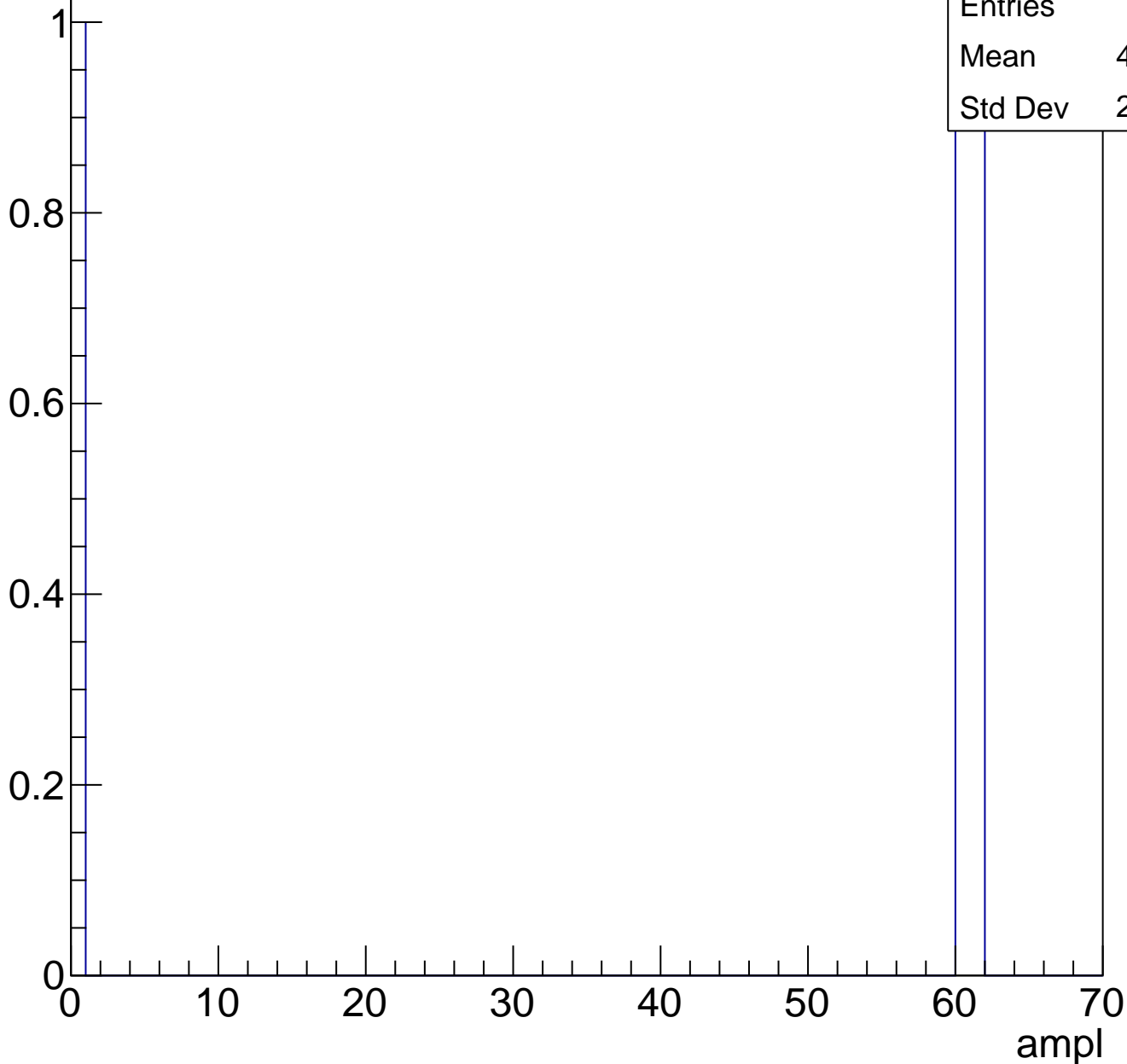
Entries	66
Mean	59.48
Std Dev	2.67



B1L003S, U21-ch41, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch41, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch42, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	29.72
Std Dev	3.485

Gaus mean : 29.7857

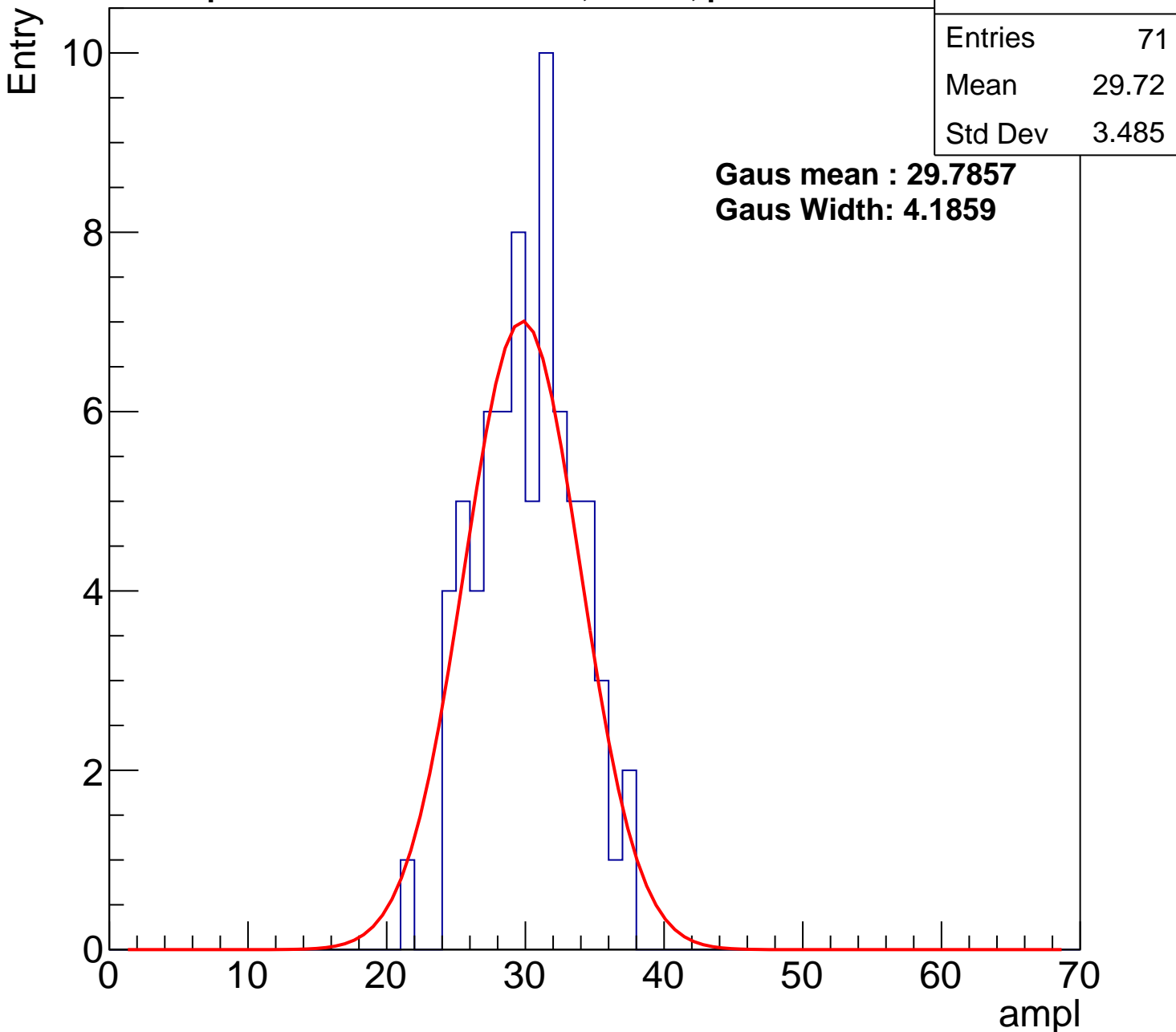
Gaus Width: 4.1859

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch42, adc1

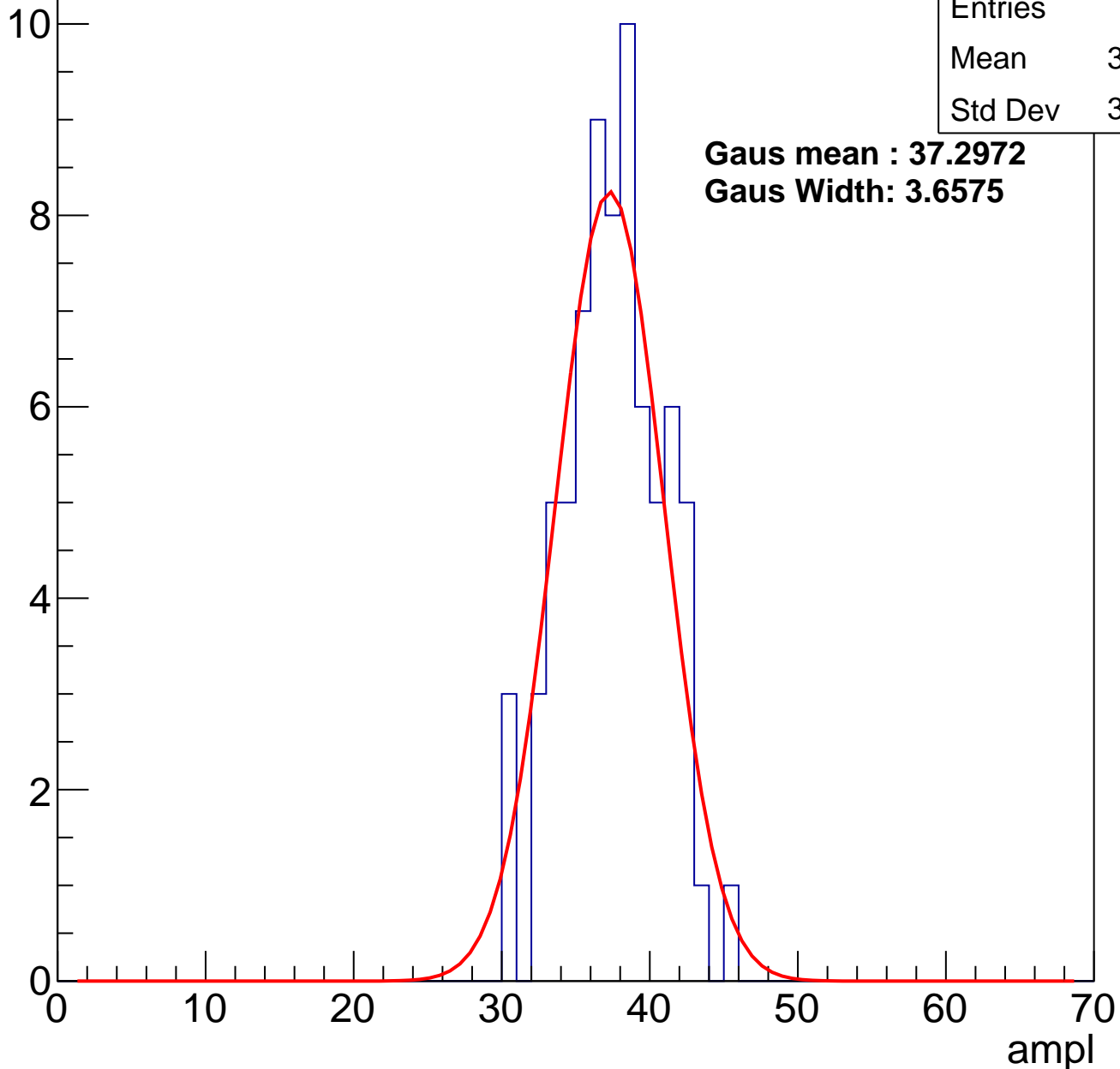
calib_packv5_042523_0143.root, FC#13, port D2

Entries	74
Mean	37.08
Std Dev	3.254

Gaus mean : 37.2972

Gaus Width: 3.6575

Entry



B1L003S, U21-ch42, adc2

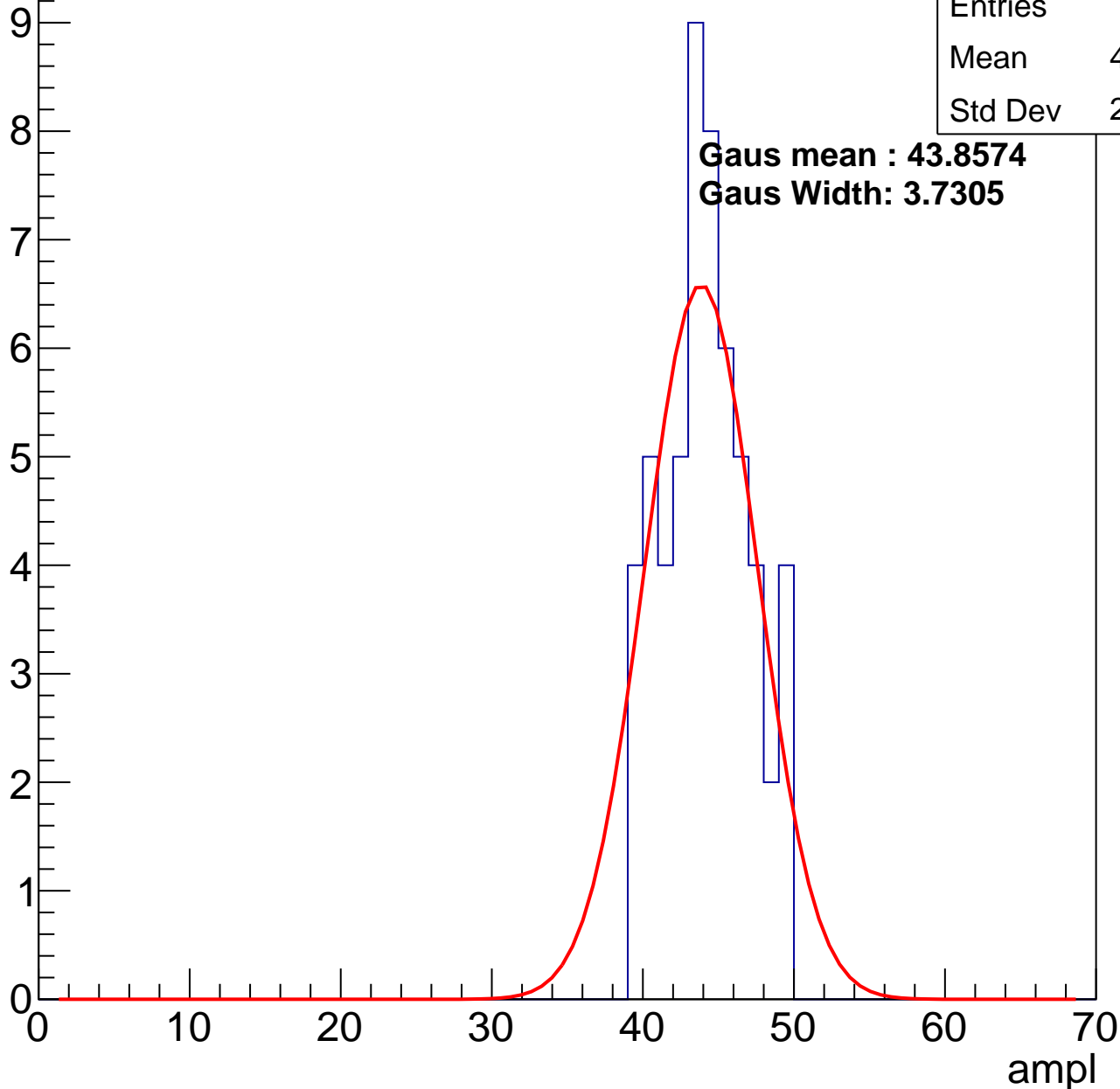
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	43.73
Std Dev	2.787

Gaus mean : 43.8574

Gaus Width: 3.7305

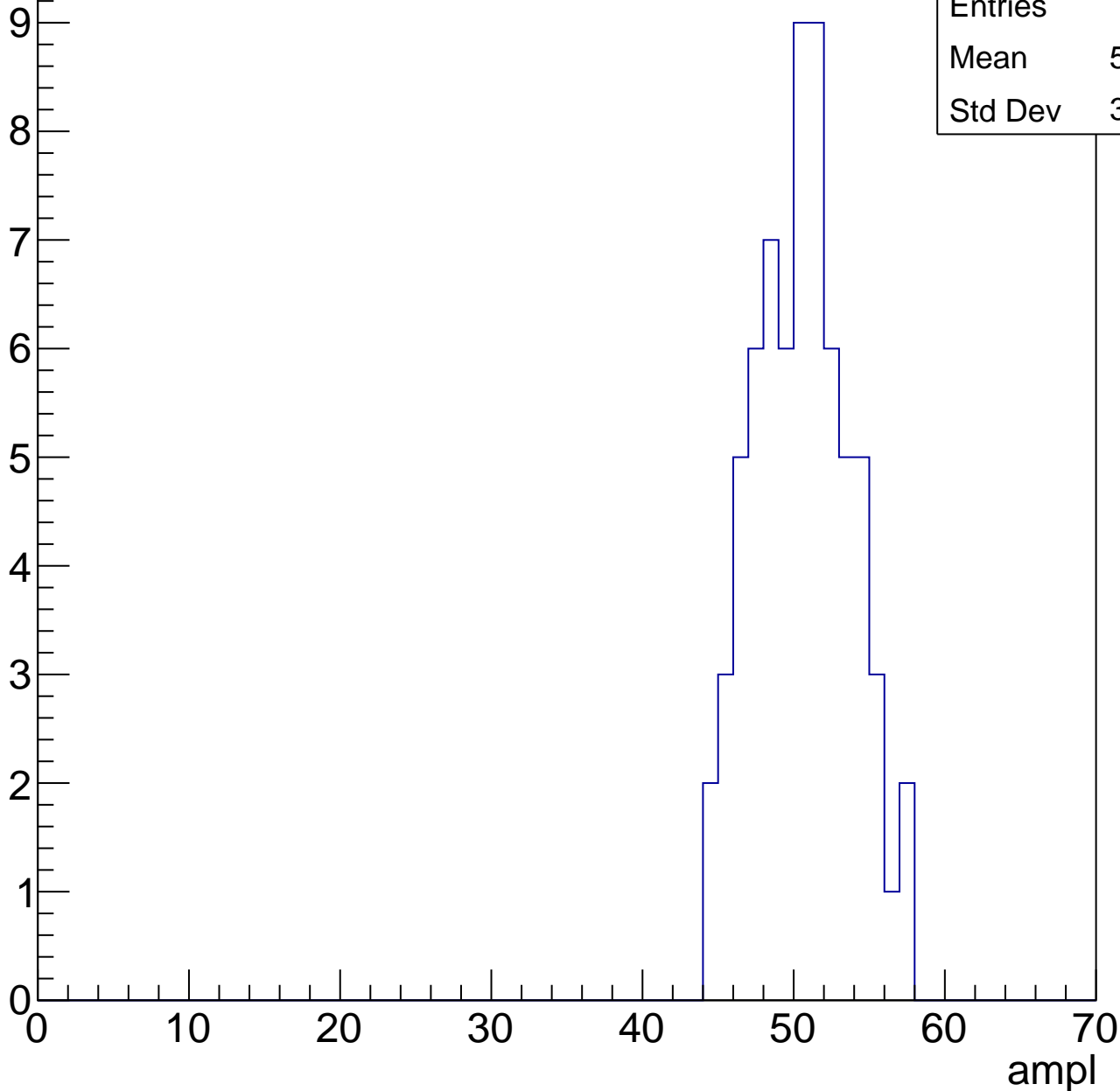


B1L003S, U21-ch42, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	50.09
Std Dev	3.143

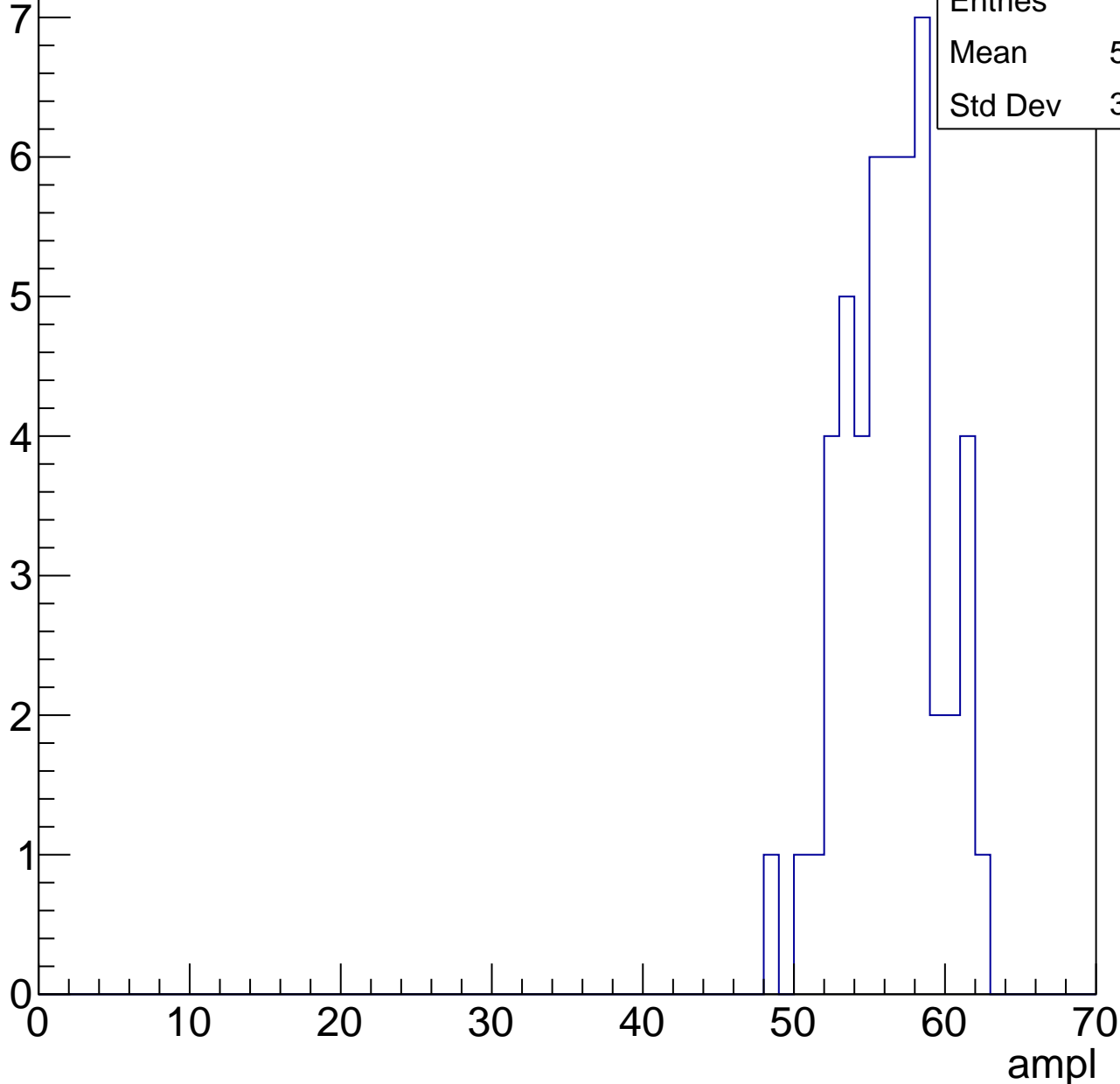


B1L003S, U21-ch42, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	55.92
Std Dev	3.084

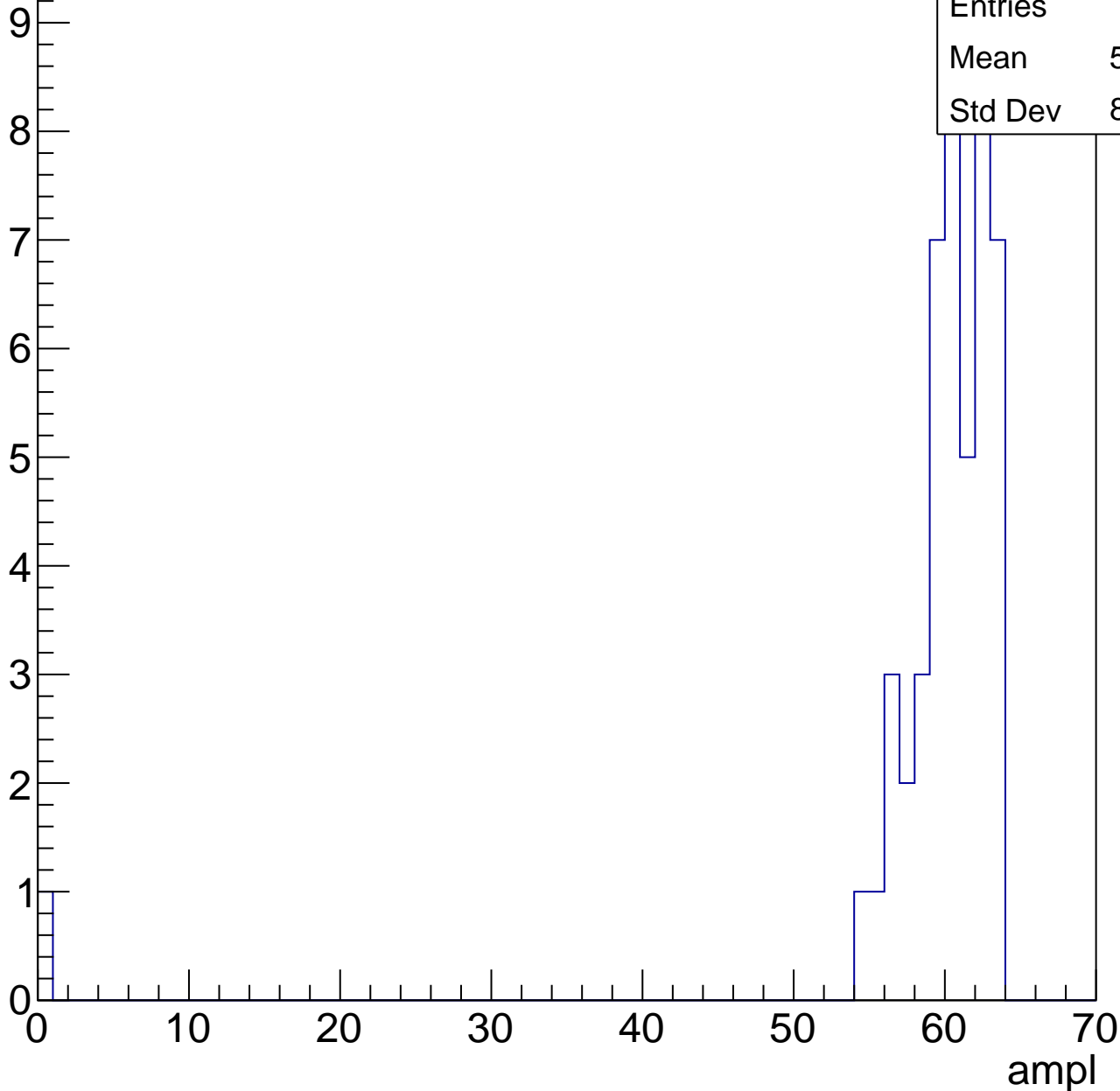


B1L003S, U21-ch42, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

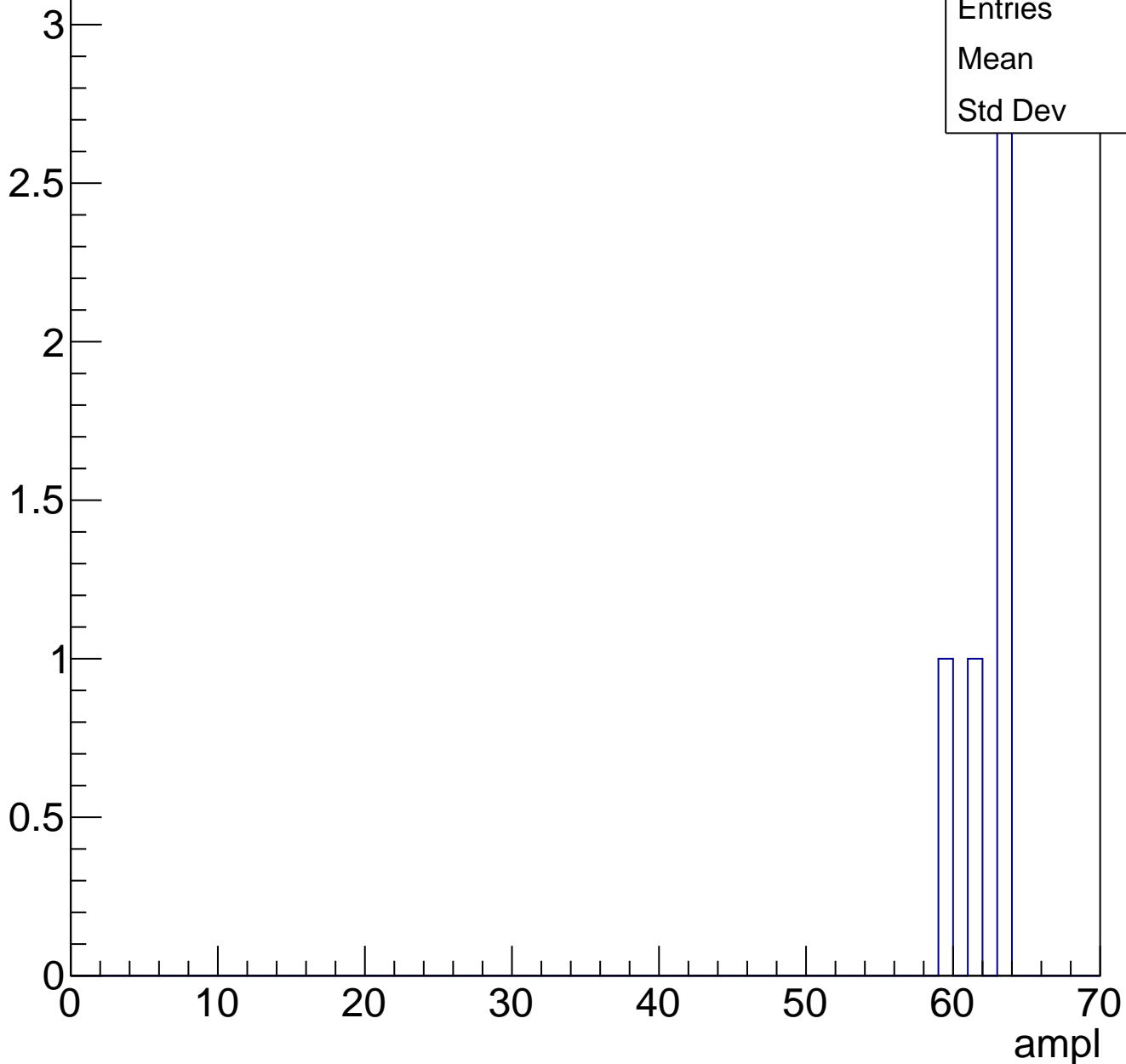
Entries	47
Mean	58.77
Std Dev	8.966



B1L003S, U21-ch42, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch42, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch43, adc0

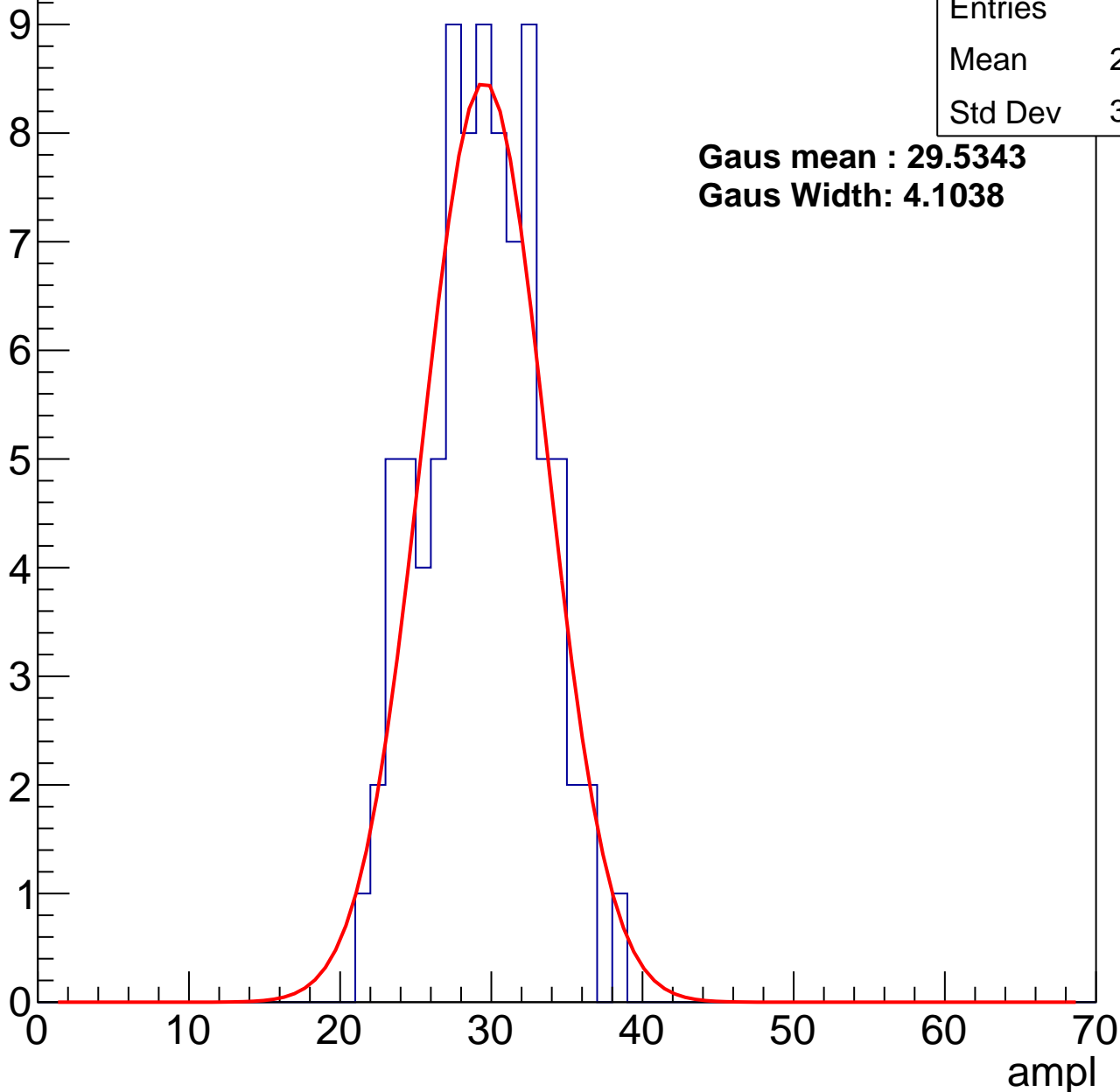
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	87
Mean	28.94
Std Dev	3.703

Gaus mean : 29.5343

Gaus Width: 4.1038



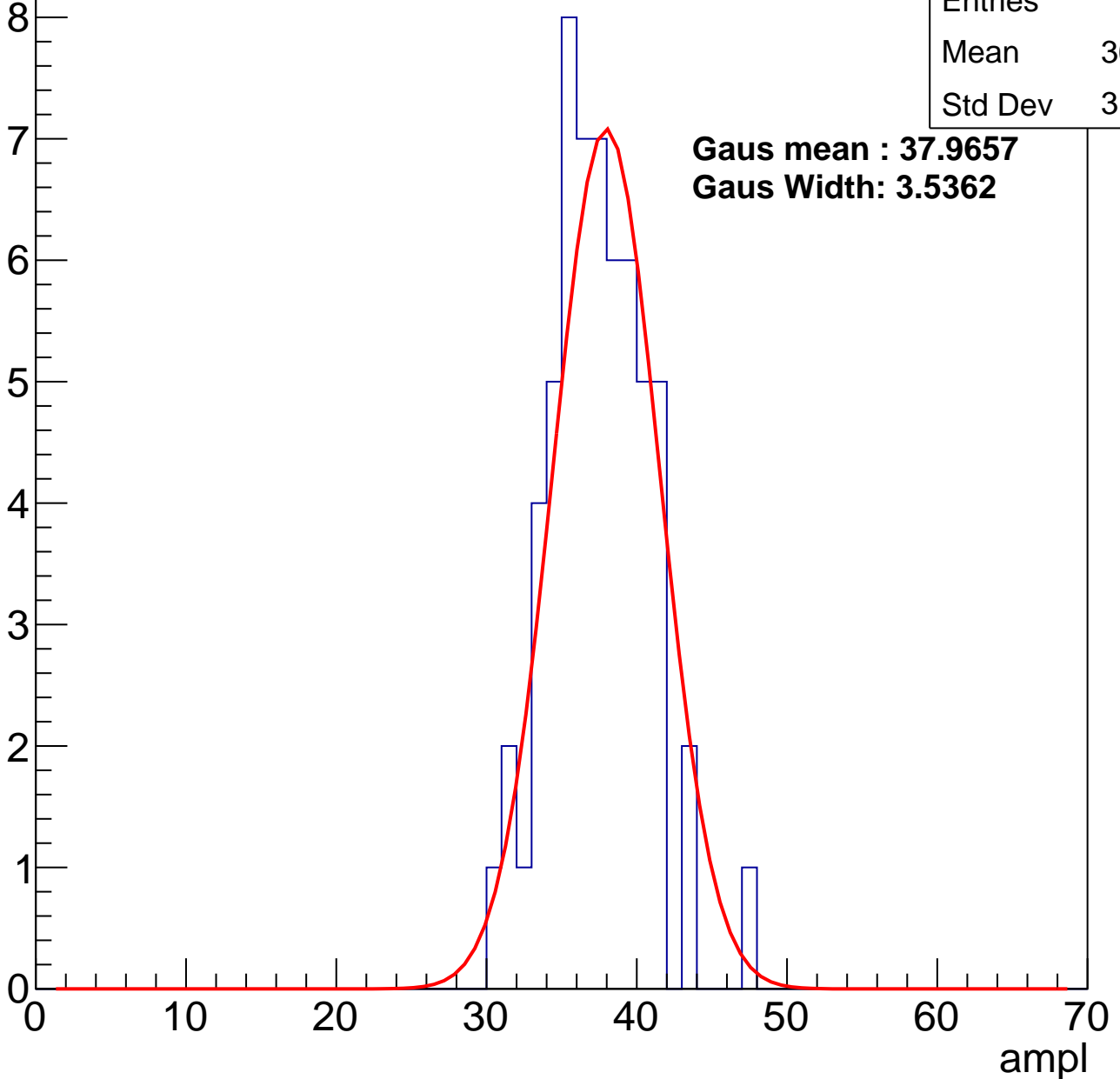
B1L003S, U21-ch43, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.95
Std Dev	3.217

Gaus mean : 37.9657
Gaus Width: 3.5362



B1L003S, U21-ch43, adc2

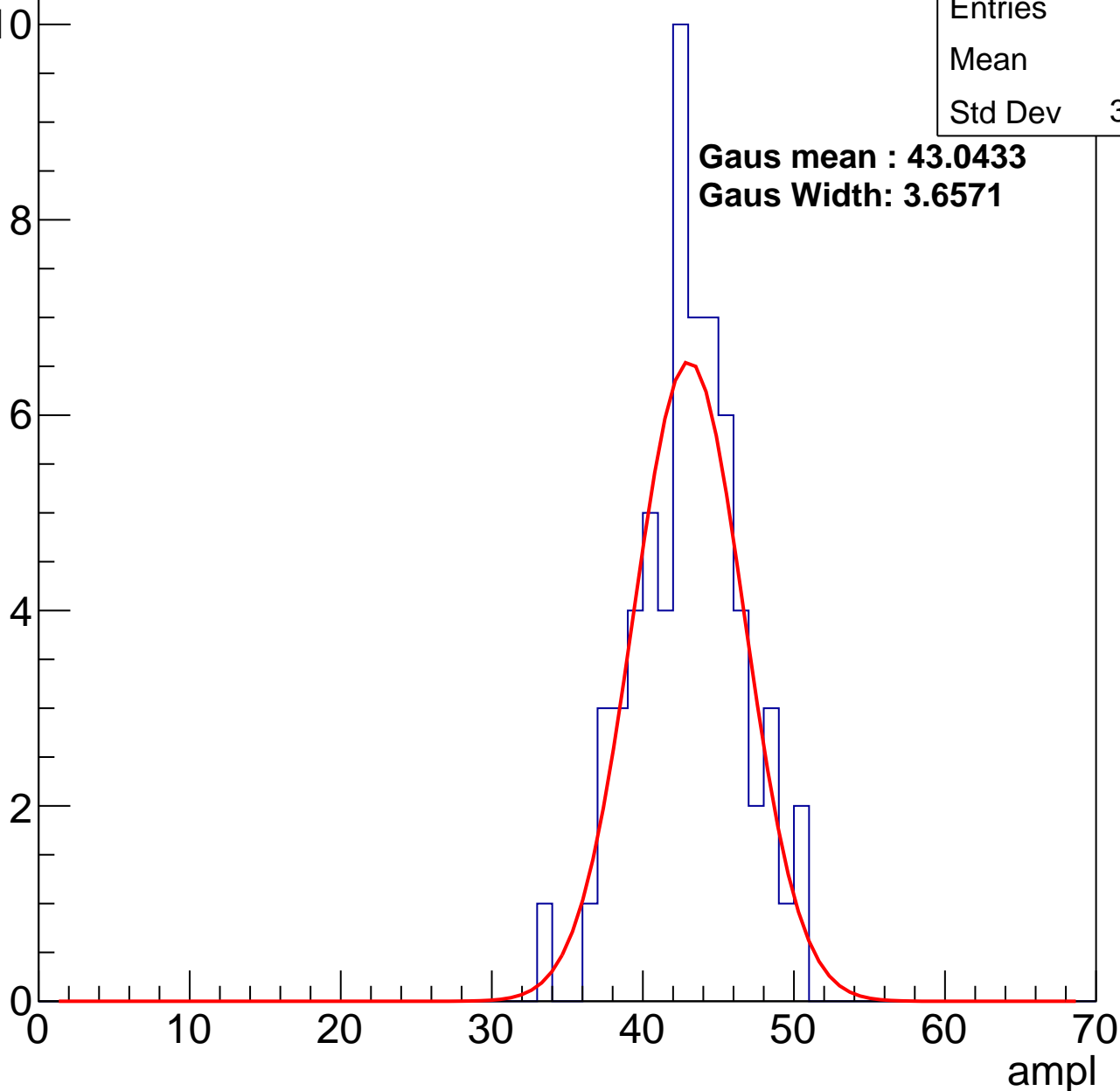
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.6
Std Dev	3.485

Gaus mean : 43.0433

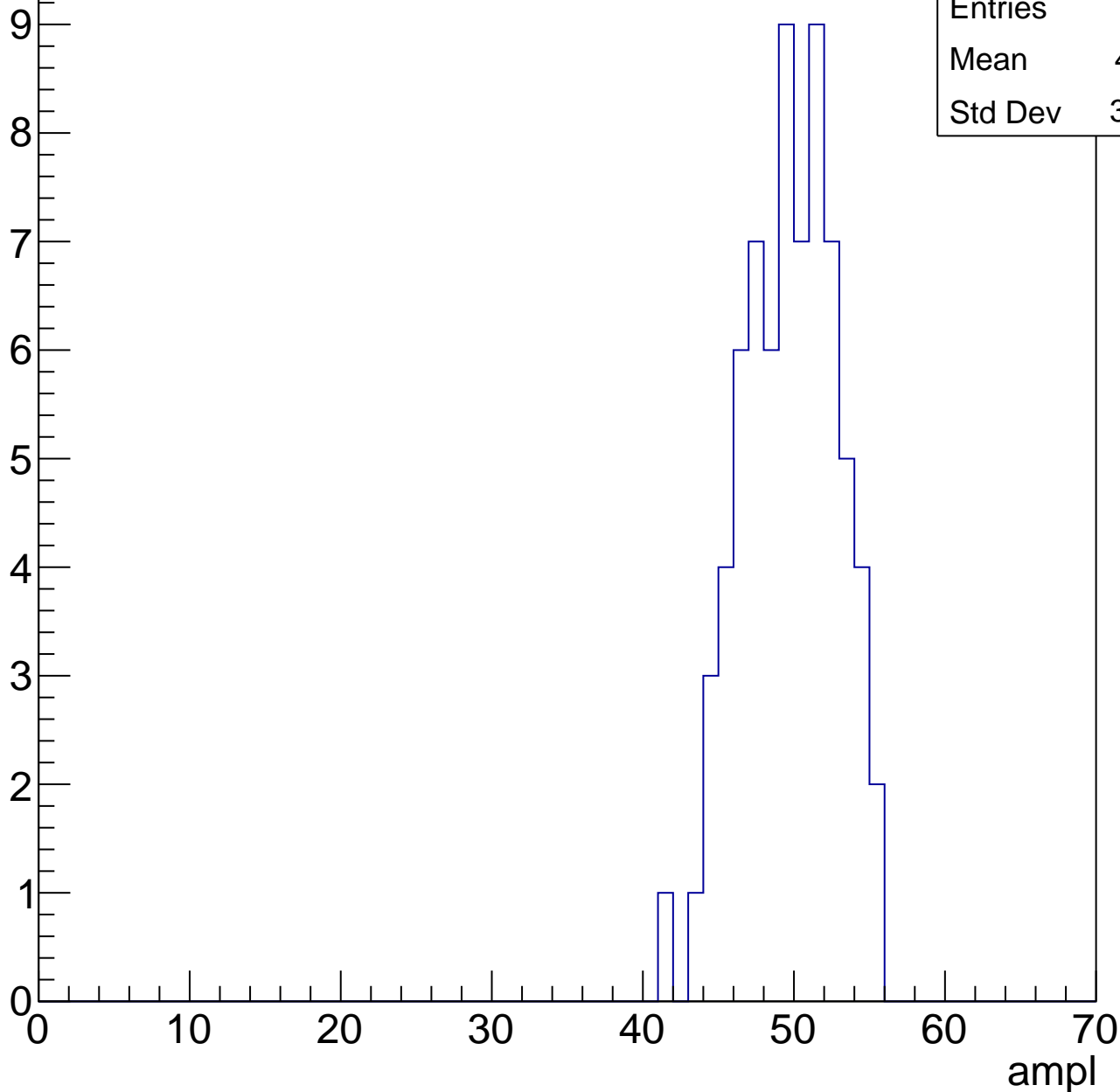
Gaus Width: 3.6571



B1L003S, U21-ch43, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

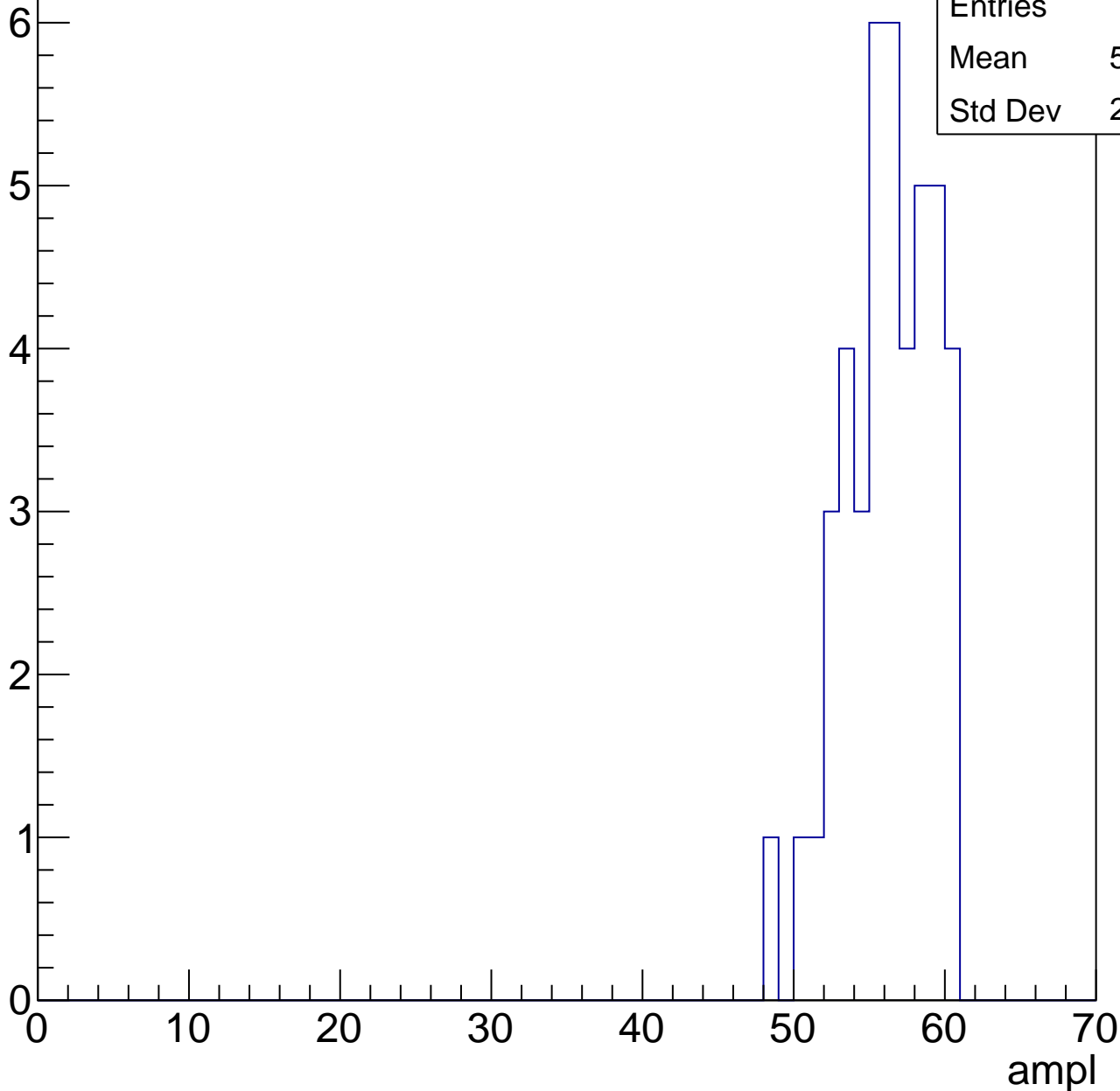


B1L003S, U21-ch43, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	55.77
Std Dev	2.884

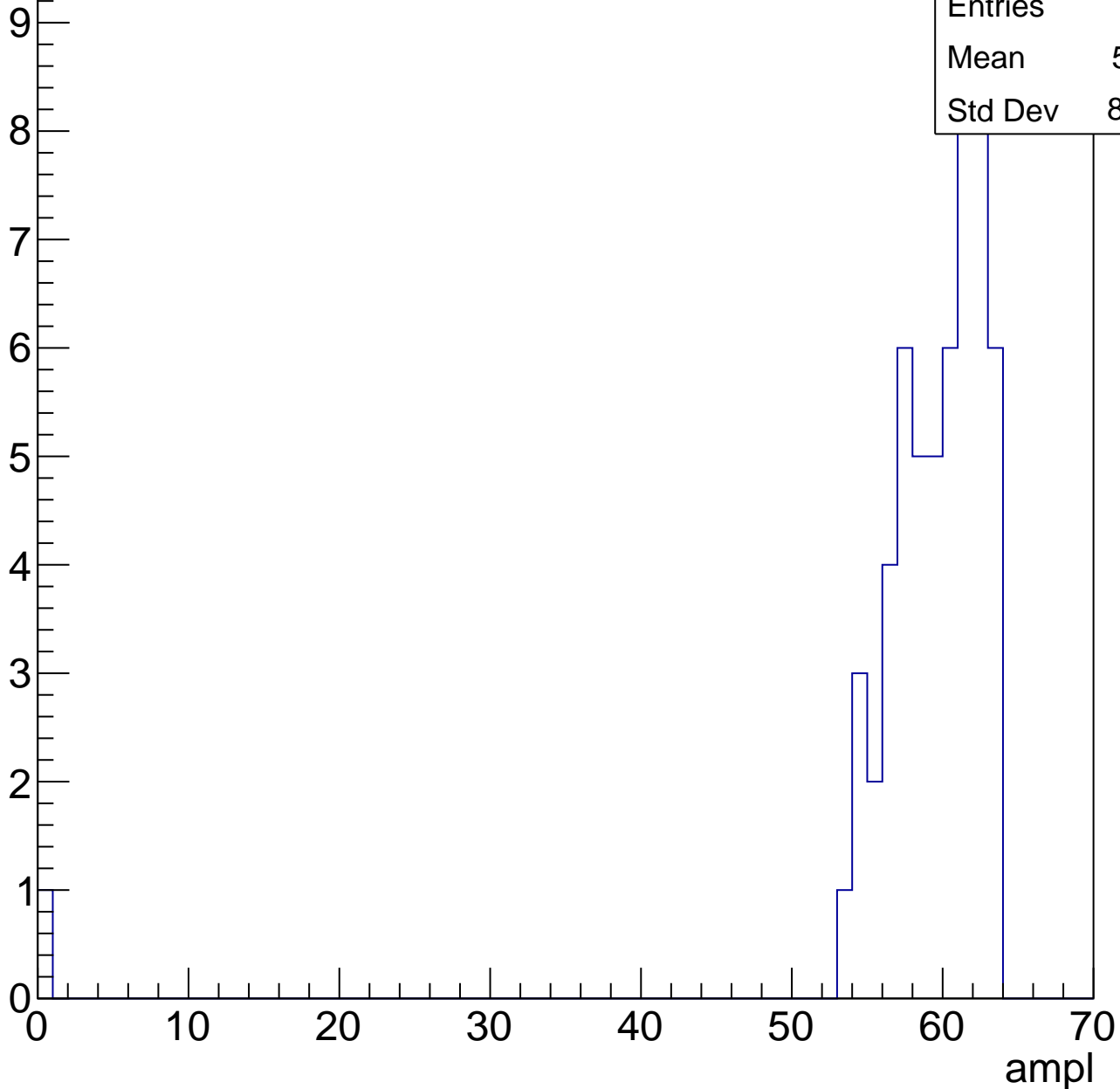


B1L003S, U21-ch43, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

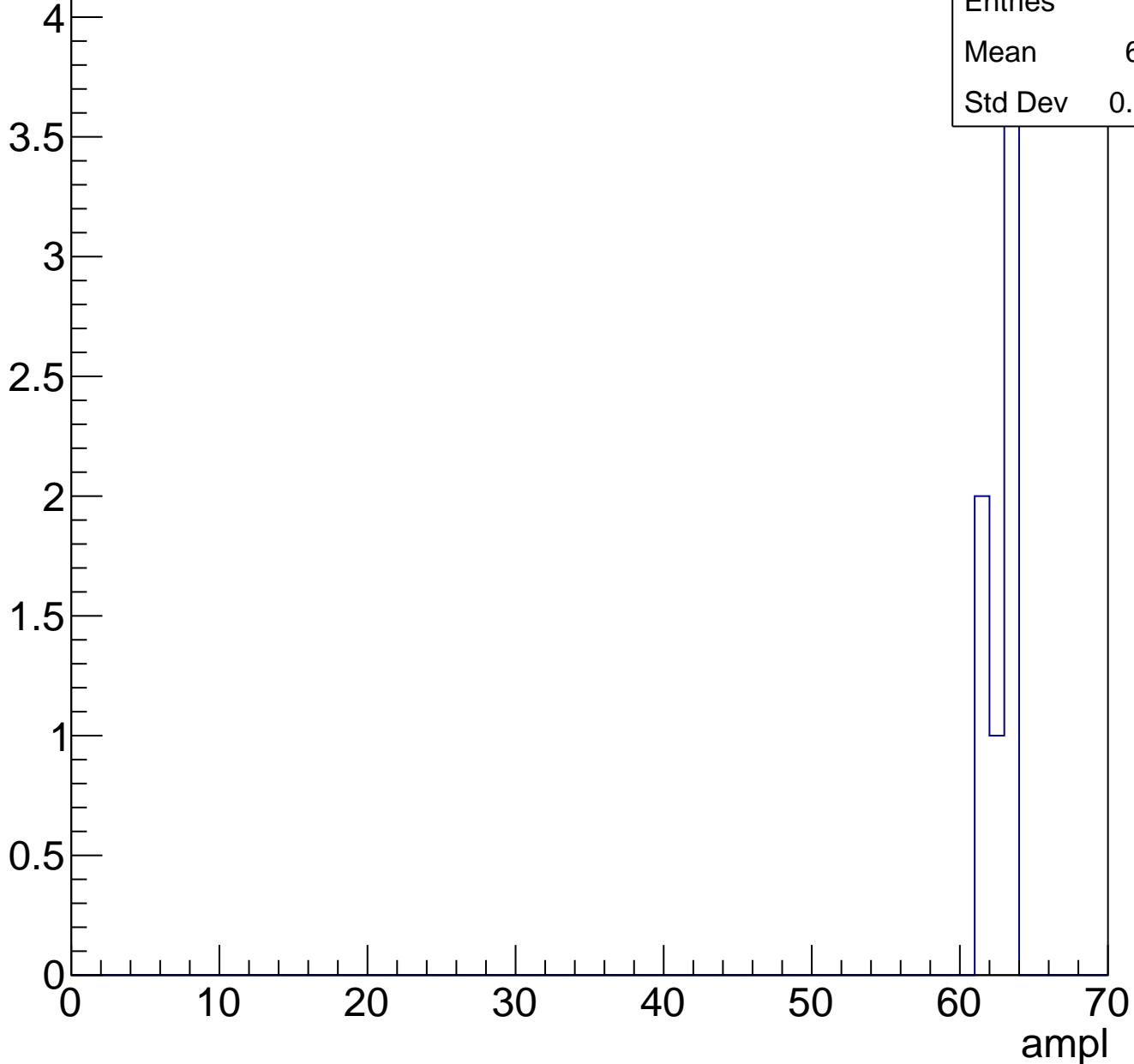
Entries	56
Mean	58.21
Std Dev	8.312



B1L003S, U21-ch43, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch43, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch44, adc0

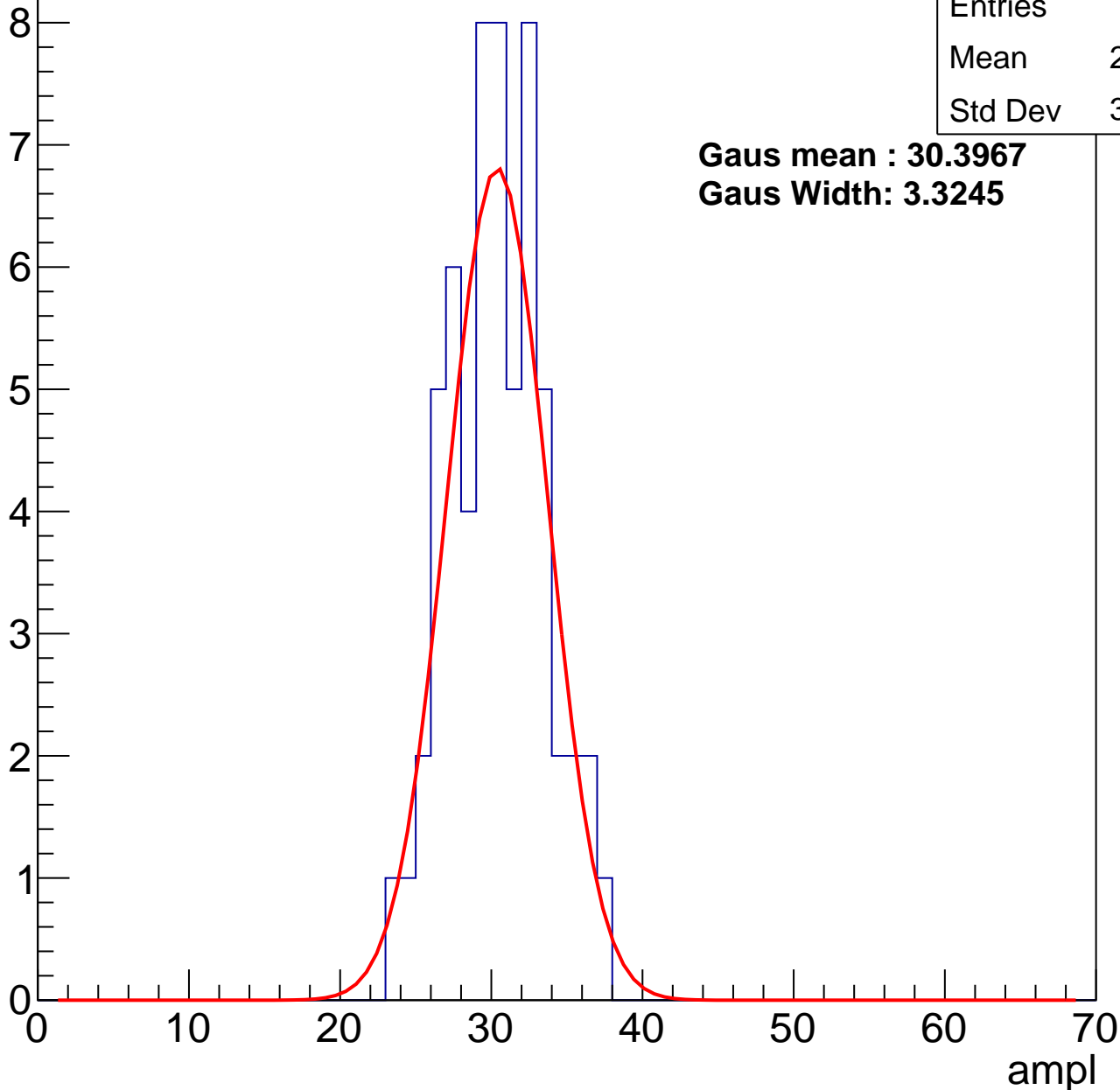
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.93
Std Dev	3.103

Gaus mean : 30.3967

Gaus Width: 3.3245



B1L003S, U21-ch44, adc1

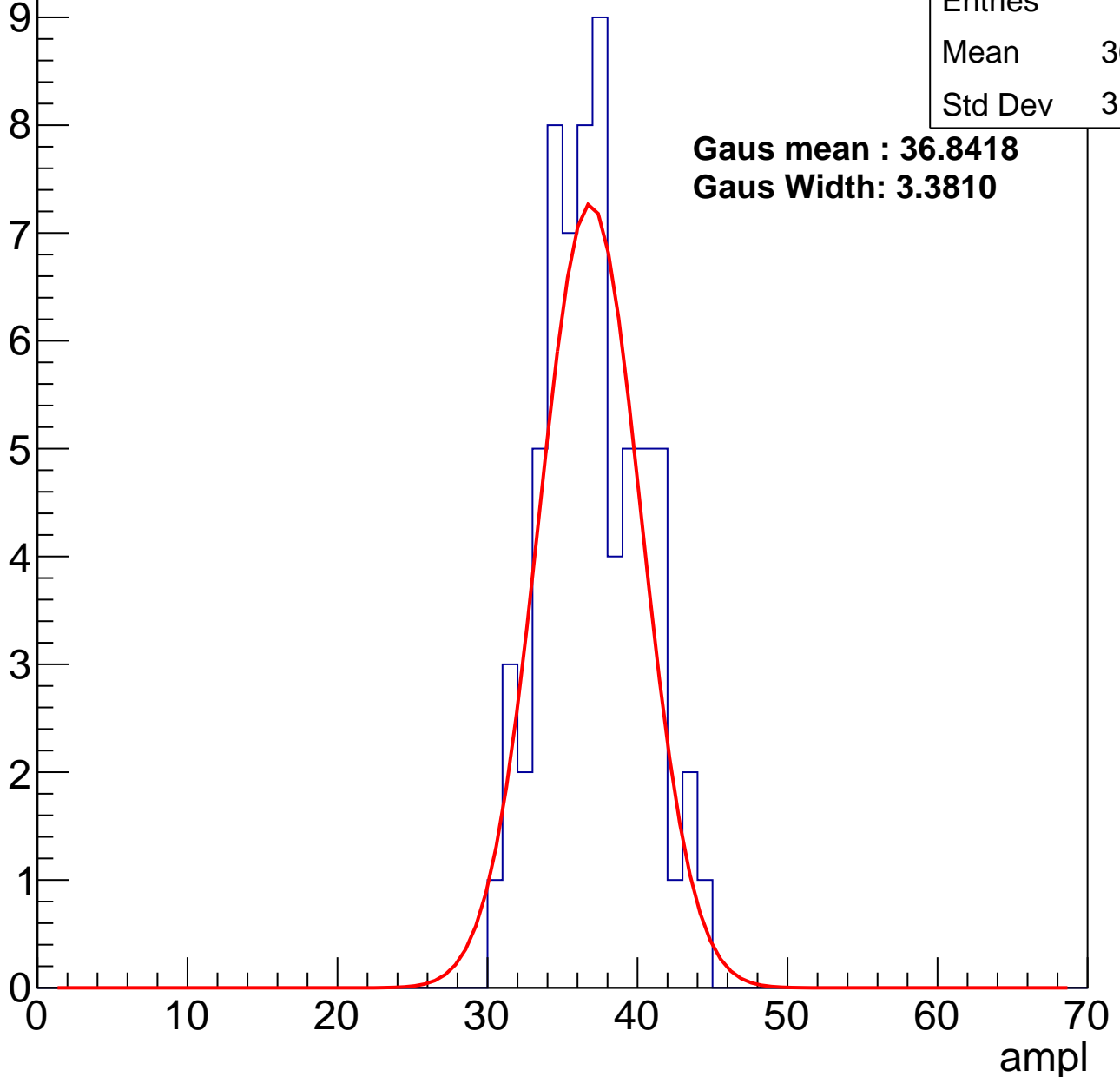
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.58
Std Dev	3.205

Gaus mean : 36.8418

Gaus Width: 3.3810



B1L003S, U21-ch44, adc2

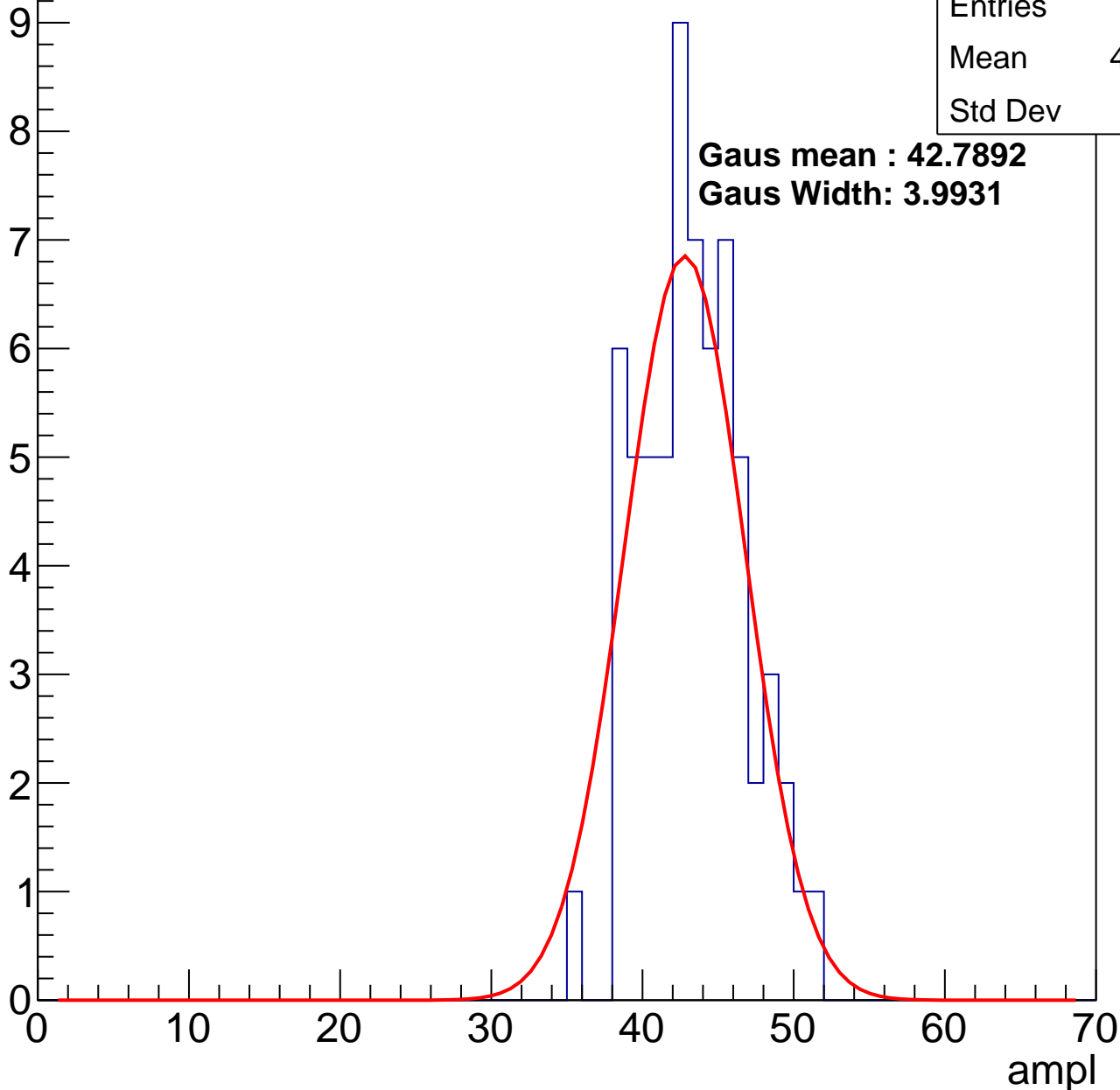
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.89
Std Dev	3.37

Gaus mean : 42.7892

Gaus Width: 3.9931

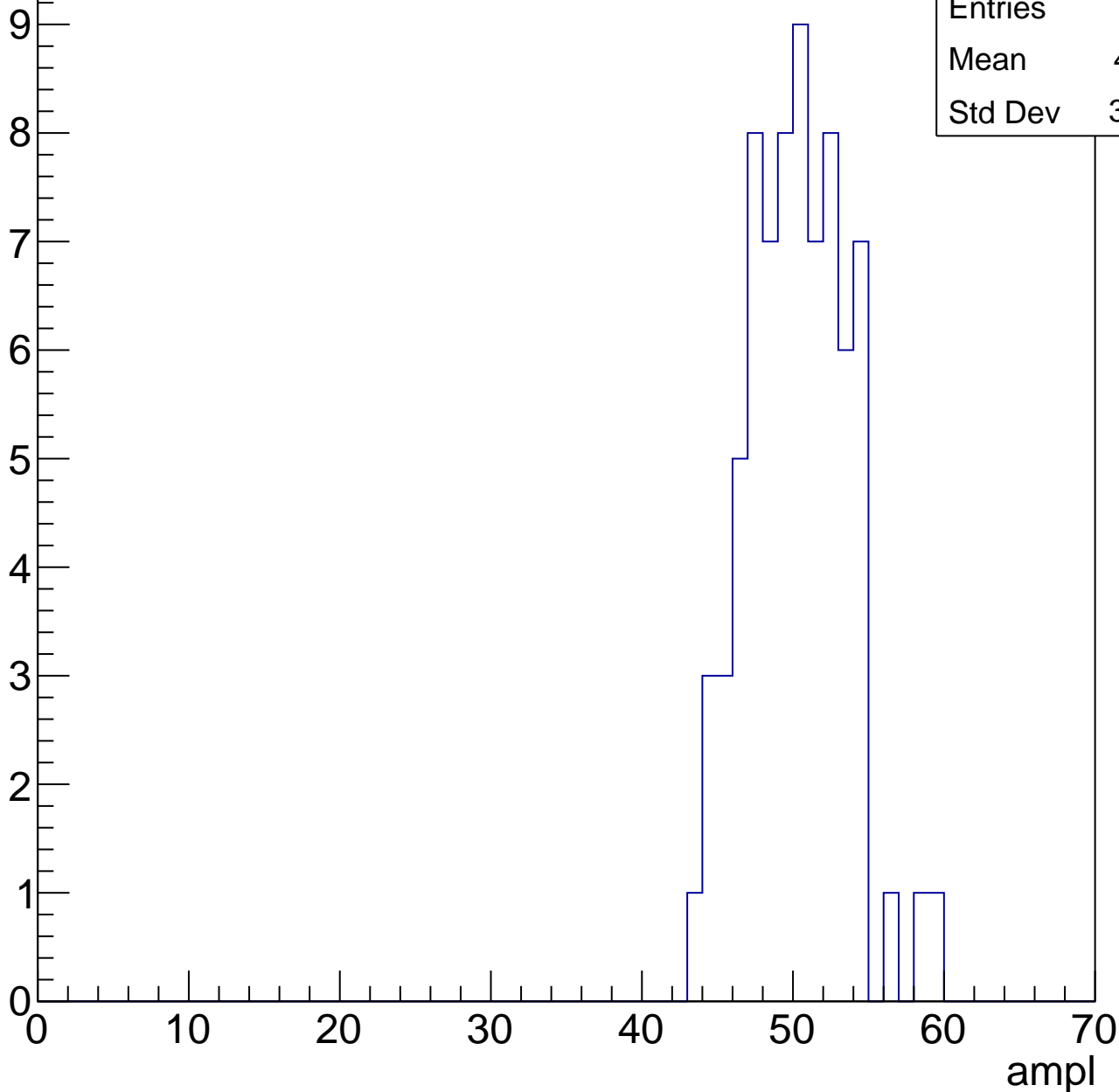


B1L003S, U21-ch44, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

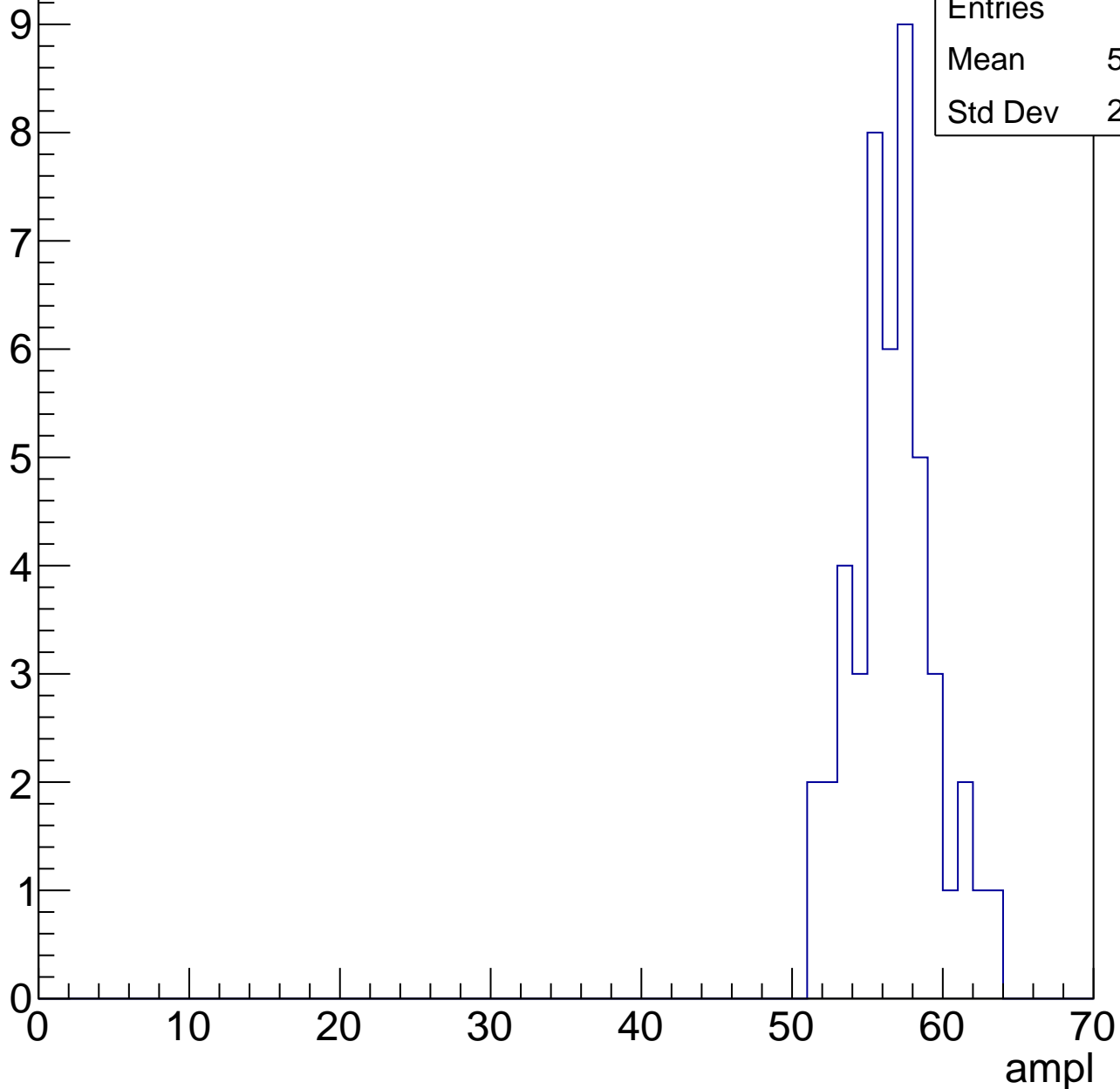
Entries	75
Mean	49.81
Std Dev	3.273



B1L003S, U21-ch44, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

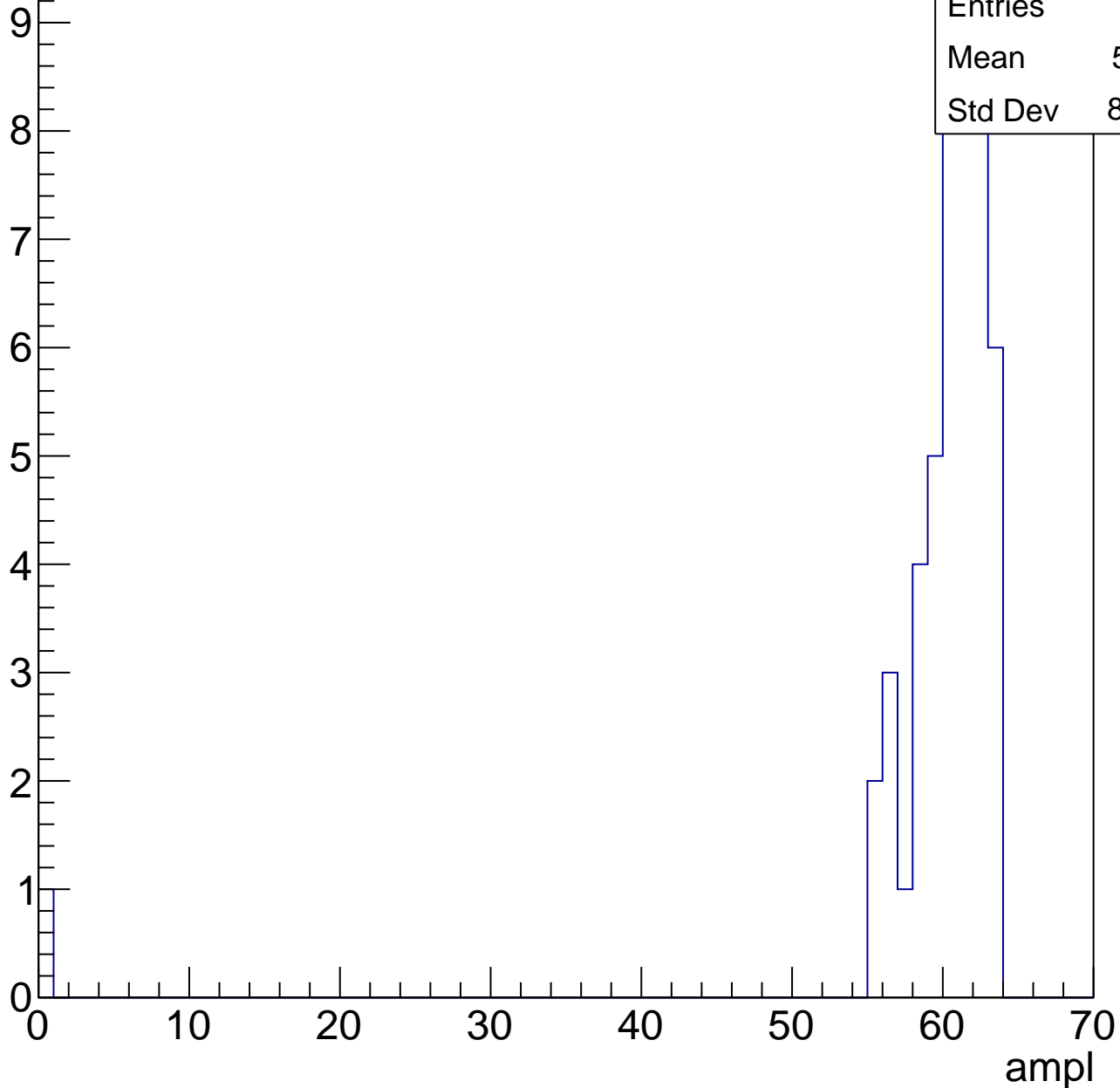


Entries	47
Mean	56.23
Std Dev	2.699

B1L003S, U21-ch44, adc5

calib_packv5_042523_0143.root, FC#13, port D2

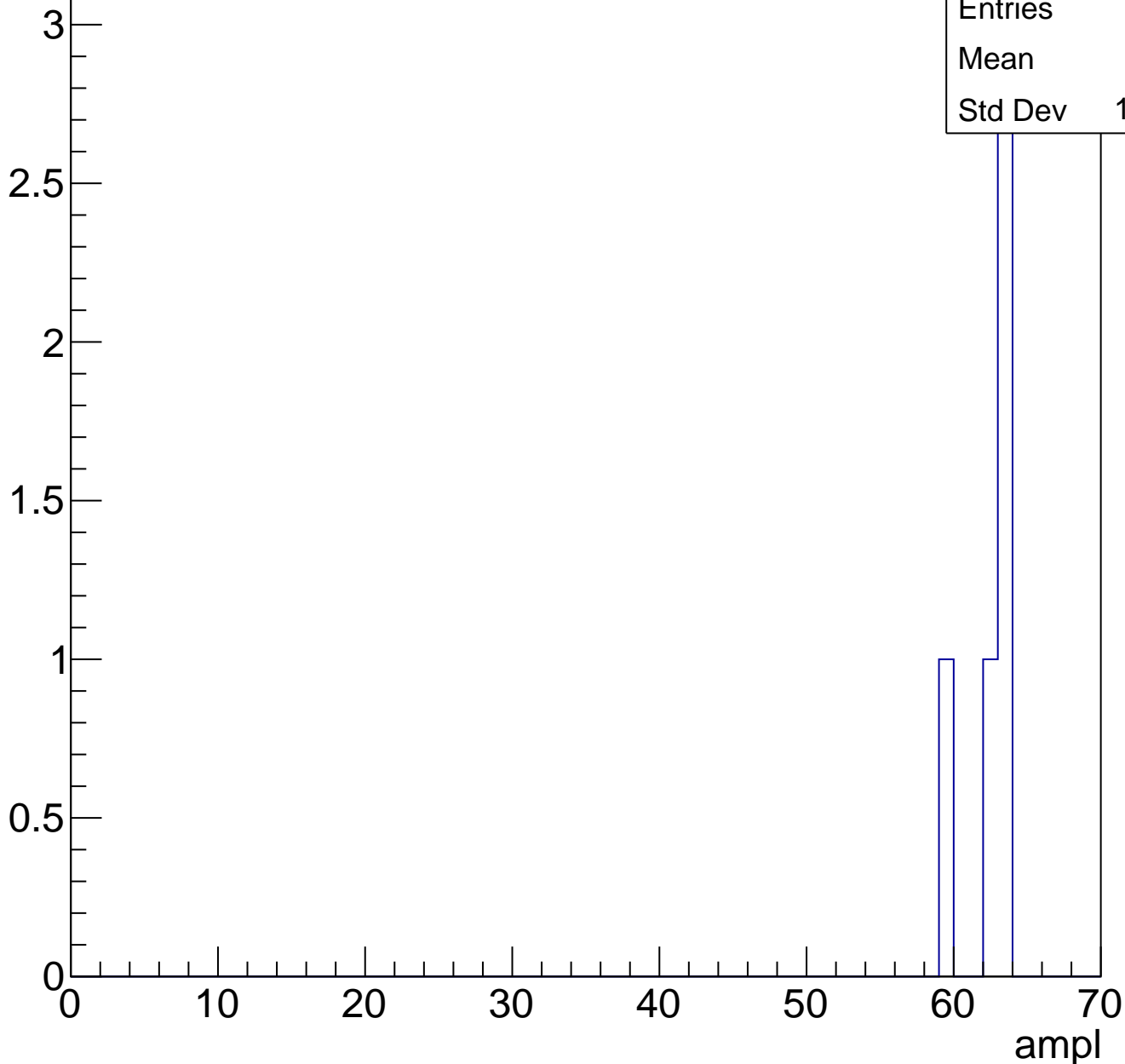
Entry



B1L003S, U21-ch44, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch44, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch45, adc0

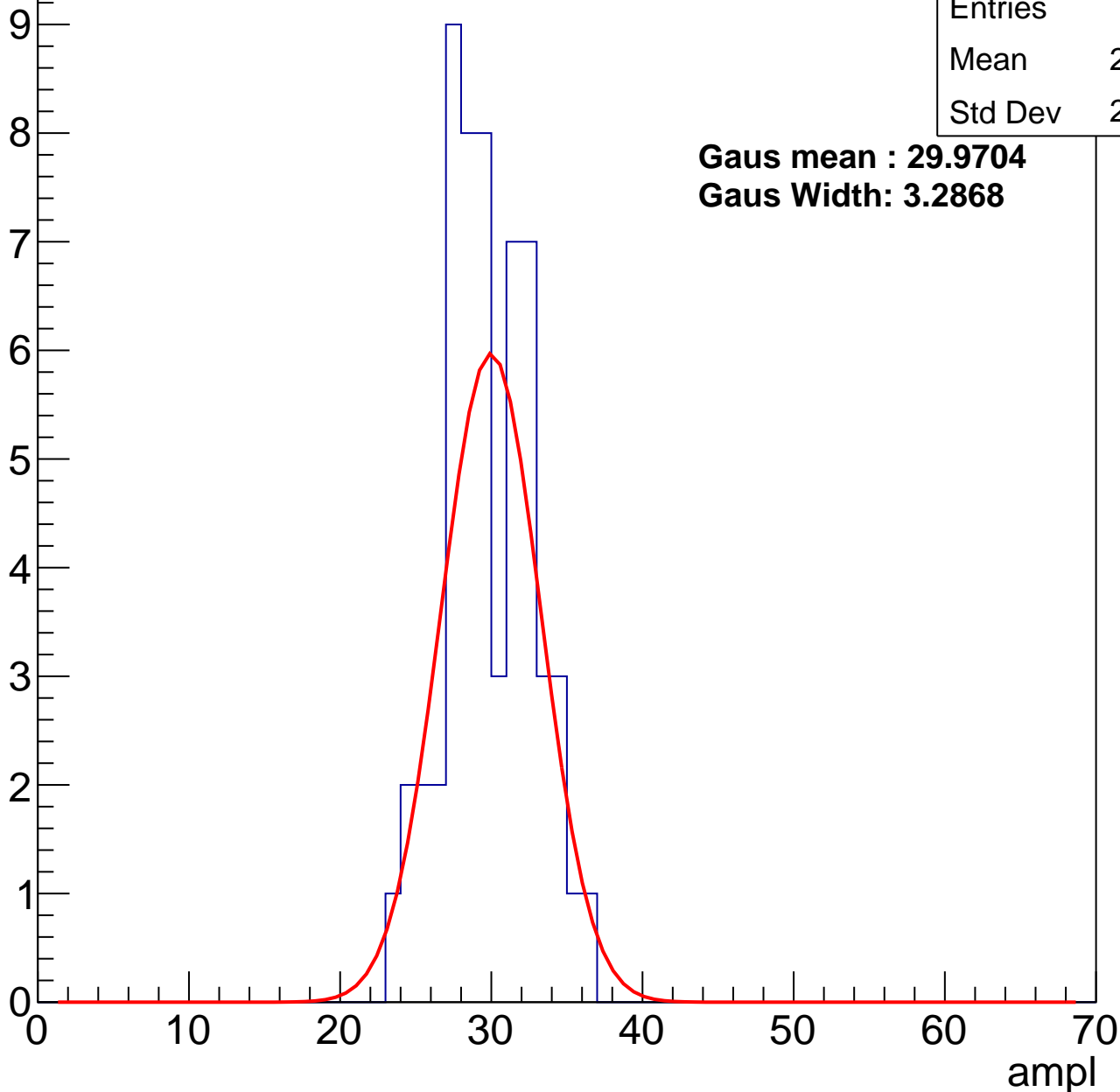
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.39
Std Dev	2.882

Gaus mean : 29.9704

Gaus Width: 3.2868



B1L003S, U21-ch45, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	35.7
Std Dev	3.326

Gaus mean : 36.4287

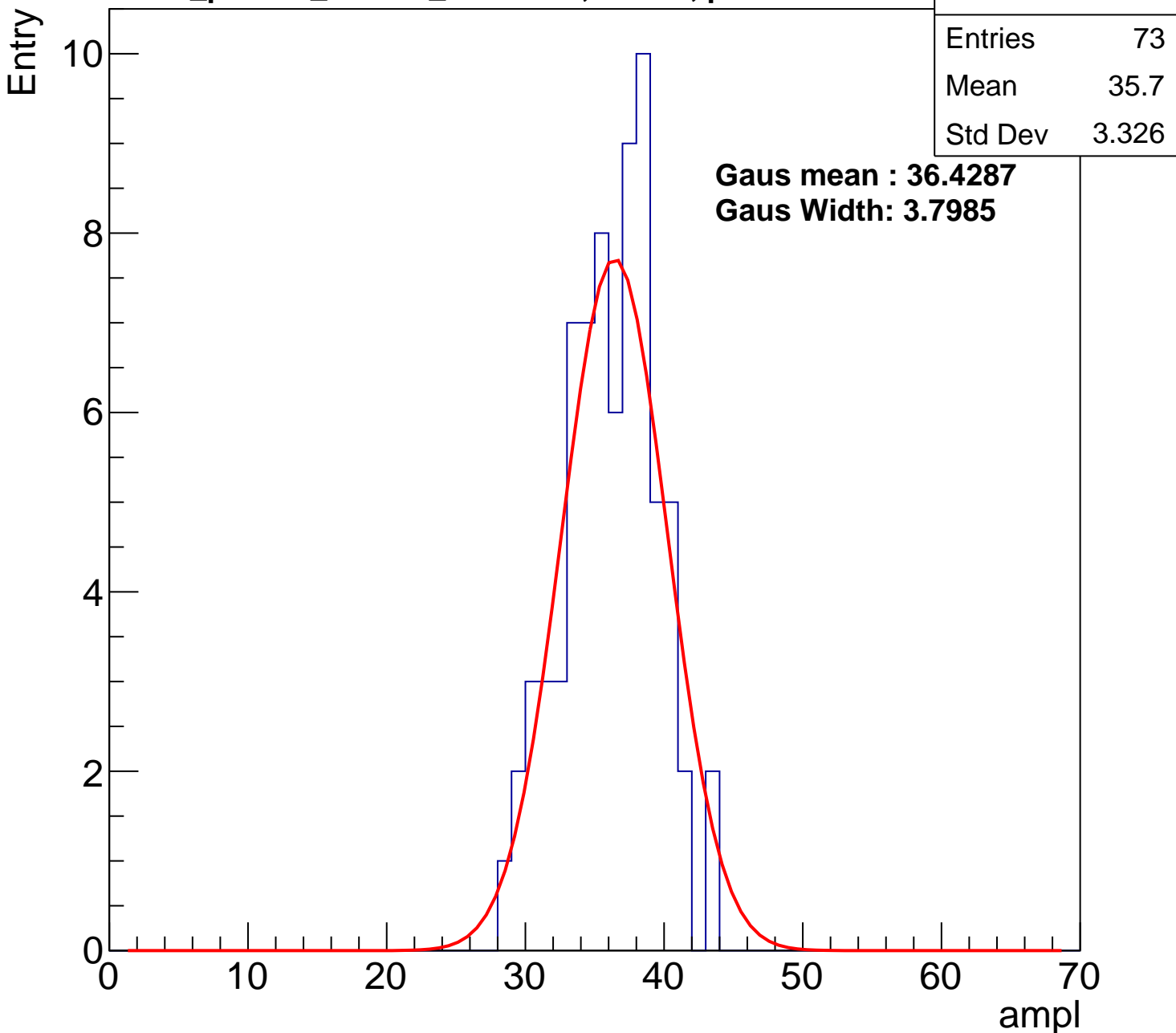
Gaus Width: 3.7985

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch45, adc2

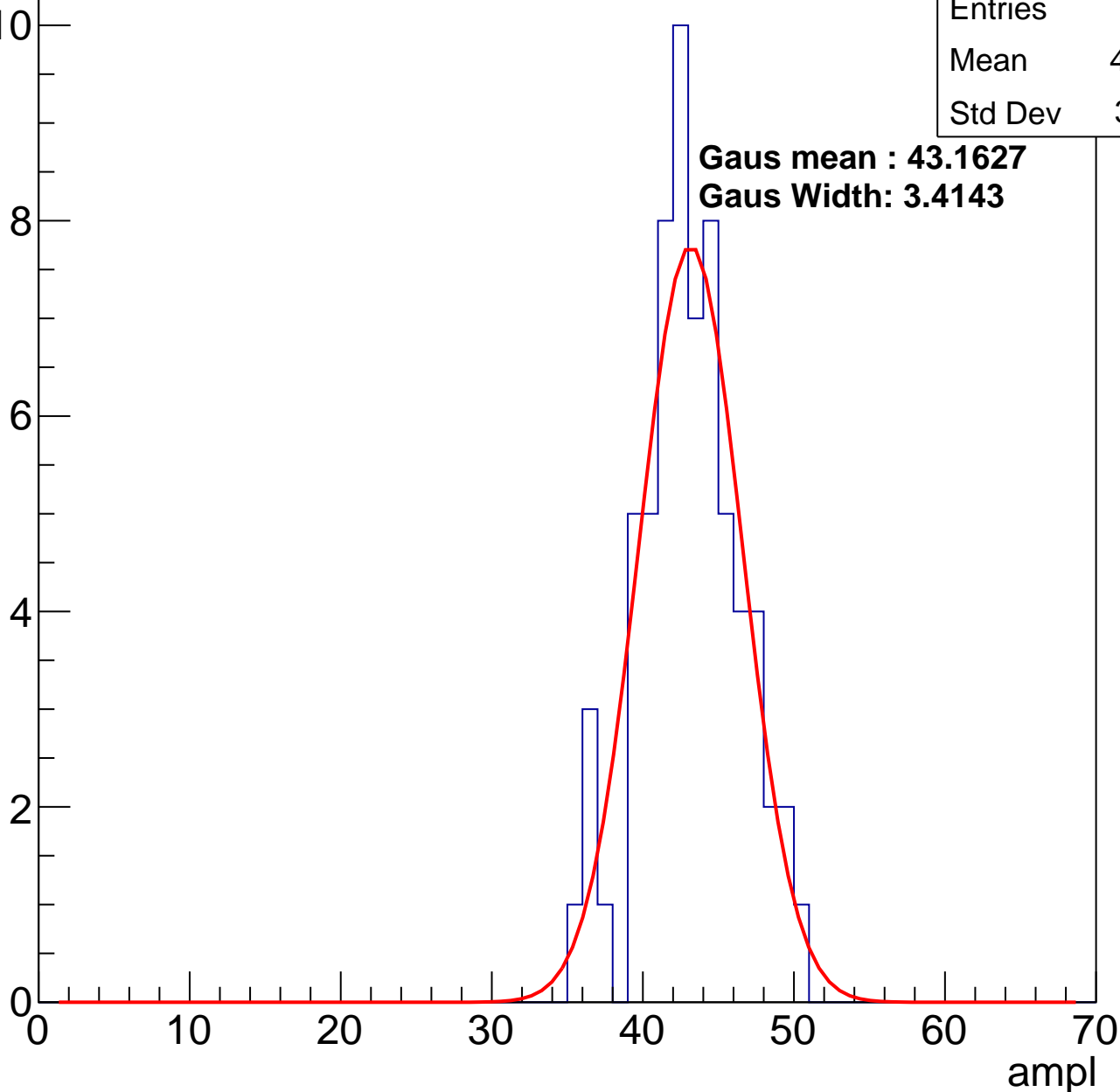
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.68
Std Dev	3.271

Gaus mean : 43.1627

Gaus Width: 3.4143

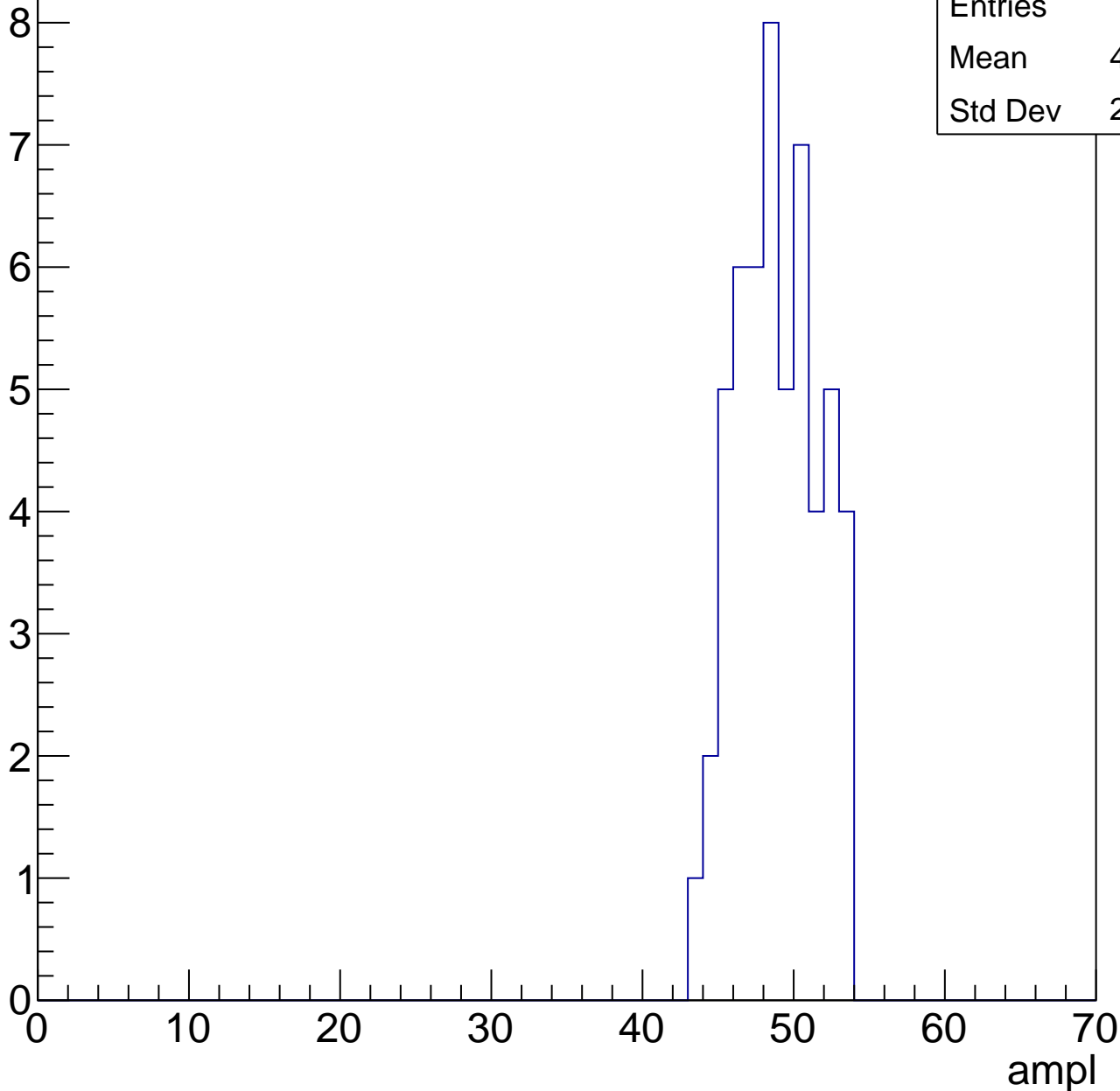


B1L003S, U21-ch45, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

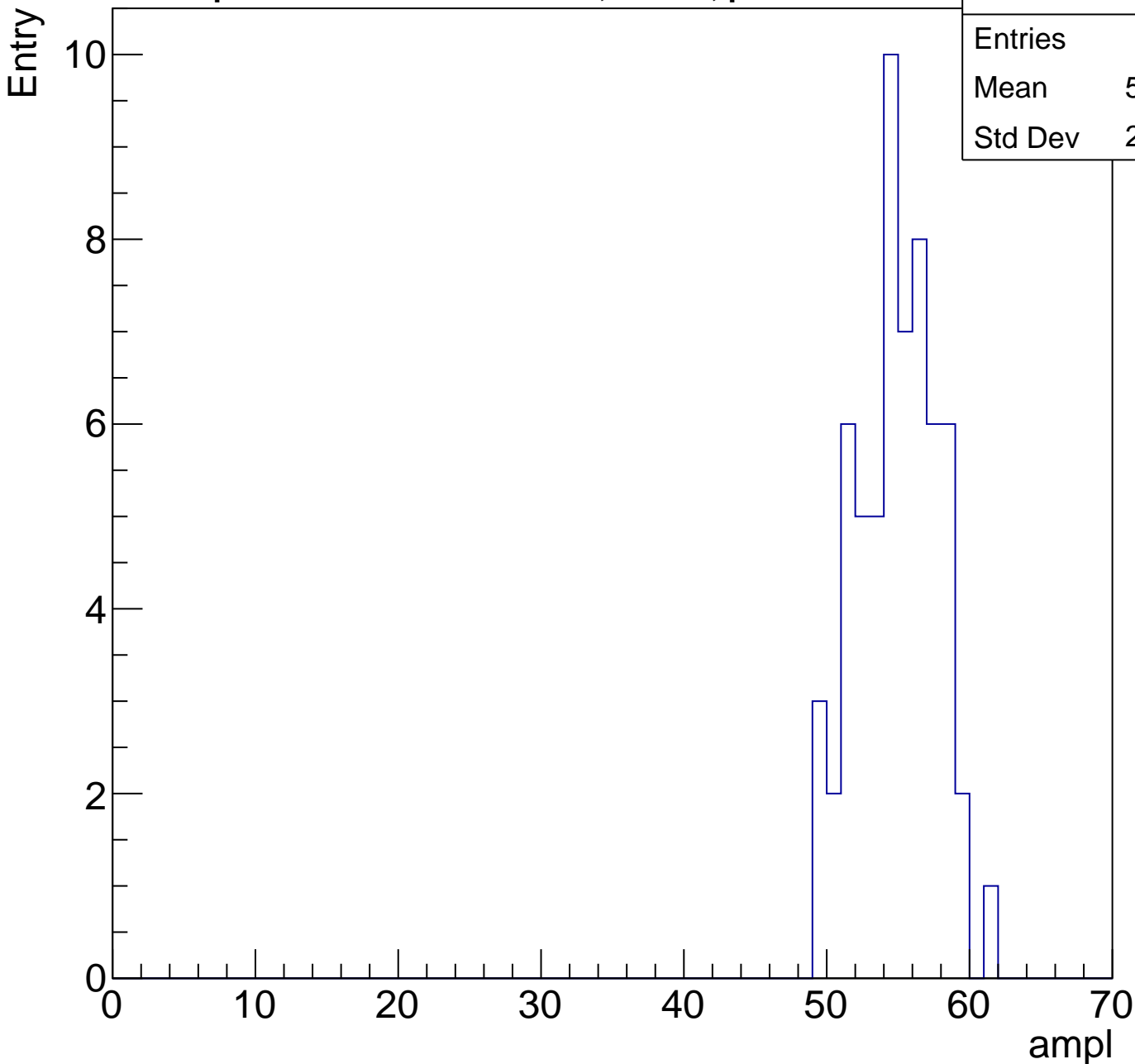
Entries	53
Mean	48.47
Std Dev	2.639



B1L003S, U21-ch45, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	54.43
Std Dev	2.755

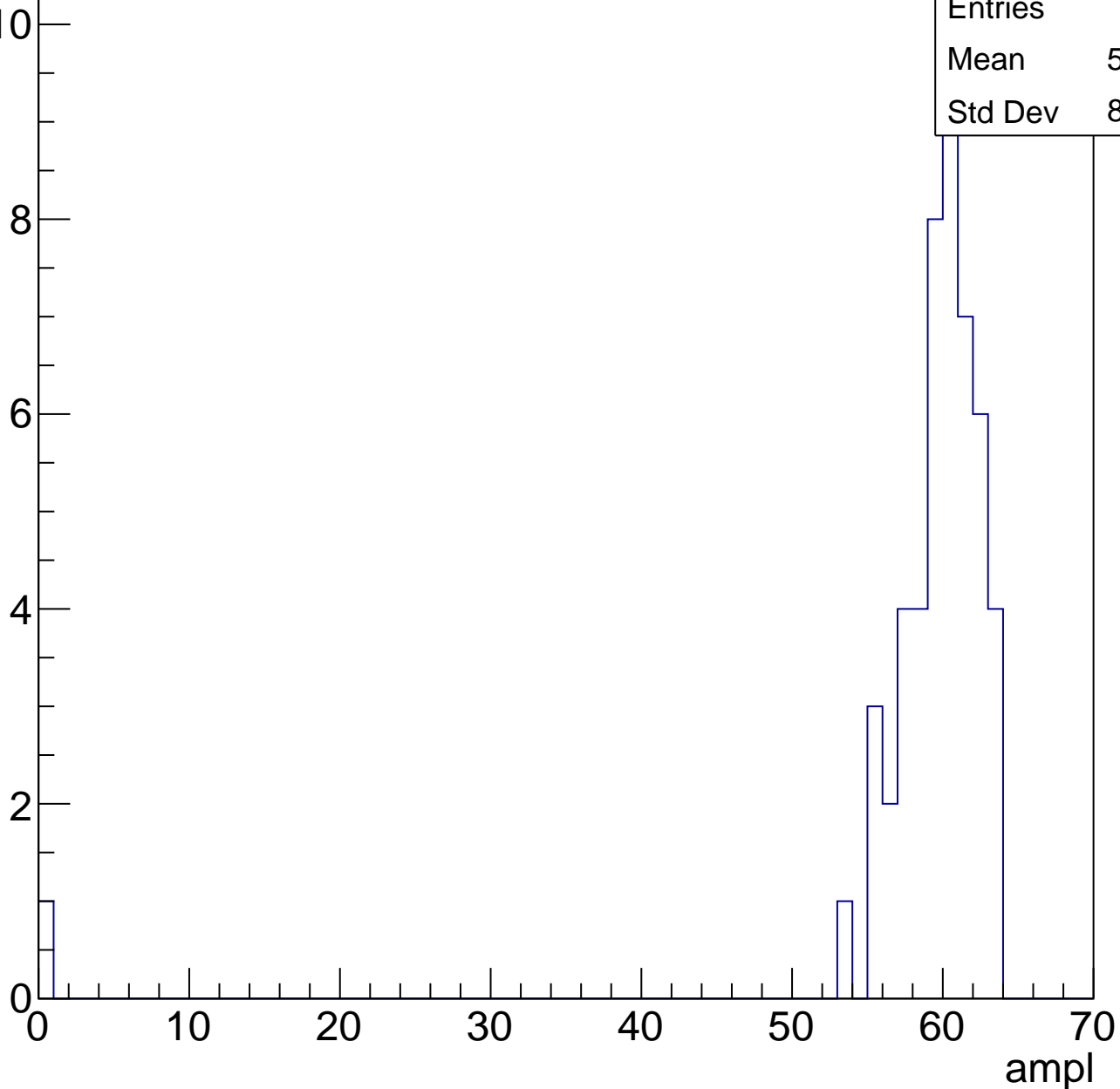


B1L003S, U21-ch45, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	58.26
Std Dev	8.639

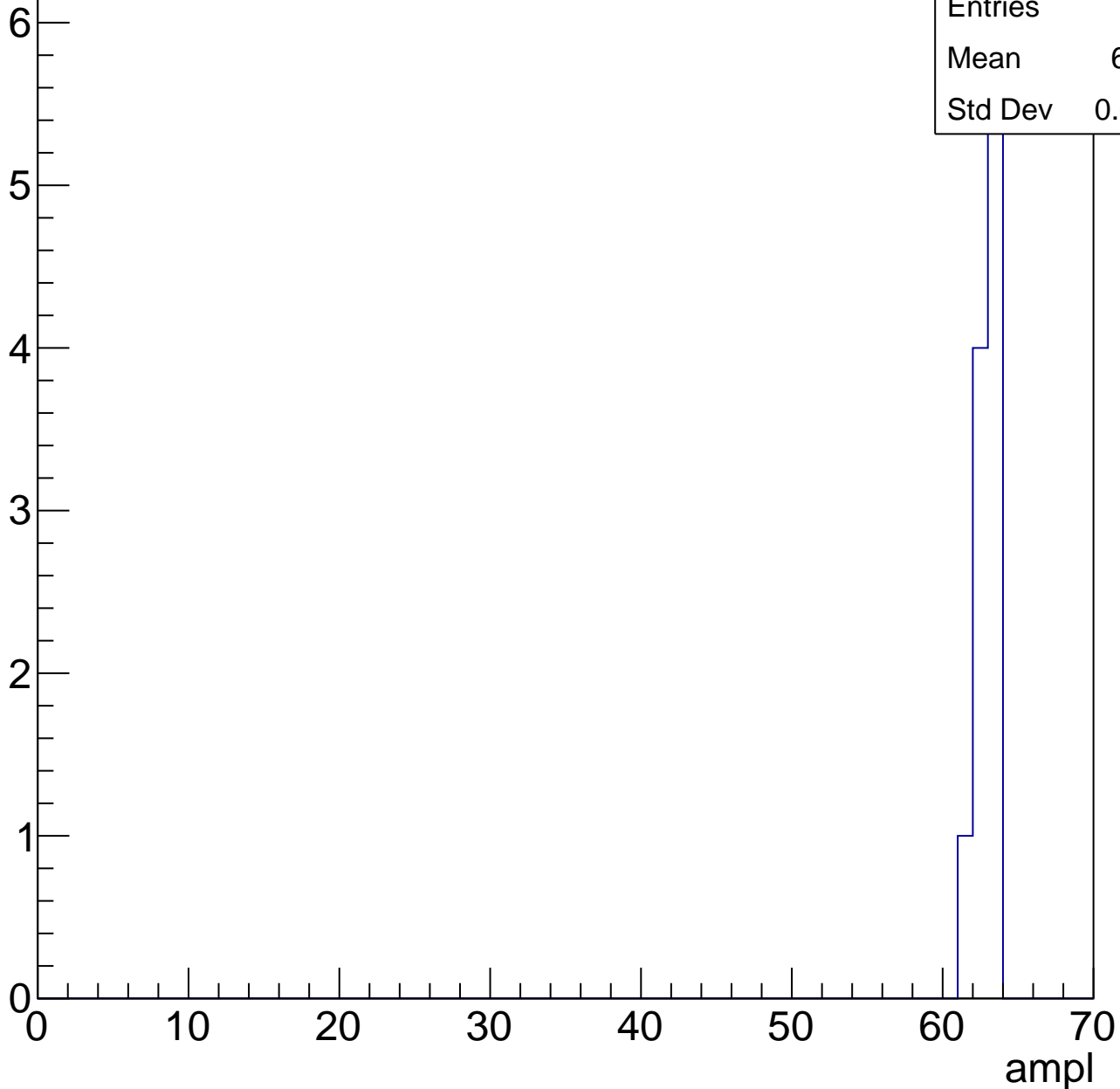


B1L003S, U21-ch45, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	62.45
Std Dev	0.6556



B1L003S, U21-ch45, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch46, adc0

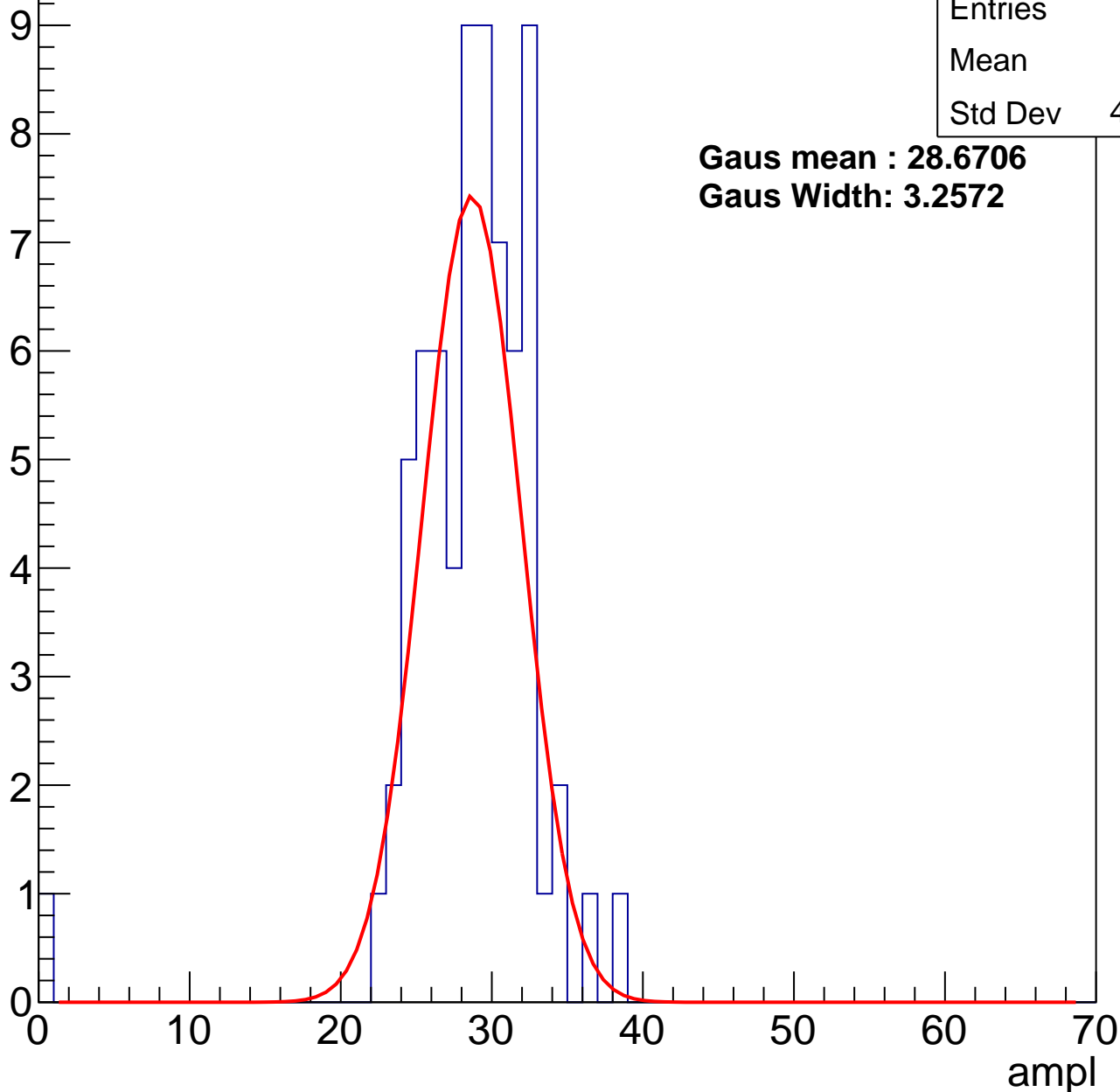
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.2
Std Dev	4.668

Gaus mean : 28.6706

Gaus Width: 3.2572



B1L003S, U21-ch46, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	35.62
Std Dev	3.297

Gaus mean : 36.3290

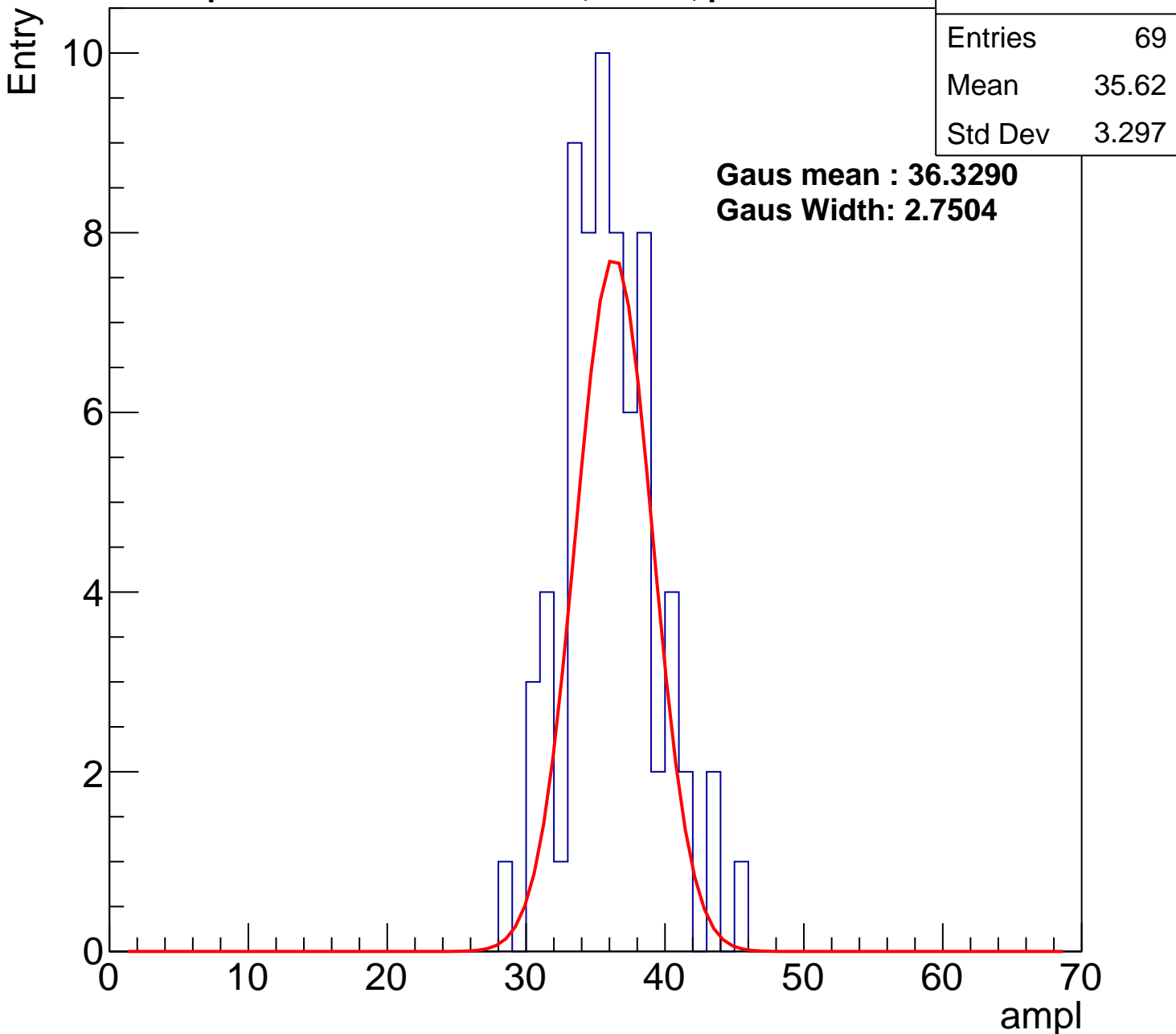
Gaus Width: 2.7504

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch46, adc2

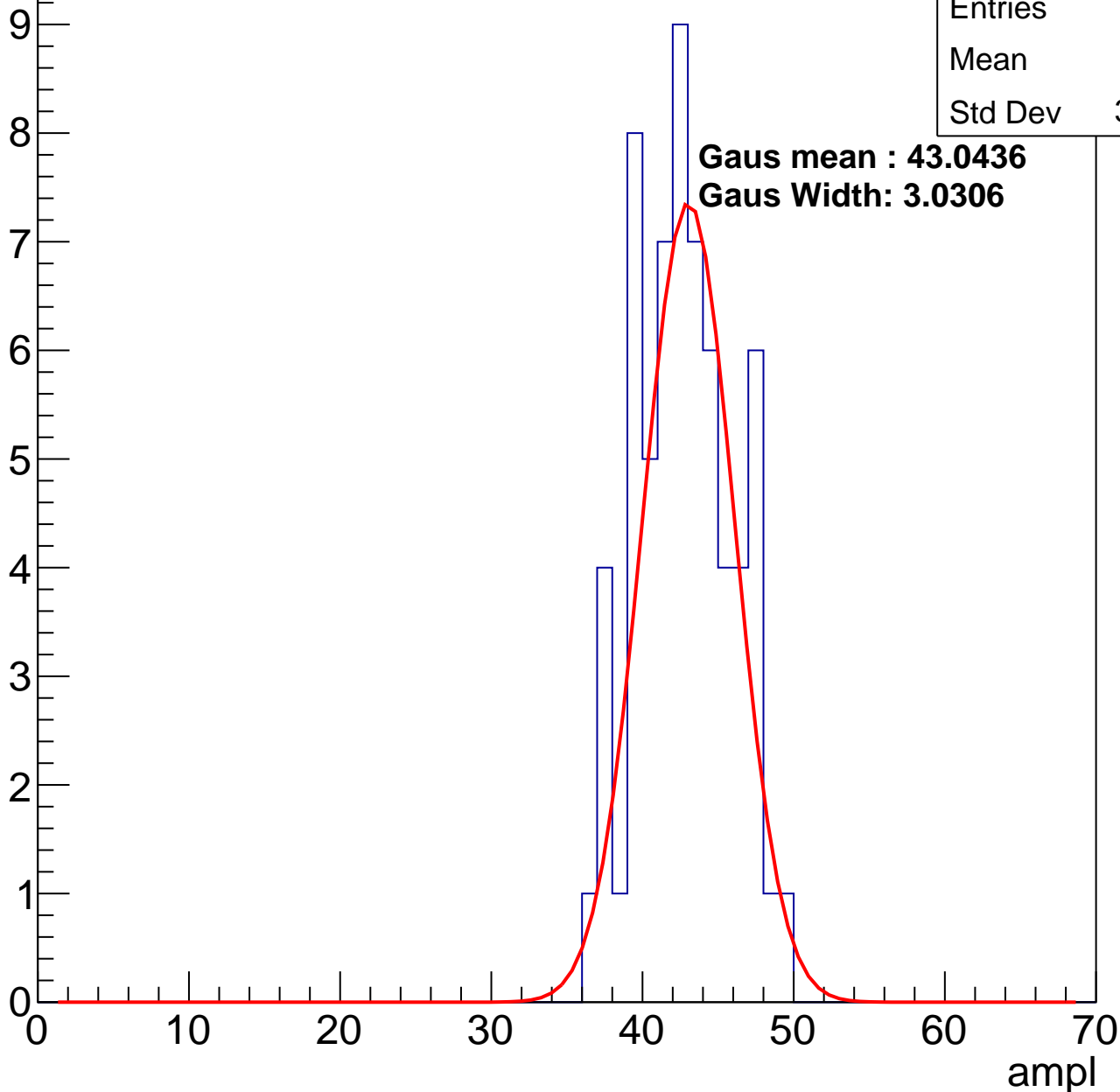
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	42.3
Std Dev	3.091

Gaus mean : 43.0436

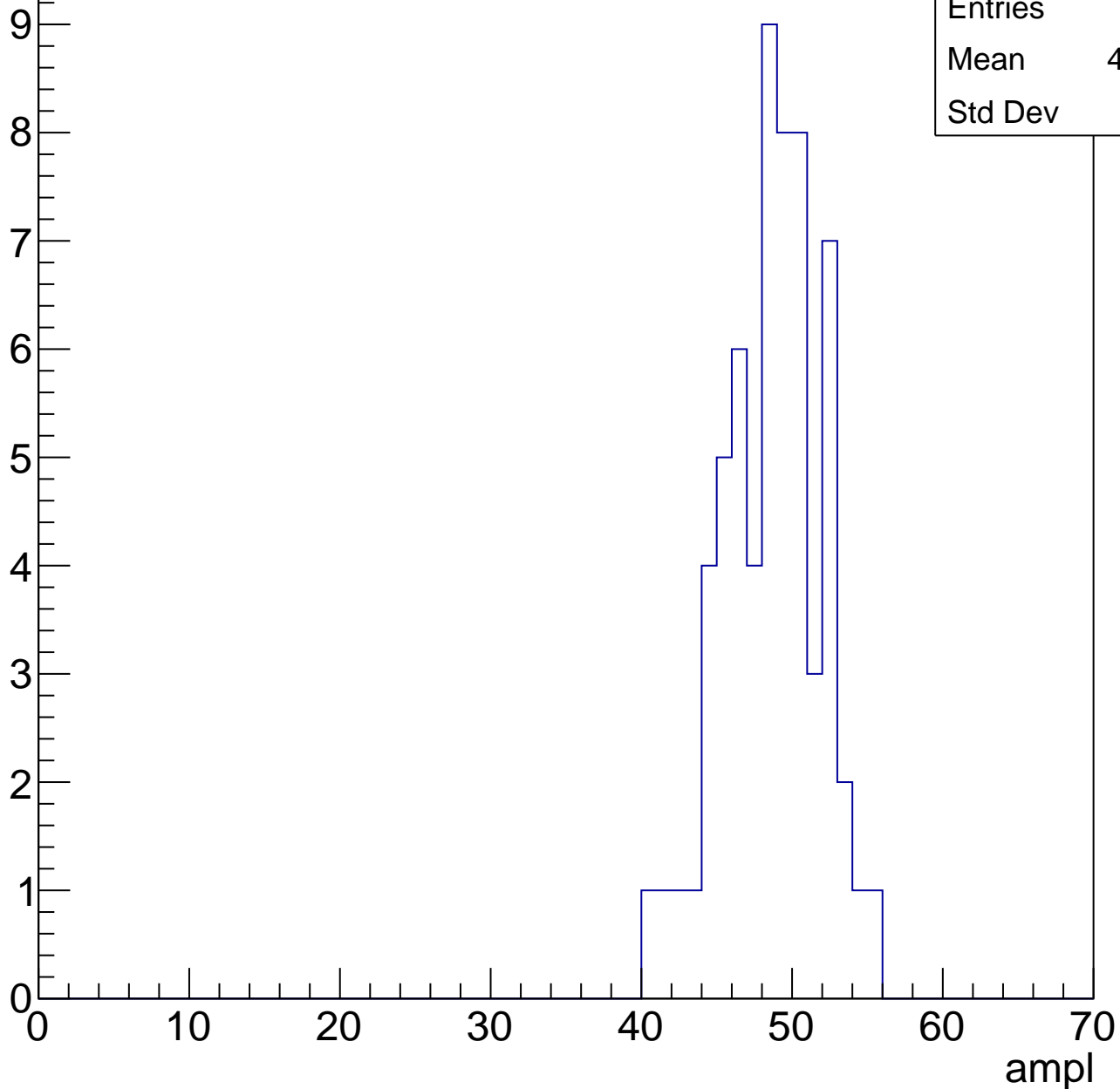
Gaus Width: 3.0306



B1L003S, U21-ch46, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



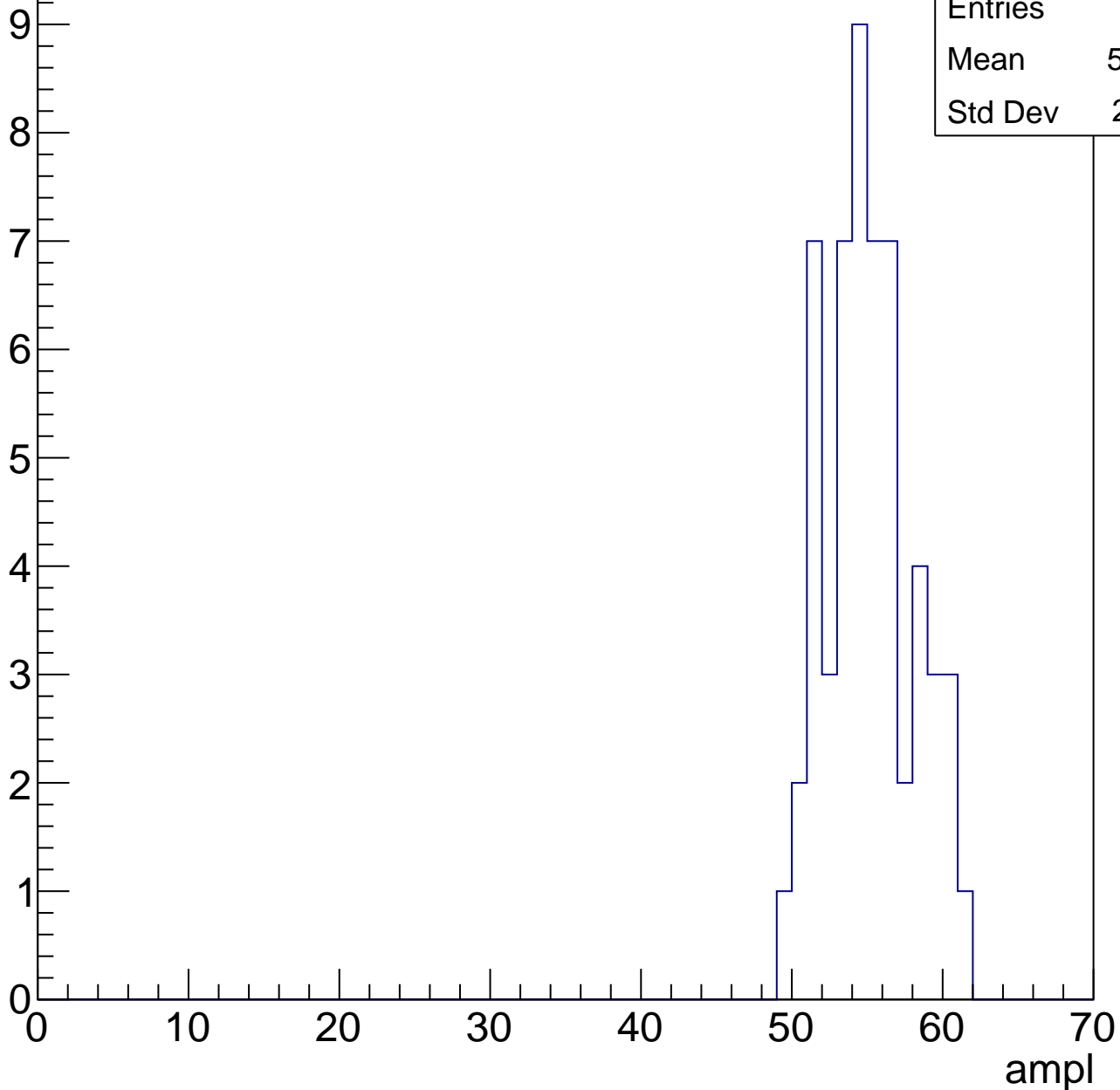
Entries	62
Mean	48.18
Std Dev	3.17

B1L003S, U21-ch46, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

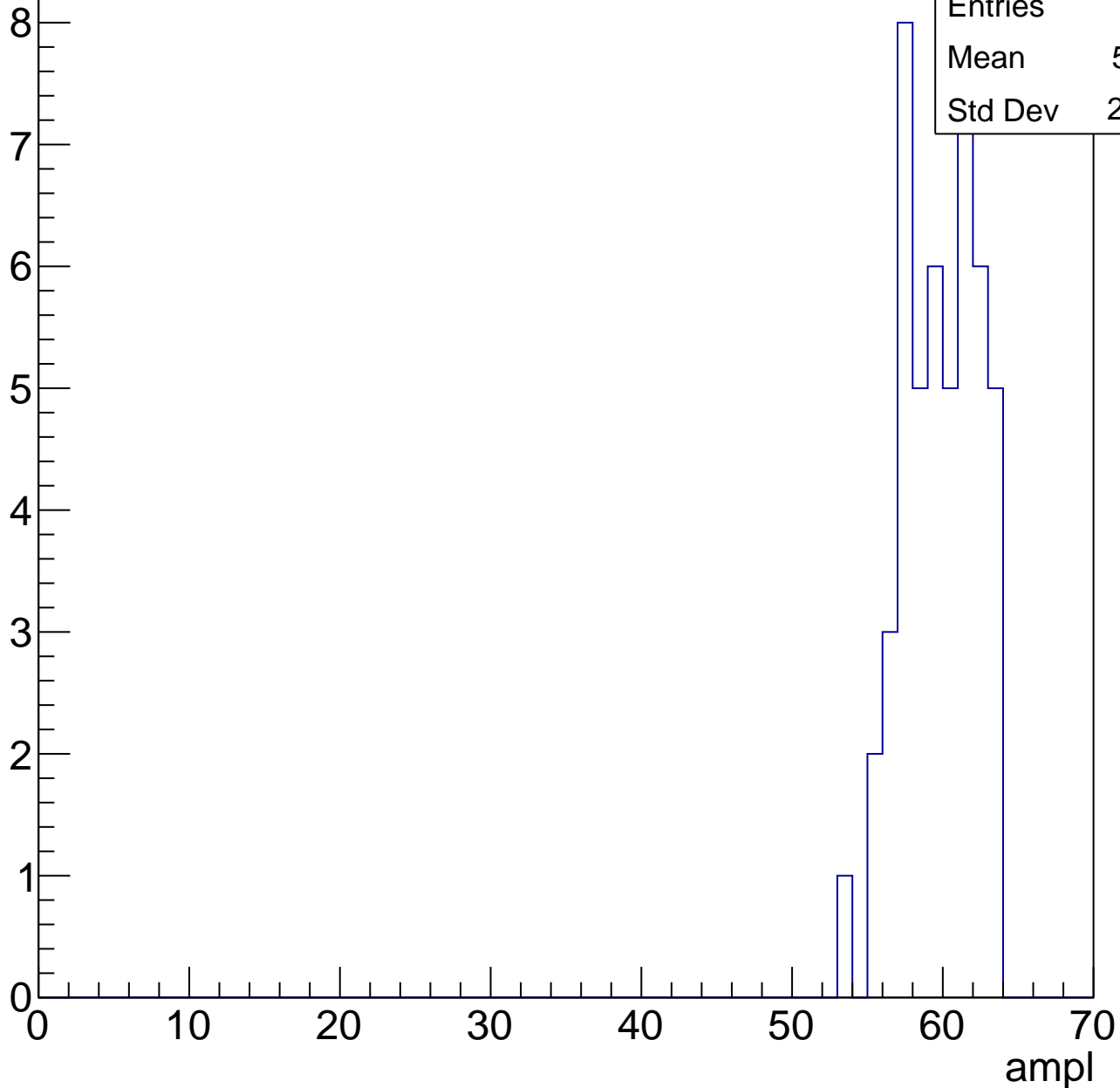
Entries	56
Mean	54.64
Std Dev	2.881



B1L003S, U21-ch46, adc5

calib_packv5_042523_0143.root, FC#13, port D2

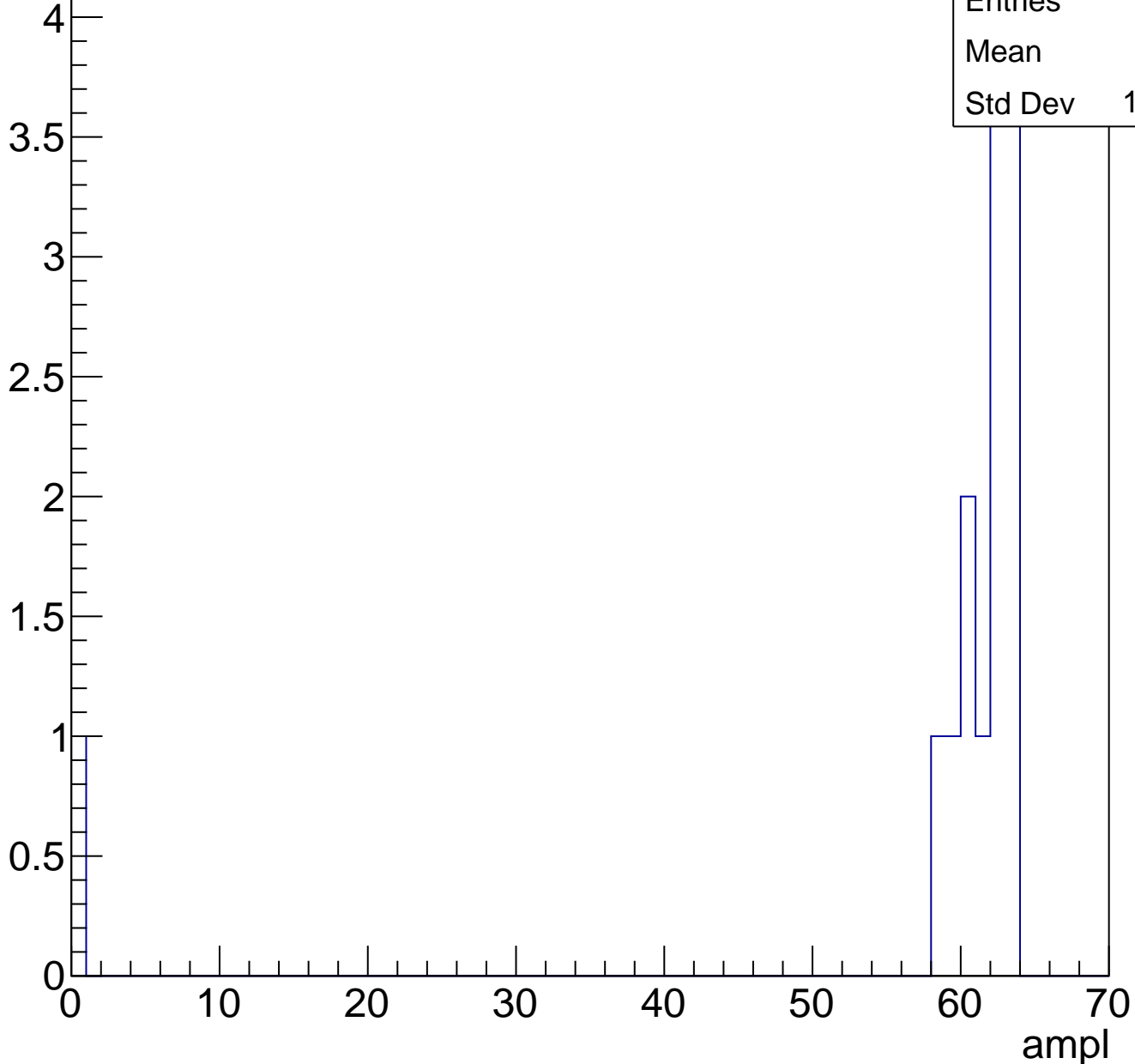
Entry



B1L003S, U21-ch46, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch46, adc7

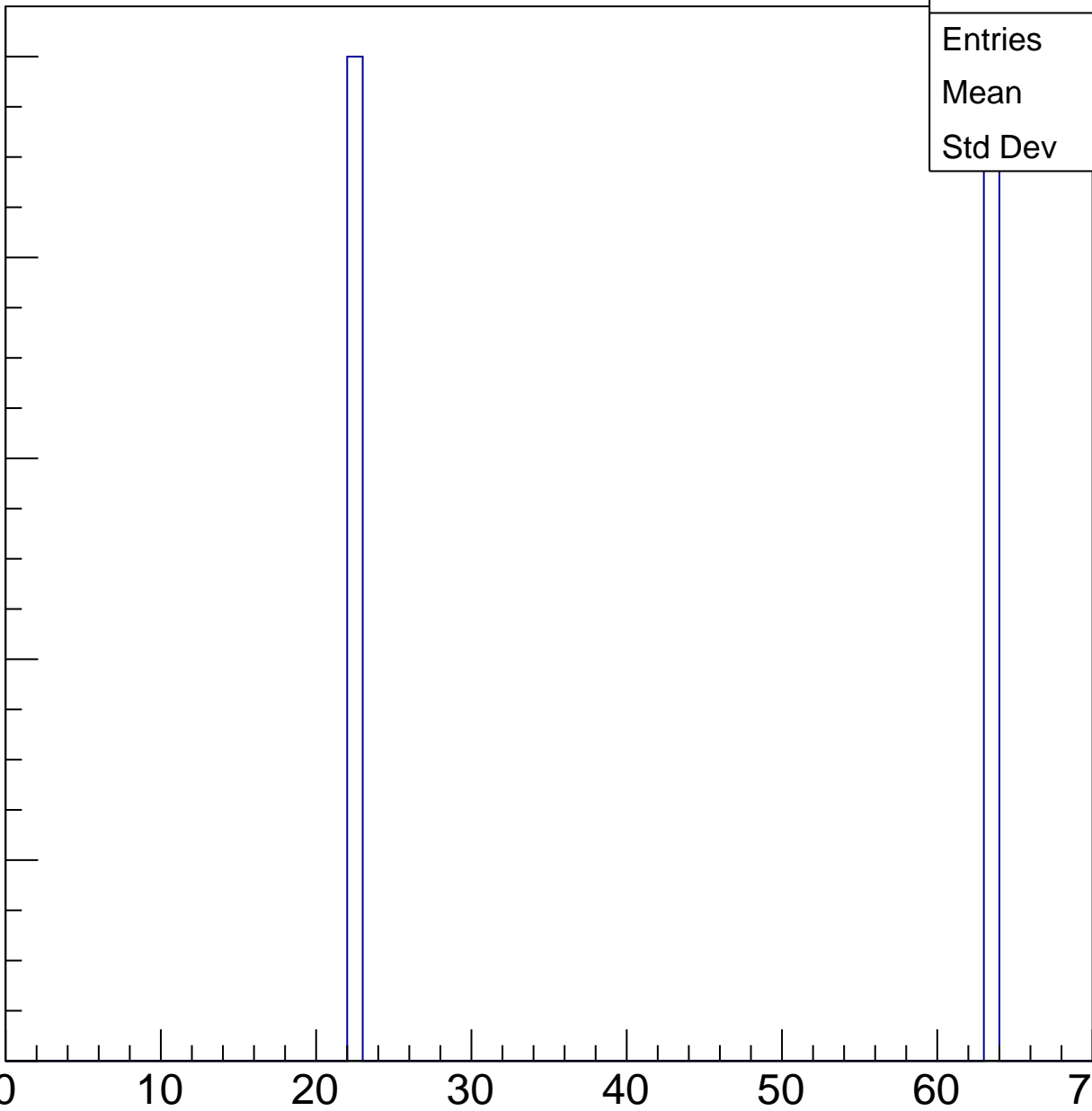
calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	42.5
Std Dev	20.5

ampl



B1L003S, U21-ch47, adc0

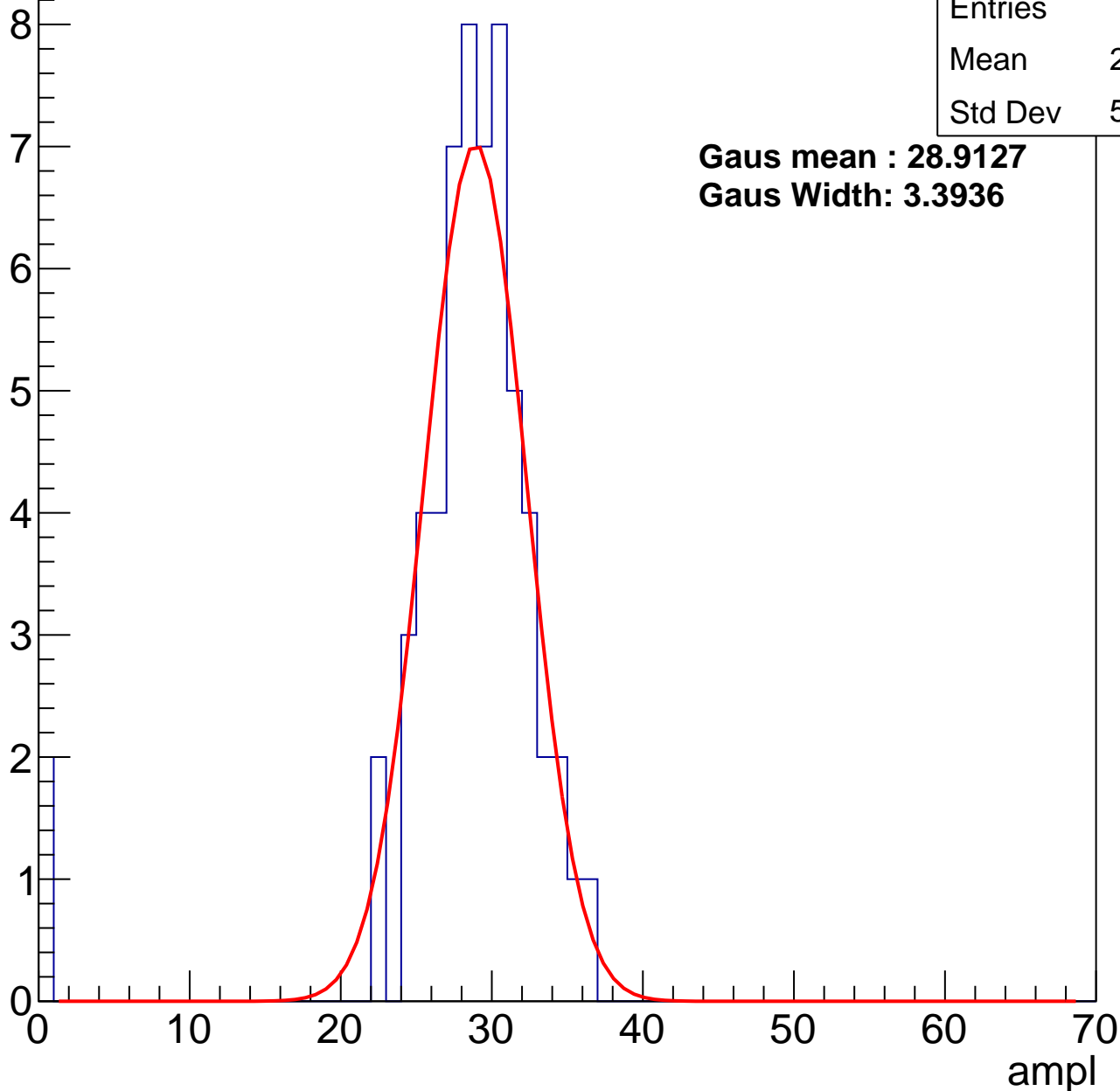
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	27.73
Std Dev	5.952

Gaus mean : 28.9127

Gaus Width: 3.3936



B1L003S, U21-ch47, adc1

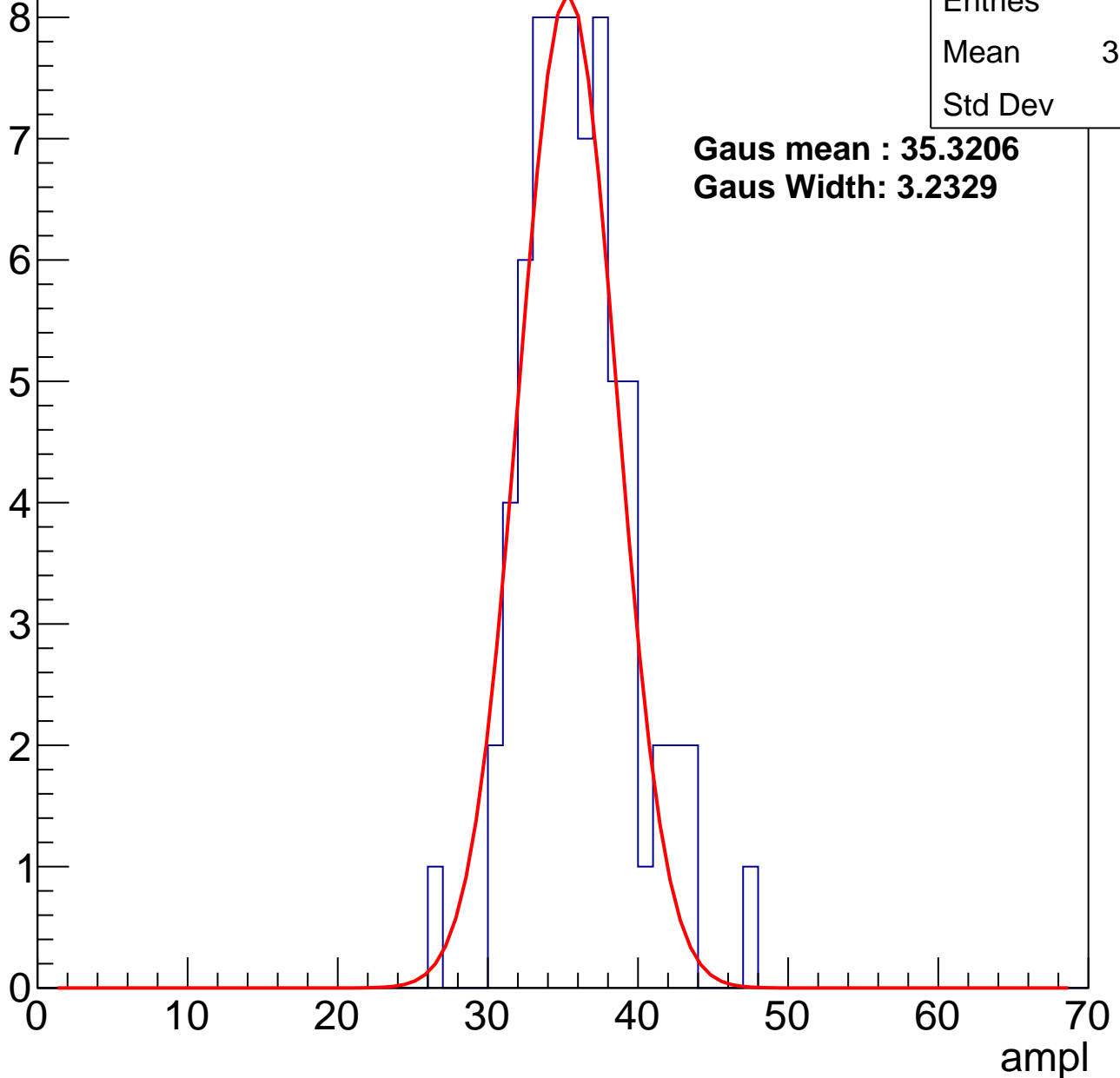
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	35.57
Std Dev	3.58

Gaus mean : 35.3206

Gaus Width: 3.2329



B1L003S, U21-ch47, adc2

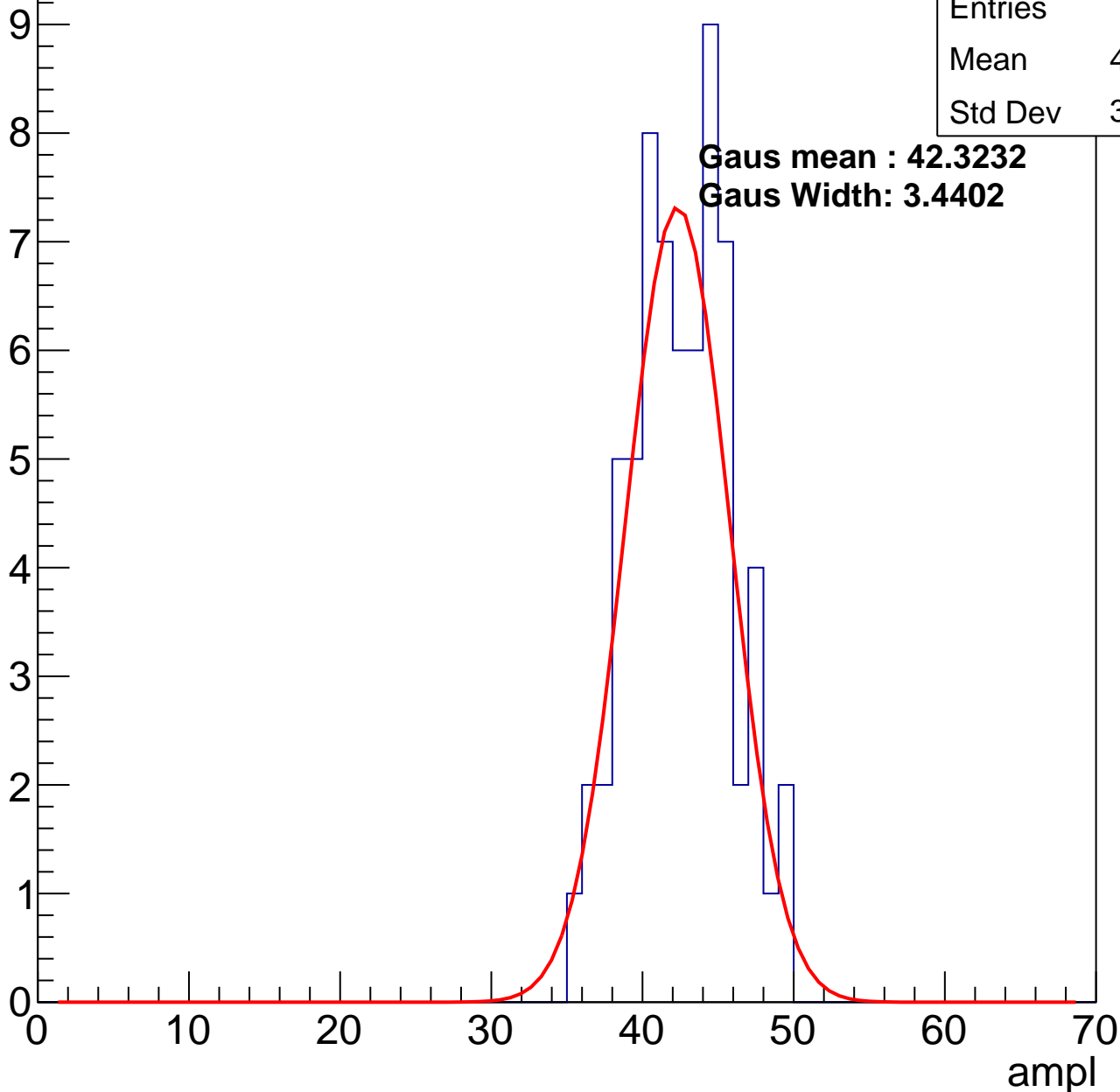
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.09
Std Dev	3.245

Gaus mean : 42.3232

Gaus Width: 3.4402

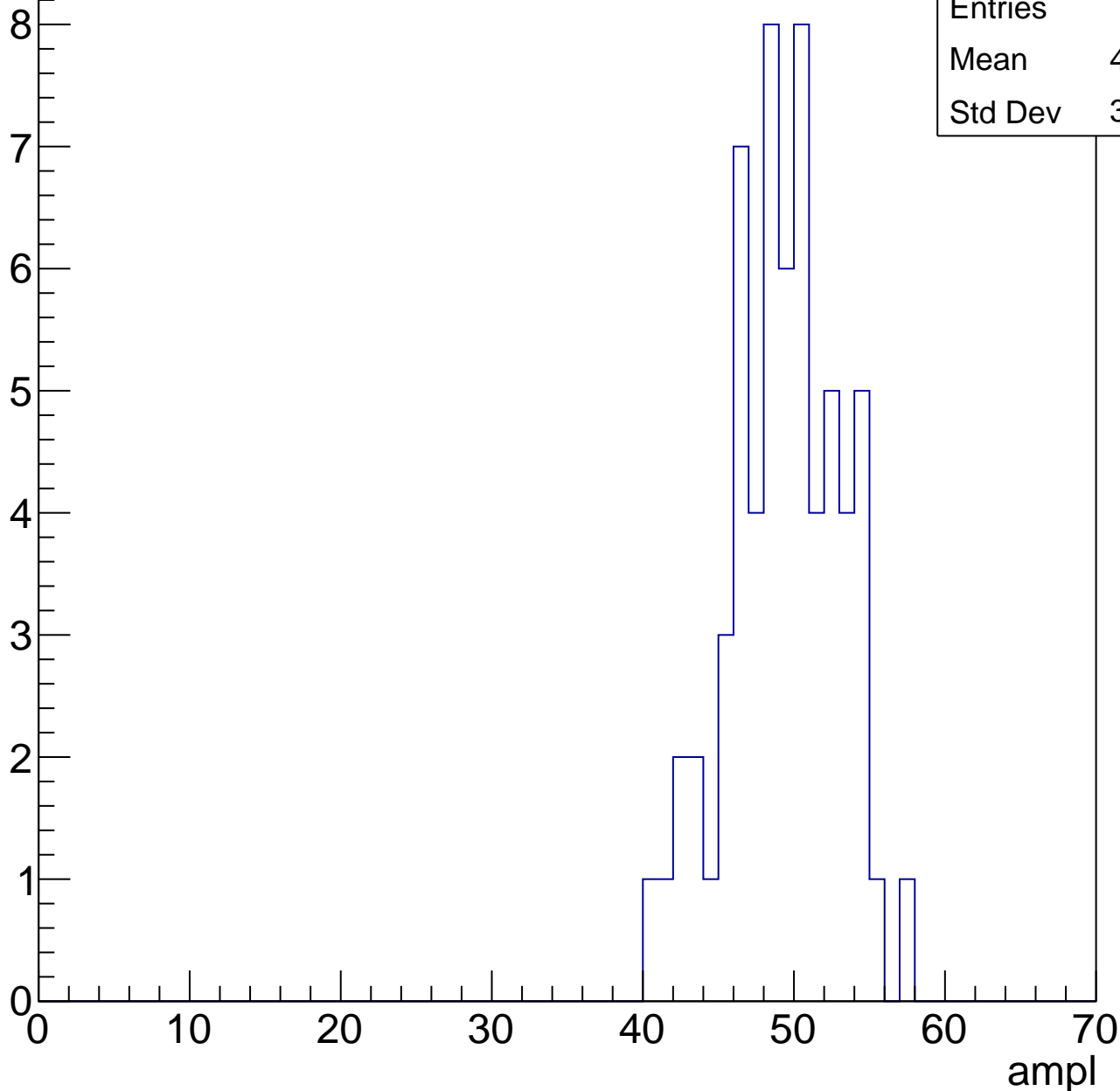


B1L003S, U21-ch47, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

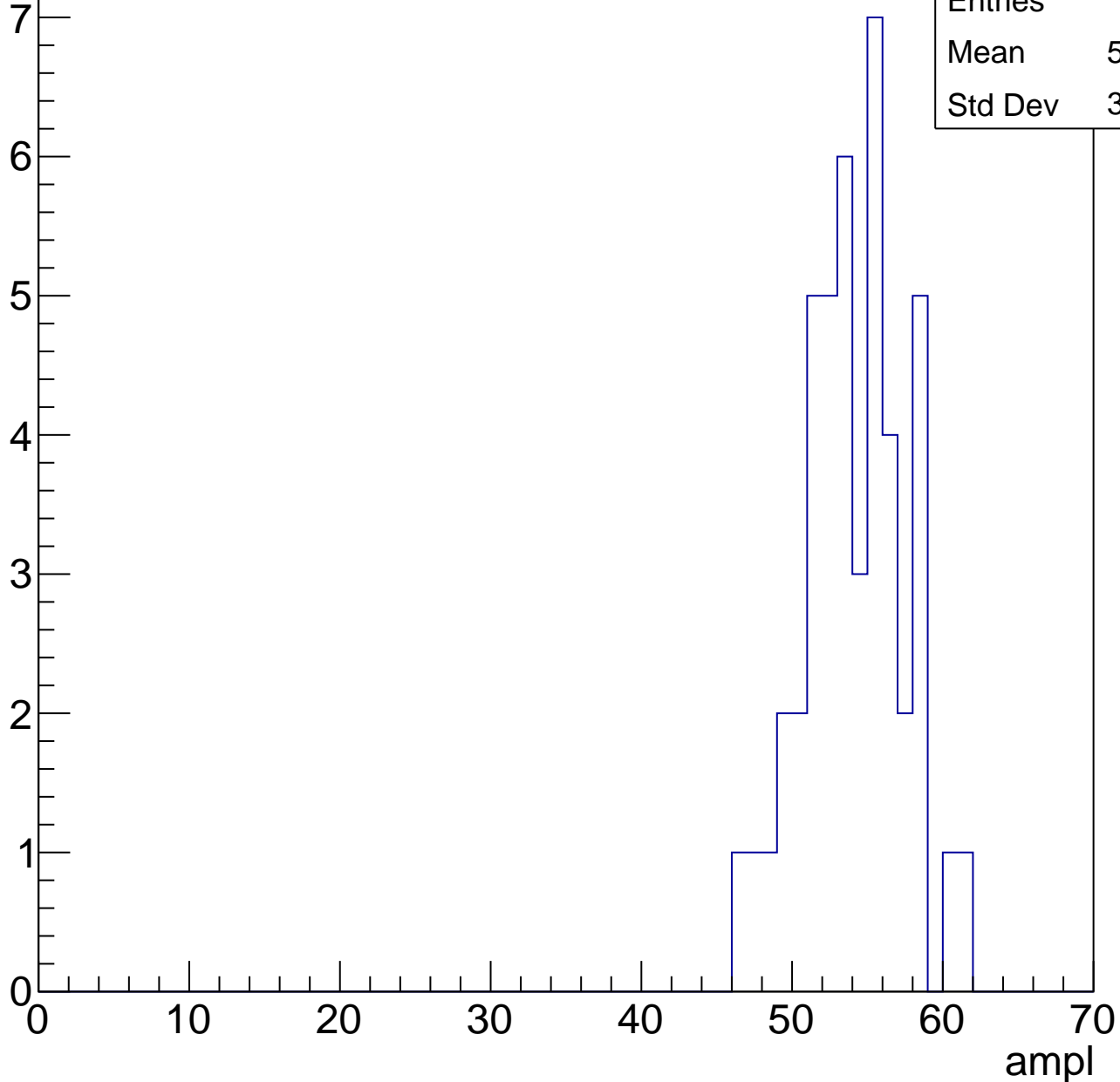
Entries	63
Mean	48.83
Std Dev	3.632



B1L003S, U21-ch47, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

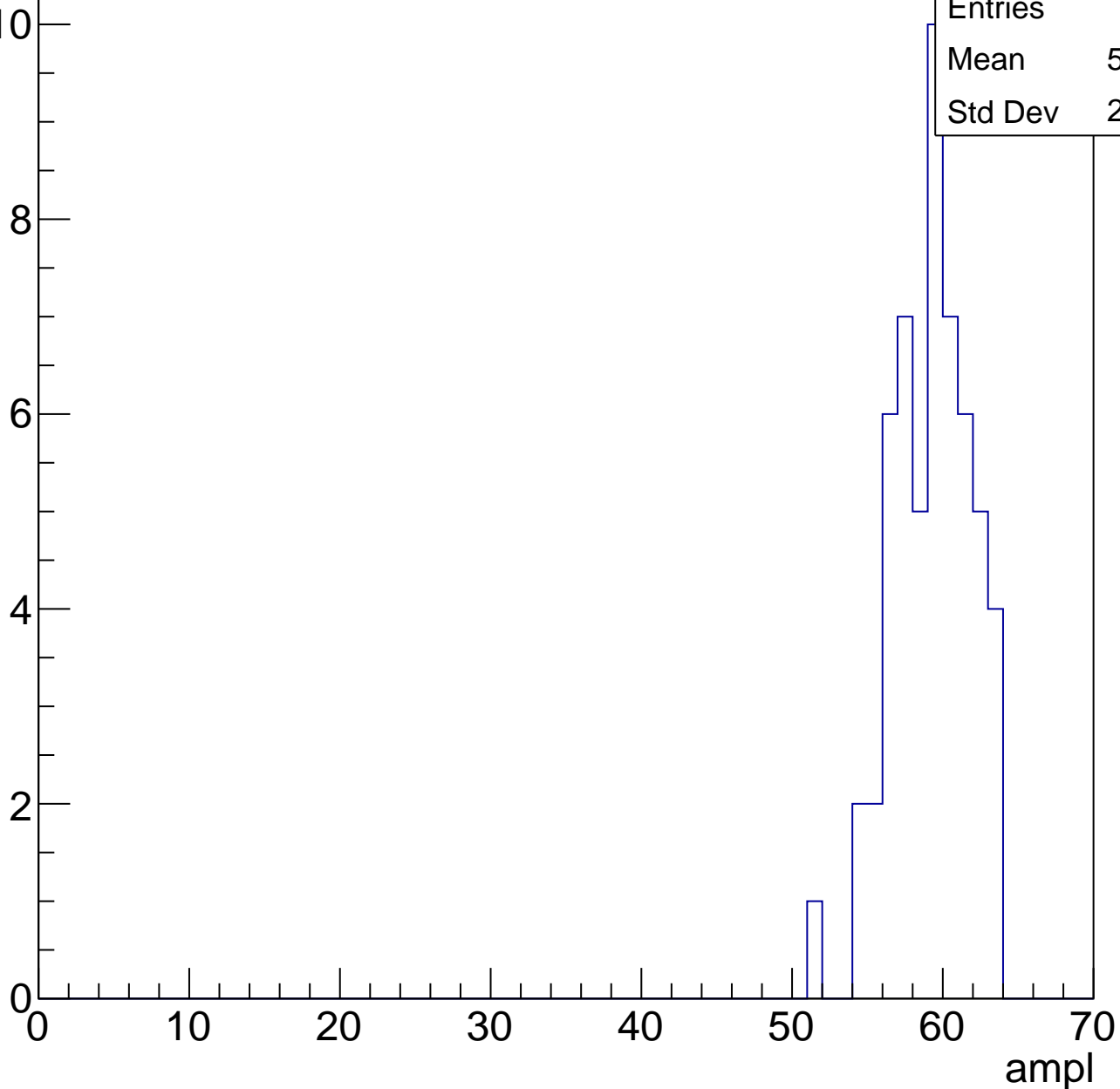


B1L003S, U21-ch47, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	58.76
Std Dev	2.593

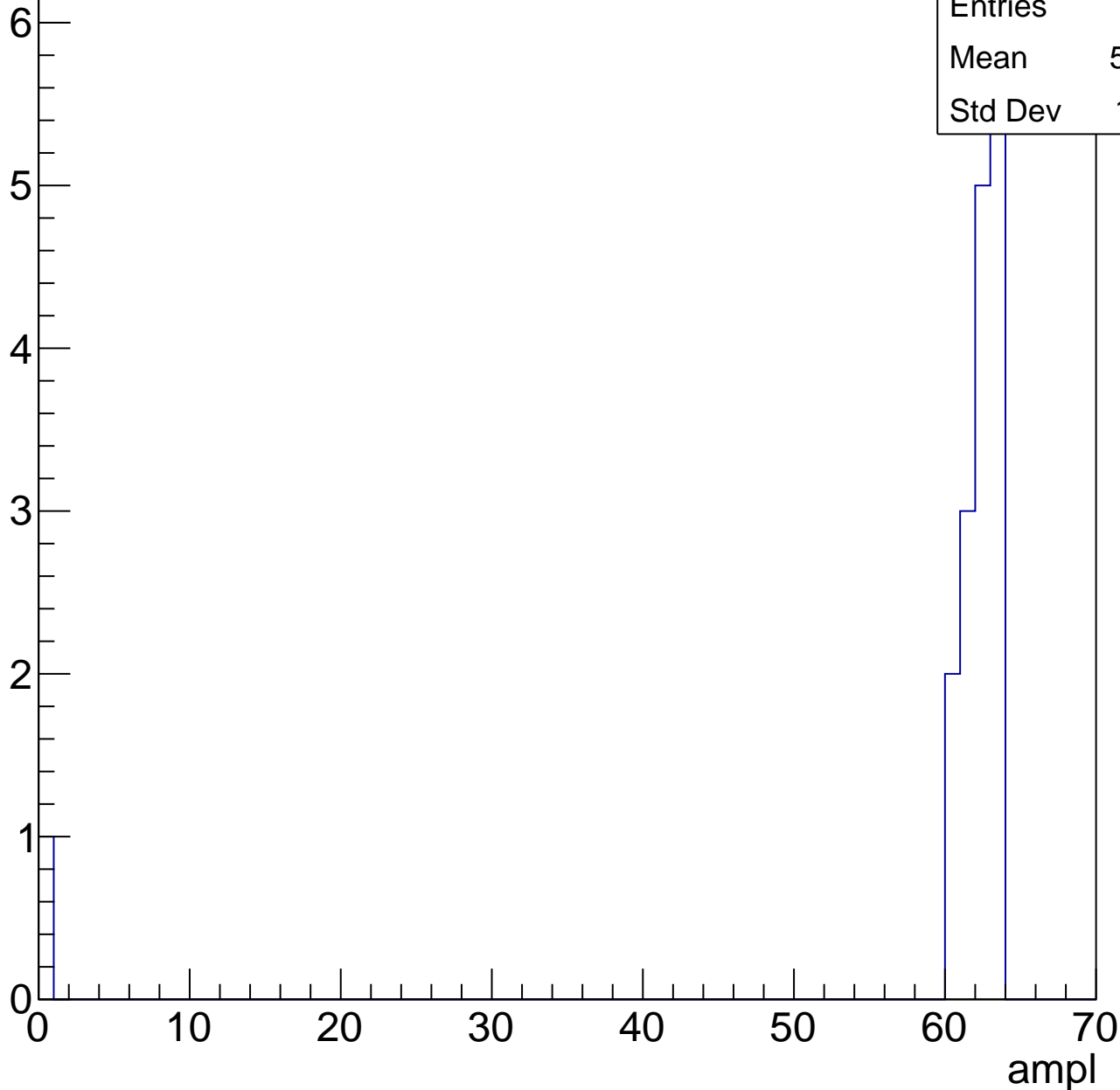


B1L003S, U21-ch47, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.29
Std Dev	14.61



B1L003S, U21-ch47, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch48, adc0

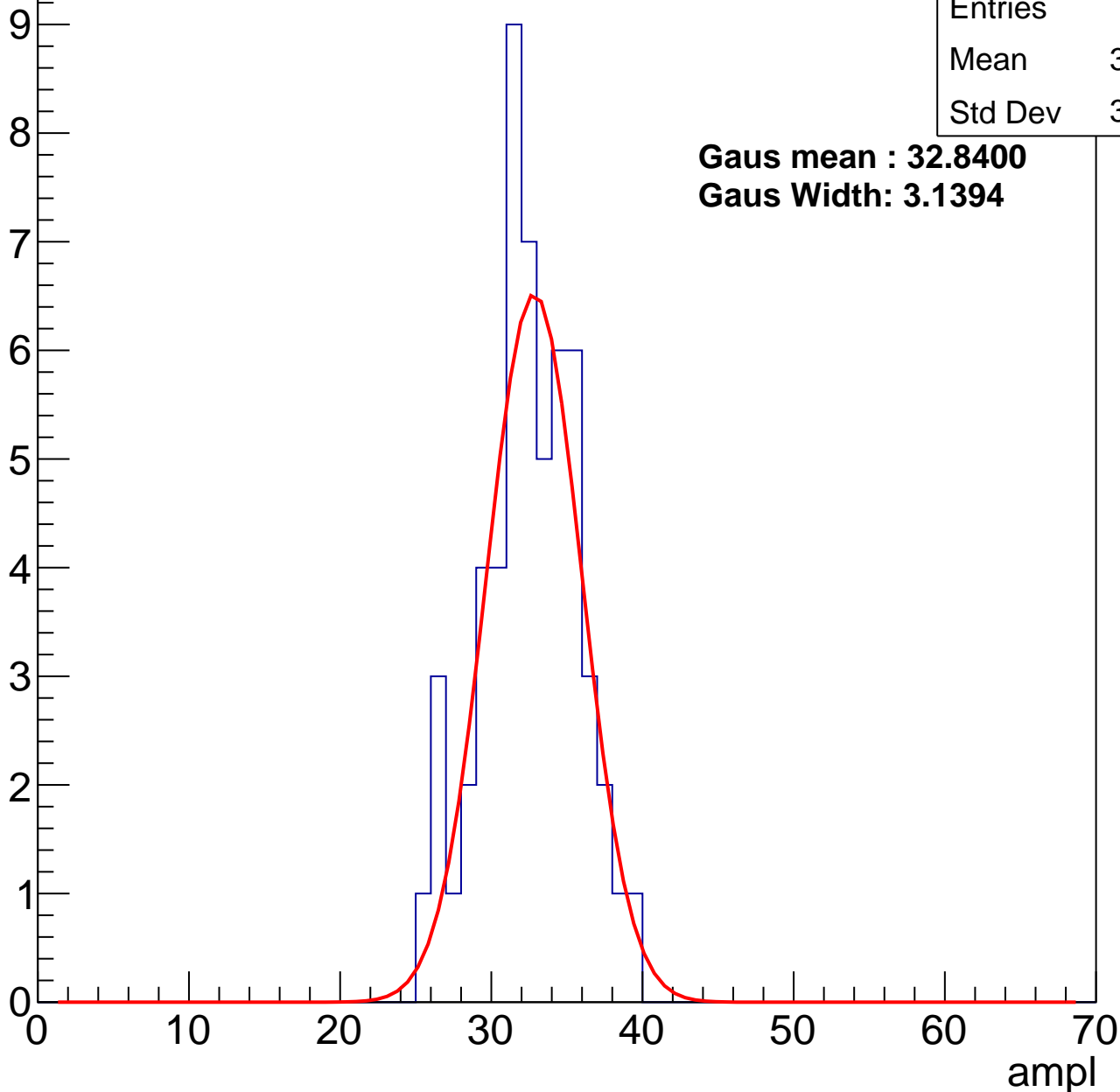
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	32.05
Std Dev	3.136

Gaus mean : 32.8400

Gaus Width: 3.1394



B1L003S, U21-ch48, adc1

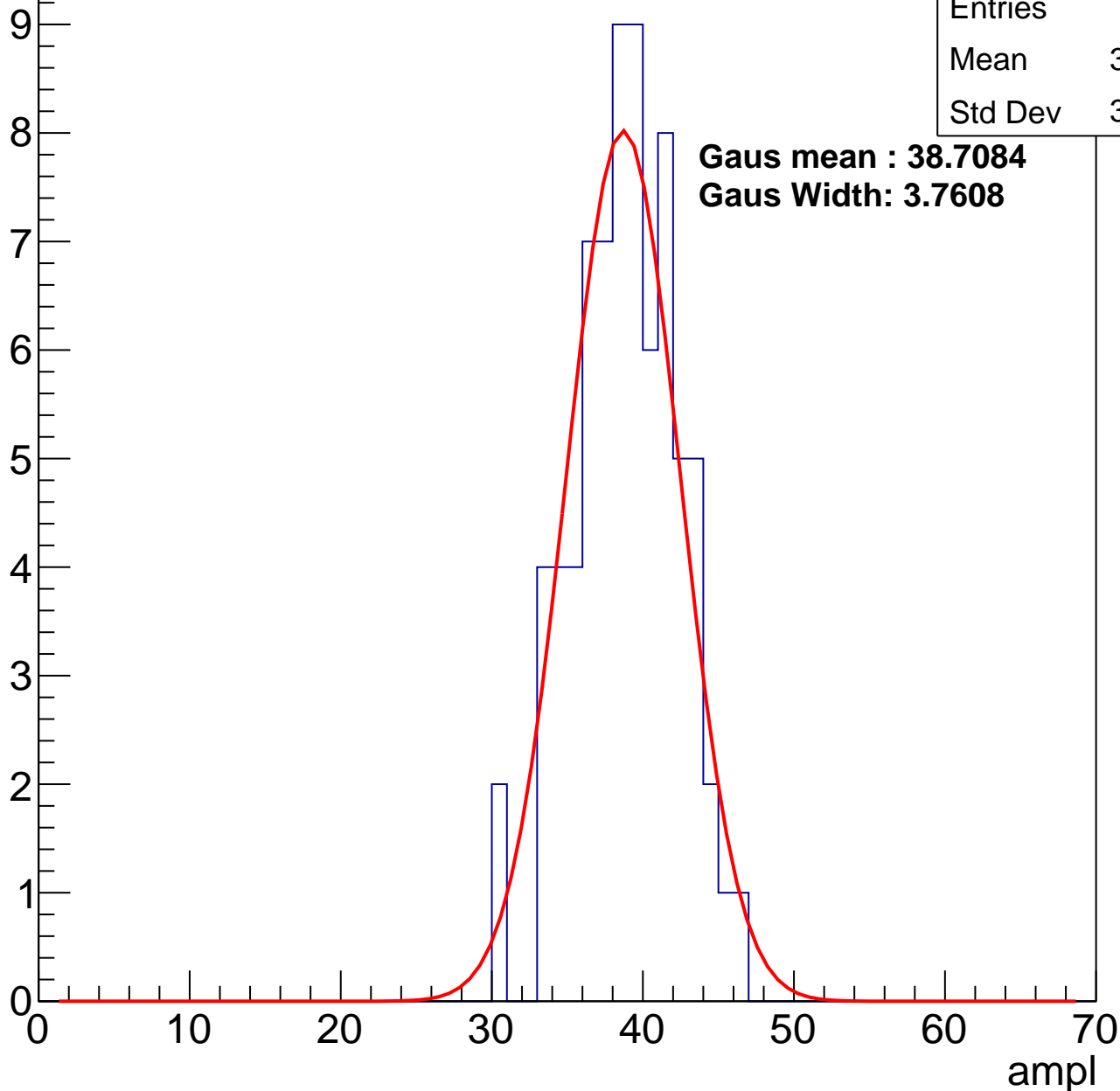
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	38.43
Std Dev	3.378

Gaus mean : 38.7084

Gaus Width: 3.7608



B1L003S, U21-ch48, adc2

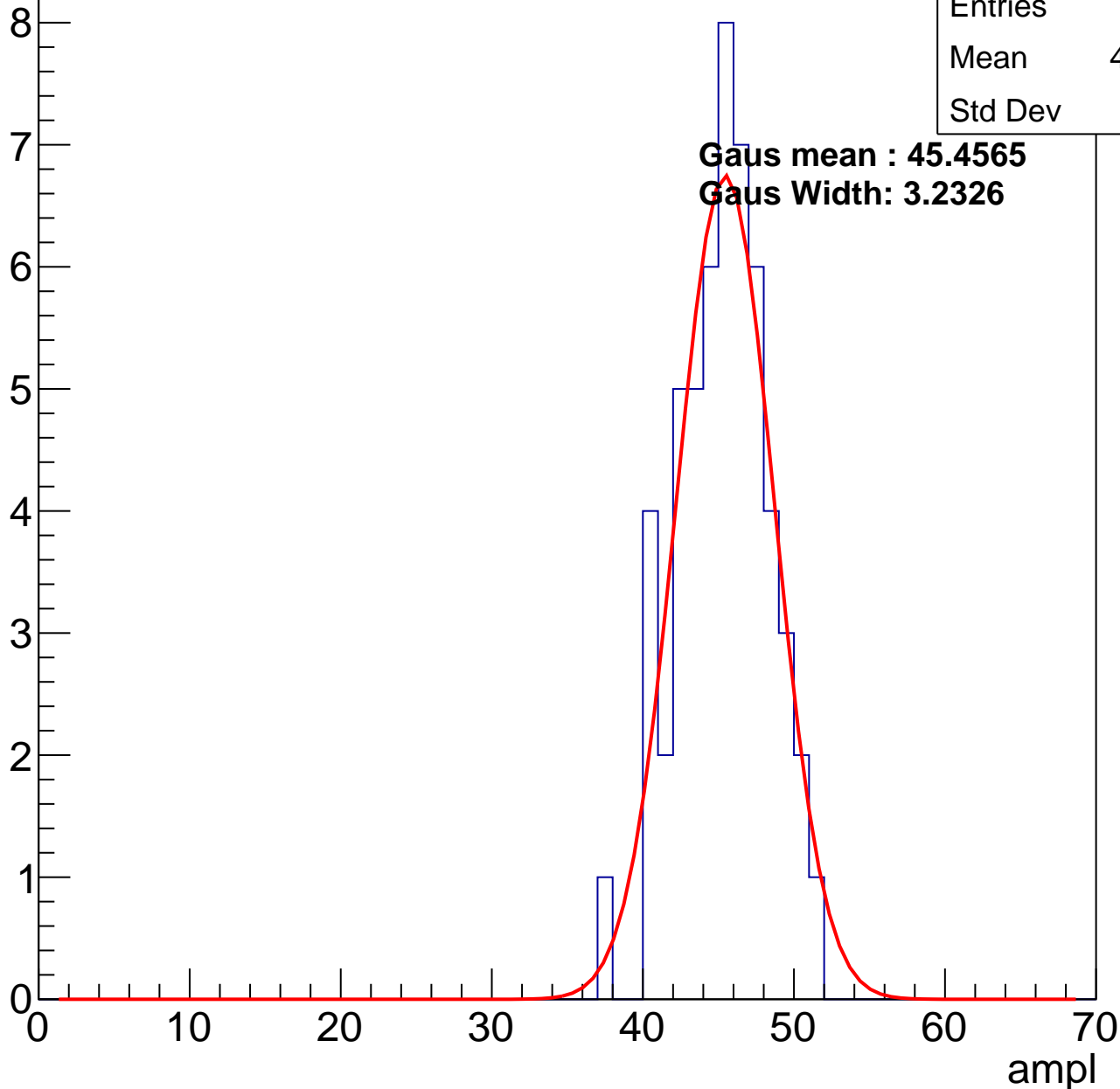
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	44.85
Std Dev	2.94

Gaus mean : 45.4565

Gaus Width: 3.2326

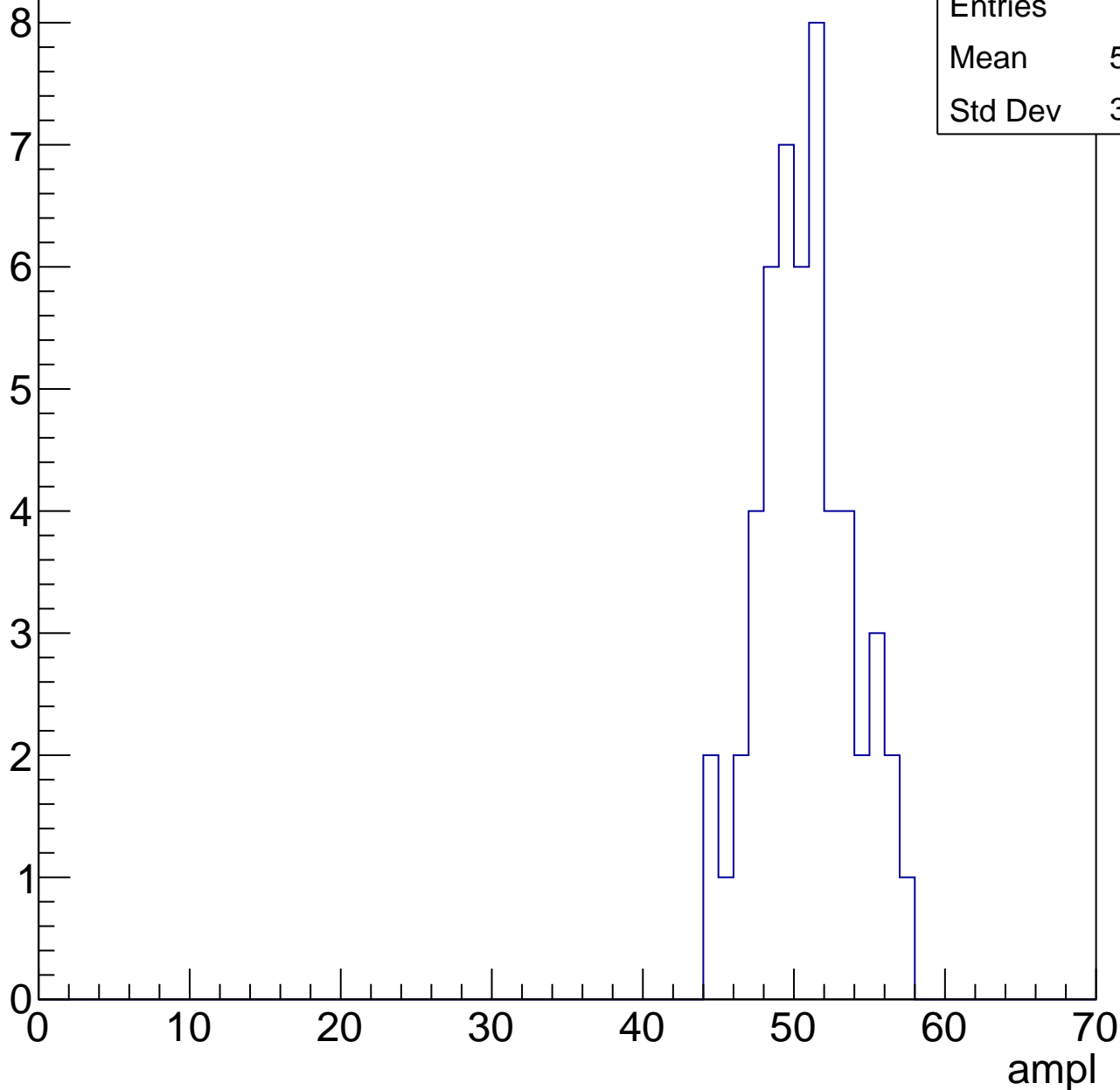


B1L003S, U21-ch48, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	50.27
Std Dev	3.039

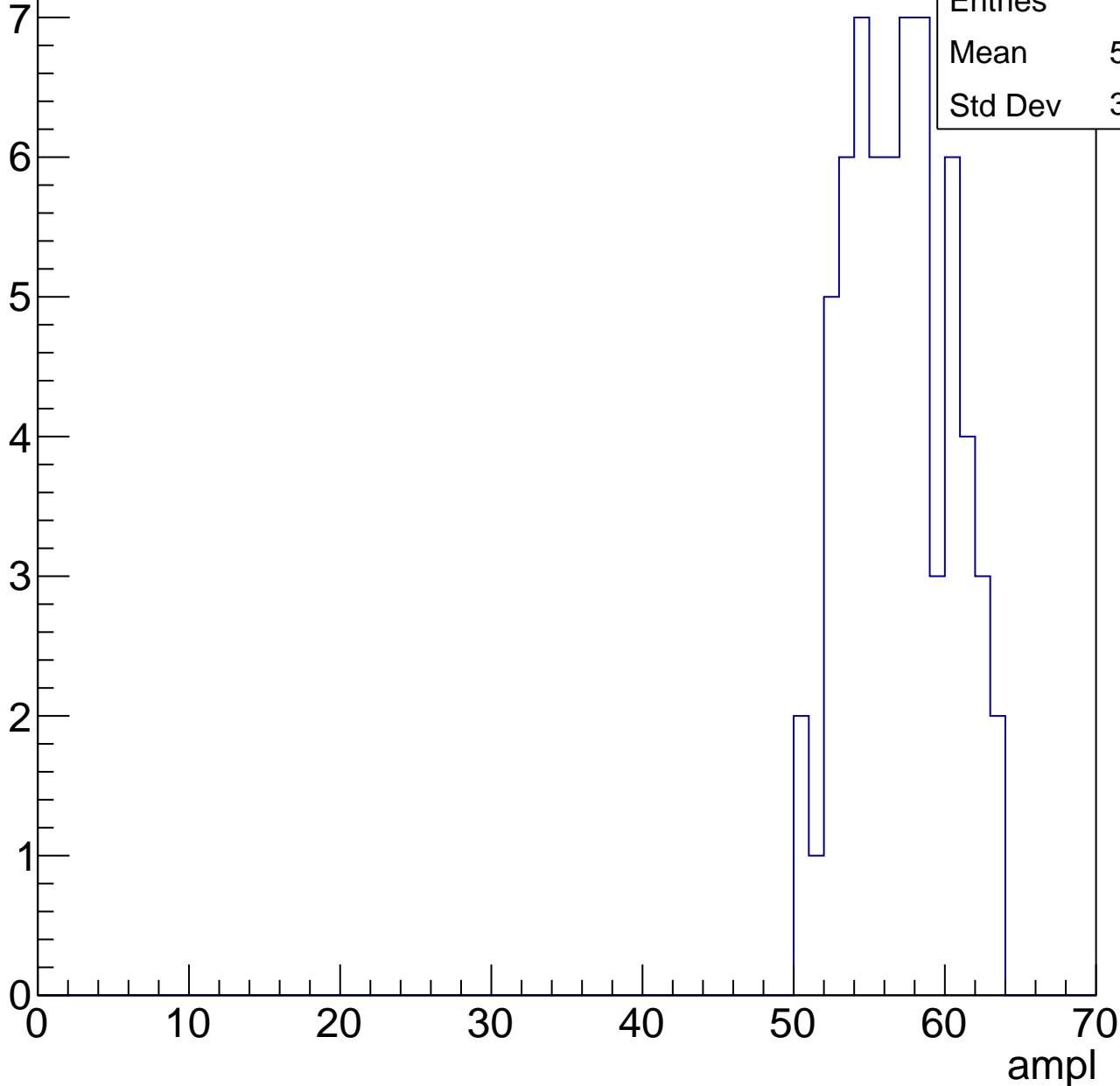


B1L003S, U21-ch48, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	56.48
Std Dev	3.315

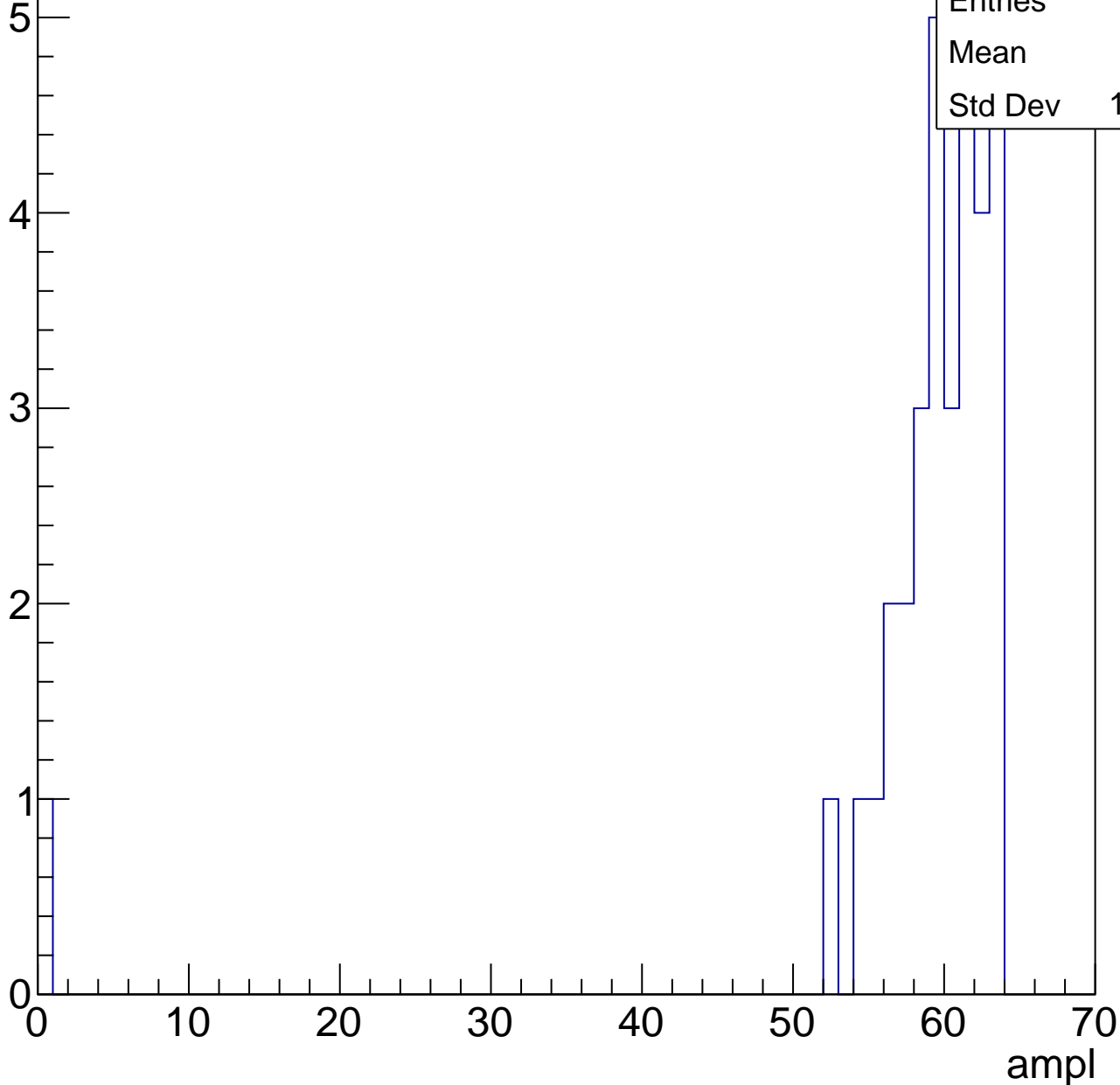


B1L003S, U21-ch48, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	57.7
Std Dev	10.56



B1L003S, U21-ch48, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries	10
Mean	61.4
Std Dev	1.497

B1L003S, U21-ch48, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch49, adc0

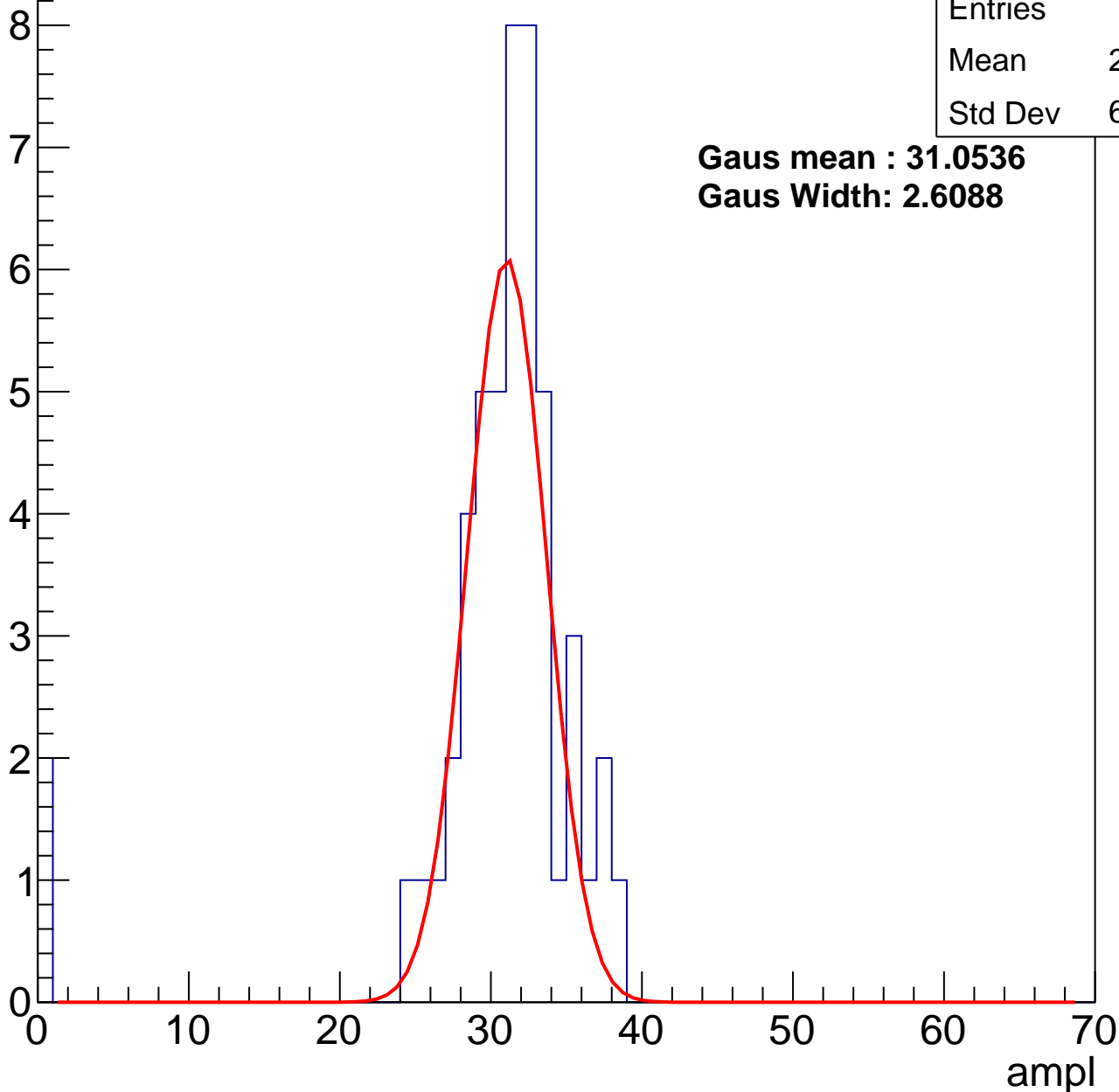
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	29.84
Std Dev	6.766

Gaus mean : 31.0536

Gaus Width: 2.6088



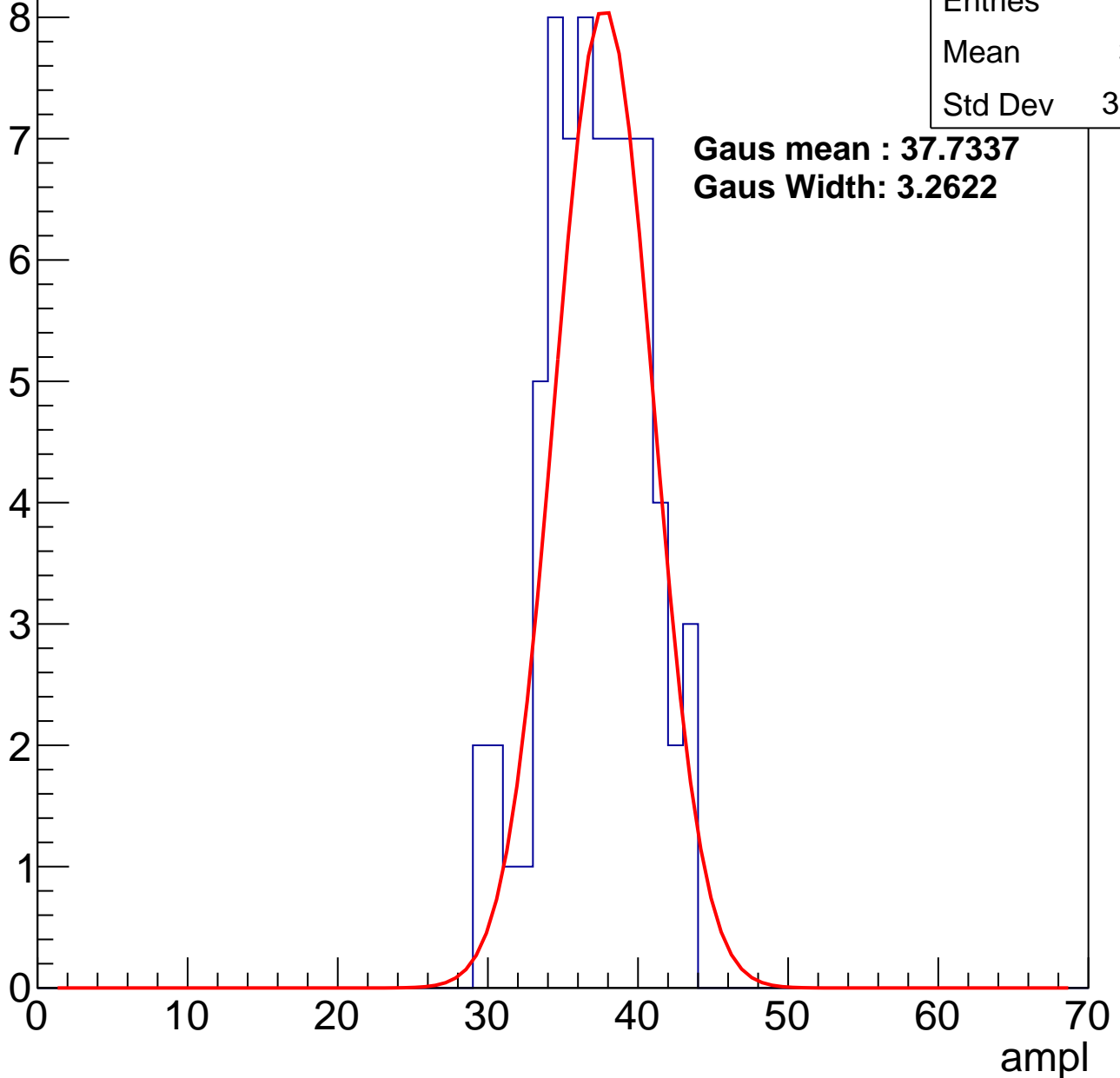
B1L003S, U21-ch49, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.7
Std Dev	3.333

Gaus mean : 37.7337
Gaus Width: 3.2622



B1L003S, U21-ch49, adc2

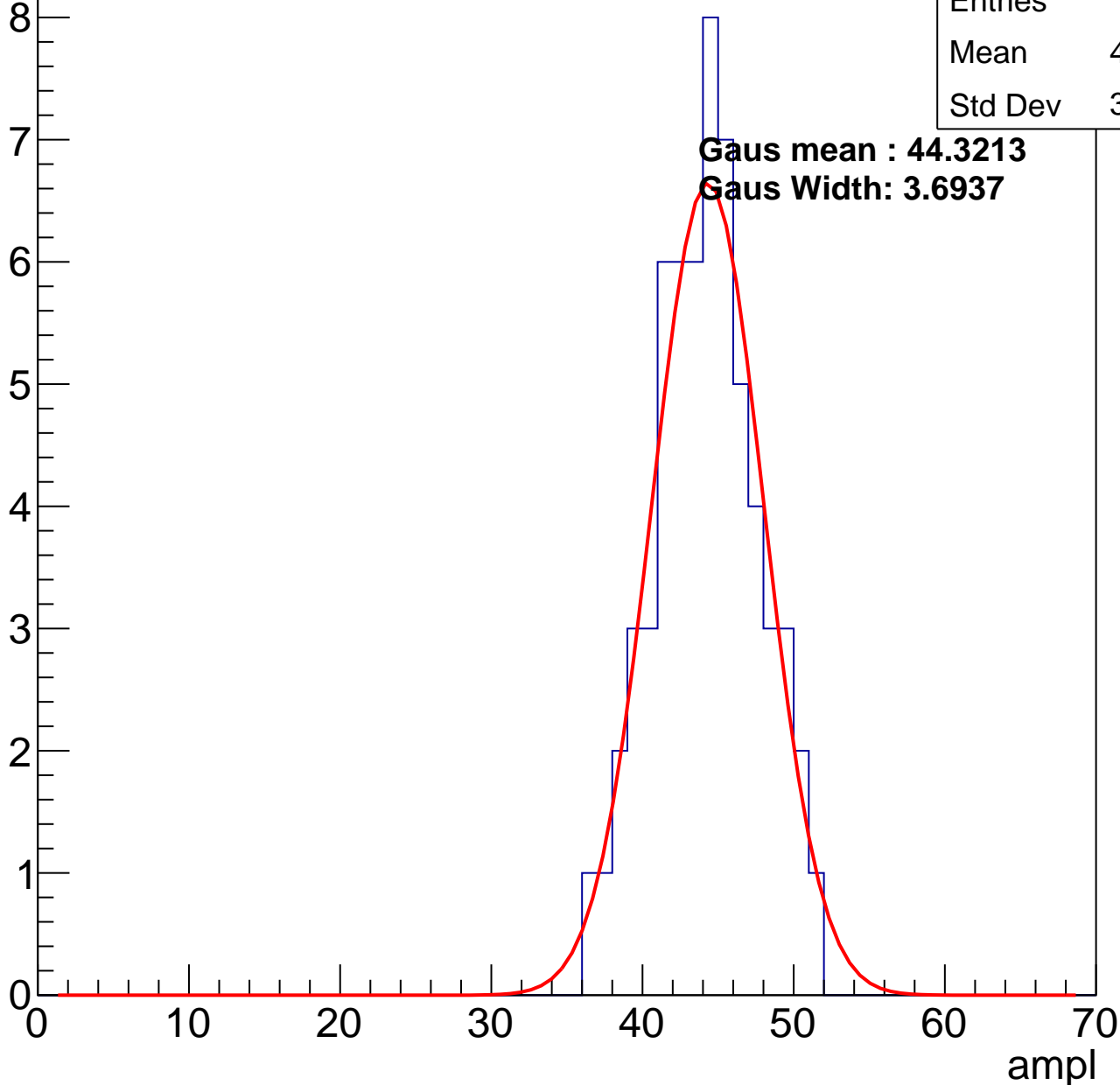
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	43.75
Std Dev	3.376

Gaus mean : 44.3213

Gaus Width: 3.6937

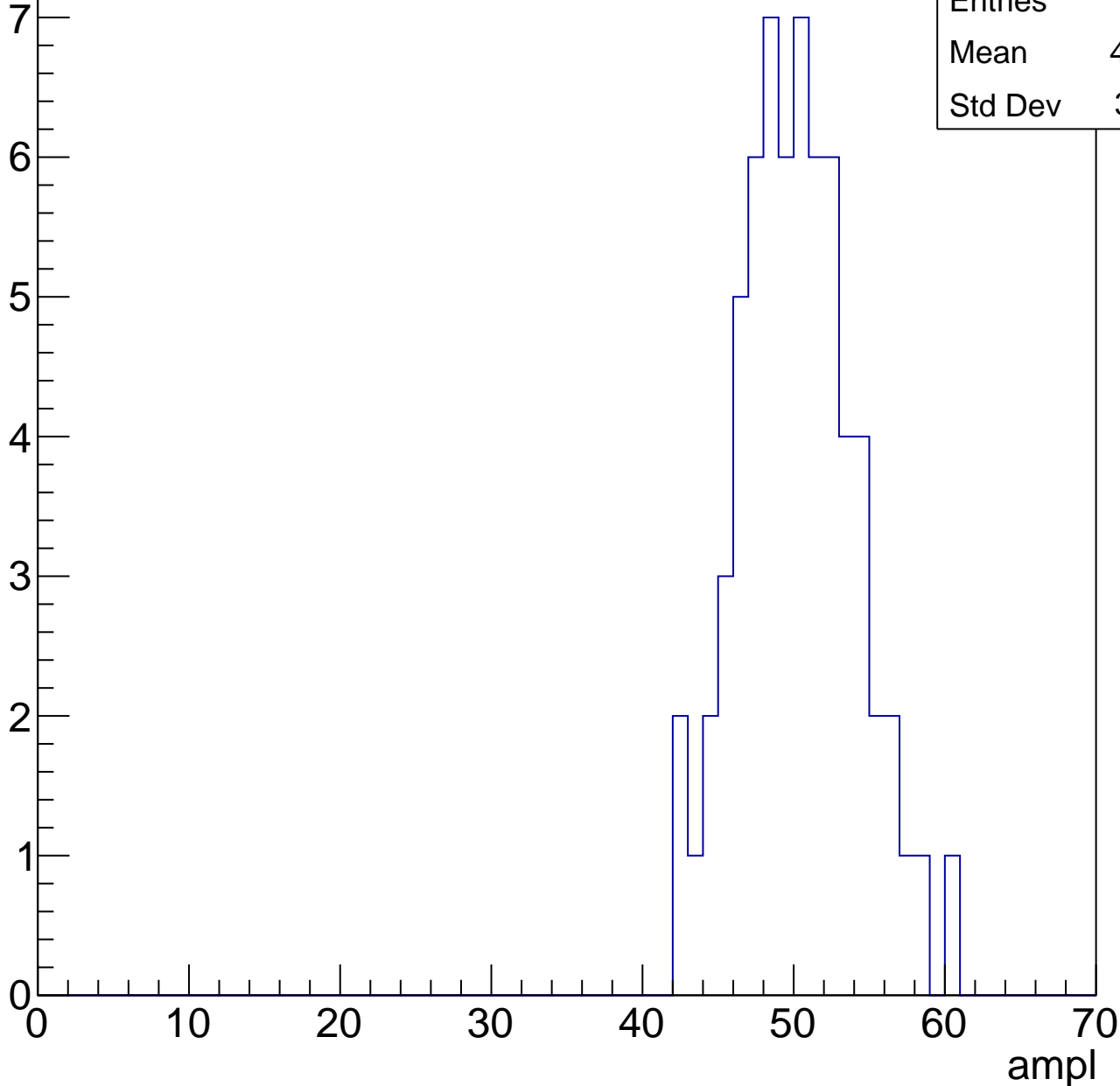


B1L003S, U21-ch49, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.77
Std Dev	3.801

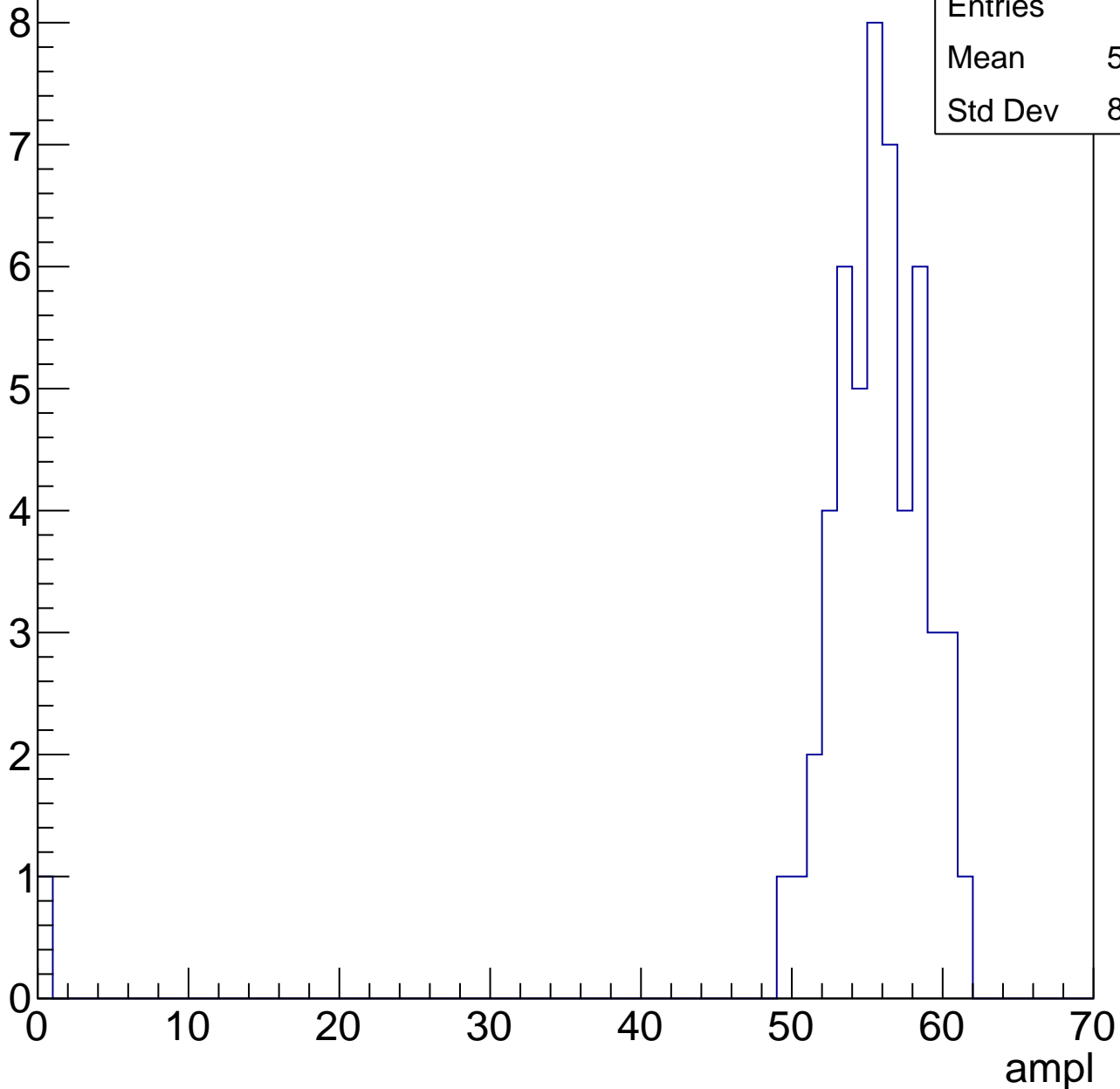


B1L003S, U21-ch49, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	54.29
Std Dev	8.077

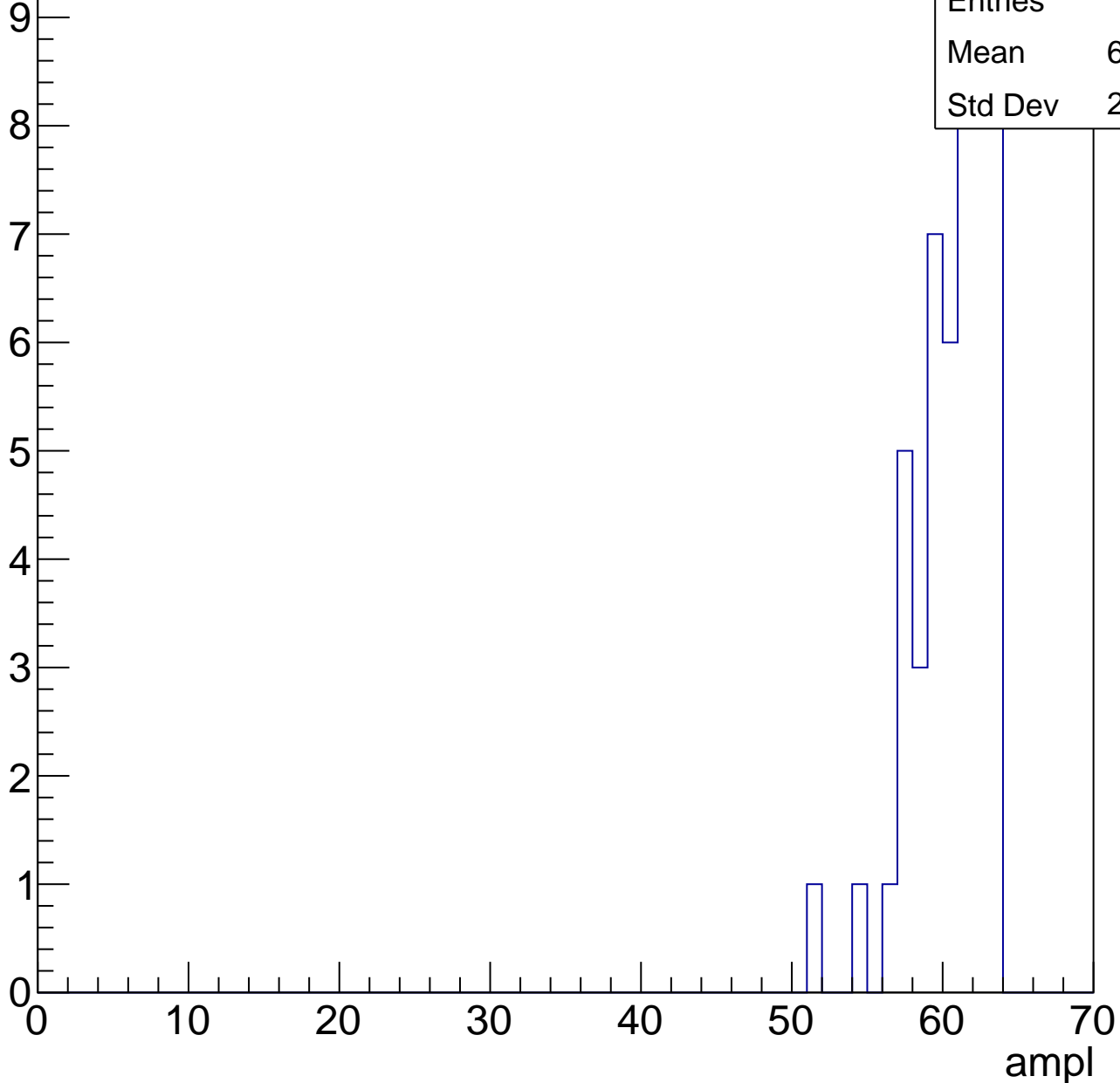


B1L003S, U21-ch49, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	60.14
Std Dev	2.513



B1L003S, U21-ch49, adc6

calib_packv5_042523_0143.root, FC#13, port D2

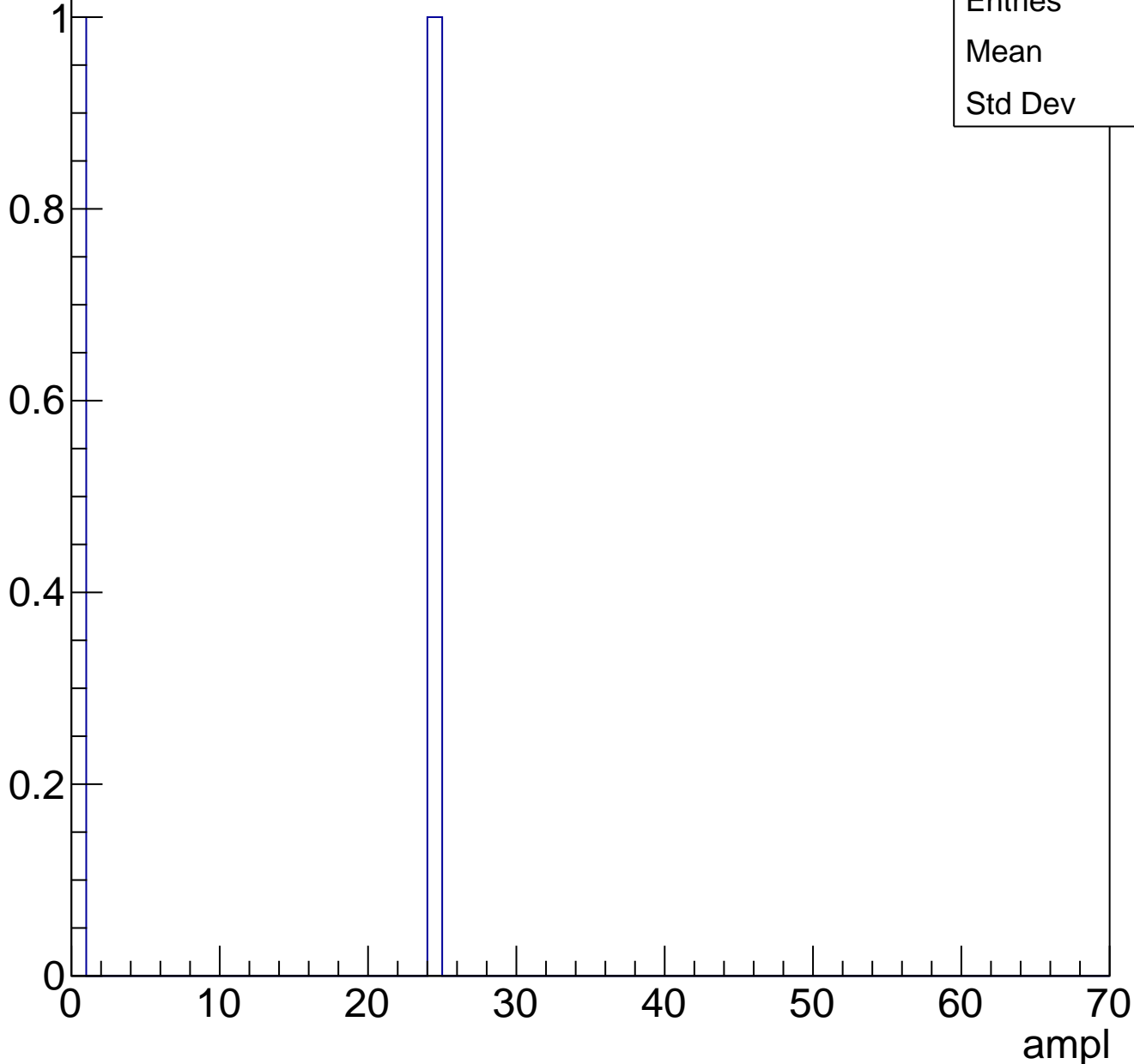
Entry



B1L003S, U21-ch49, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch50, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	77
Mean	29.66
Std Dev	3.177

Gaus mean : 30.0261

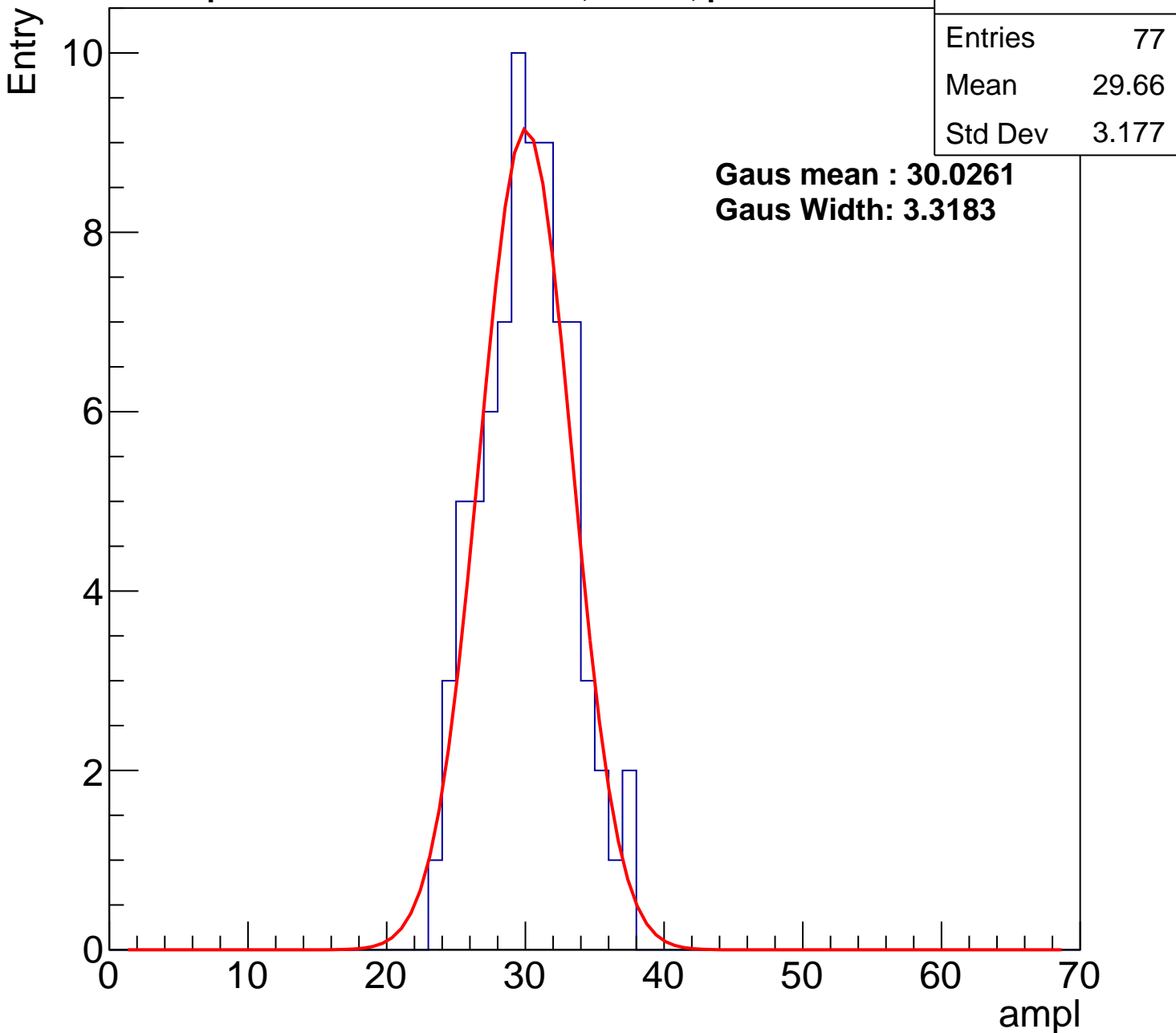
Gaus Width: 3.3183

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

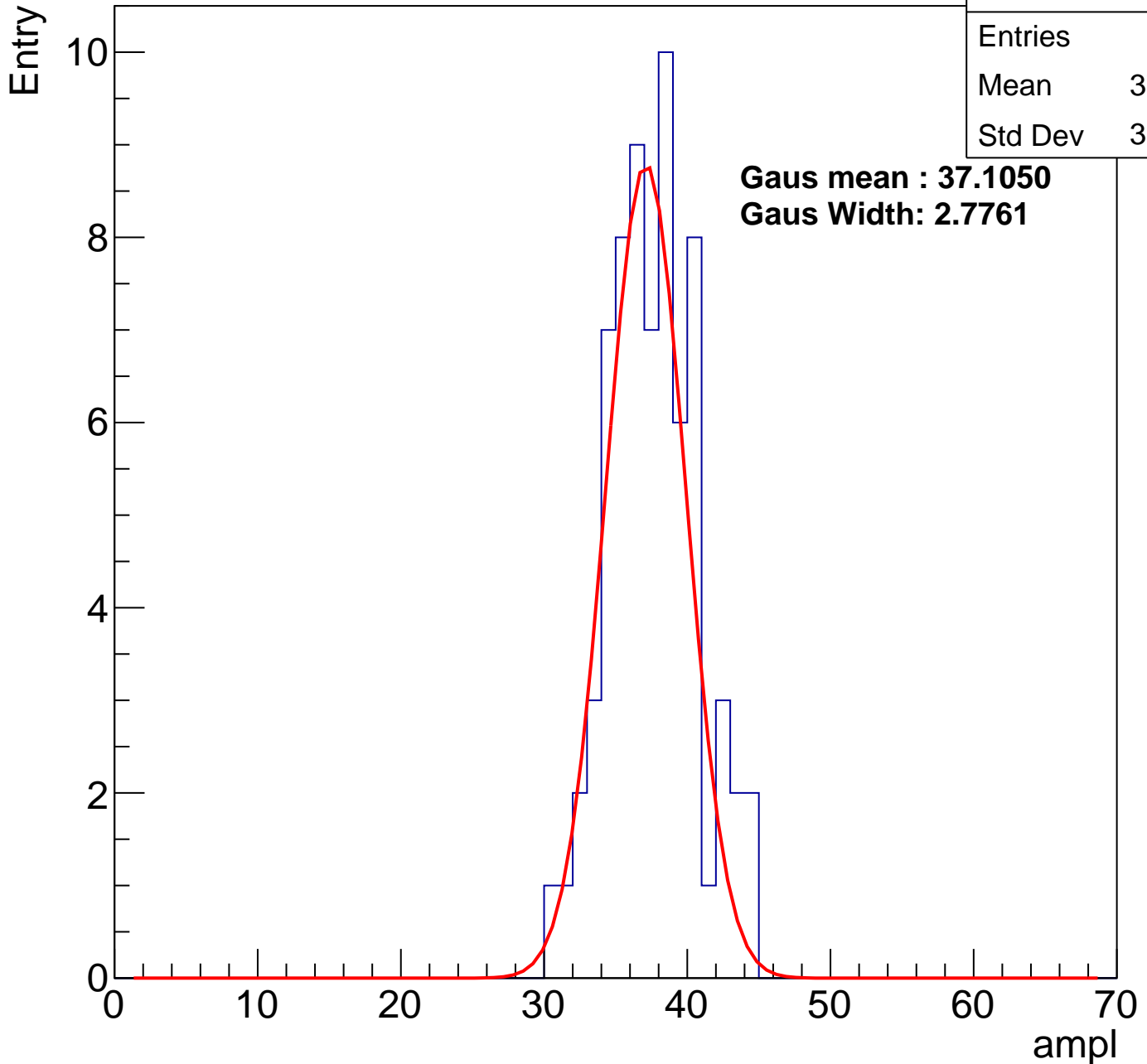


B1L003S, U21-ch50, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	37.14
Std Dev	3.053

Gaus mean : 37.1050
Gaus Width: 2.7761



B1L003S, U21-ch50, adc2

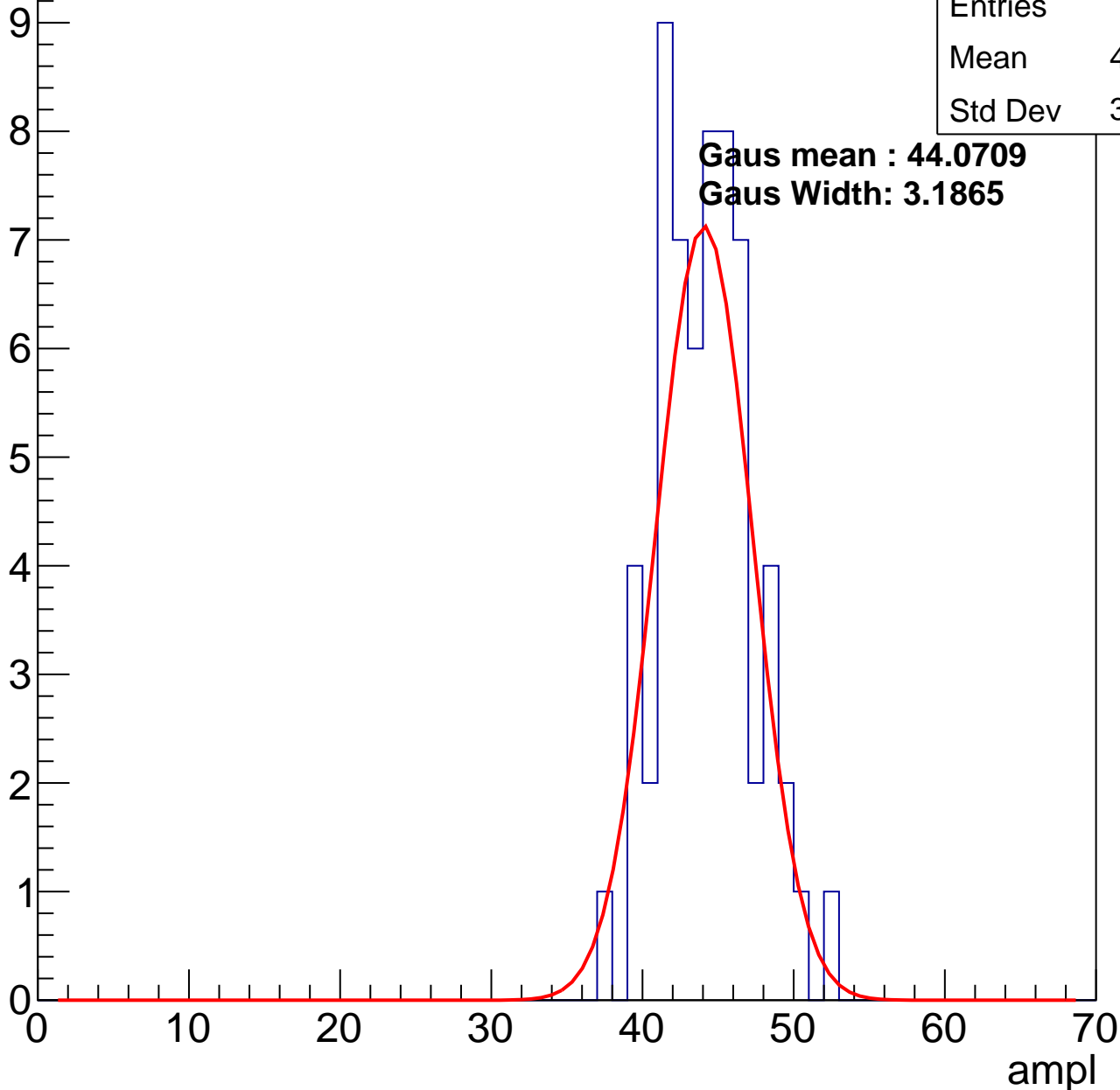
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	43.77
Std Dev	3.008

Gaus mean : 44.0709

Gaus Width: 3.1865



B1L003S, U21-ch50, adc3

calib_packv5_042523_0143.root, FC#13, port D2

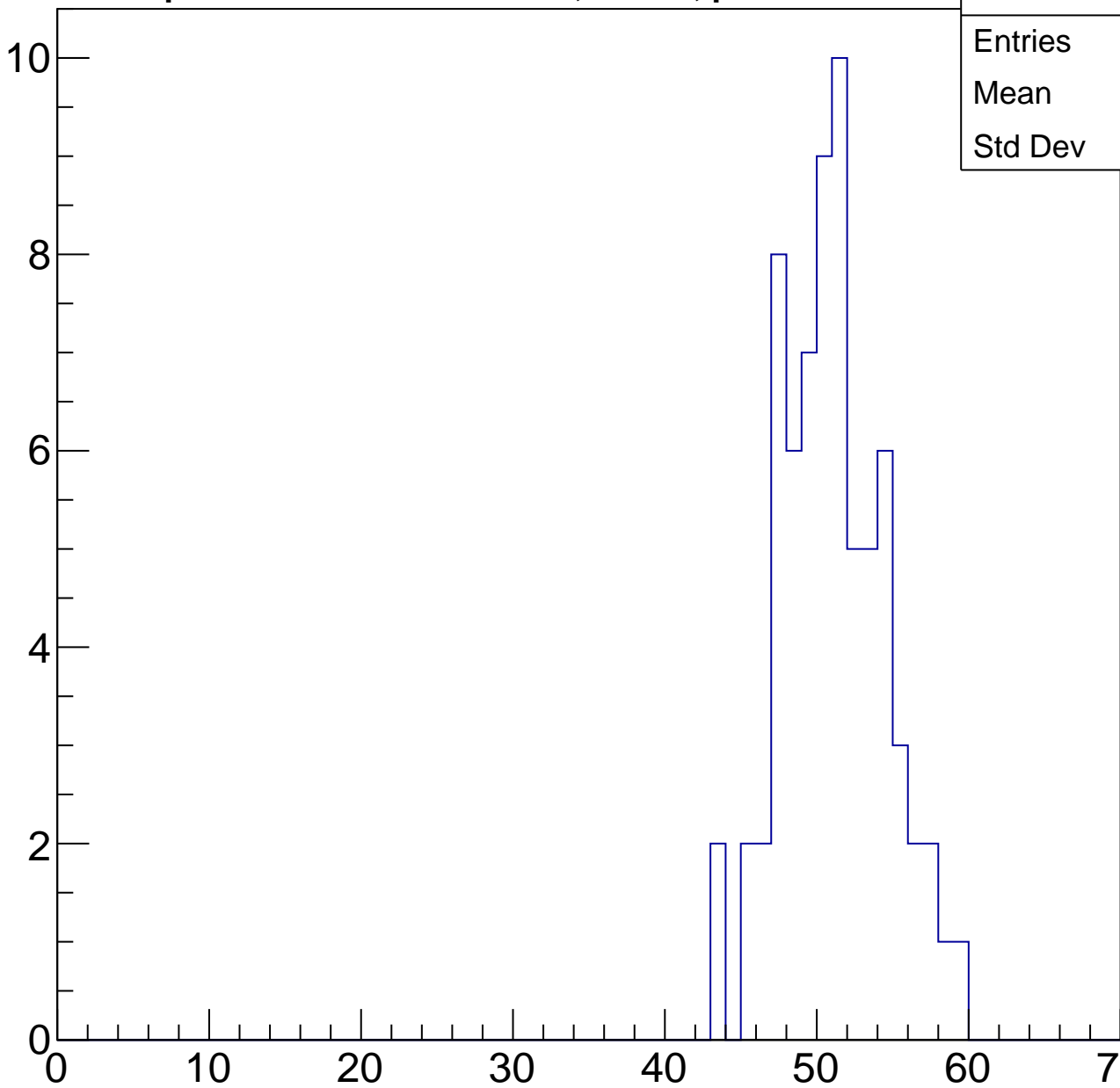
Entries	71
Mean	50.59
Std Dev	3.397

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

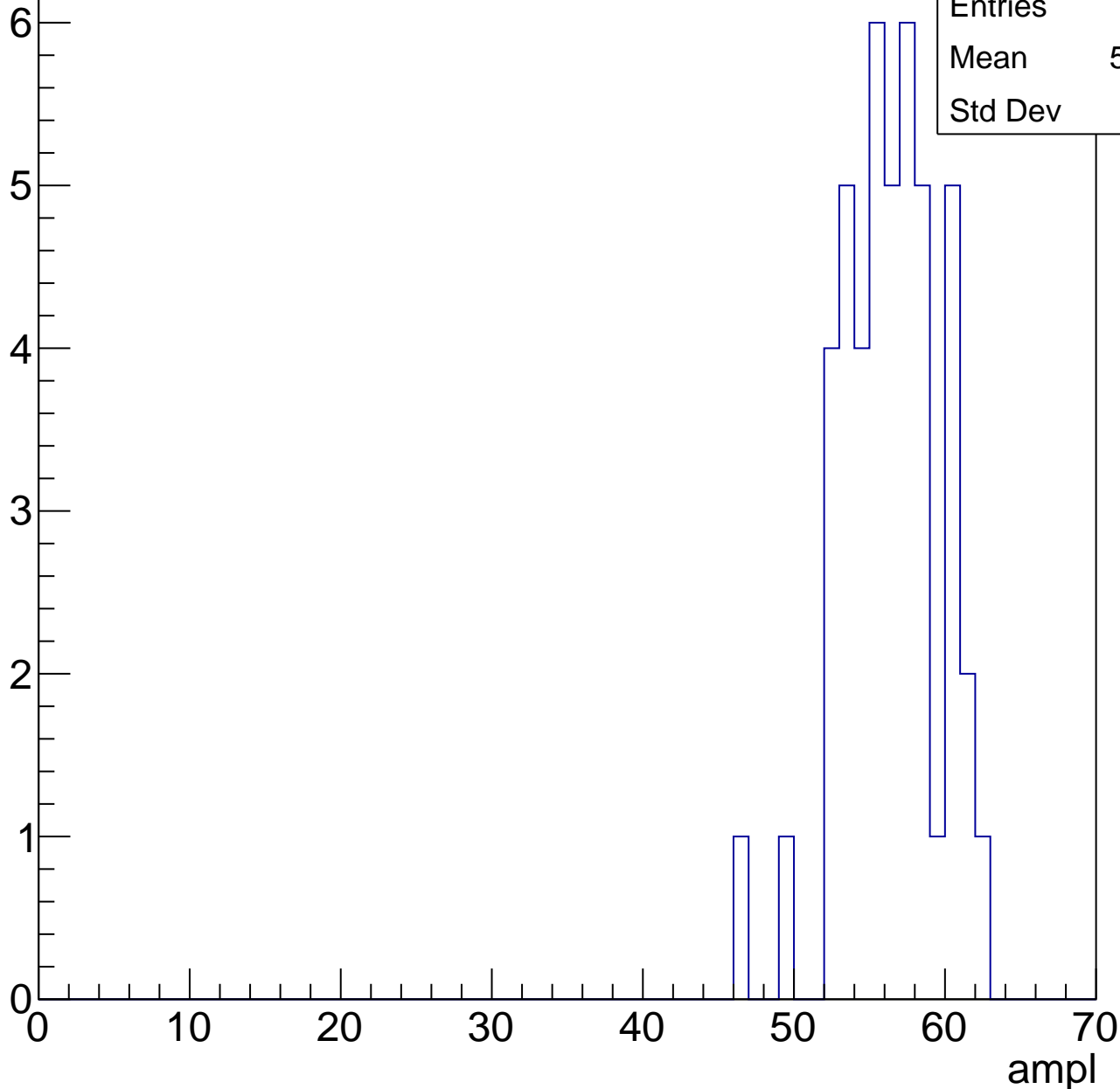
ampl



B1L003S, U21-ch50, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

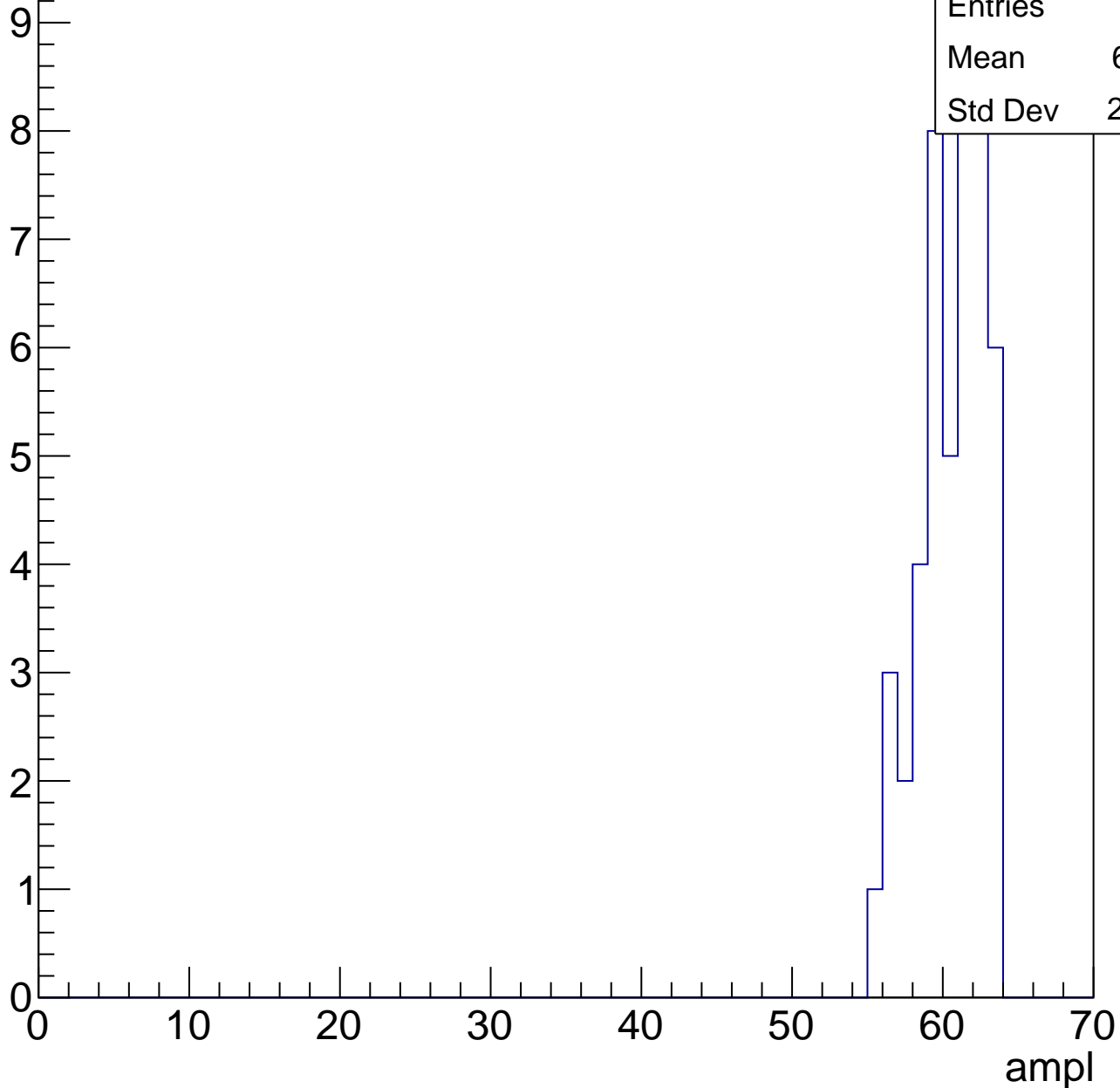


Entries	46
Mean	55.85
Std Dev	3.23

B1L003S, U21-ch50, adc5

calib_packv5_042523_0143.root, FC#13, port D2

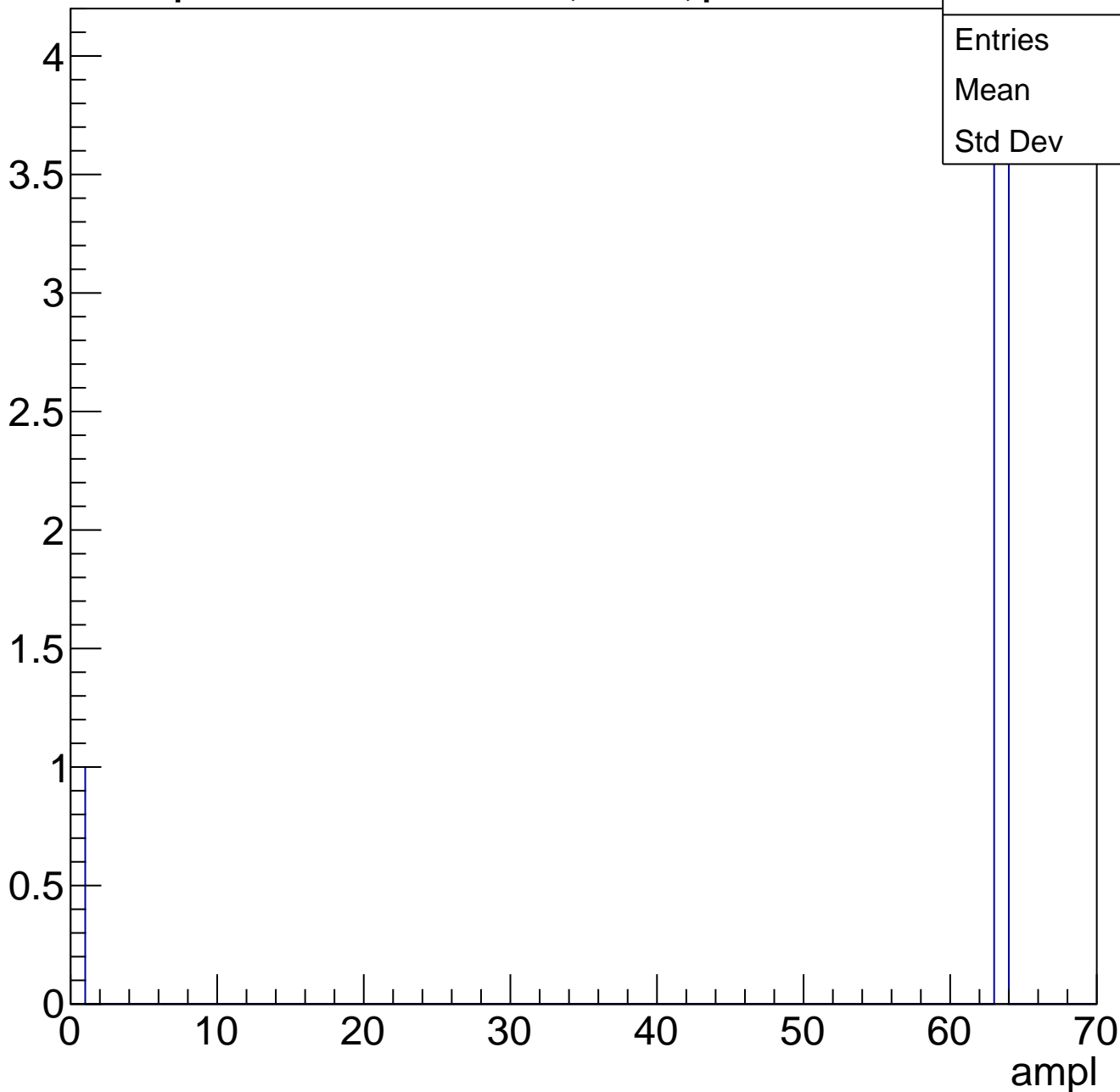
Entry



B1L003S, U21-ch50, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch50, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L003S, U21-ch51, adc0

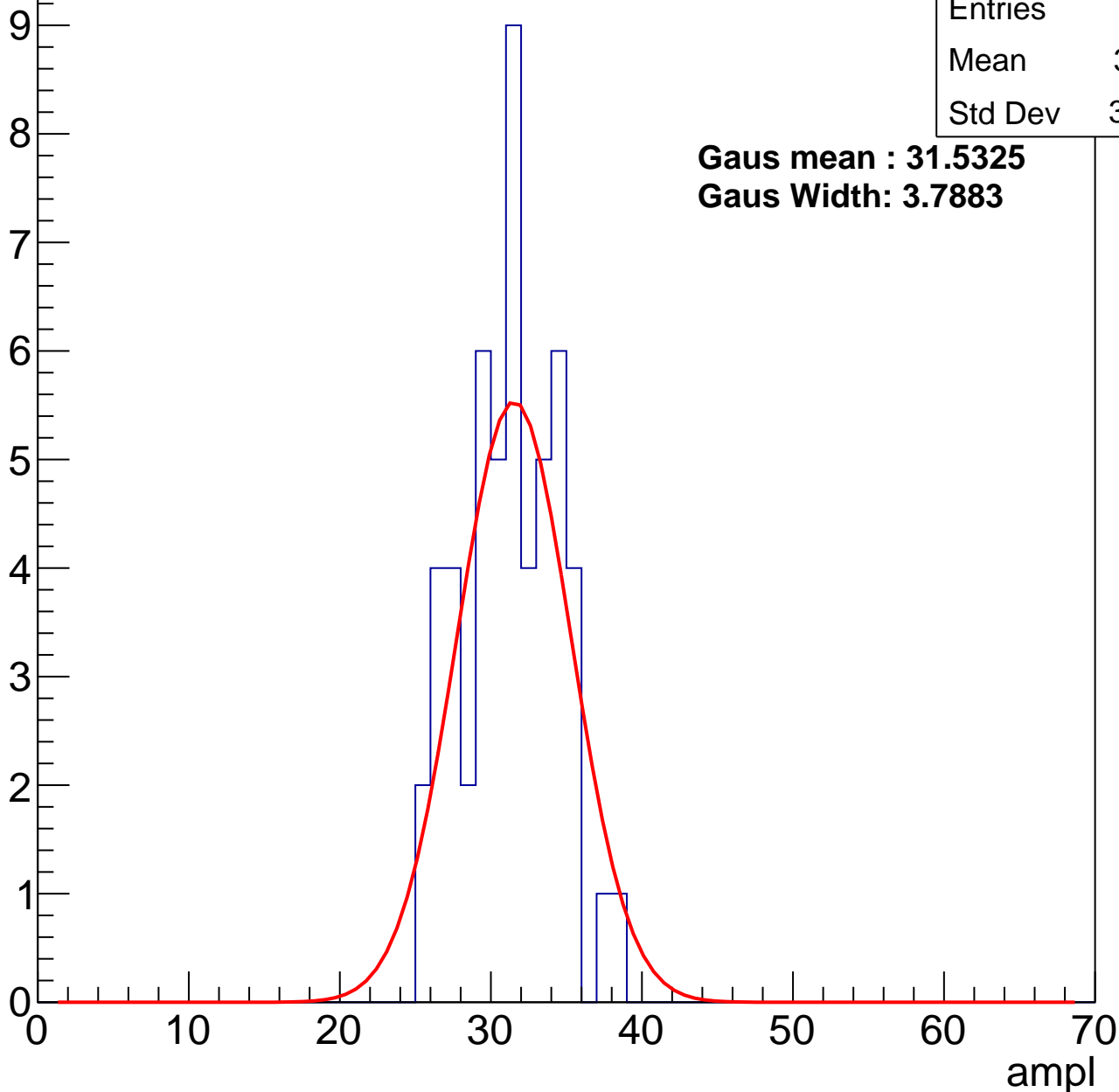
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	30.81
Std Dev	3.096

Gaus mean : 31.5325

Gaus Width: 3.7883



B1L003S, U21-ch51, adc1

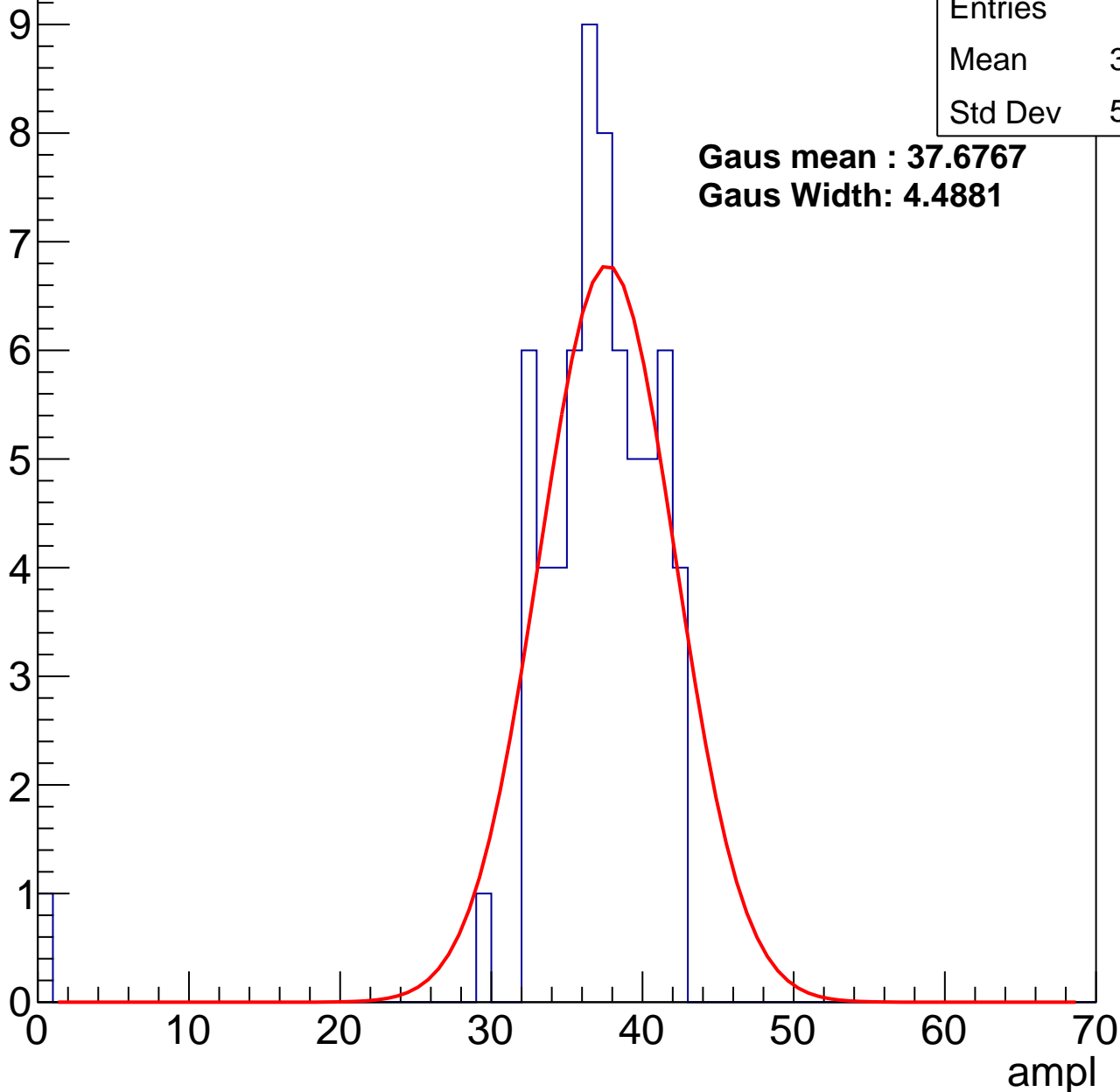
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	36.25
Std Dev	5.472

Gaus mean : 37.6767

Gaus Width: 4.4881



B1L003S, U21-ch51, adc2

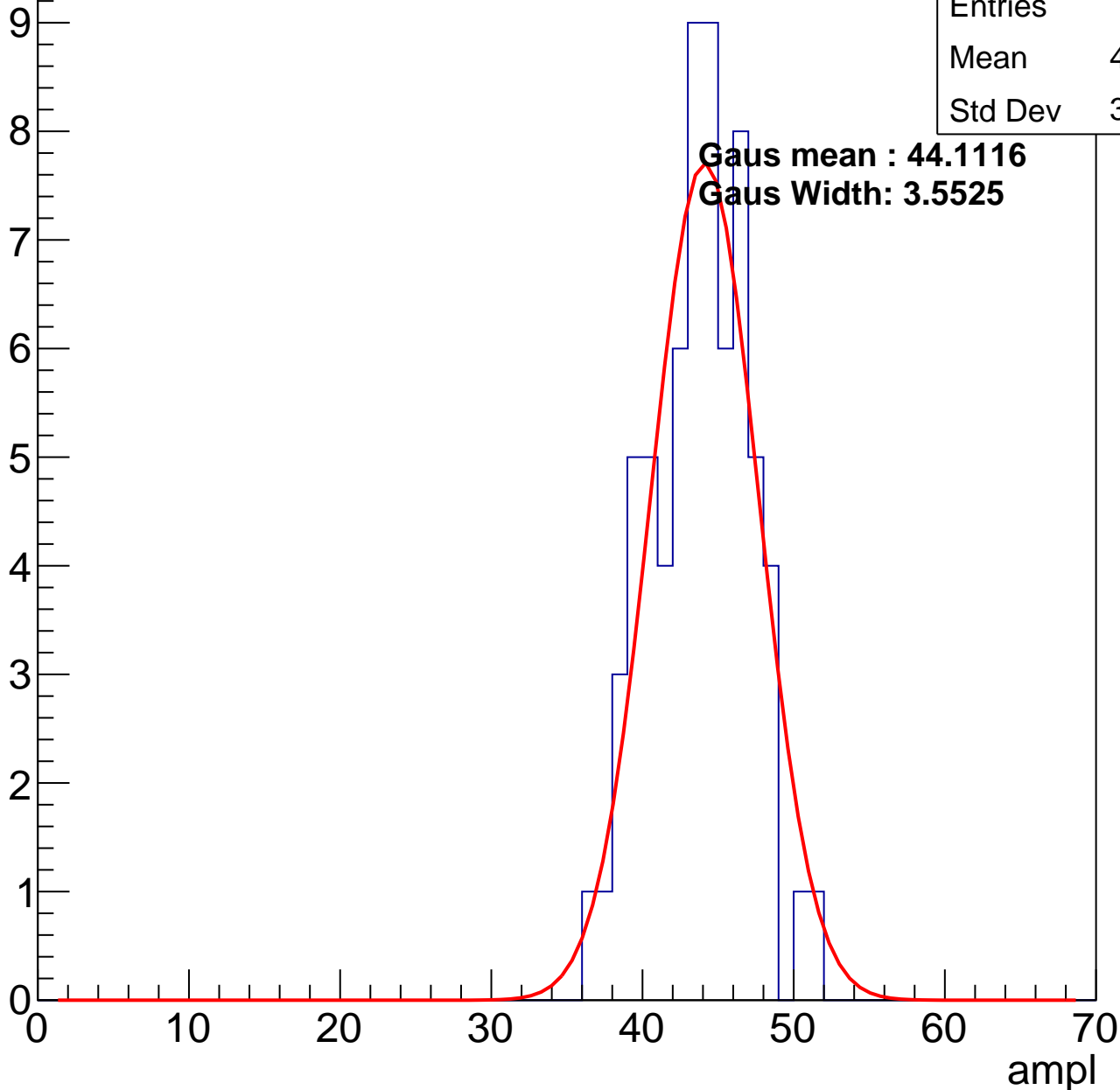
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	43.34
Std Dev	3.202

Gaus mean : 44.1116

Gaus Width: 3.5525

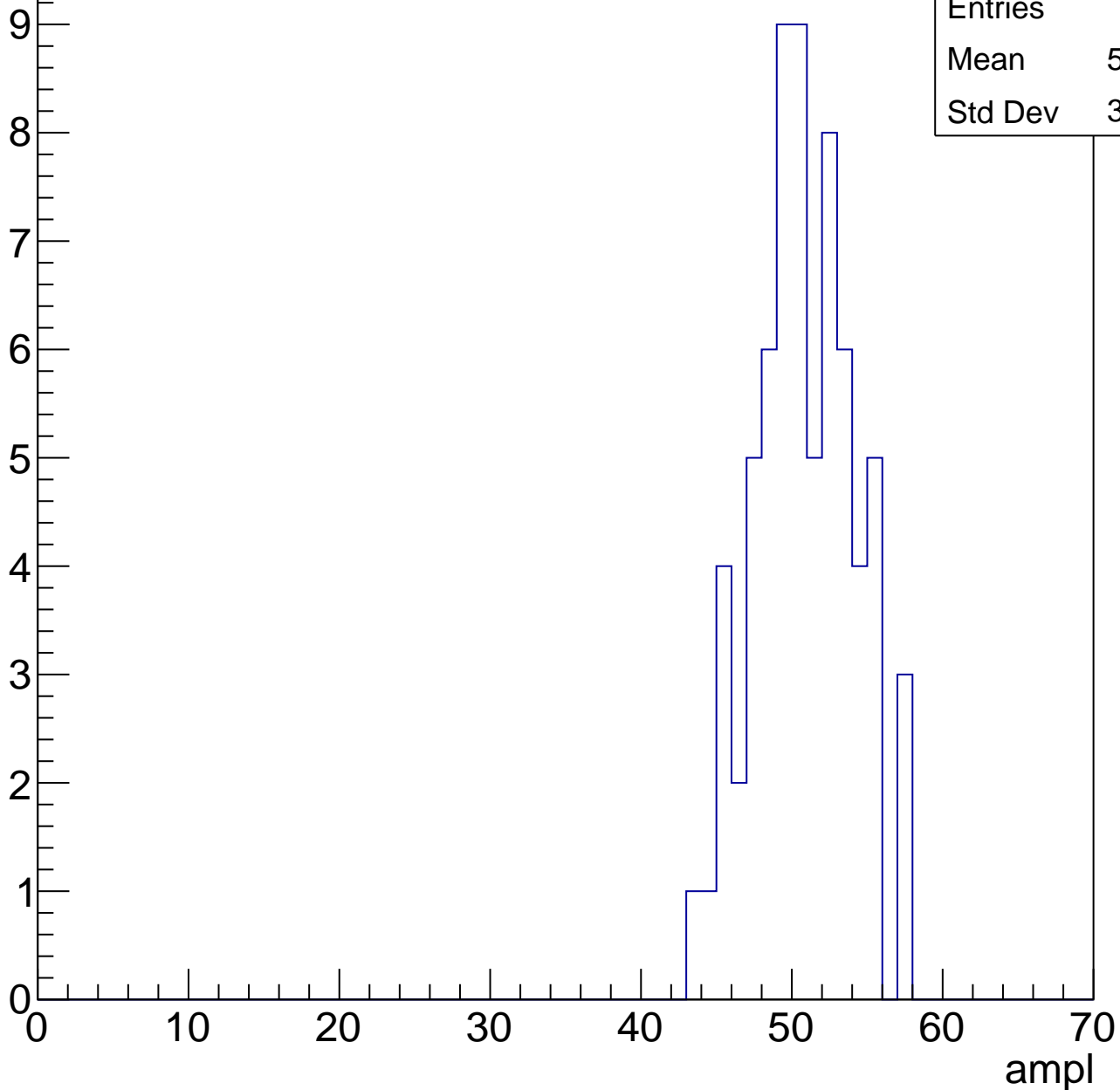


B1L003S, U21-ch51, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	50.35
Std Dev	3.239

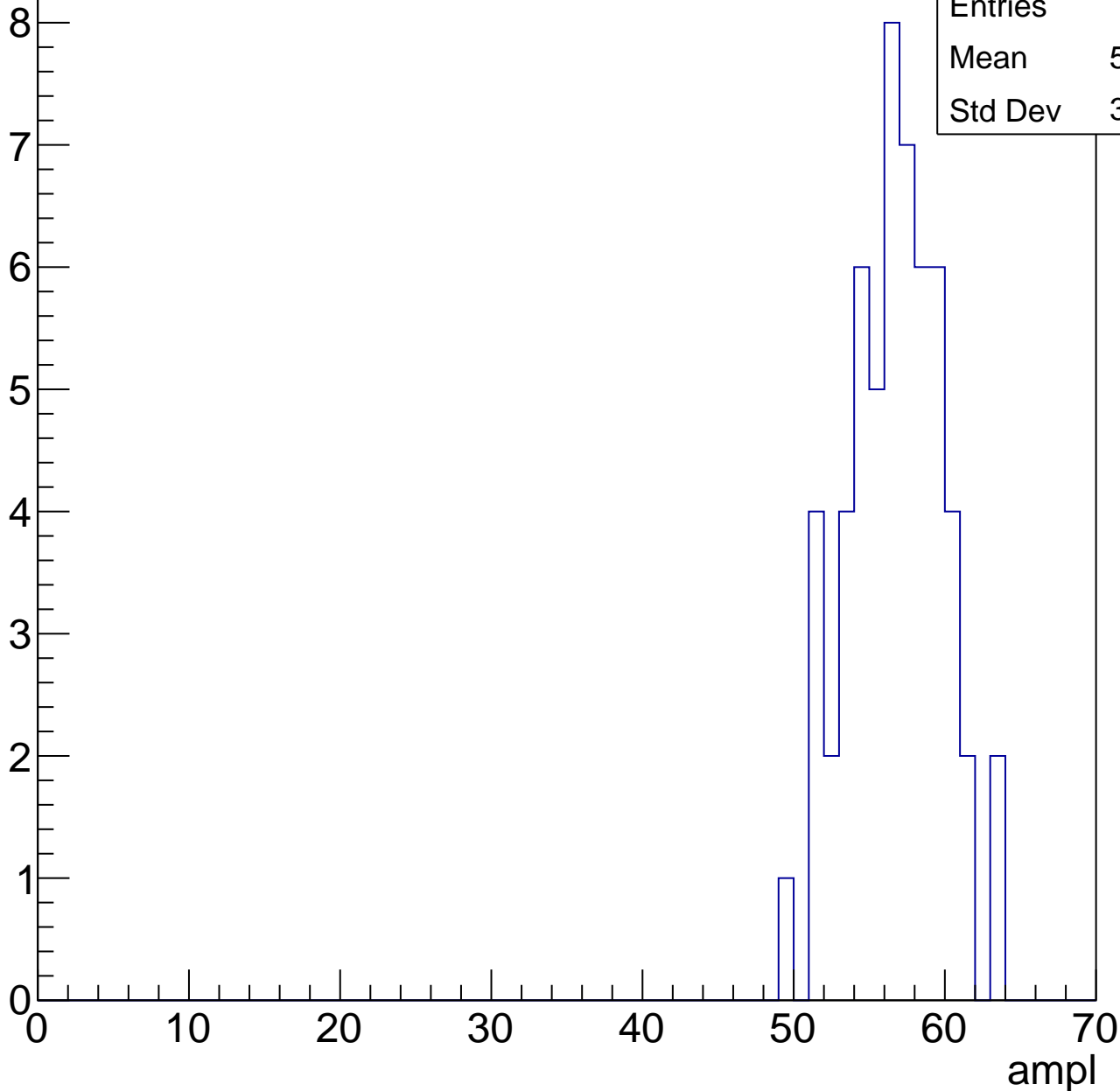


B1L003S, U21-ch51, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.23
Std Dev	3.078

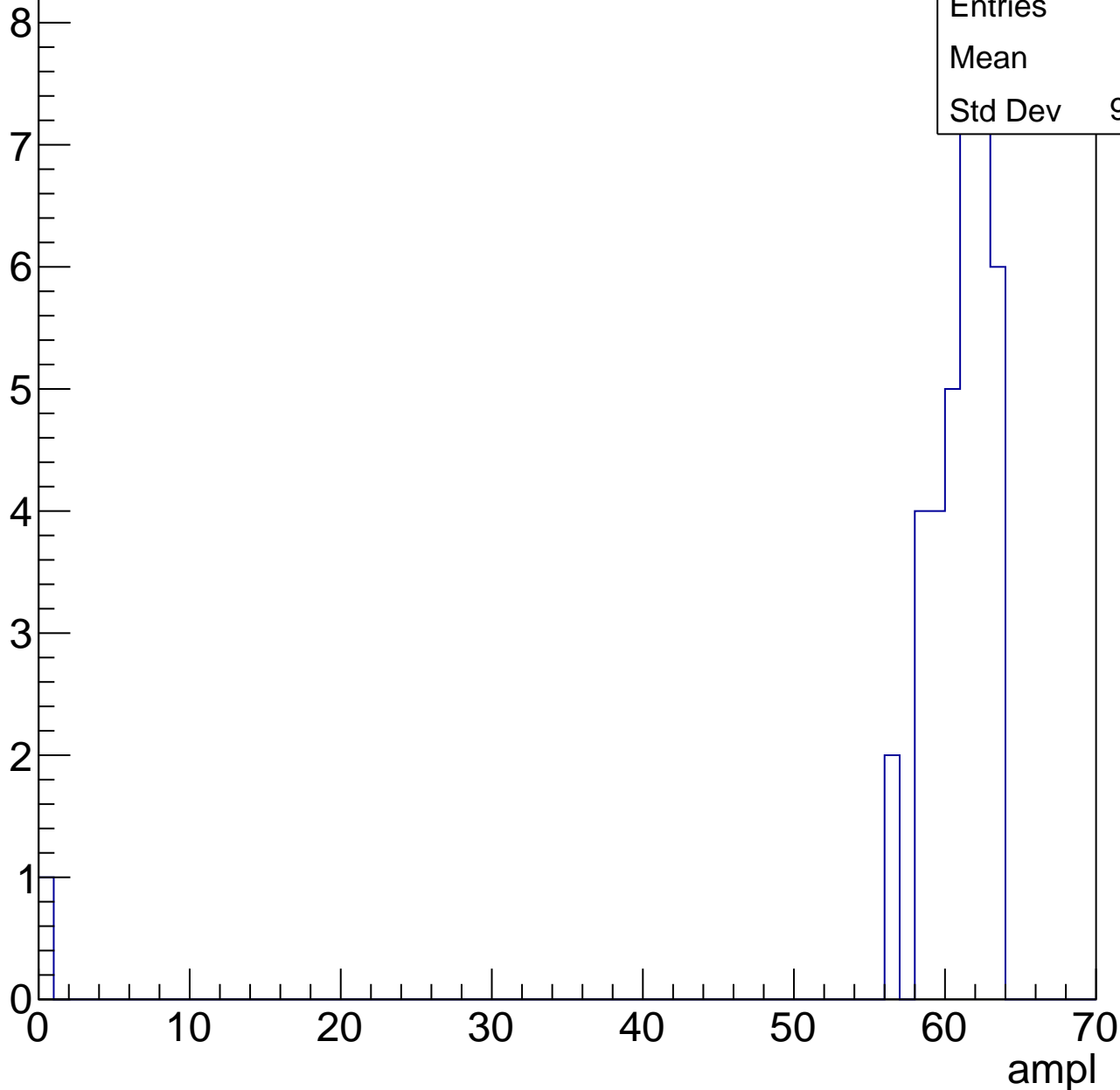


B1L003S, U21-ch51, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59
Std Dev	9.878



B1L003S, U21-ch51, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch51, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L003S, U21-ch52, adc0

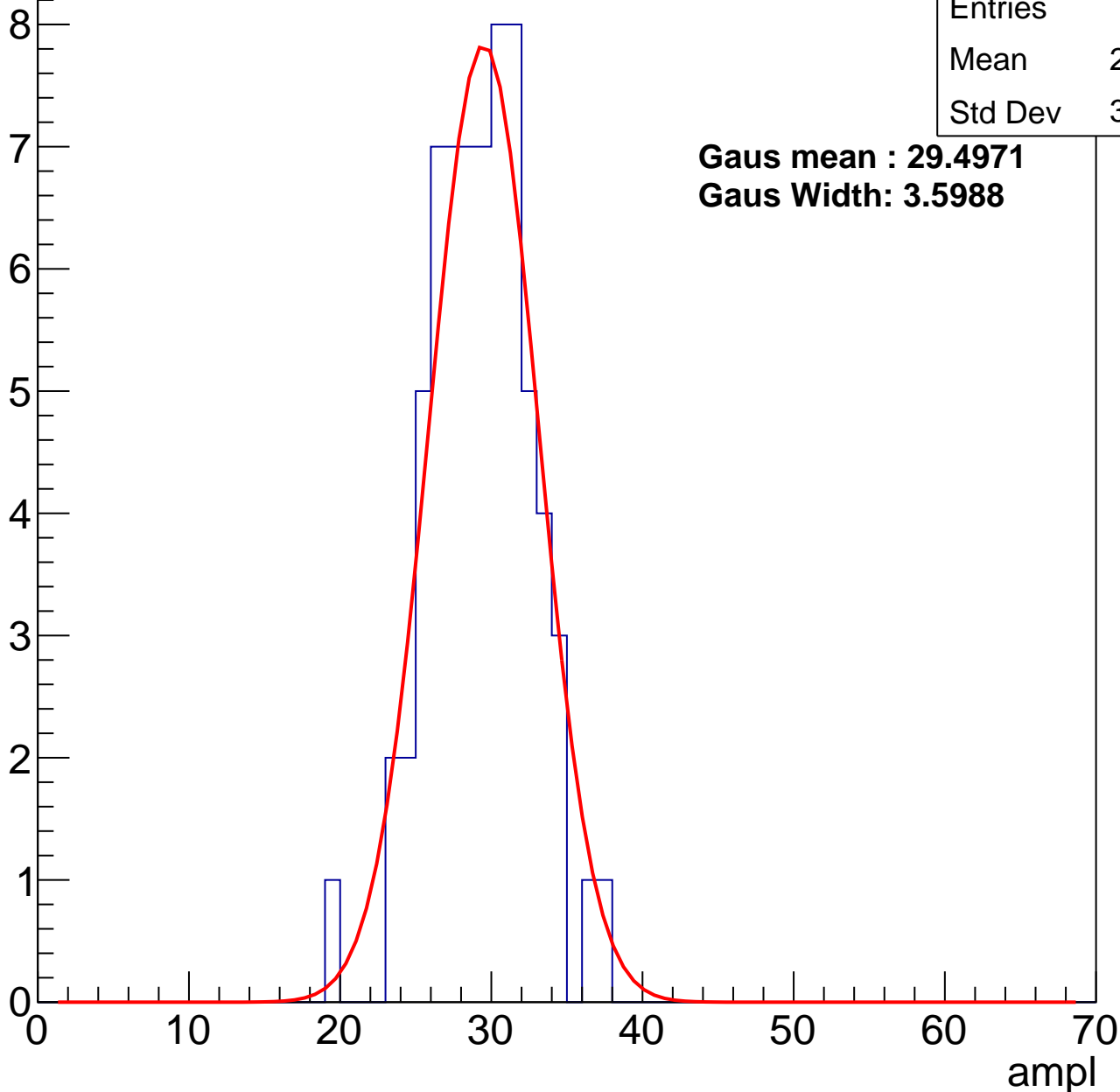
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.87
Std Dev	3.289

Gaus mean : 29.4971

Gaus Width: 3.5988



B1L003S, U21-ch52, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	70
Mean	35.64
Std Dev	3.047

Gaus mean : 36.5079

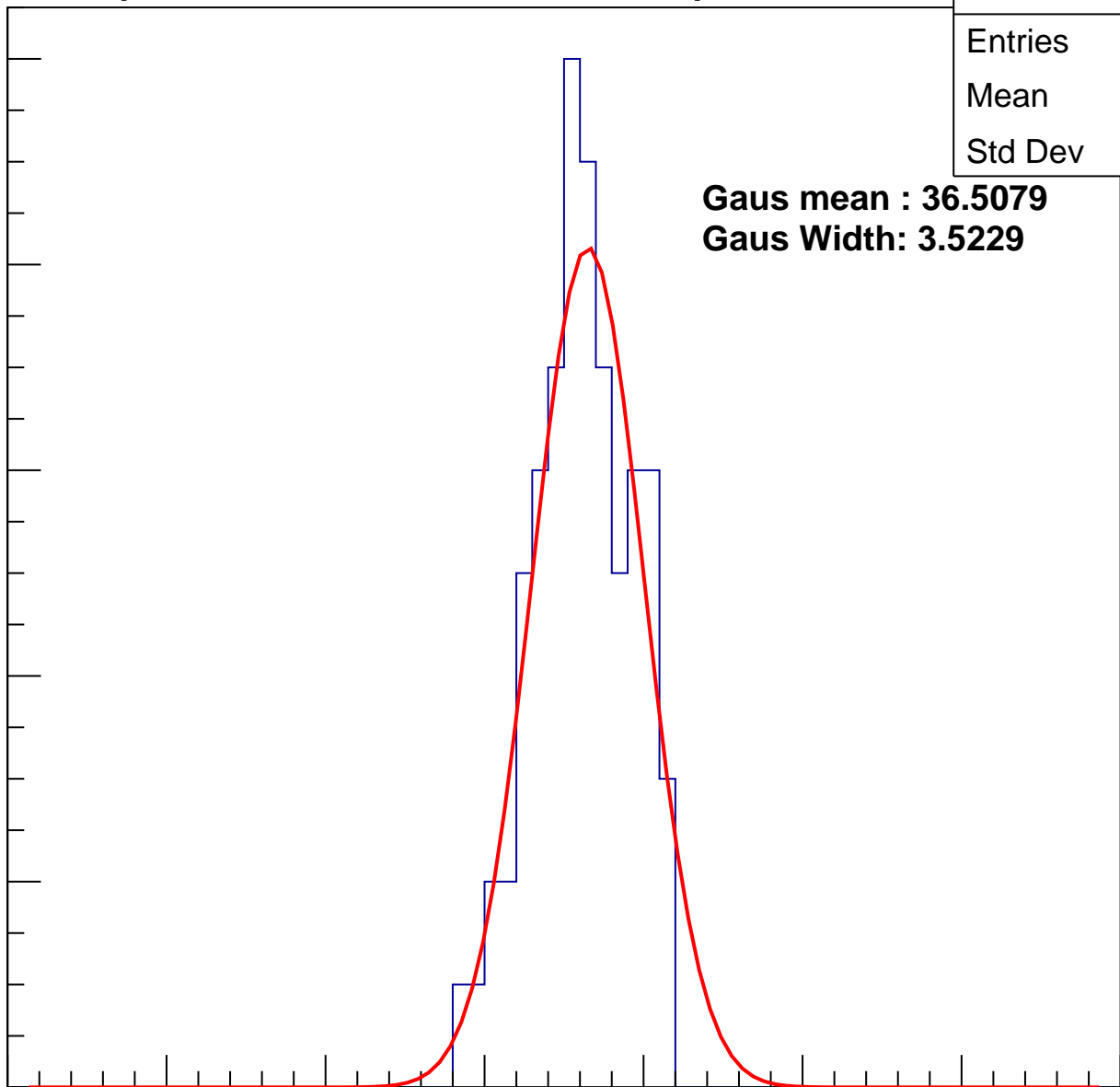
Gaus Width: 3.5229

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch52, adc2

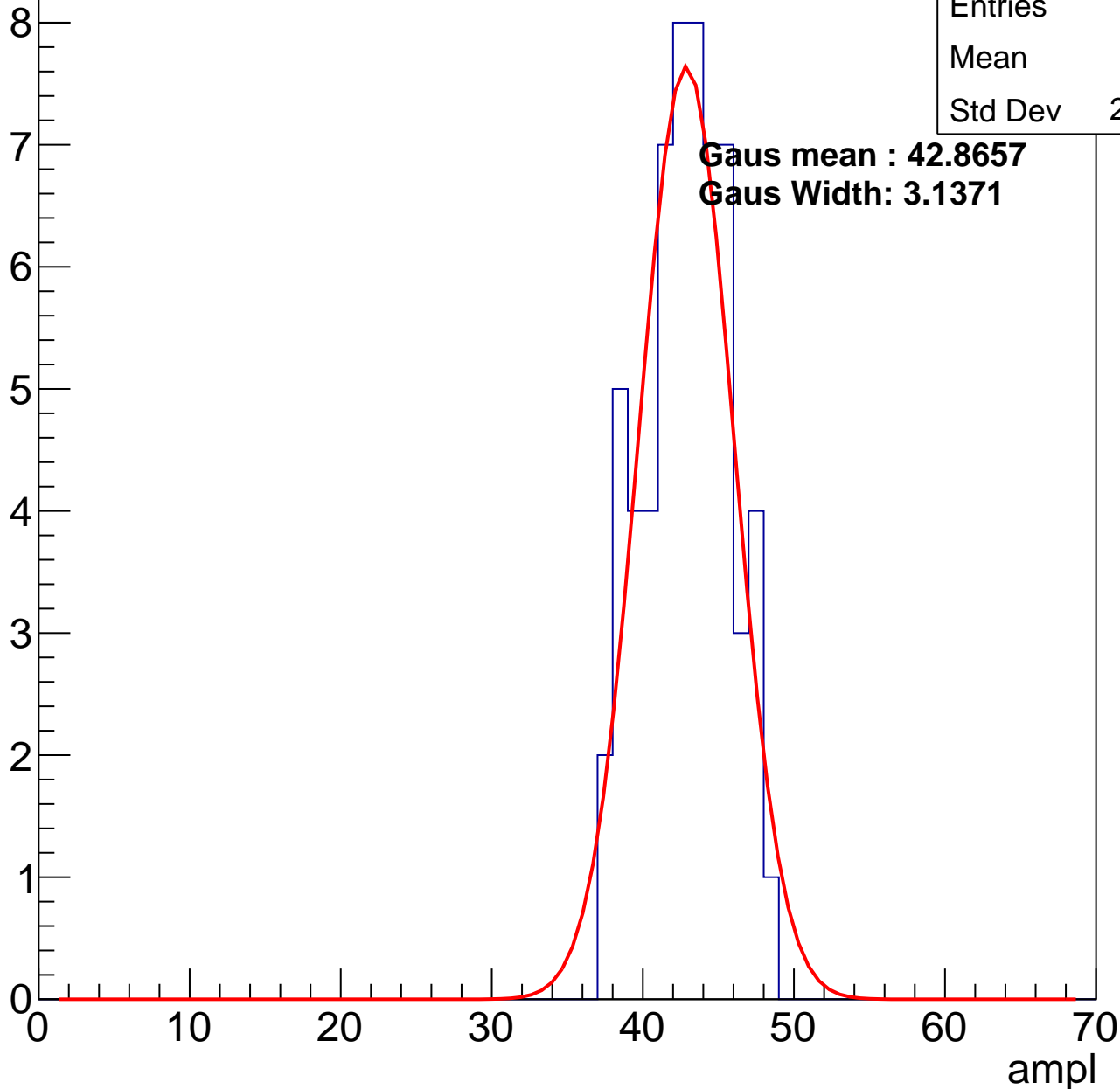
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.4
Std Dev	2.776

Gaus mean : 42.8657

Gaus Width: 3.1371

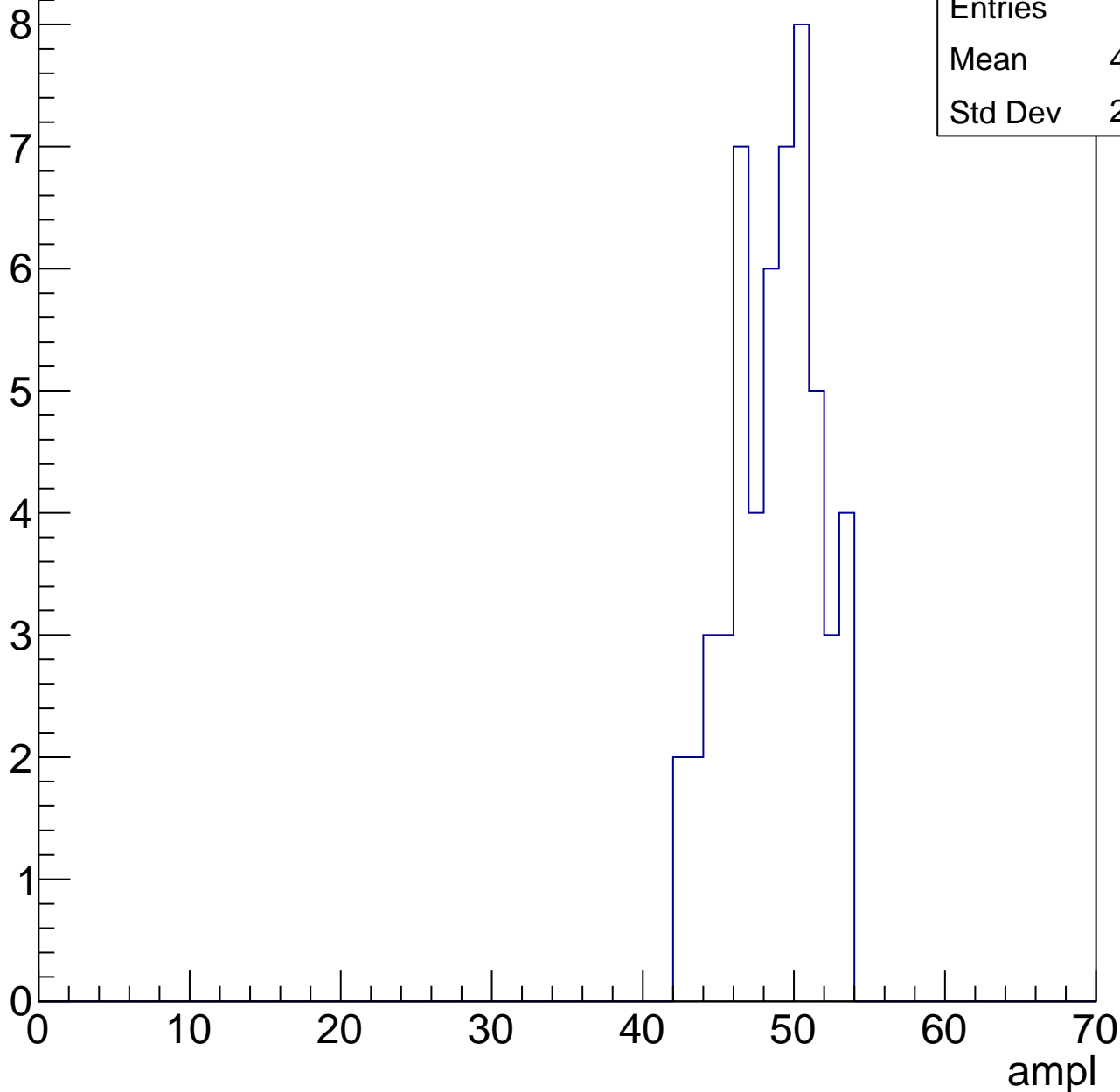


B1L003S, U21-ch52, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

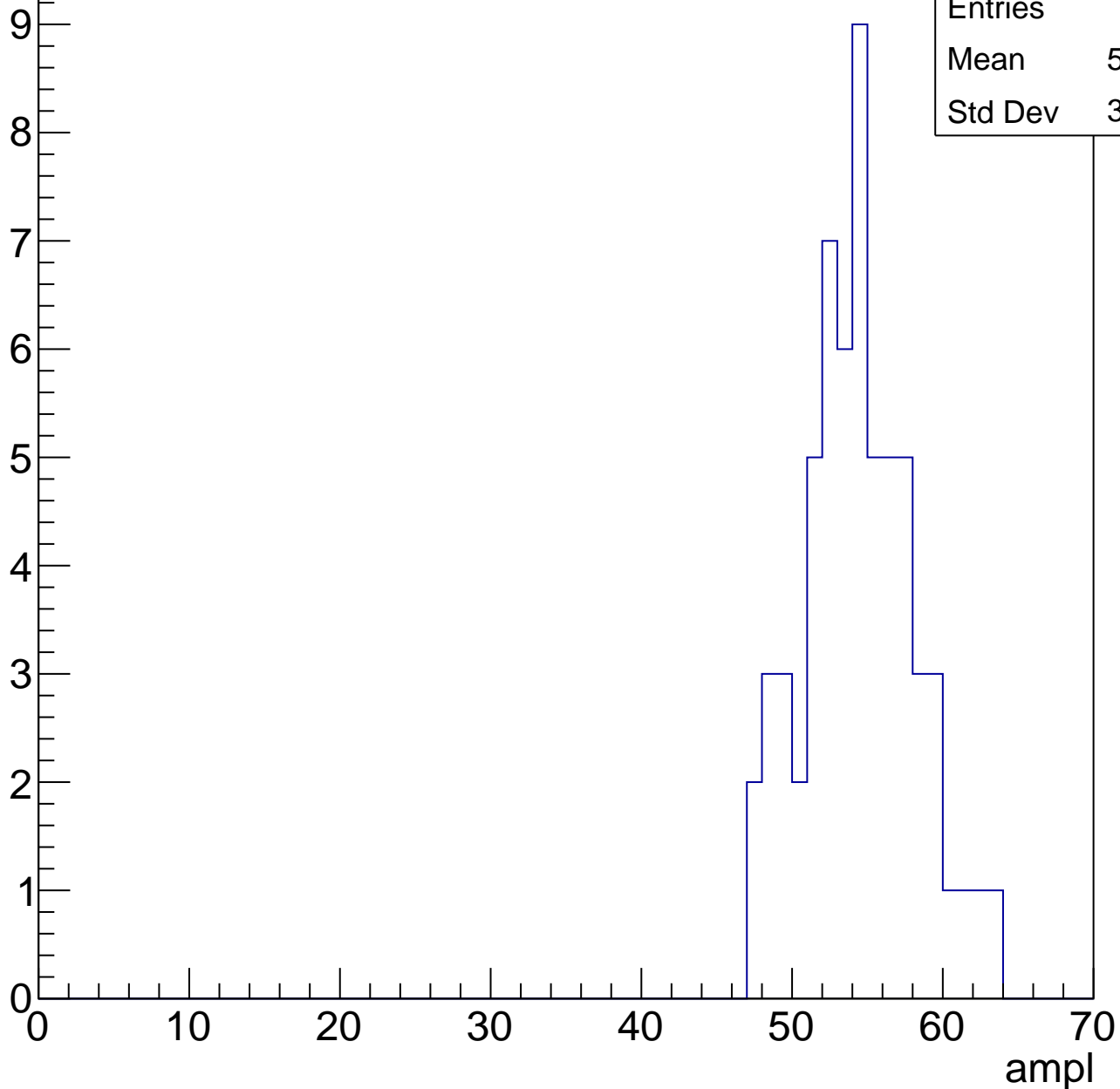
Entries	54
Mean	48.17
Std Dev	2.917



B1L003S, U21-ch52, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	62
Mean	53.95
Std Dev	3.625

B1L003S, U21-ch52, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	63
Mean	58.37
Std Dev	7.818

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

8

9

B1L003S, U21-ch52, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch52, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch53, adc0

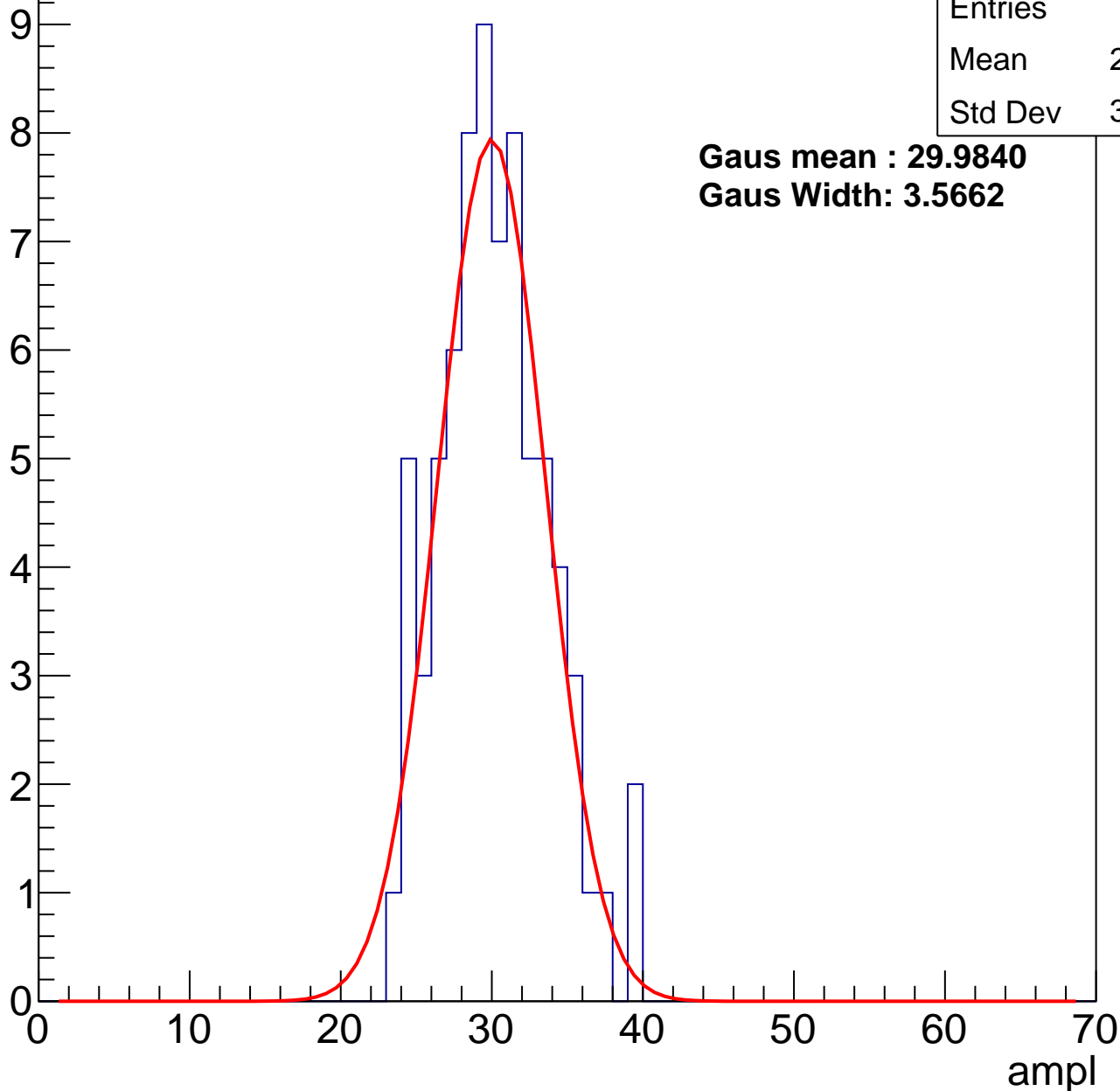
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	29.73
Std Dev	3.574

Gaus mean : 29.9840

Gaus Width: 3.5662



B1L003S, U21-ch53, adc1

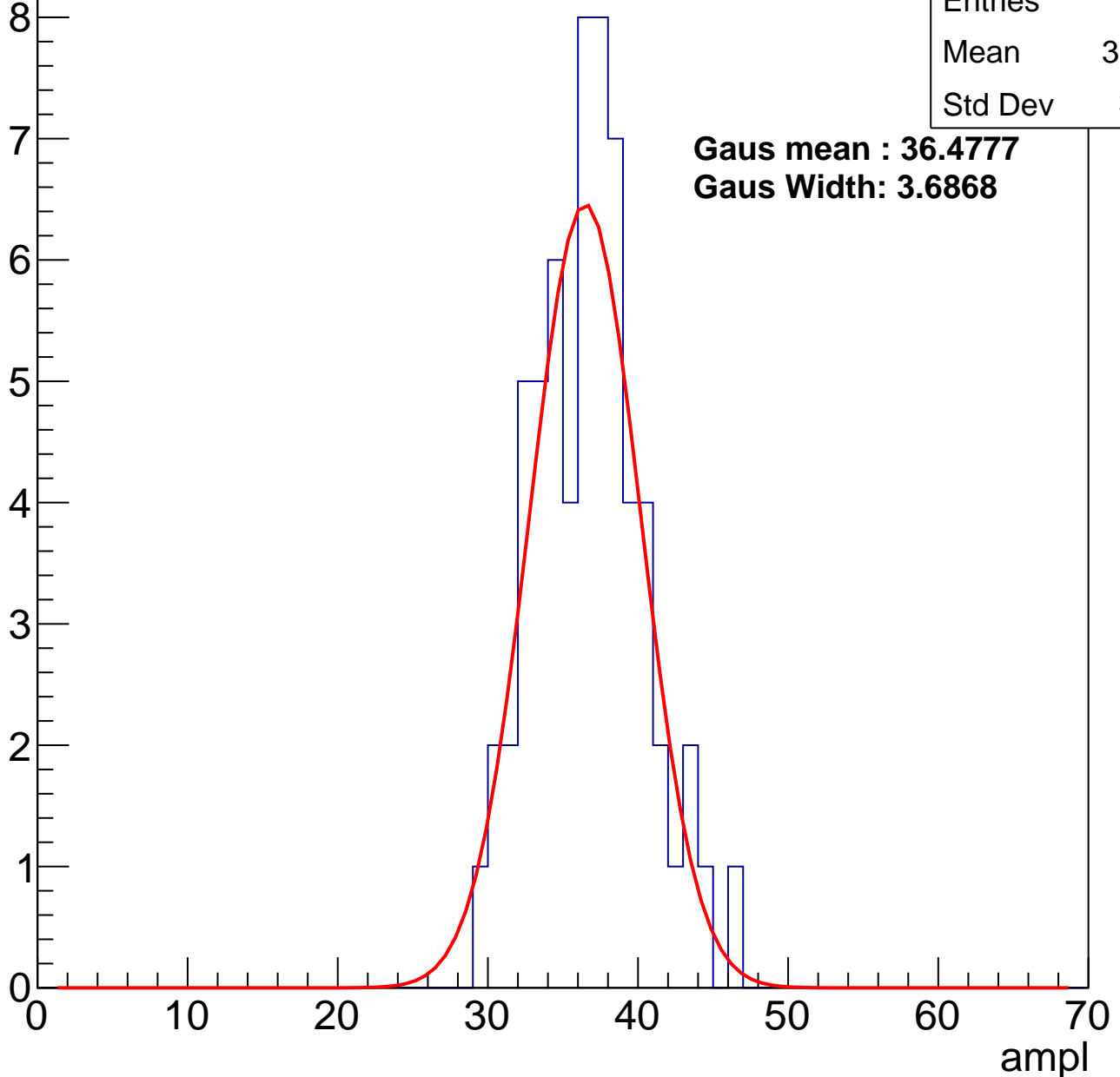
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	36.29
Std Dev	3.57

Gaus mean : 36.4777

Gaus Width: 3.6868



B1L003S, U21-ch53, adc2

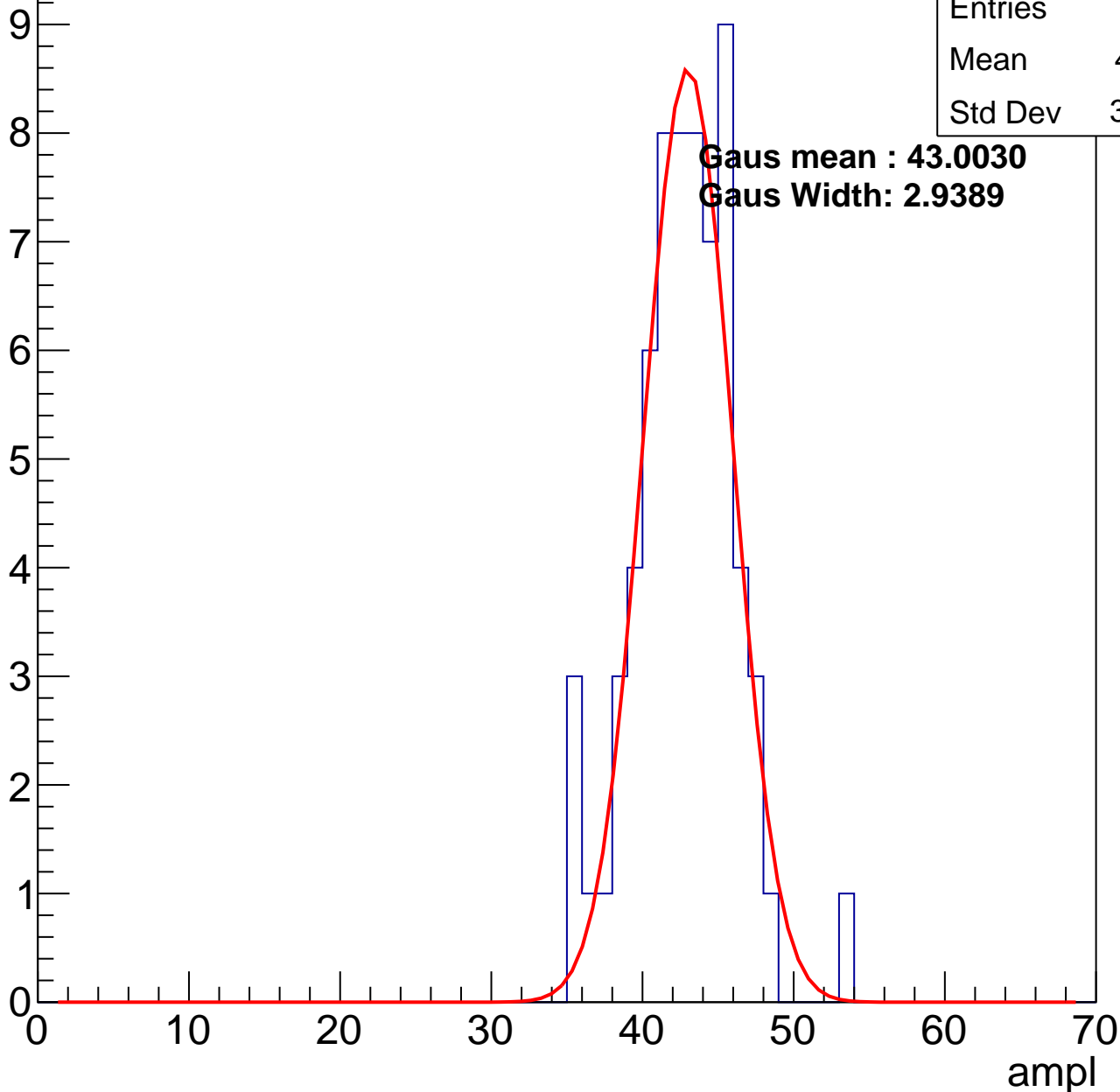
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	42.31
Std Dev	3.302

Gaus mean : 43.0030

Gaus Width: 2.9389

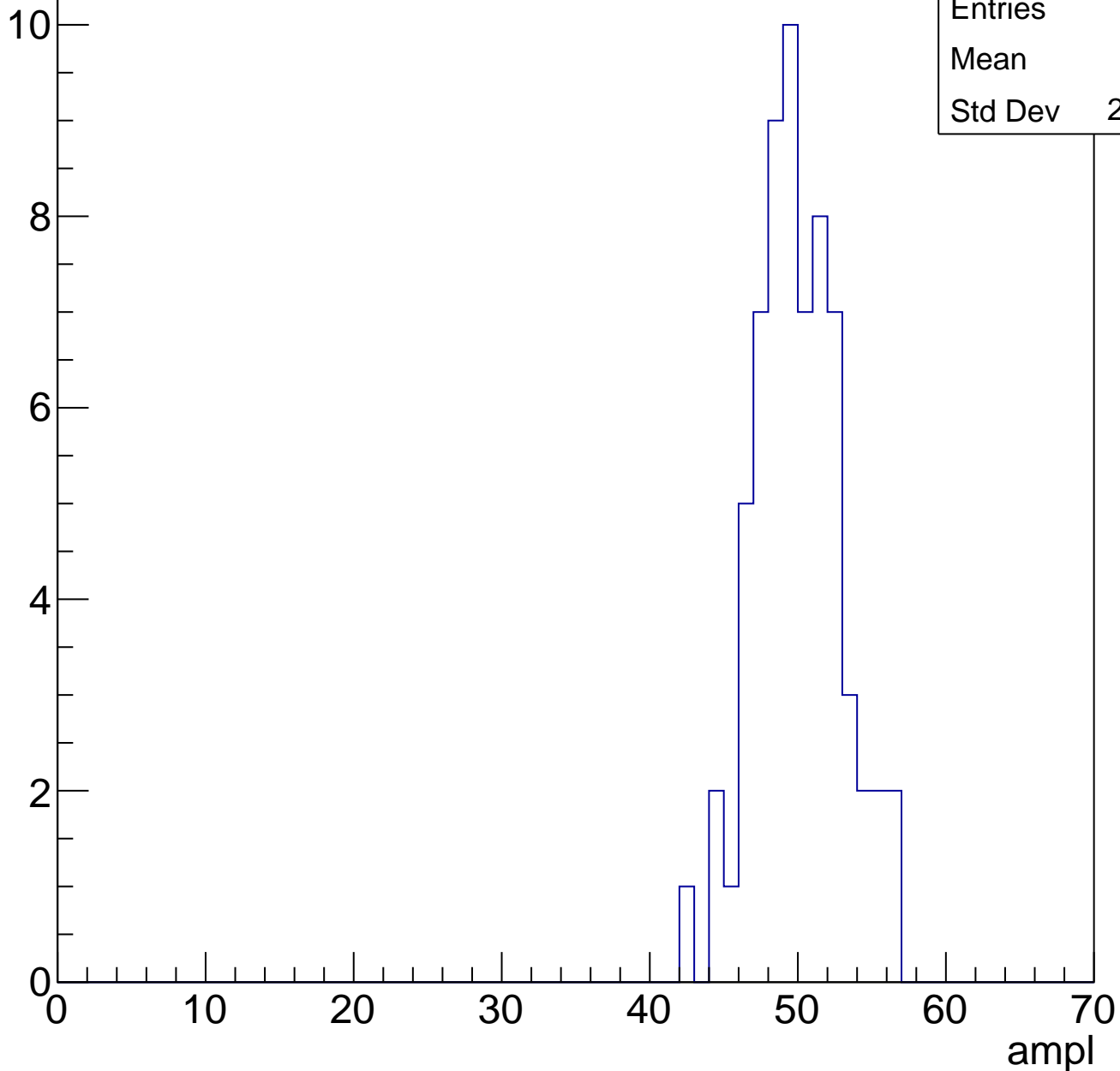


B1L003S, U21-ch53, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.5
Std Dev	2.888

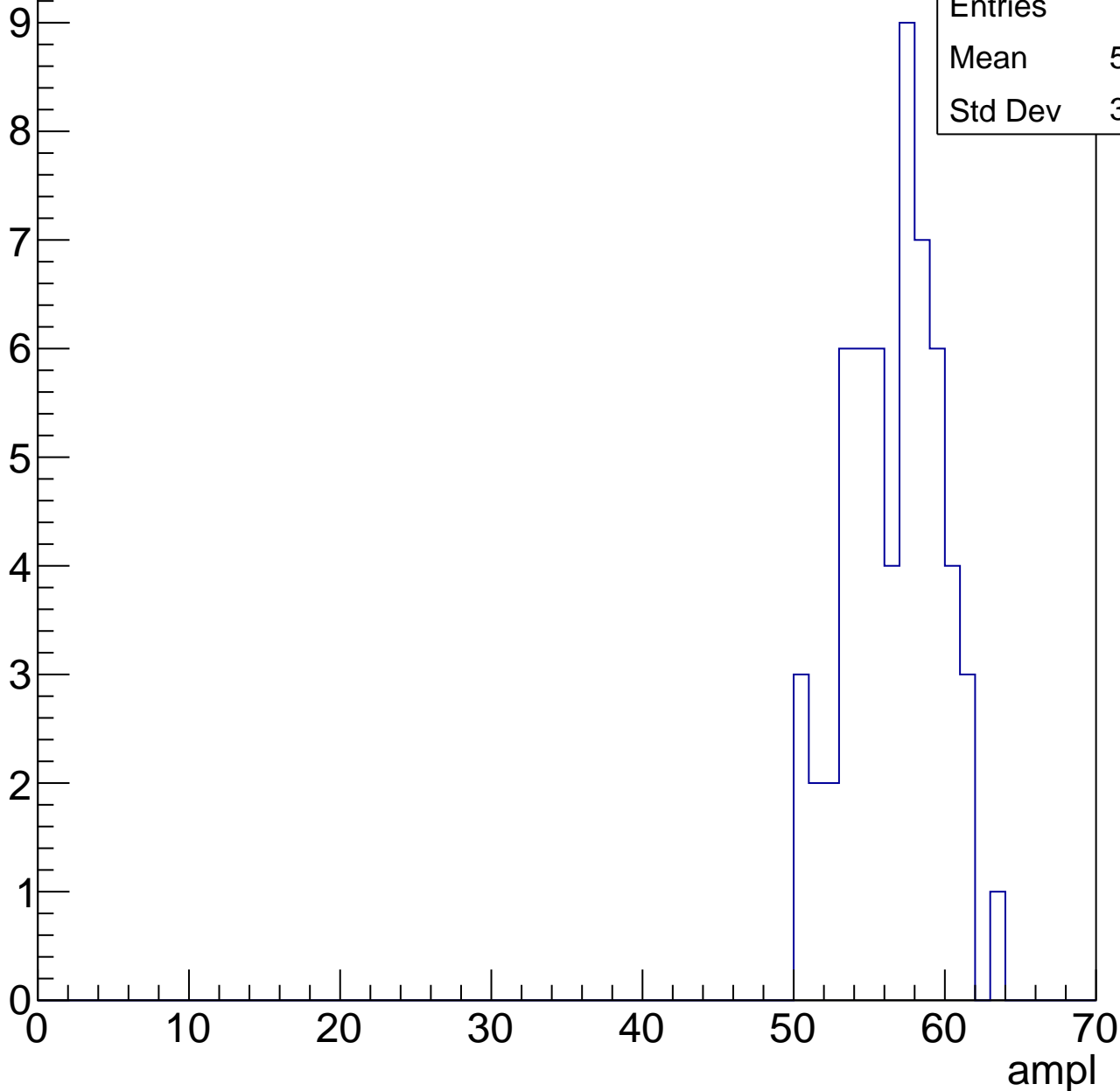


B1L003S, U21-ch53, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	56.12
Std Dev	3.059

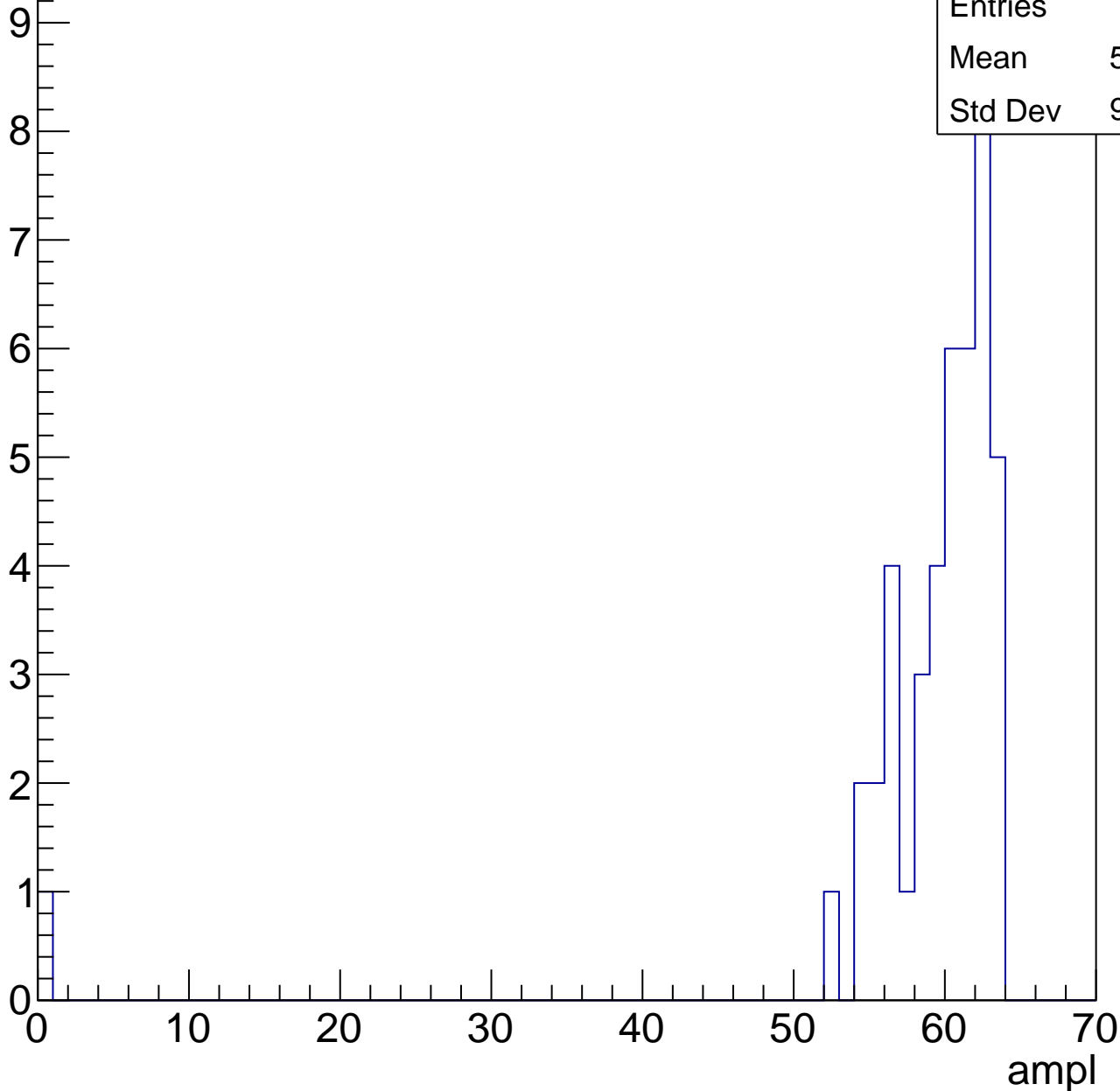


B1L003S, U21-ch53, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

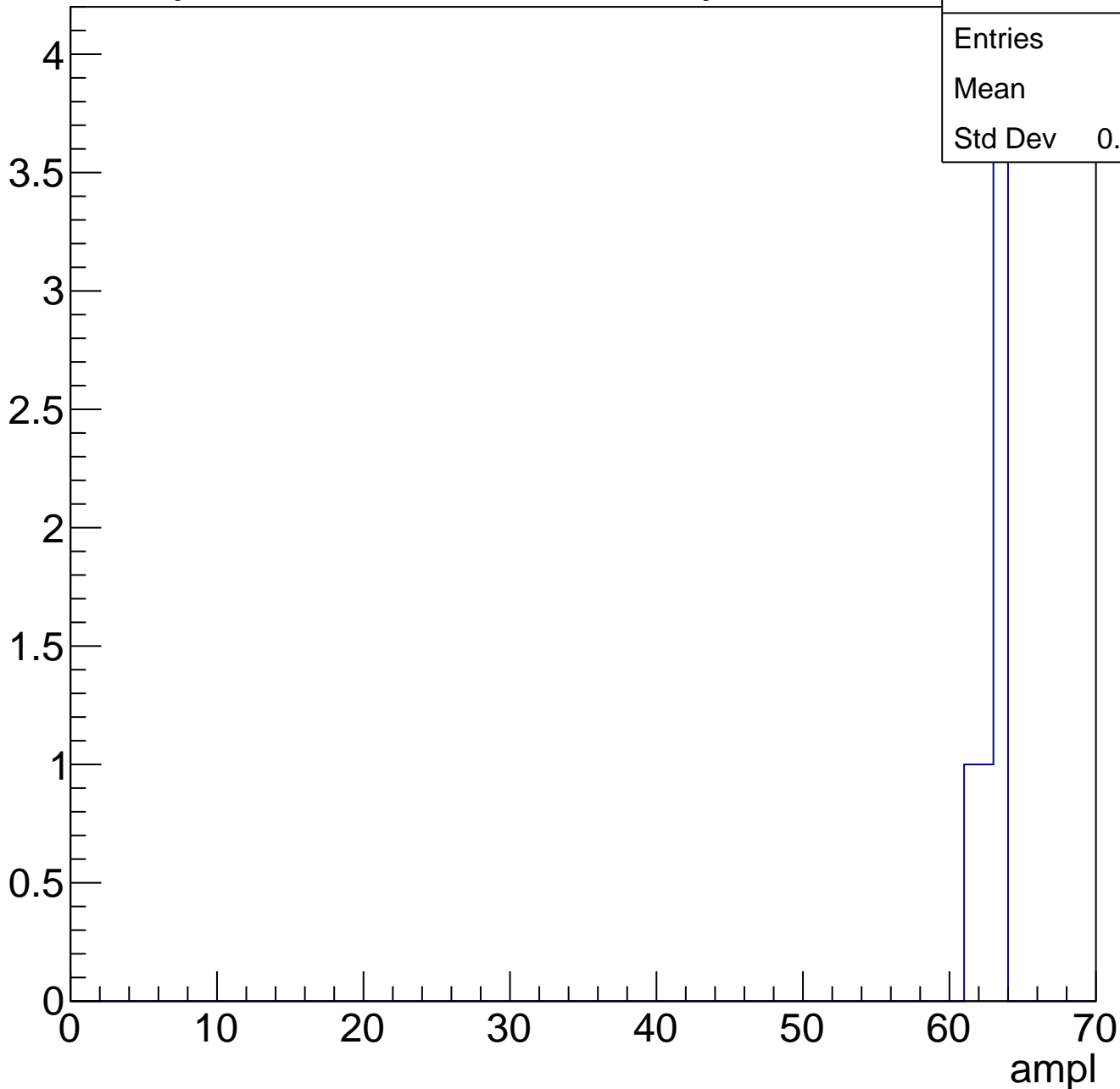
Entries	44
Mean	58.18
Std Dev	9.313



B1L003S, U21-ch53, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch53, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	21
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch54, adc0

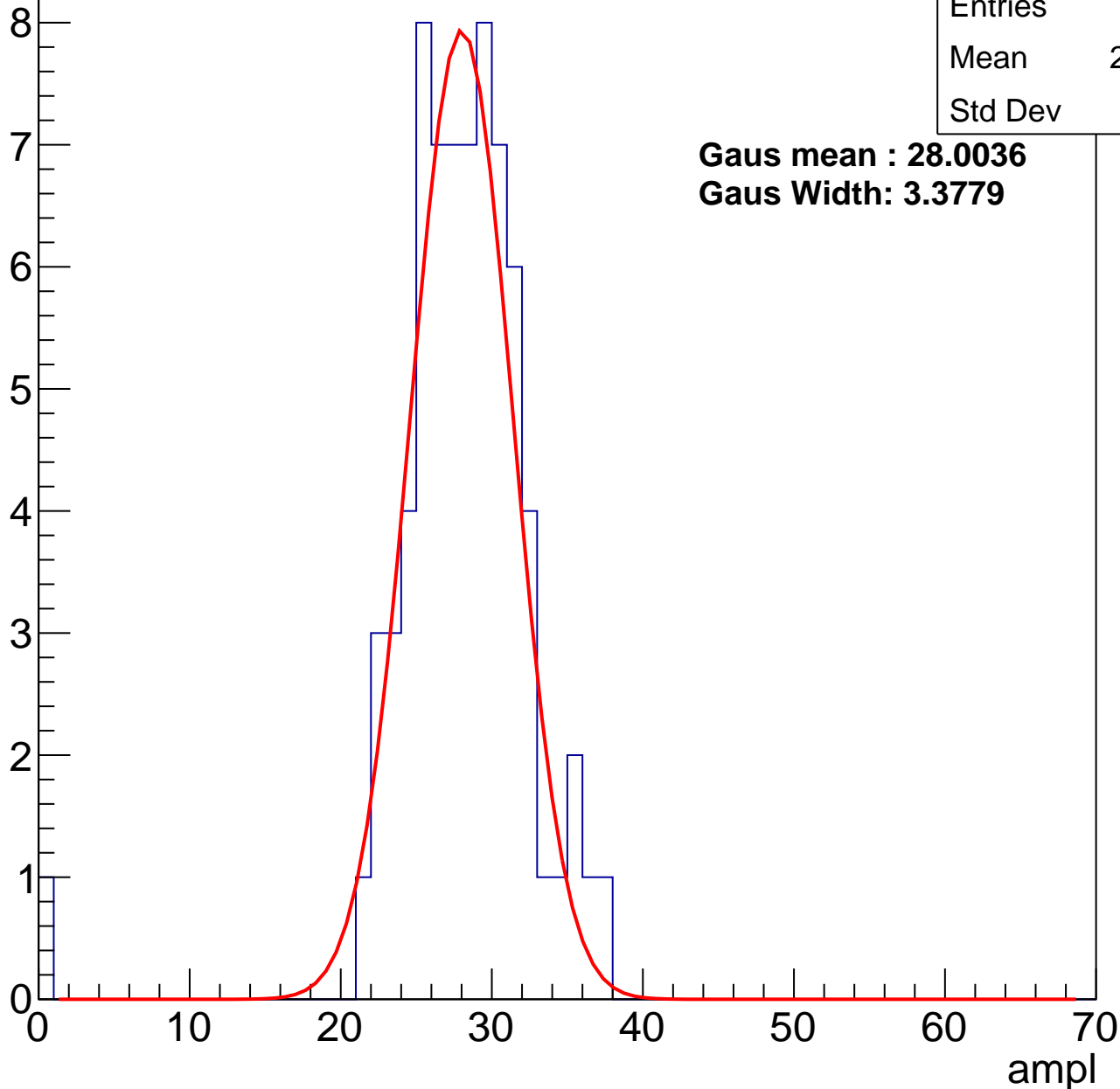
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	27.57
Std Dev	4.76

Gaus mean : 28.0036

Gaus Width: 3.3779



B1L003S, U21-ch54, adc1

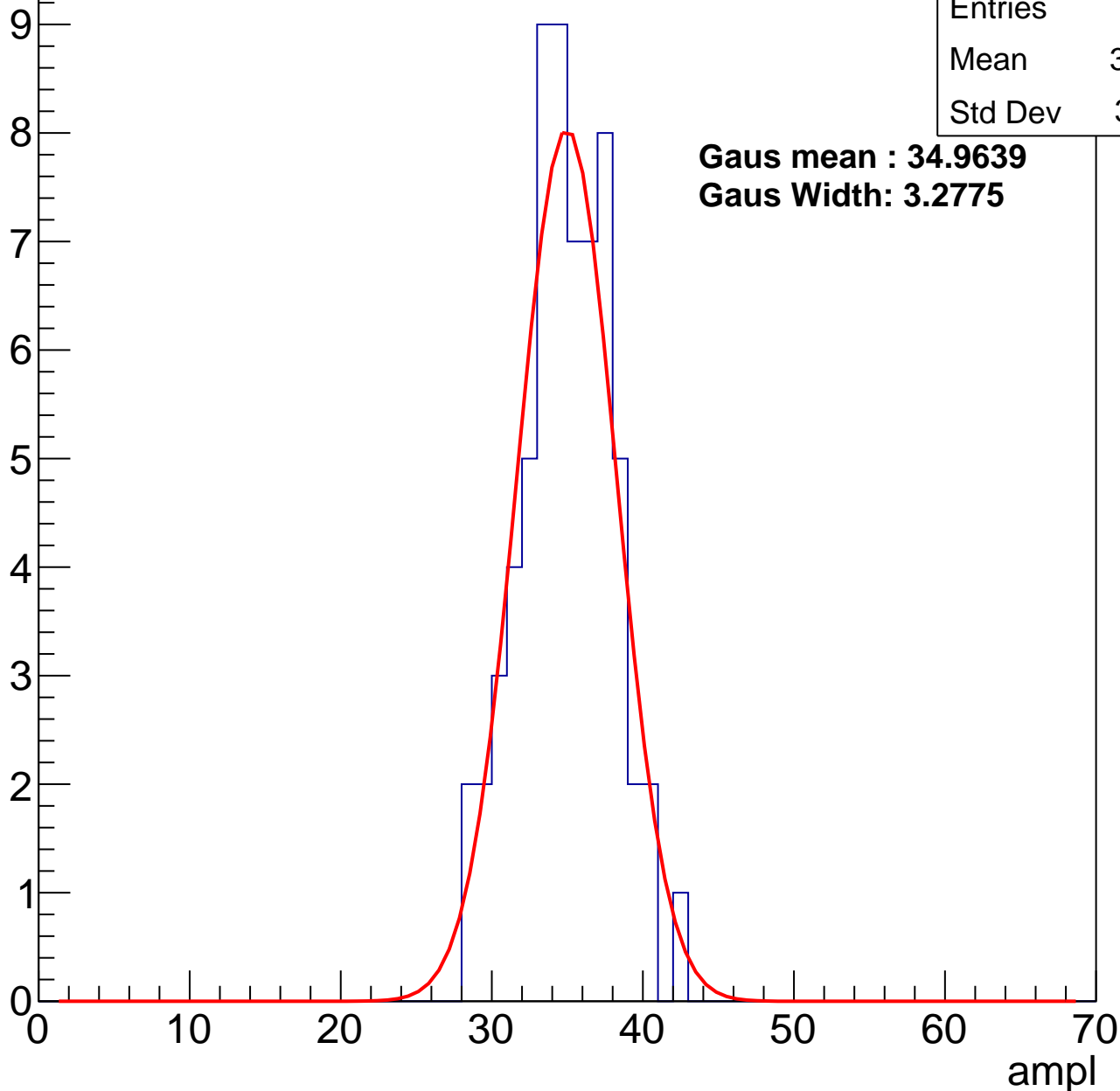
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	34.45
Std Dev	3.001

Gaus mean : 34.9639

Gaus Width: 3.2775



B1L003S, U21-ch54, adc2

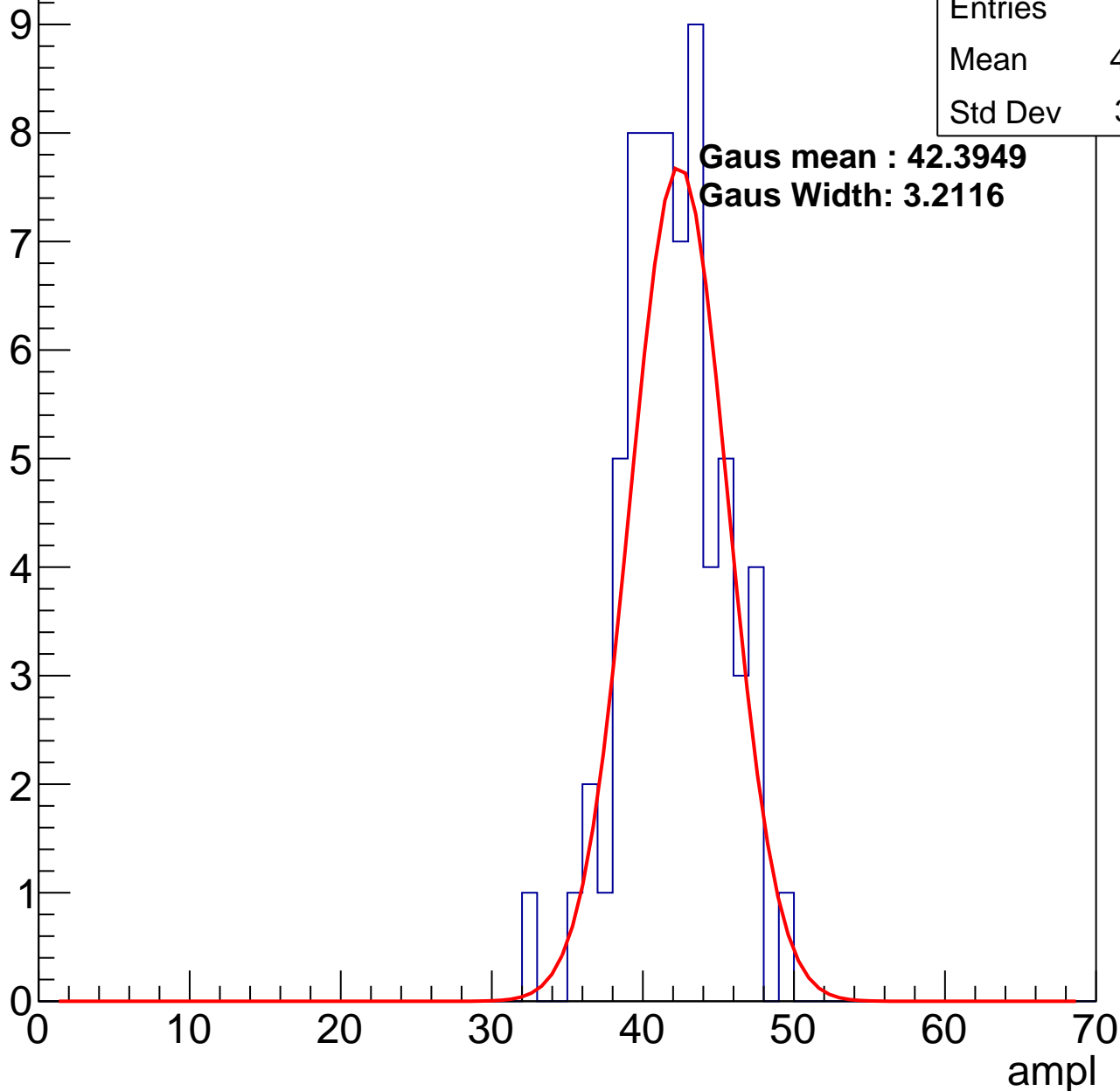
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.54
Std Dev	3.211

Gaus mean : 42.3949

Gaus Width: 3.2116

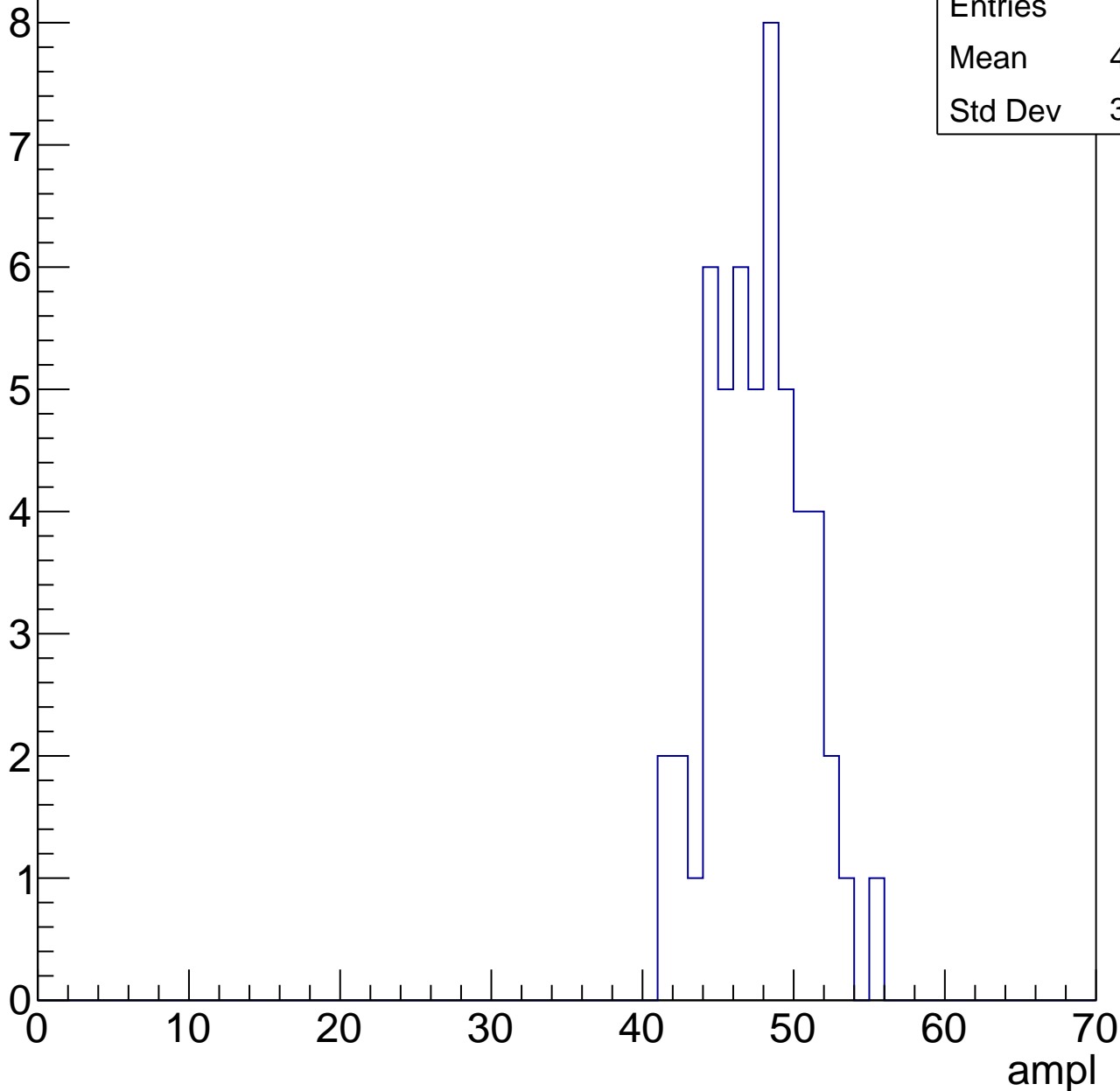


B1L003S, U21-ch54, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	47.19
Std Dev	3.082

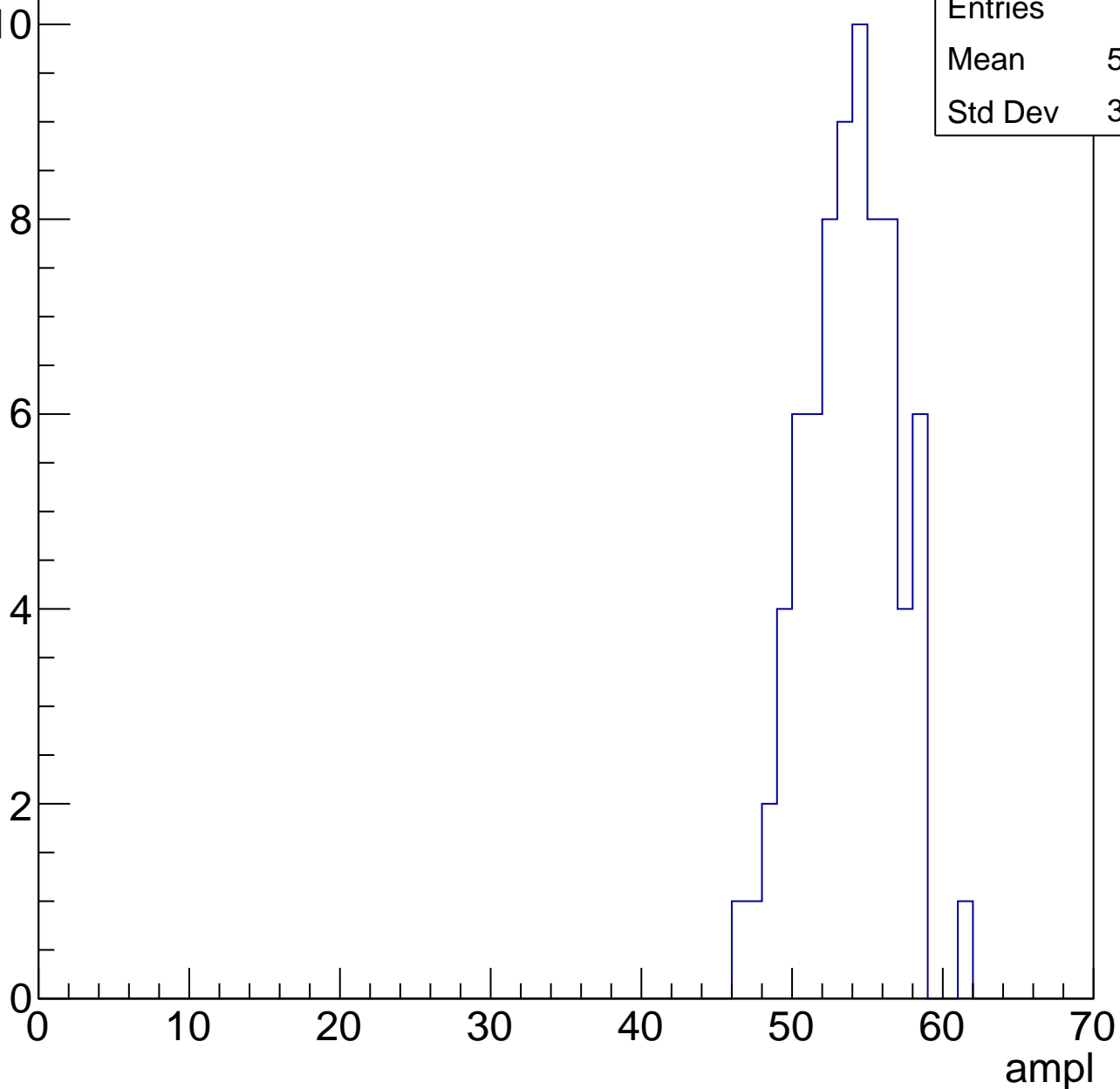


B1L003S, U21-ch54, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

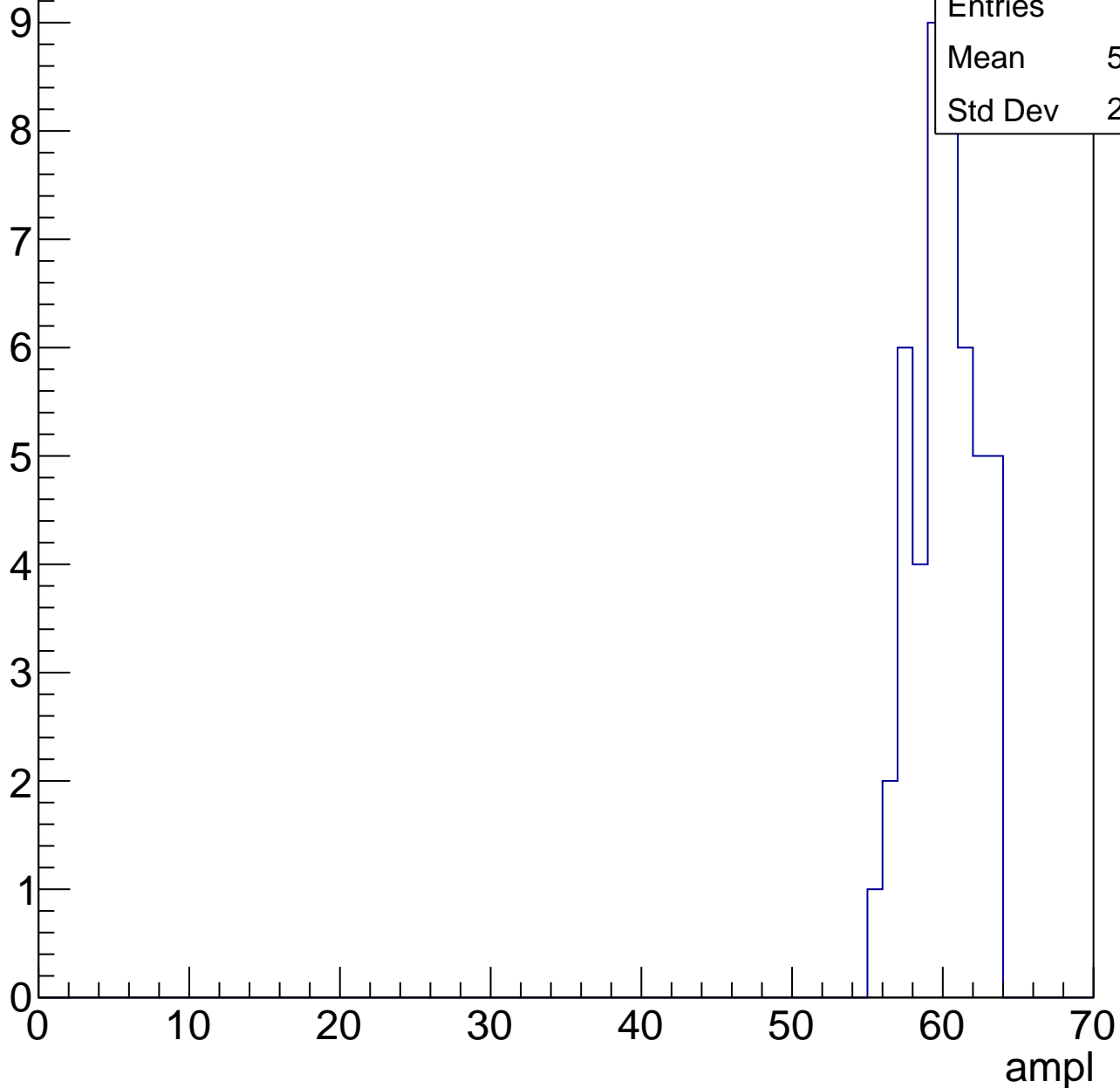
Entries	74
Mean	53.36
Std Dev	3.003



B1L003S, U21-ch54, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



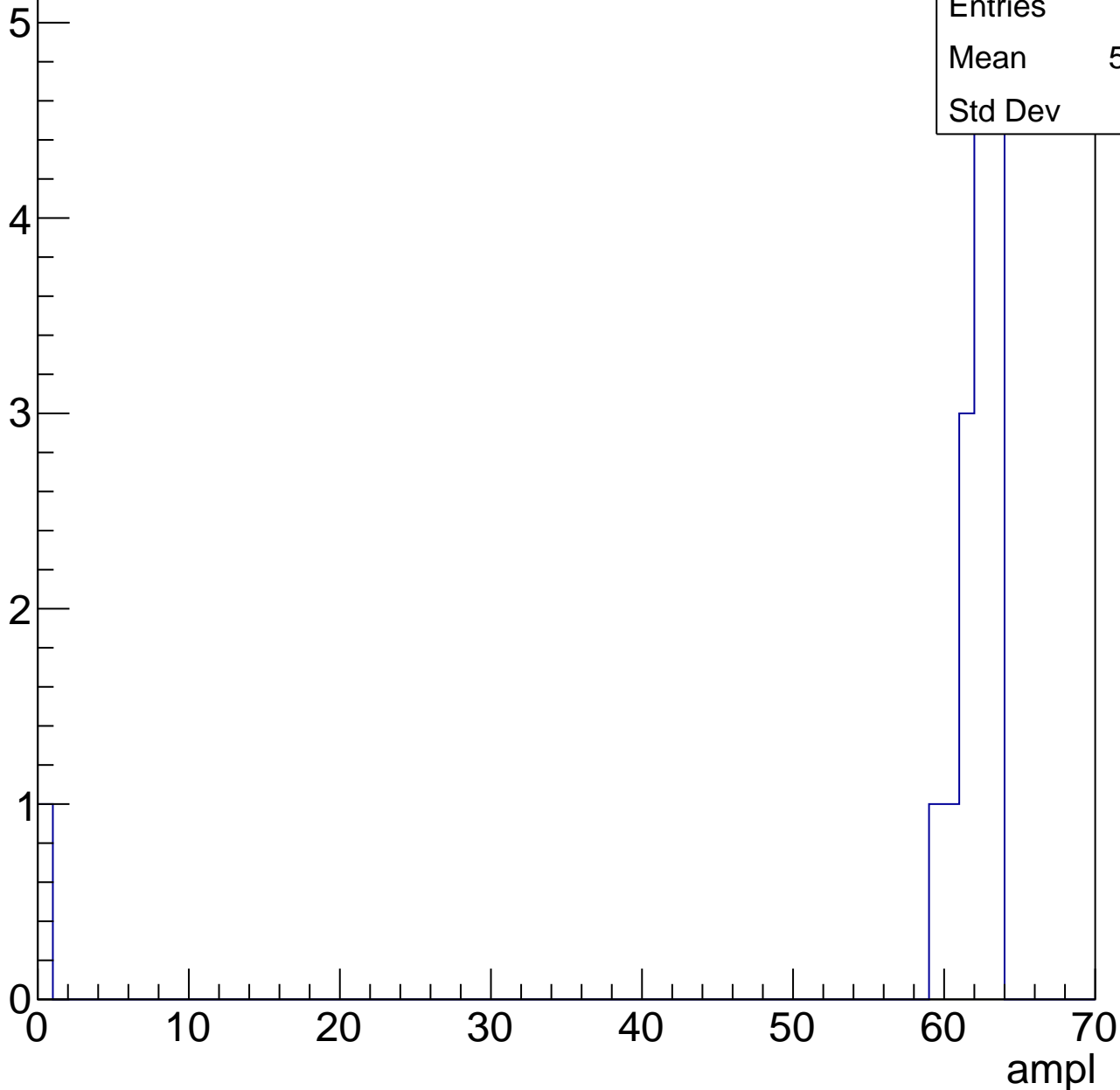
Entries	47
Mean	59.64
Std Dev	2.067

B1L003S, U21-ch54, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.94
Std Dev	15



B1L003S, U21-ch54, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch55, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	30.04
Std Dev	3.399

Gaus mean : 30.6459

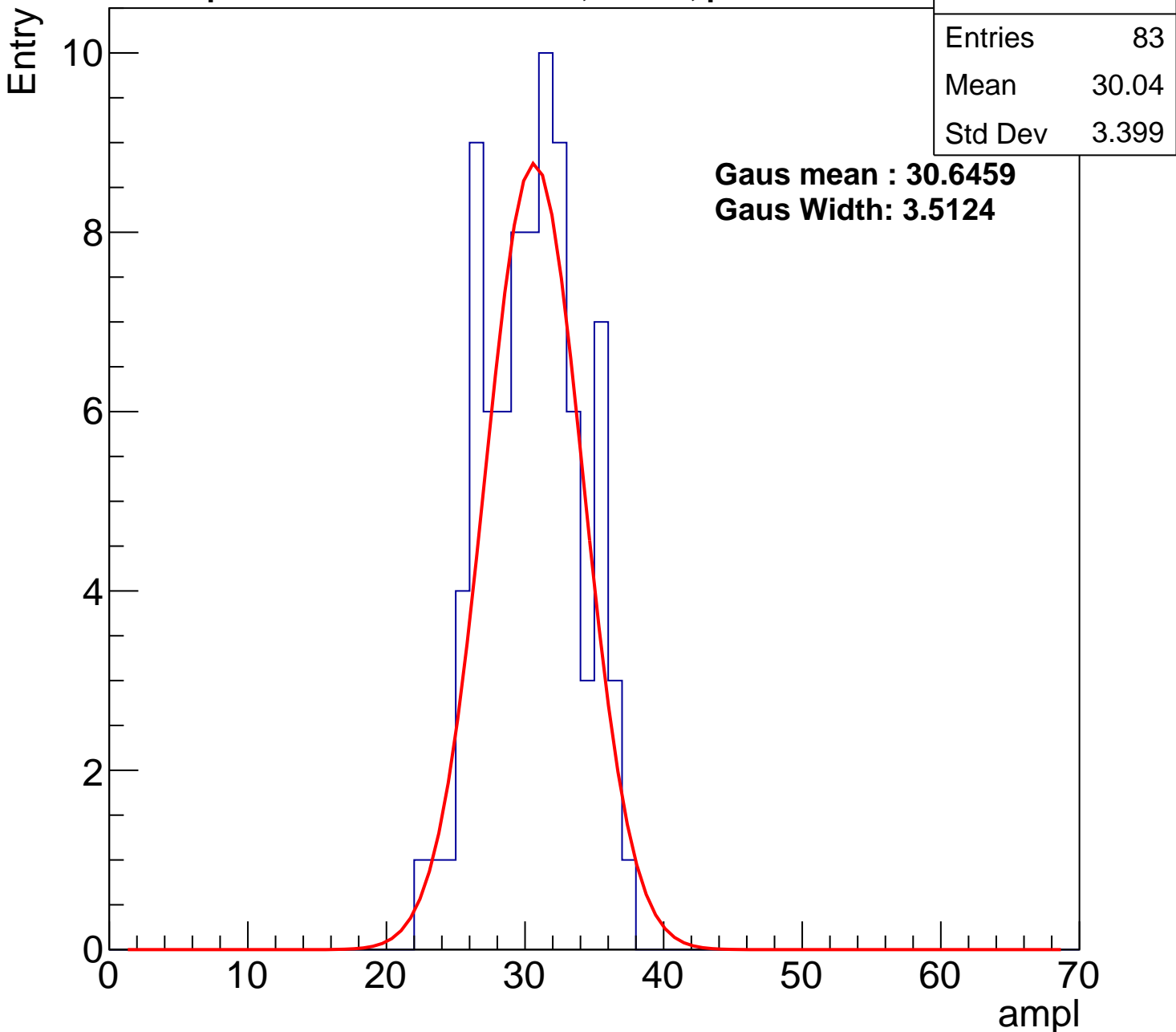
Gaus Width: 3.5124

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



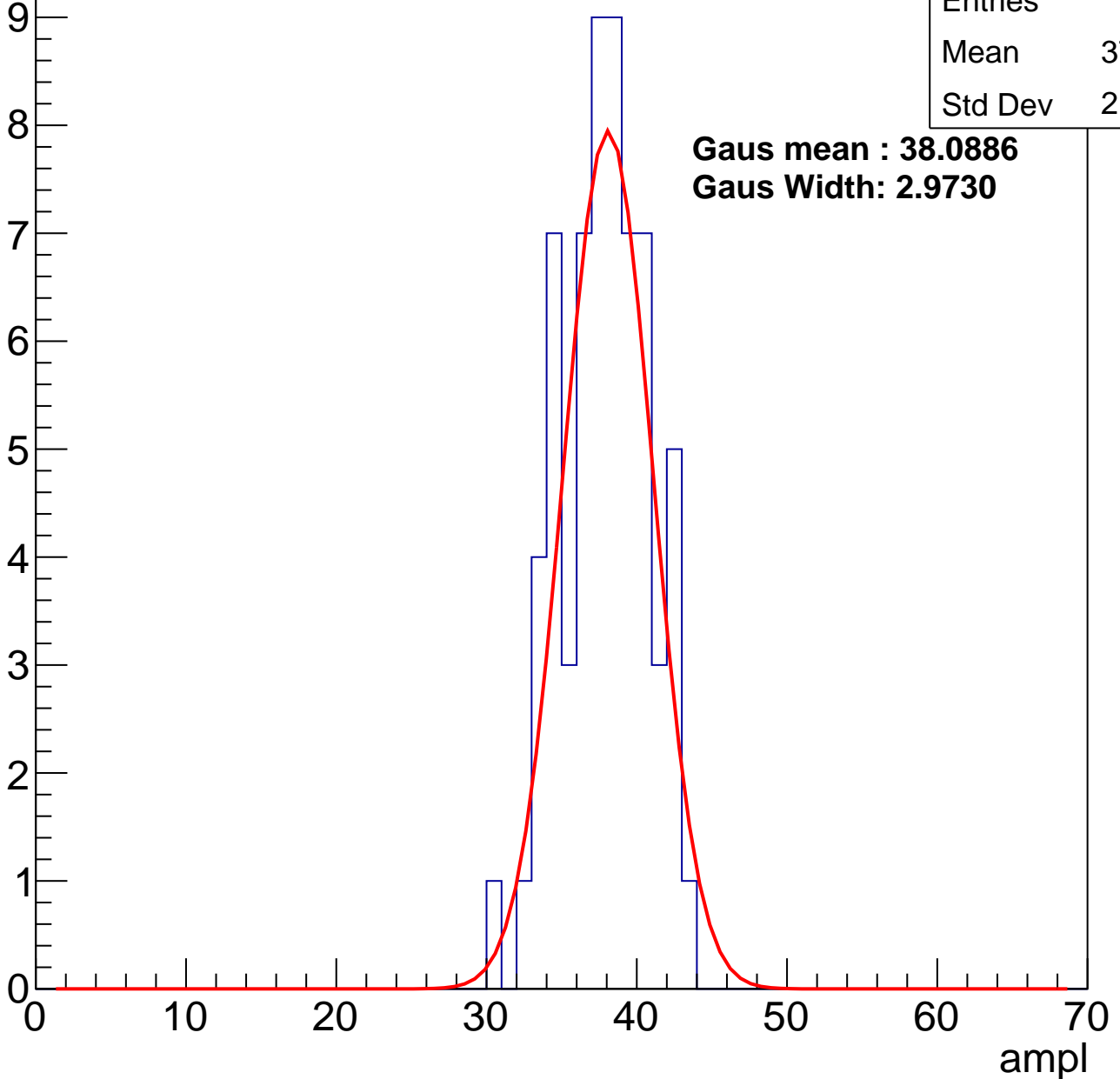
B1L003S, U21-ch55, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	37.39
Std Dev	2.848

Gaus mean : 38.0886
Gaus Width: 2.9730



B1L003S, U21-ch55, adc2

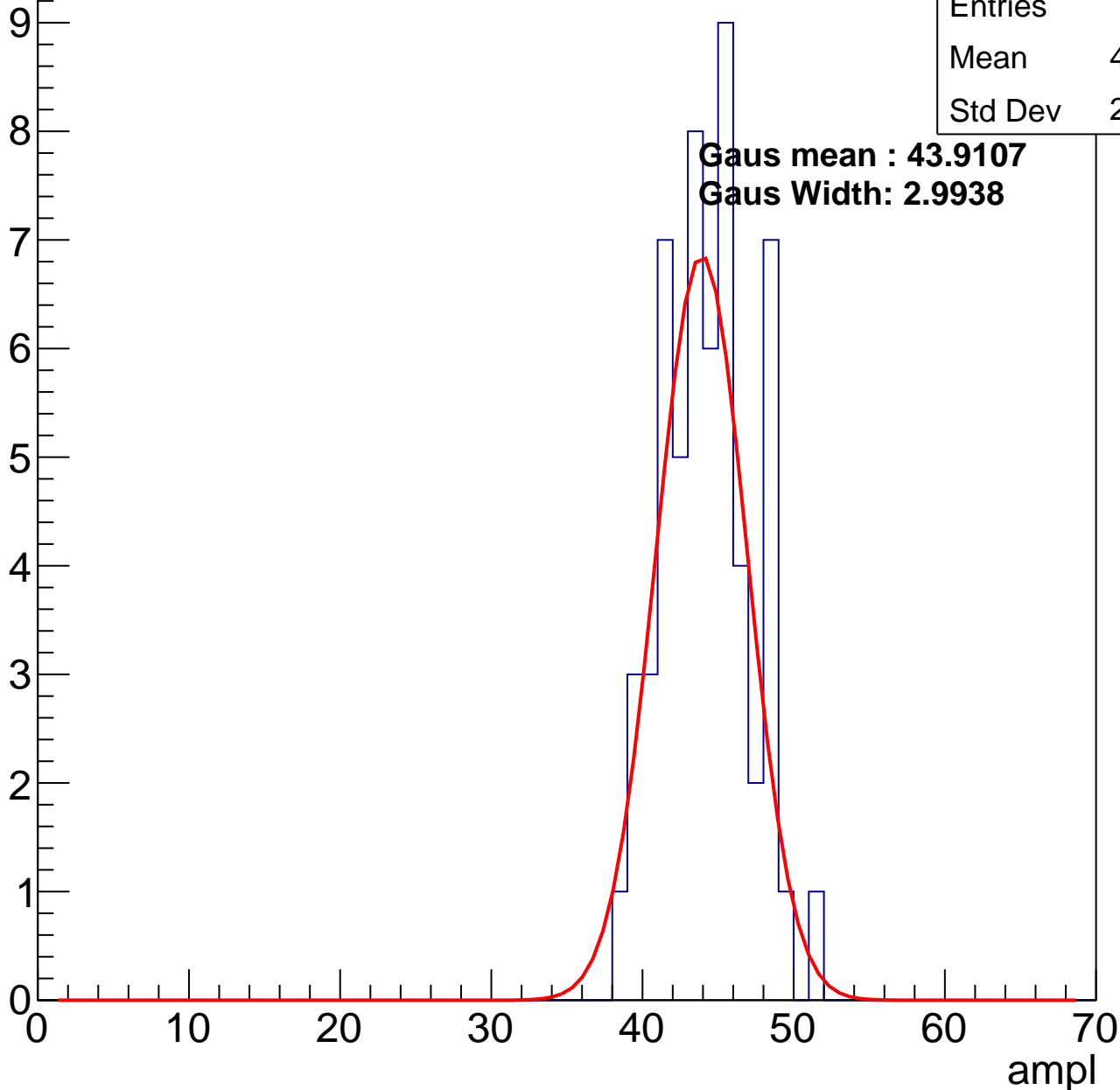
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	43.84
Std Dev	2.895

Gaus mean : 43.9107

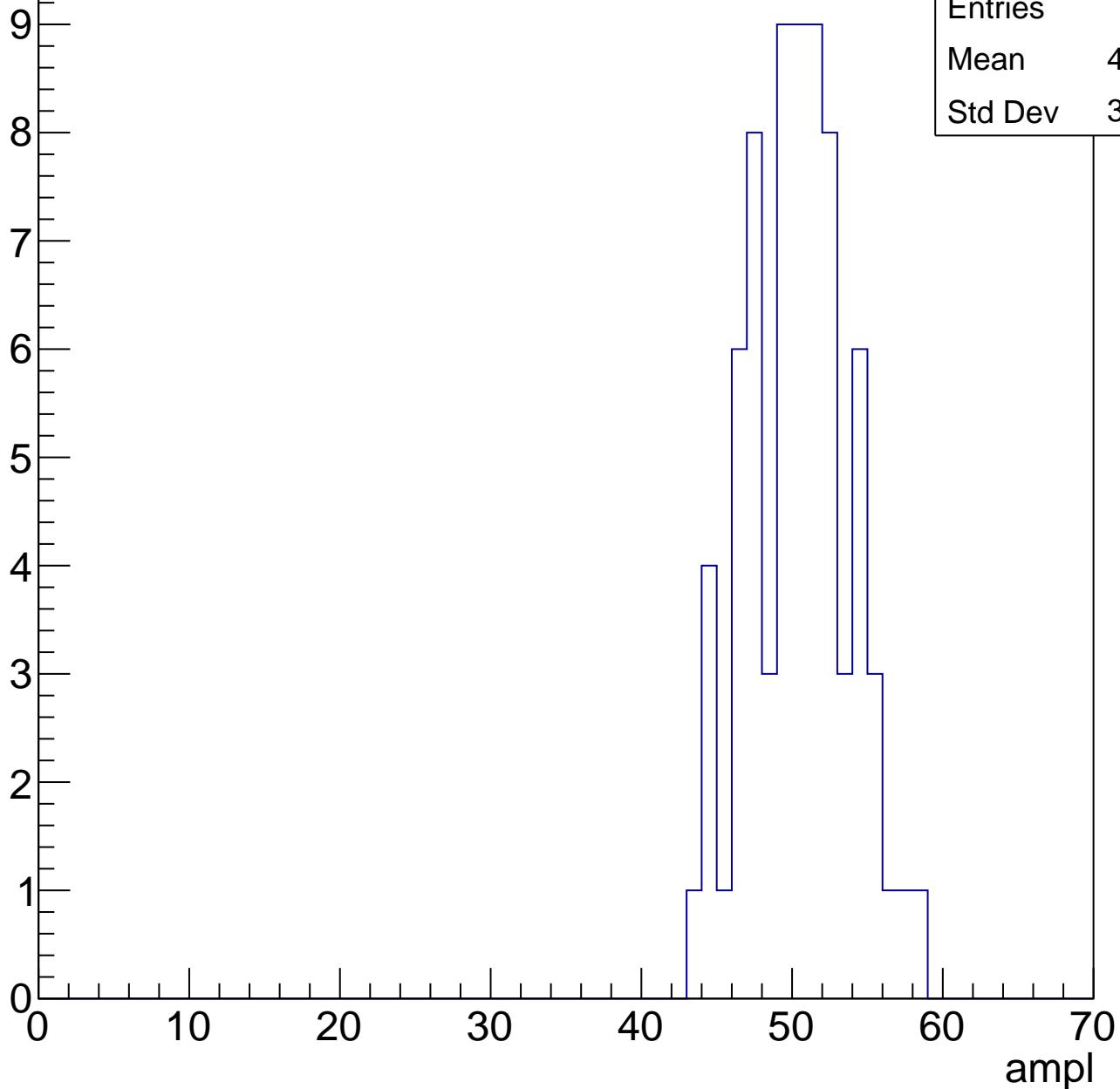
Gaus Width: 2.9938



B1L003S, U21-ch55, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

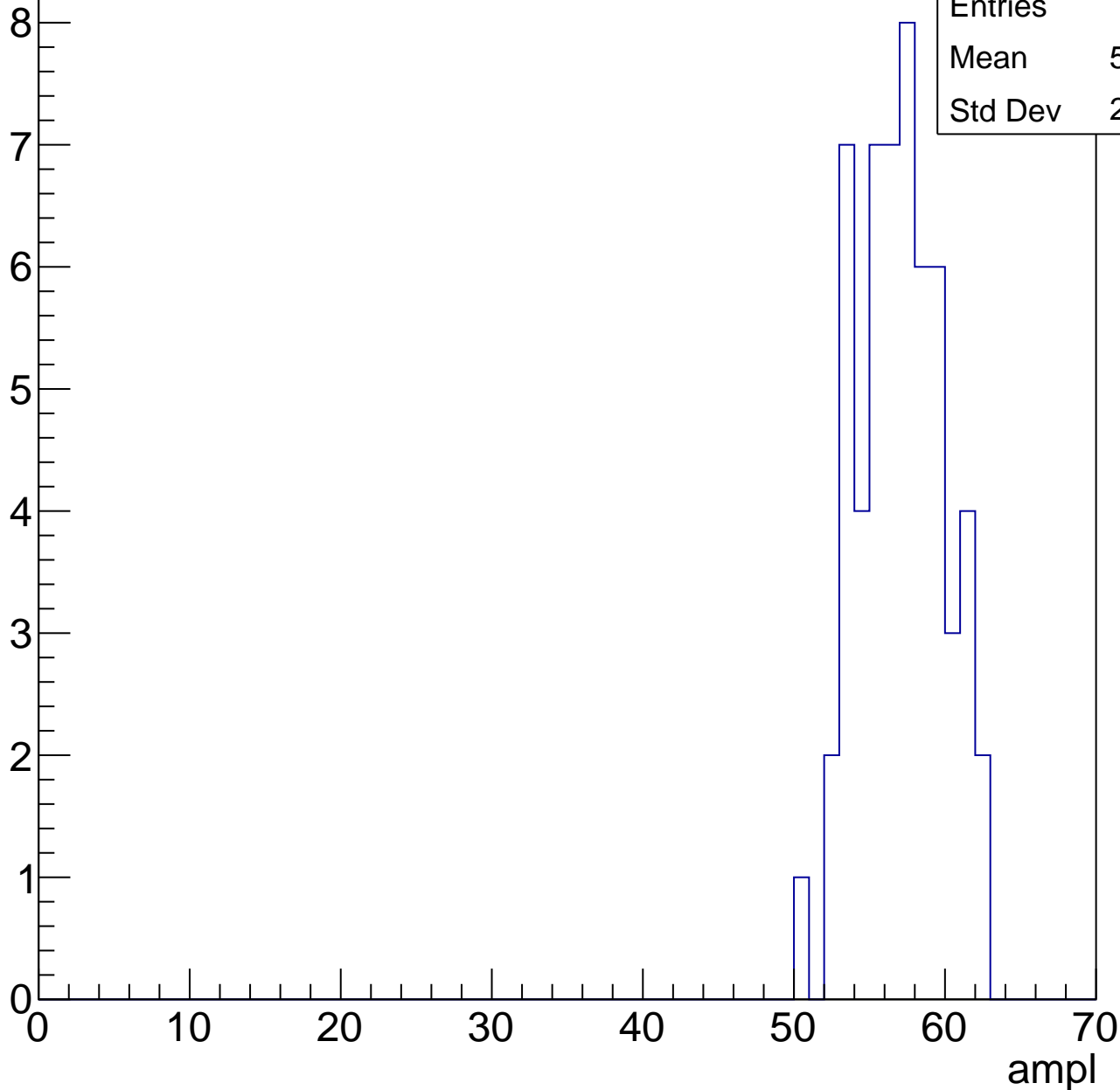


B1L003S, U21-ch55, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	56.56
Std Dev	2.785



B1L003S, U21-ch55, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

7

6

5

4

3

2

1

0

Entries 36

Mean 60.53

Std Dev 1.993

ampl

0

10

20

30

40

50

60

70

B1L003S, U21-ch55, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	6
Mean	52
Std Dev	23.27

B1L003S, U21-ch55, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch56, adc0

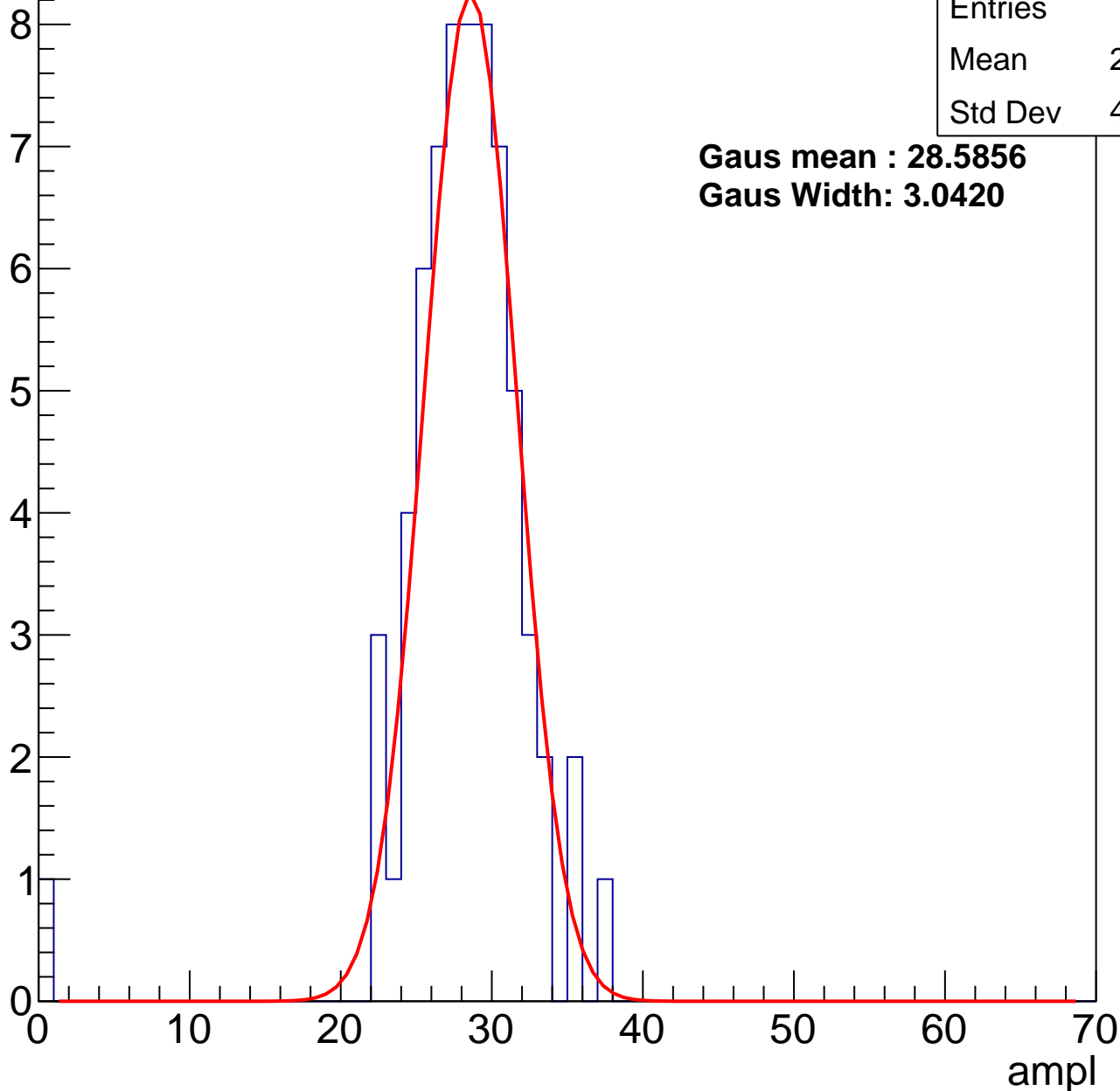
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.62
Std Dev	4.638

Gaus mean : 28.5856

Gaus Width: 3.0420



B1L003S, U21-ch56, adc1

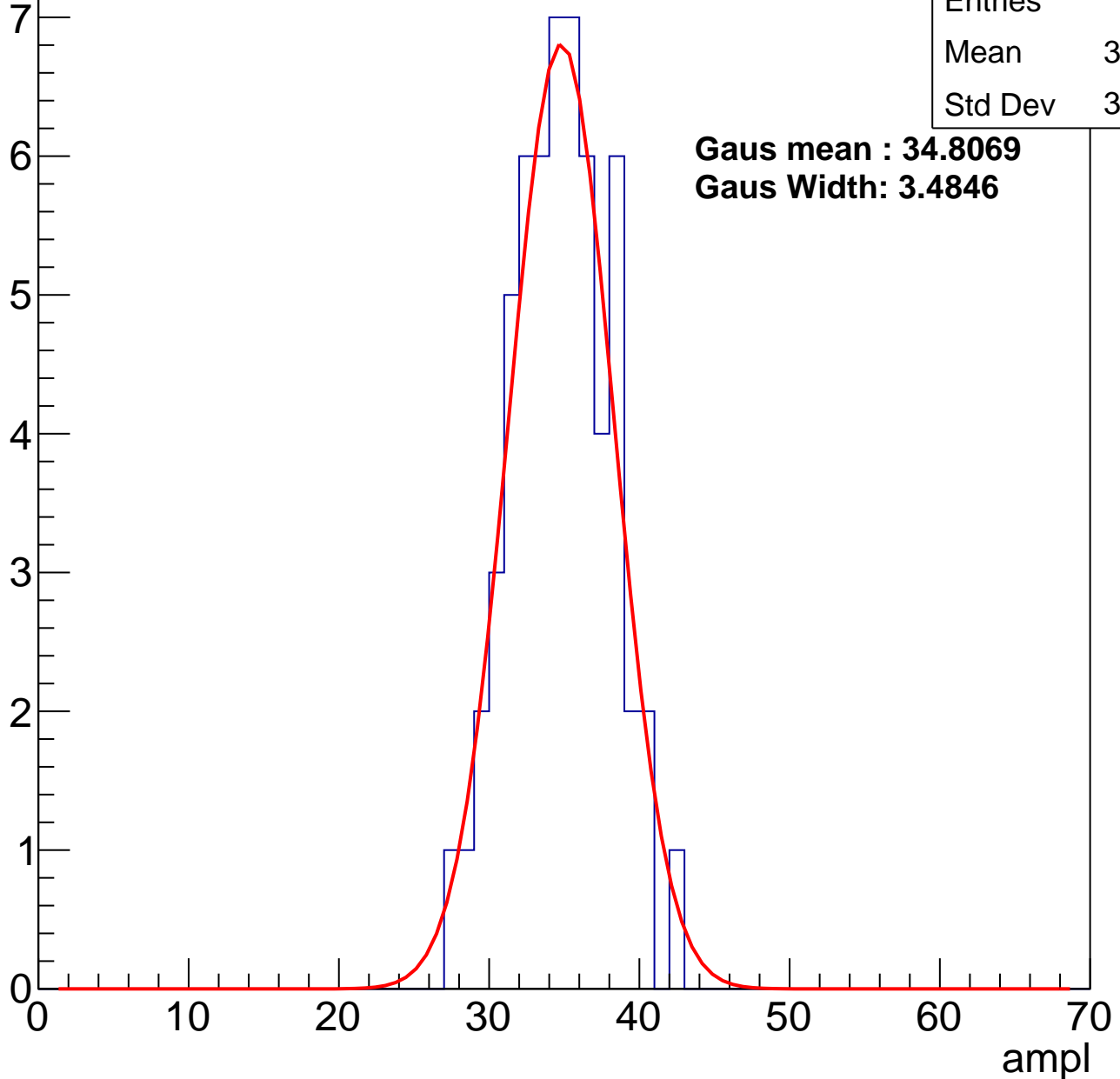
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	34.29
Std Dev	3.195

Gaus mean : 34.8069

Gaus Width: 3.4846



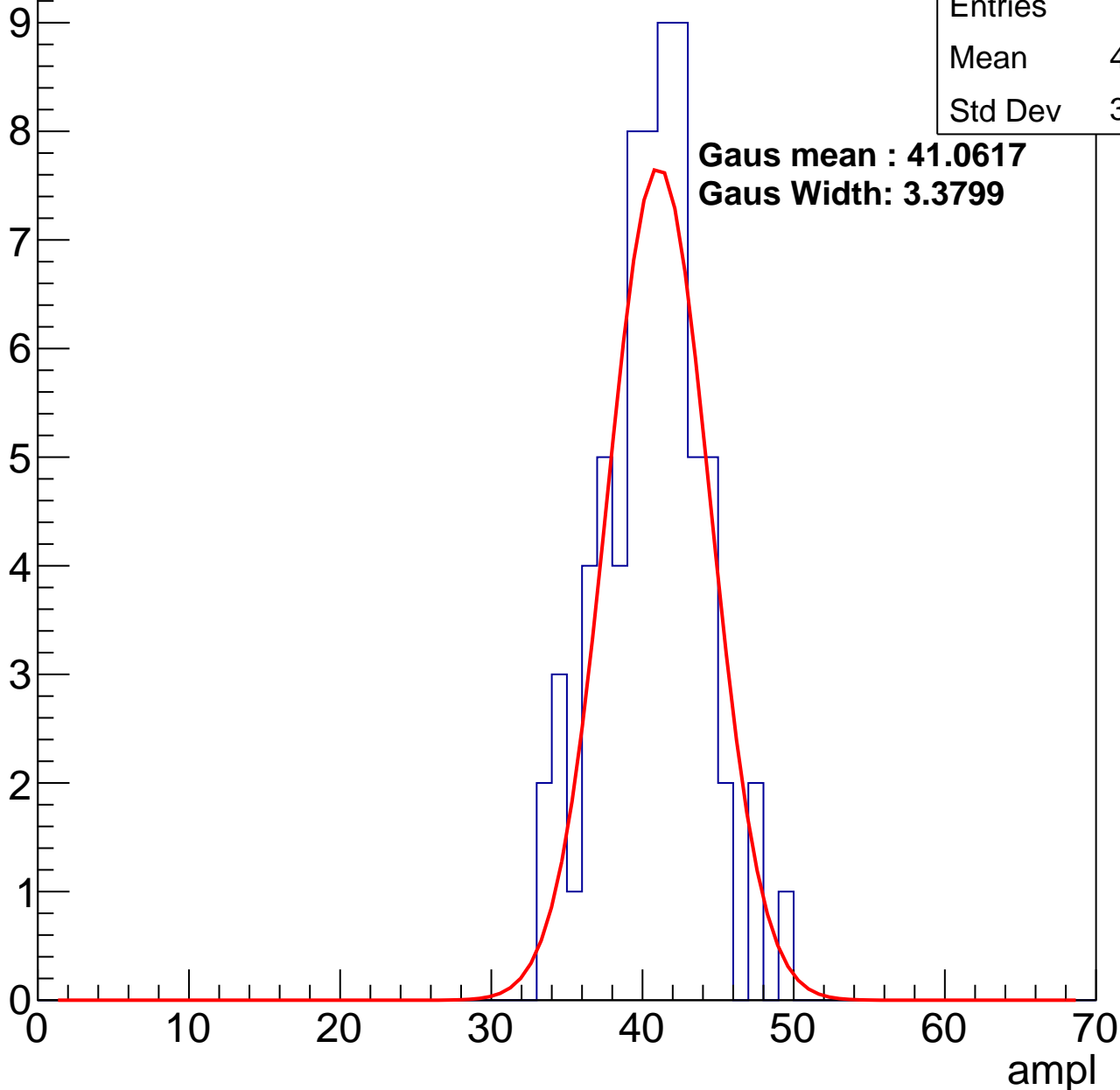
B1L003S, U21-ch56, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	40.16
Std Dev	3.346

Gaus mean : 41.0617
Gaus Width: 3.3799

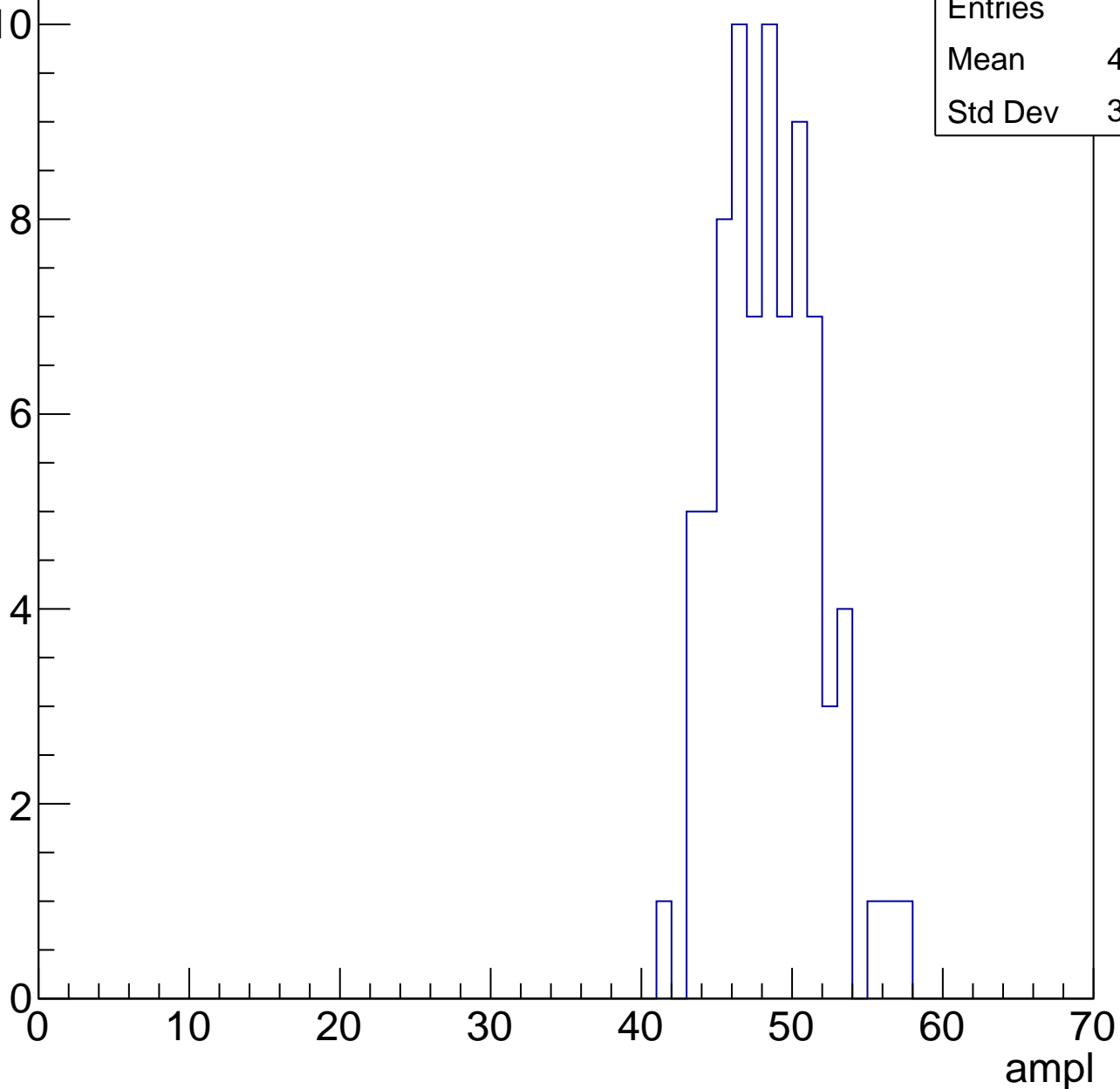


B1L003S, U21-ch56, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	79
Mean	47.99
Std Dev	3.224

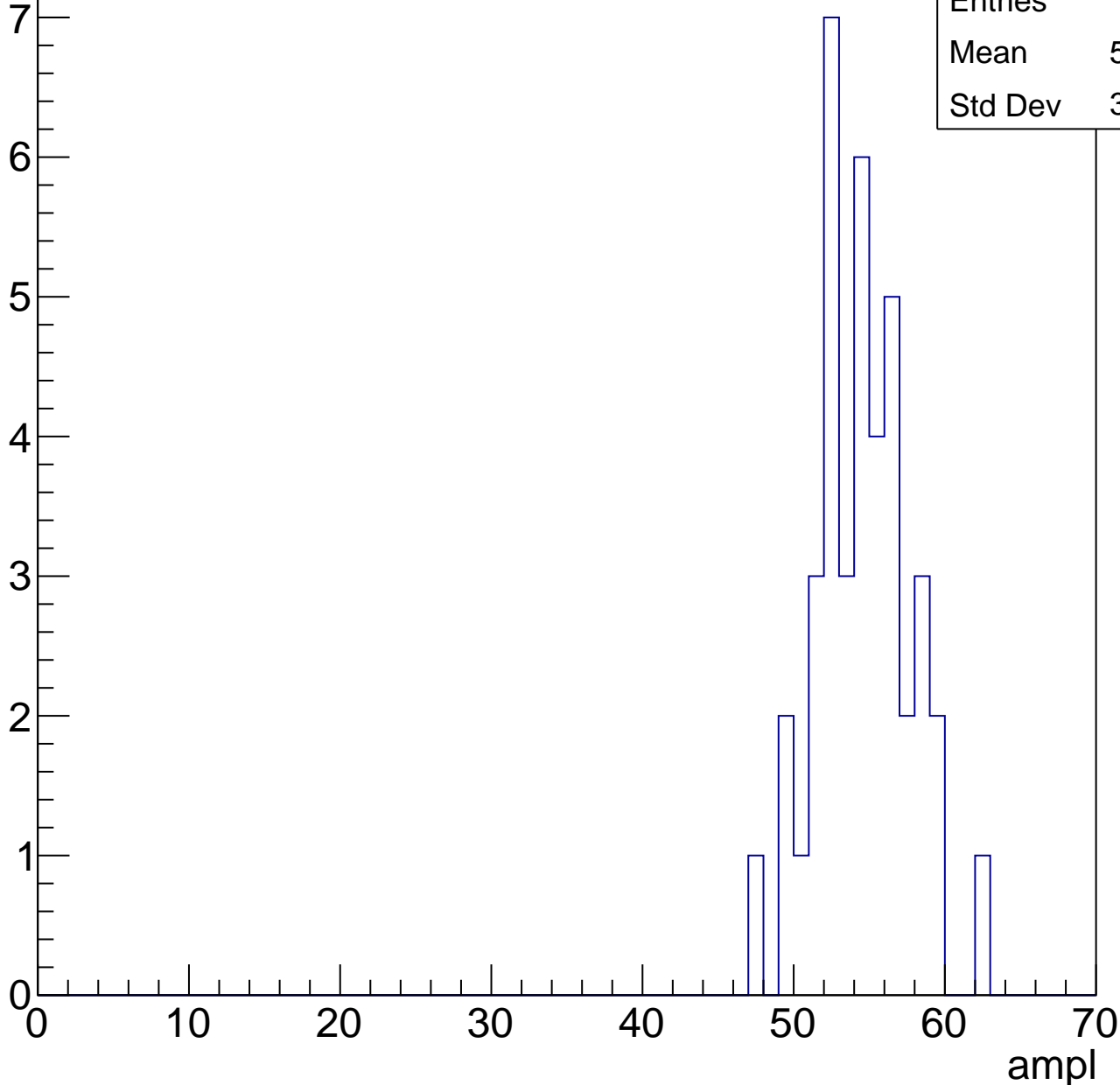


B1L003S, U21-ch56, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	40
Mean	54.08
Std Dev	3.069



B1L003S, U21-ch56, adc5

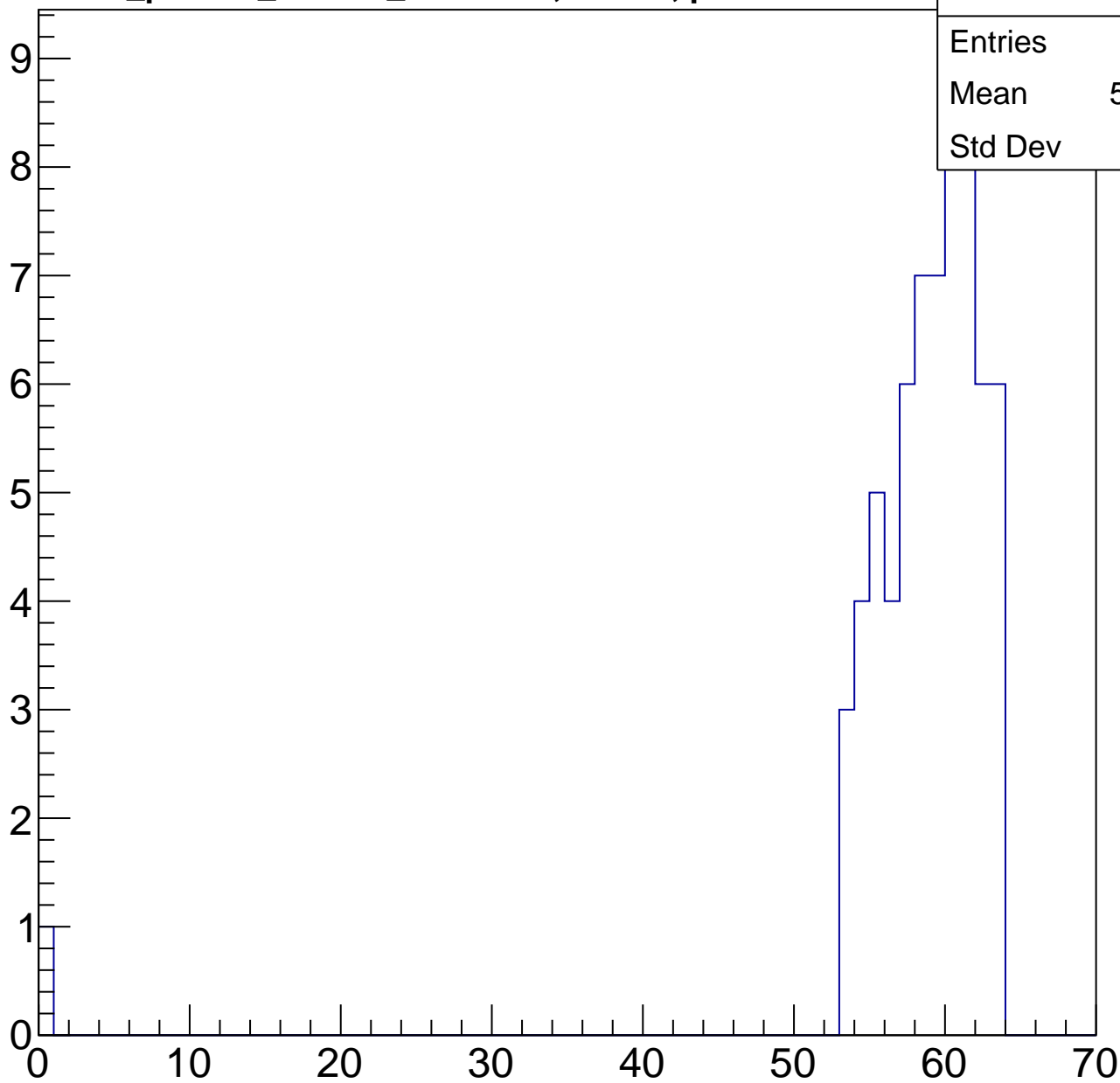
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	66
Mean	57.79
Std Dev	7.72

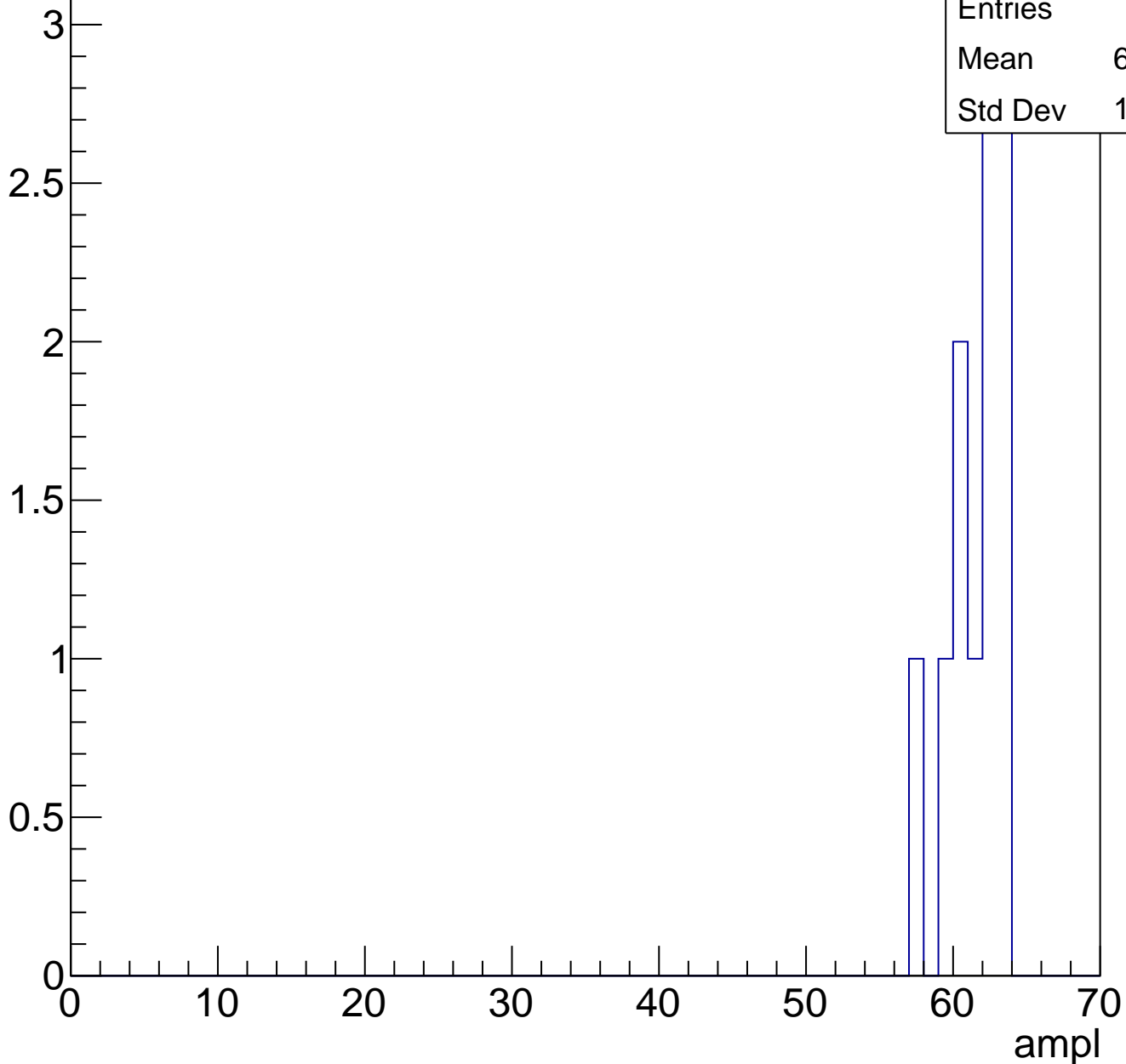
ampl



B1L003S, U21-ch56, adc6

calib_packv5_042523_0143.root, FC#13, port D2

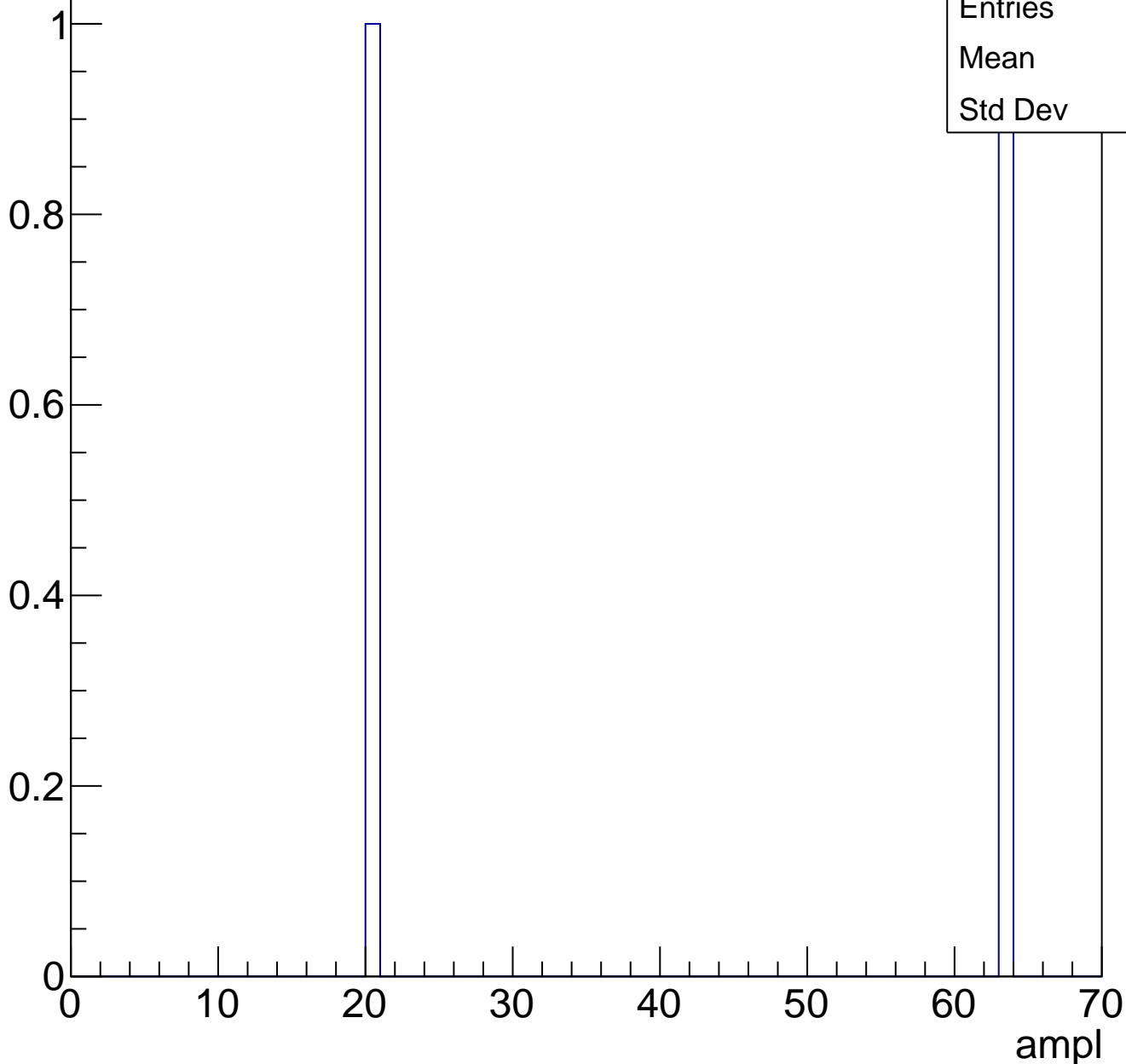
Entry



B1L003S, U21-ch56, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch57, adc0

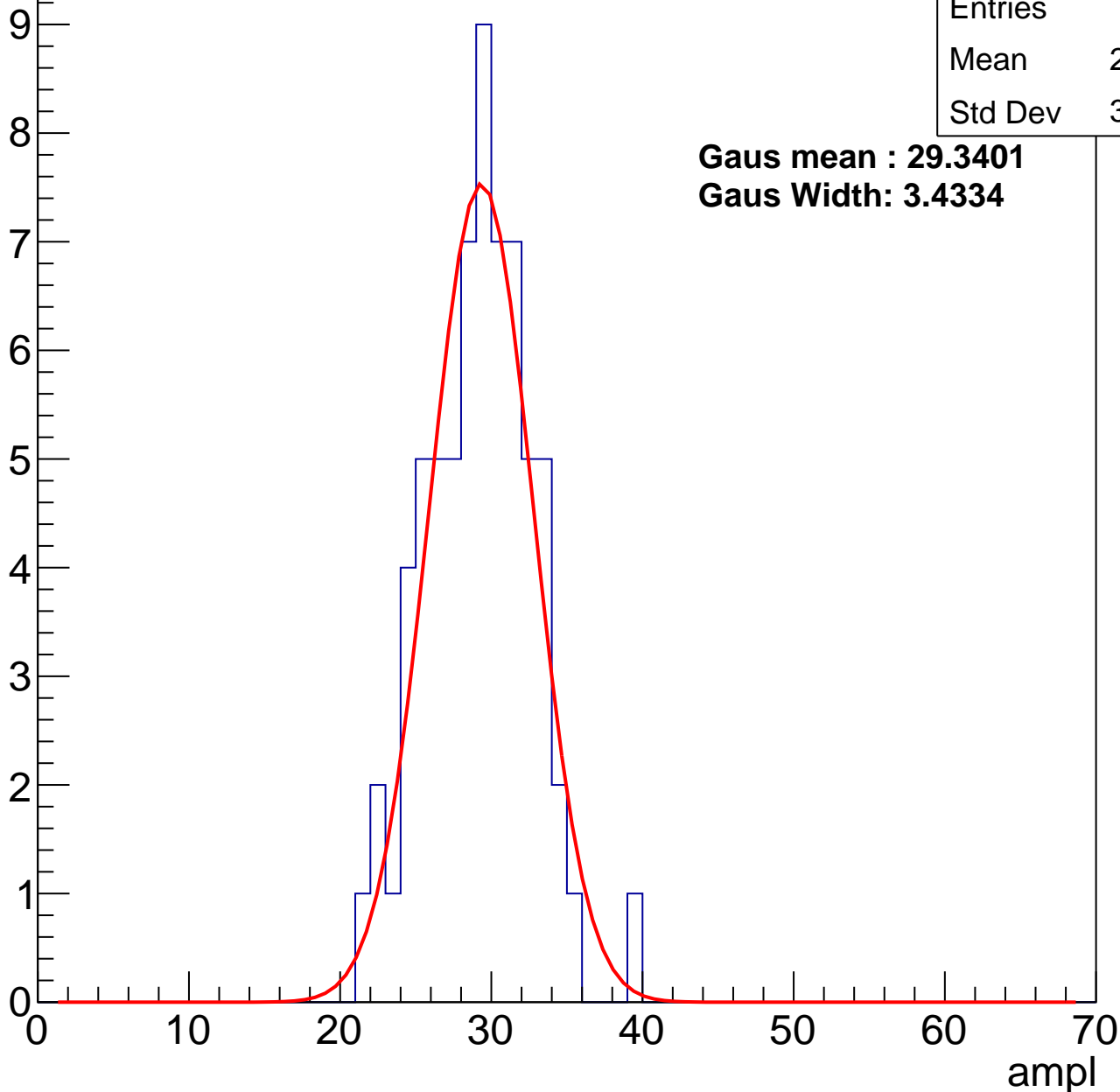
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.73
Std Dev	3.436

Gaus mean : 29.3401

Gaus Width: 3.4334



B1L003S, U21-ch57, adc1

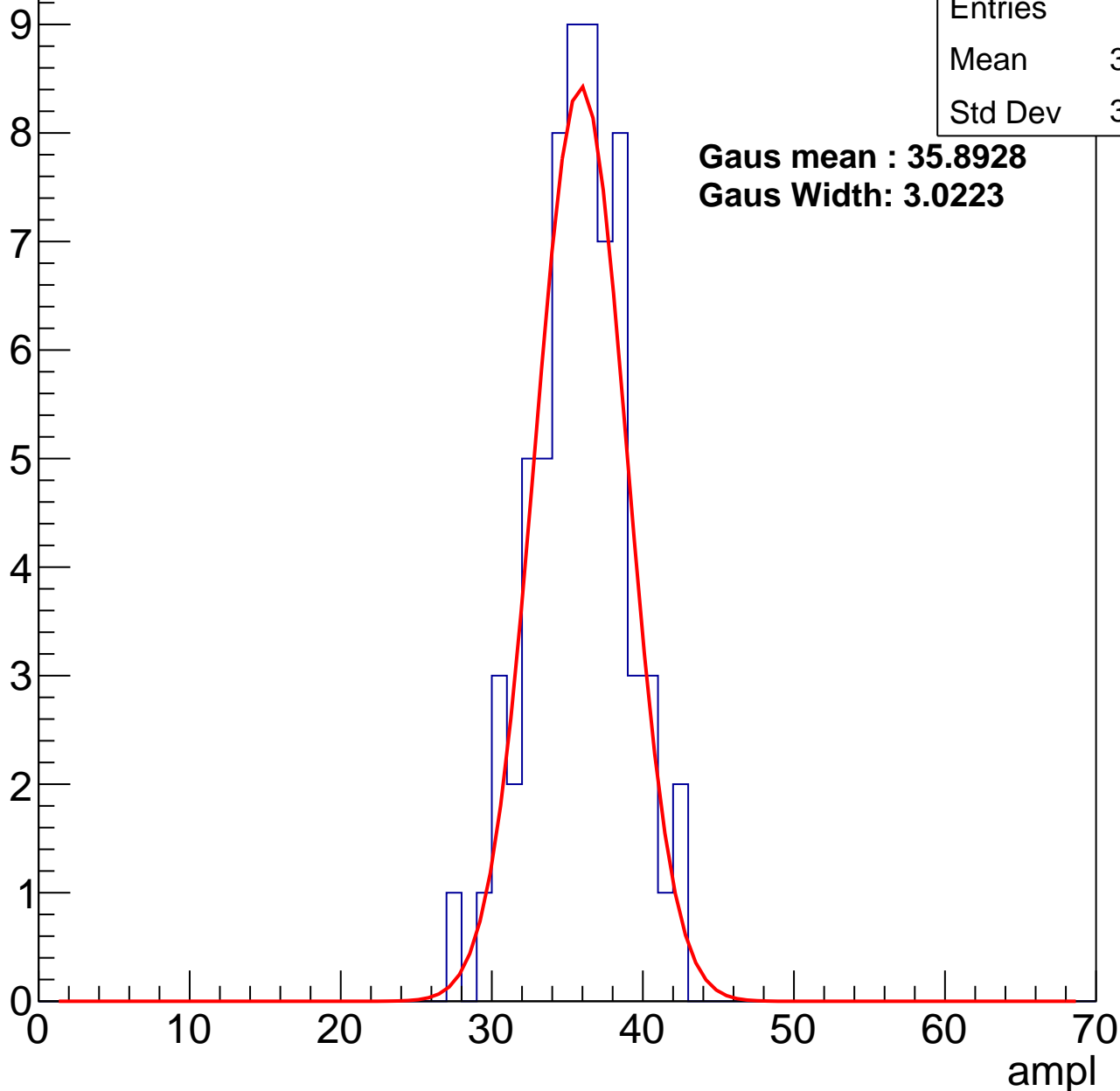
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.36
Std Dev	3.084

Gaus mean : 35.8928

Gaus Width: 3.0223



B1L003S, U21-ch57, adc2

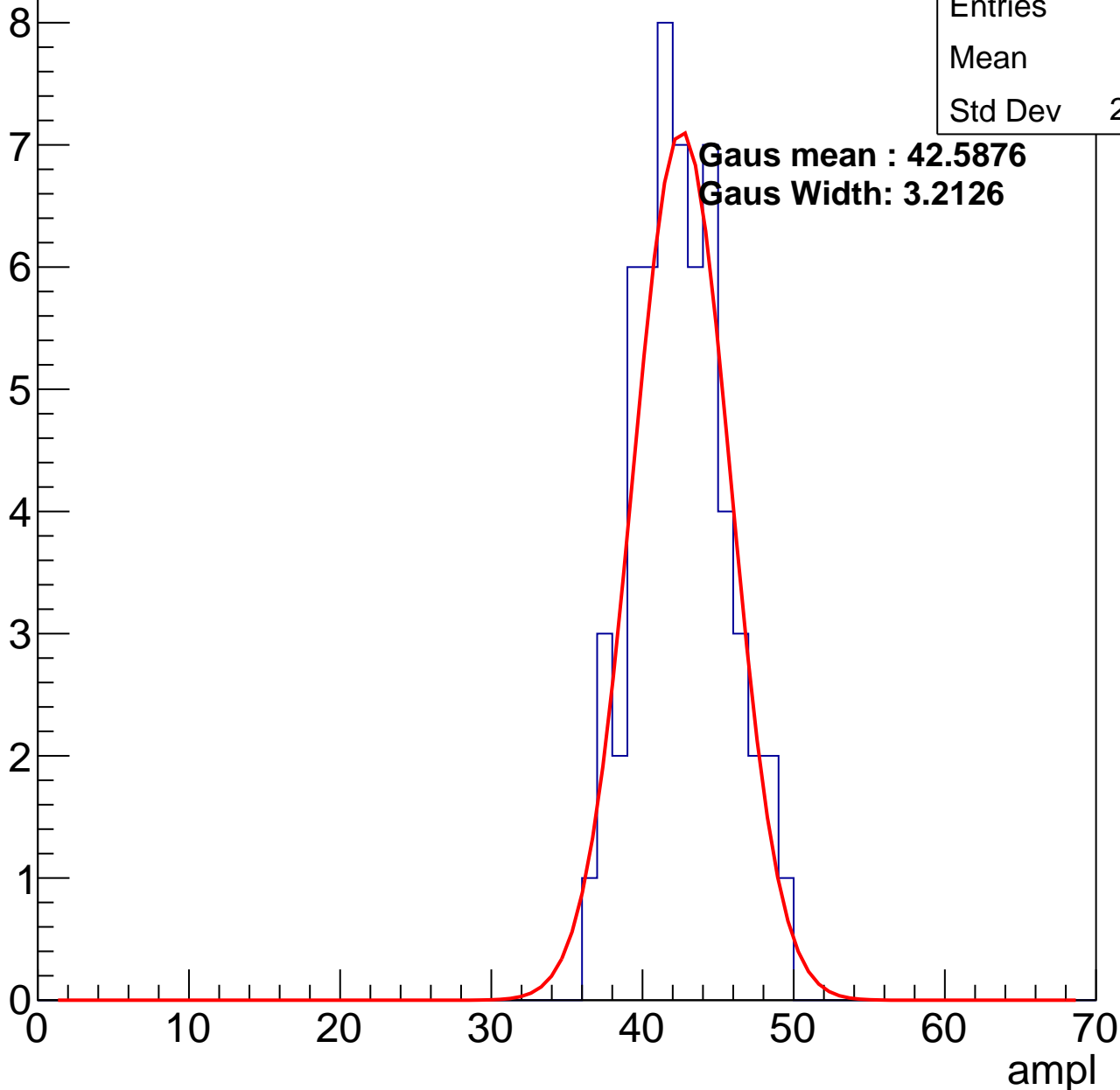
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	42.1
Std Dev	2.987

Gaus mean : 42.5876

Gaus Width: 3.2126

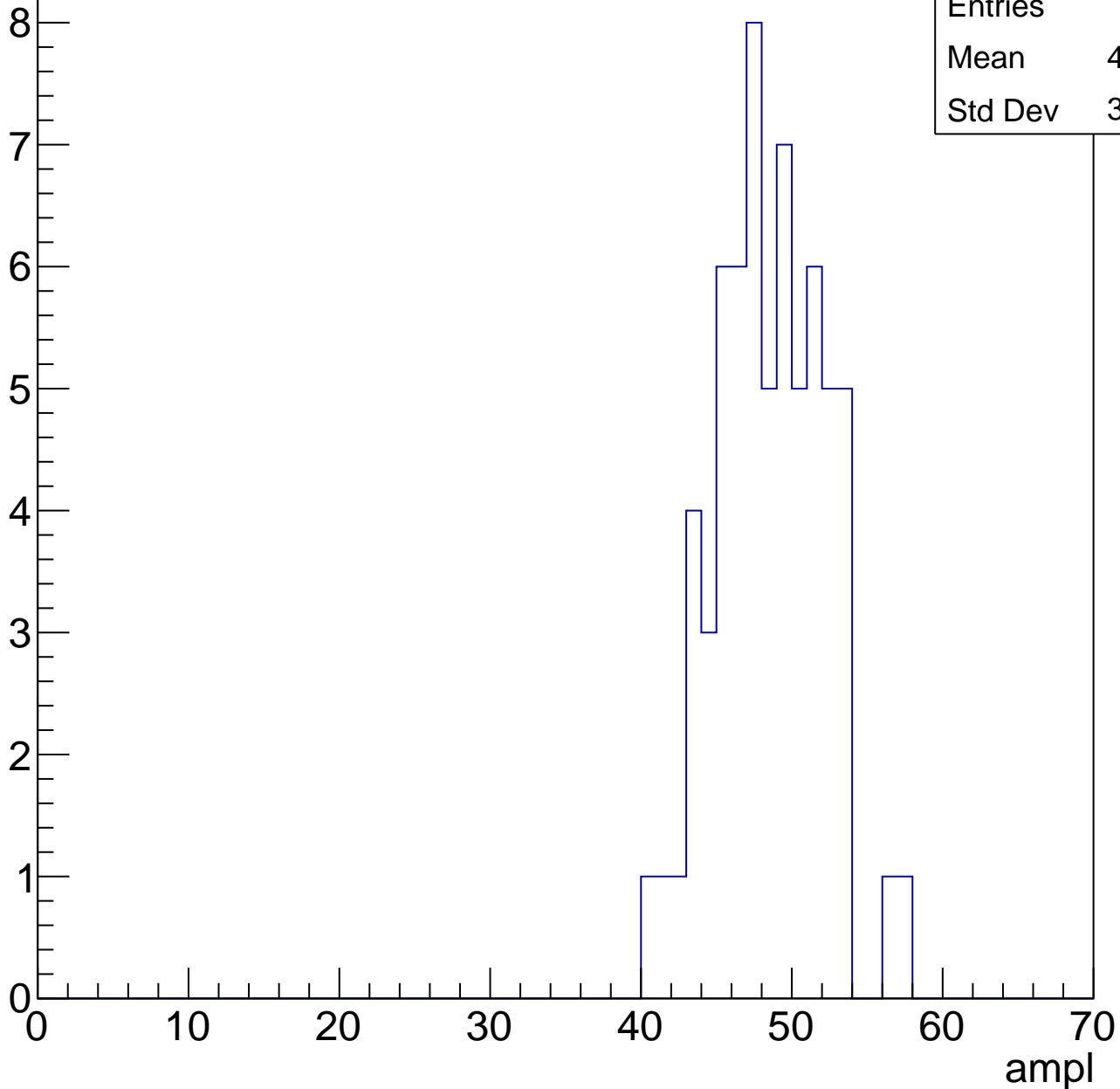


B1L003S, U21-ch57, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	48.09
Std Dev	3.538



B1L003S, U21-ch57, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	61
Mean	53.79
Std Dev	3.209

Entry

10

8

6

4

2

0

0

10

20

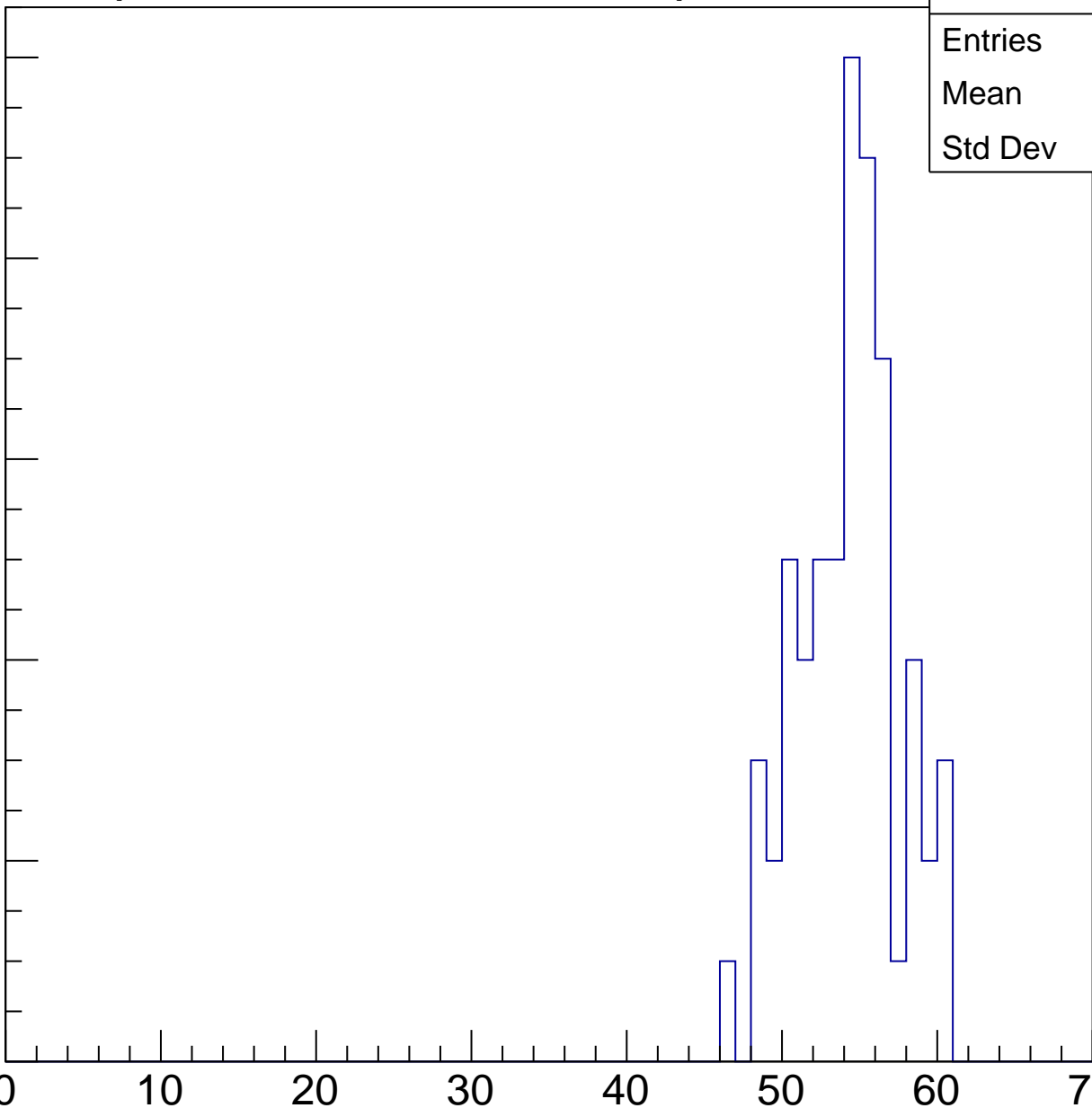
30

40

50

60

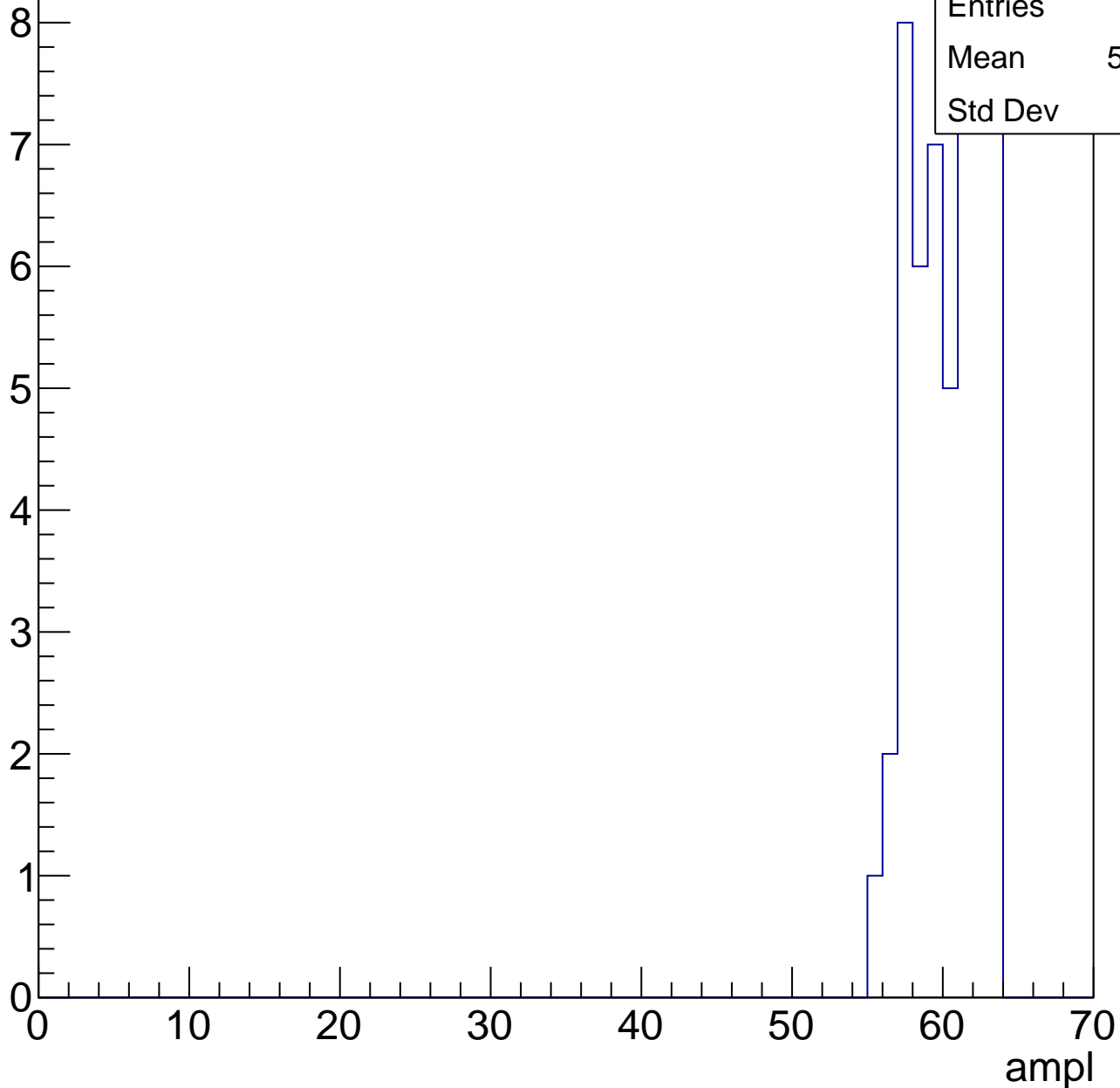
ampl



B1L003S, U21-ch57, adc5

calib_packv5_042523_0143.root, FC#13, port D2

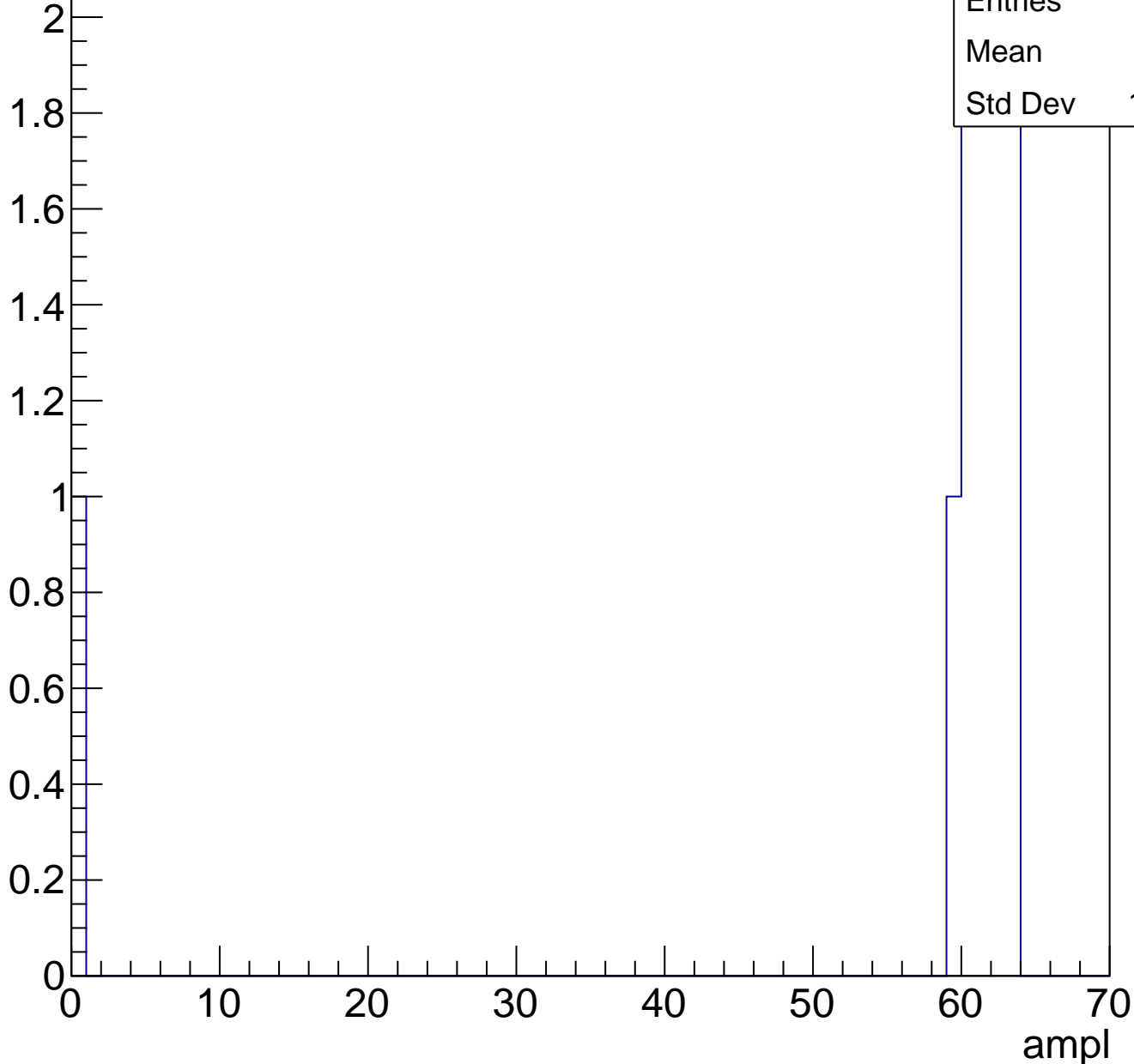
Entry



B1L003S, U21-ch57, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch57, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch58, adc0

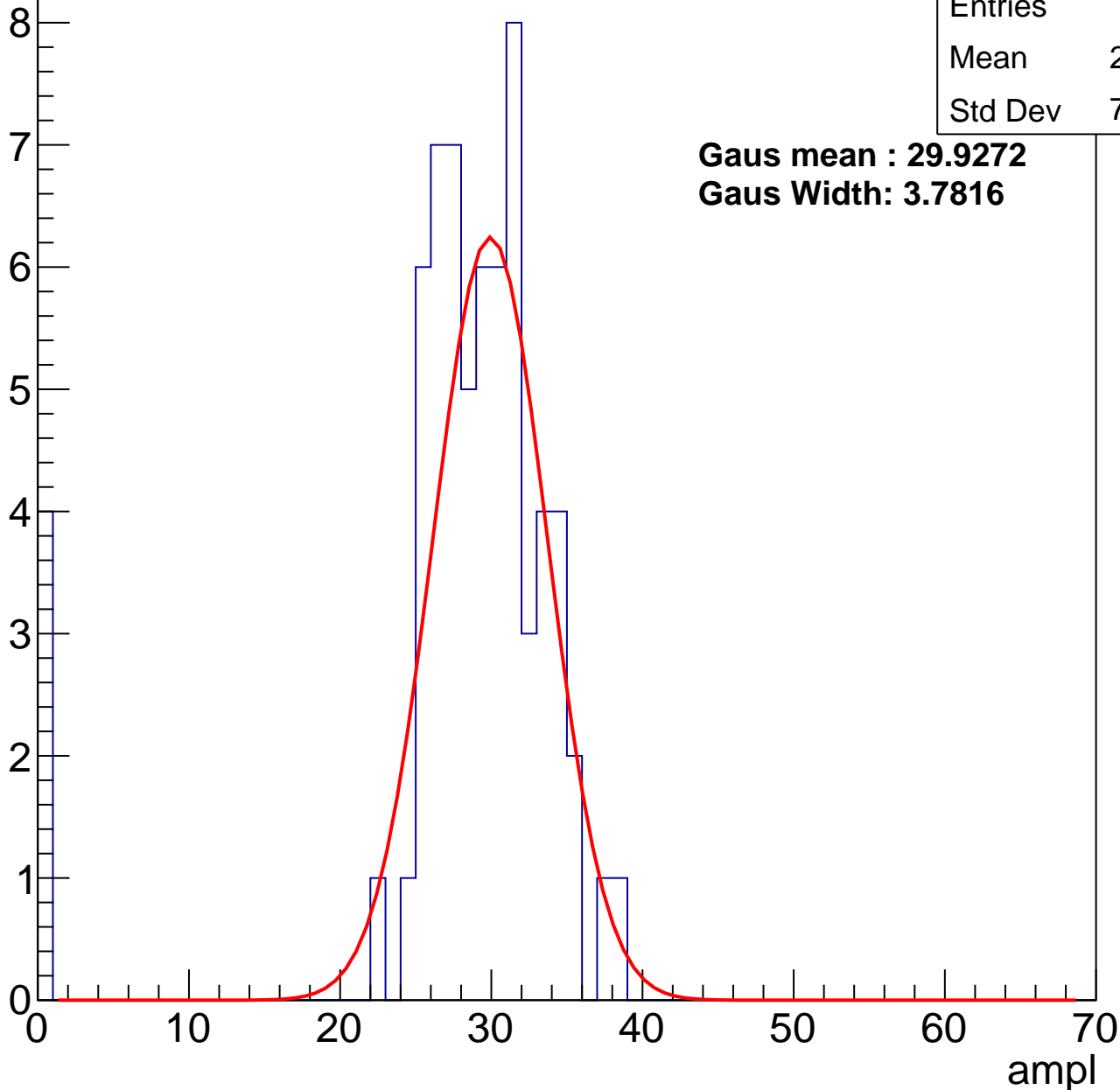
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	27.55
Std Dev	7.723

Gaus mean : 29.9272

Gaus Width: 3.7816



B1L003S, U21-ch58, adc1

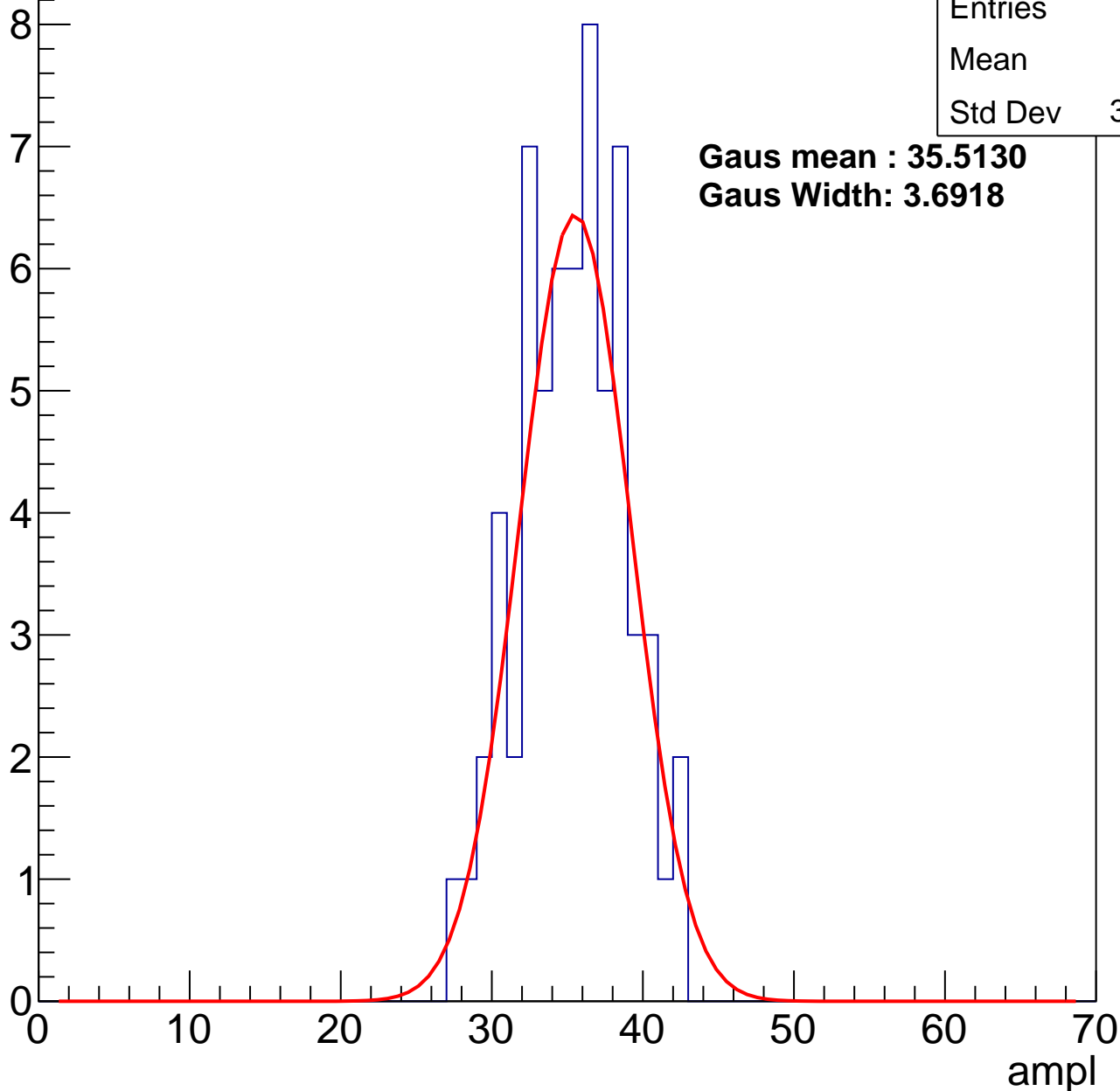
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	34.9
Std Dev	3.458

Gaus mean : 35.5130

Gaus Width: 3.6918



B1L003S, U21-ch58, adc2

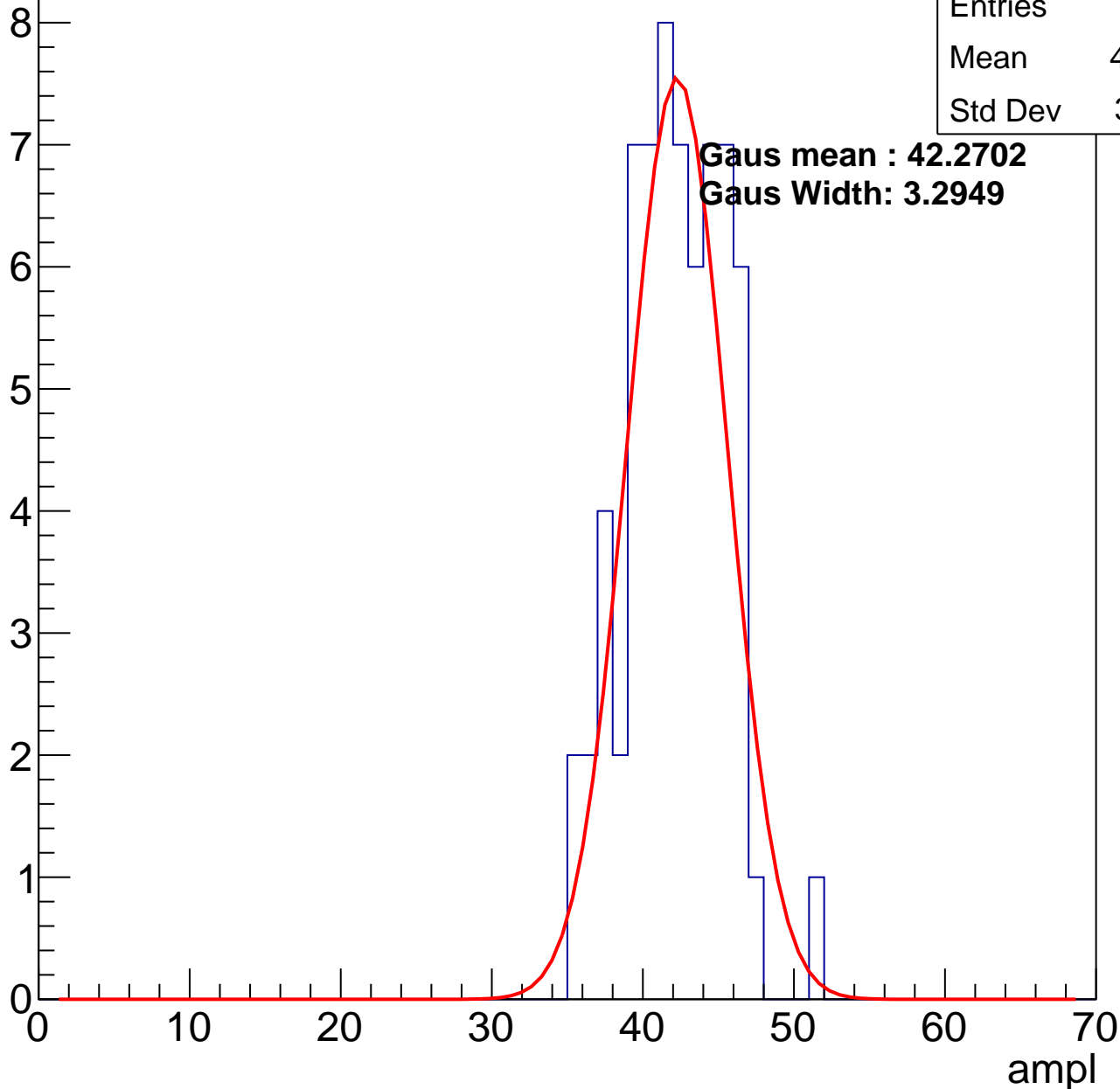
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.73
Std Dev	3.221

Gaus mean : 42.2702

Gaus Width: 3.2949



B1L003S, U21-ch58, adc3

calib_packv5_042523_0143.root, FC#13, port D2

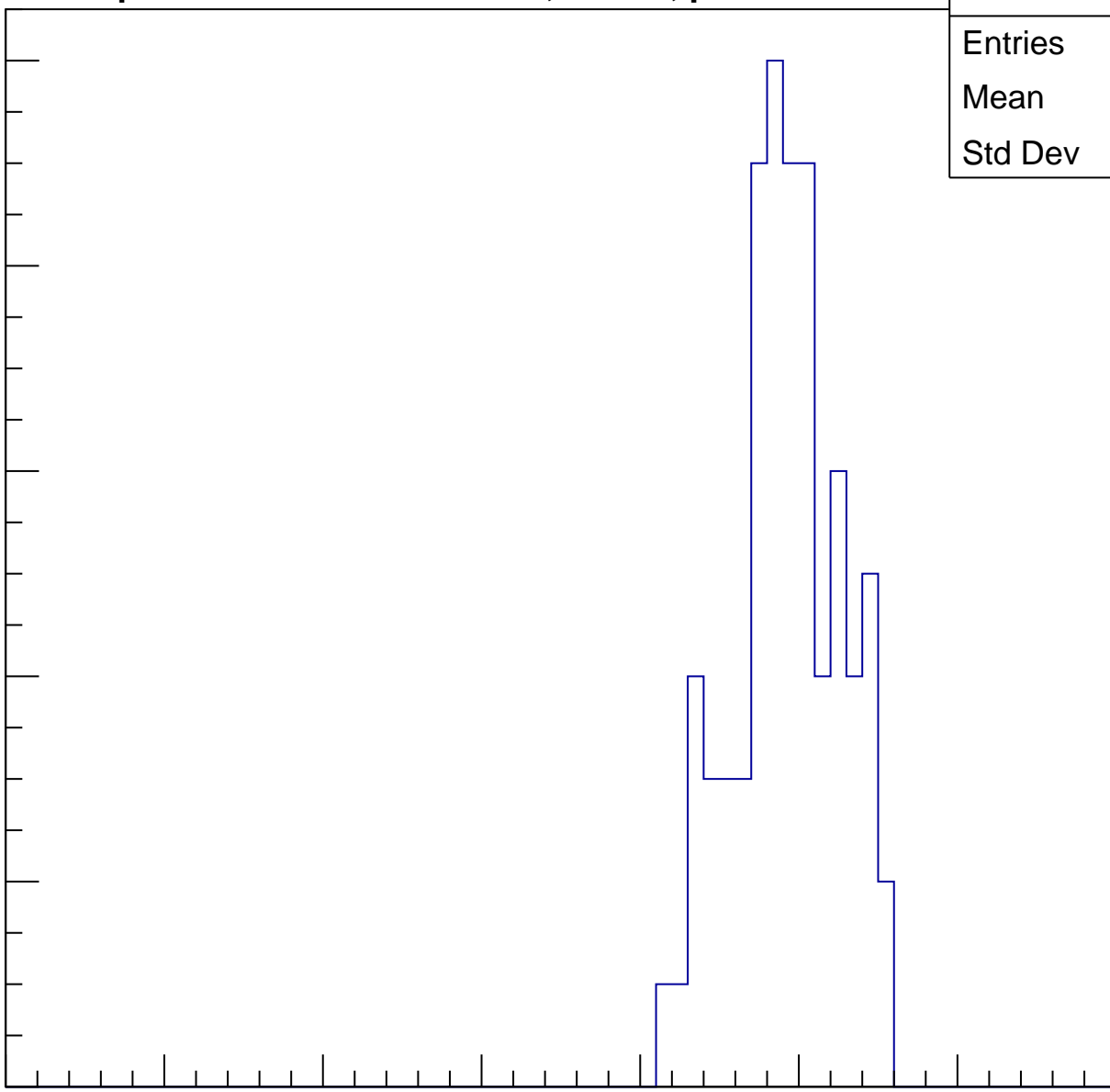
Entries	73
Mean	48.79
Std Dev	3.289

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

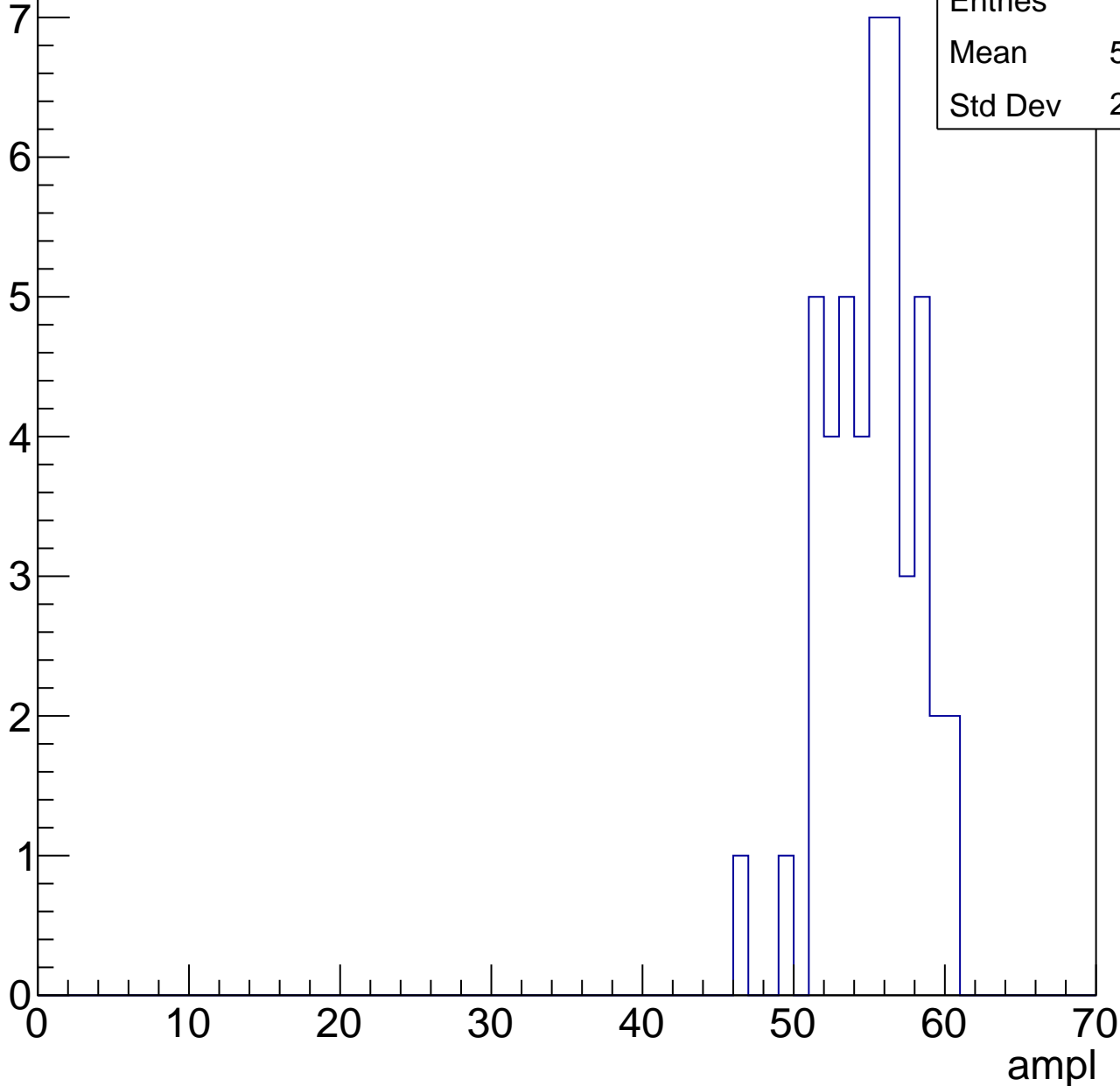


B1L003S, U21-ch58, adc4

calib_packv5_042523_0143.root, FC#13, port D2

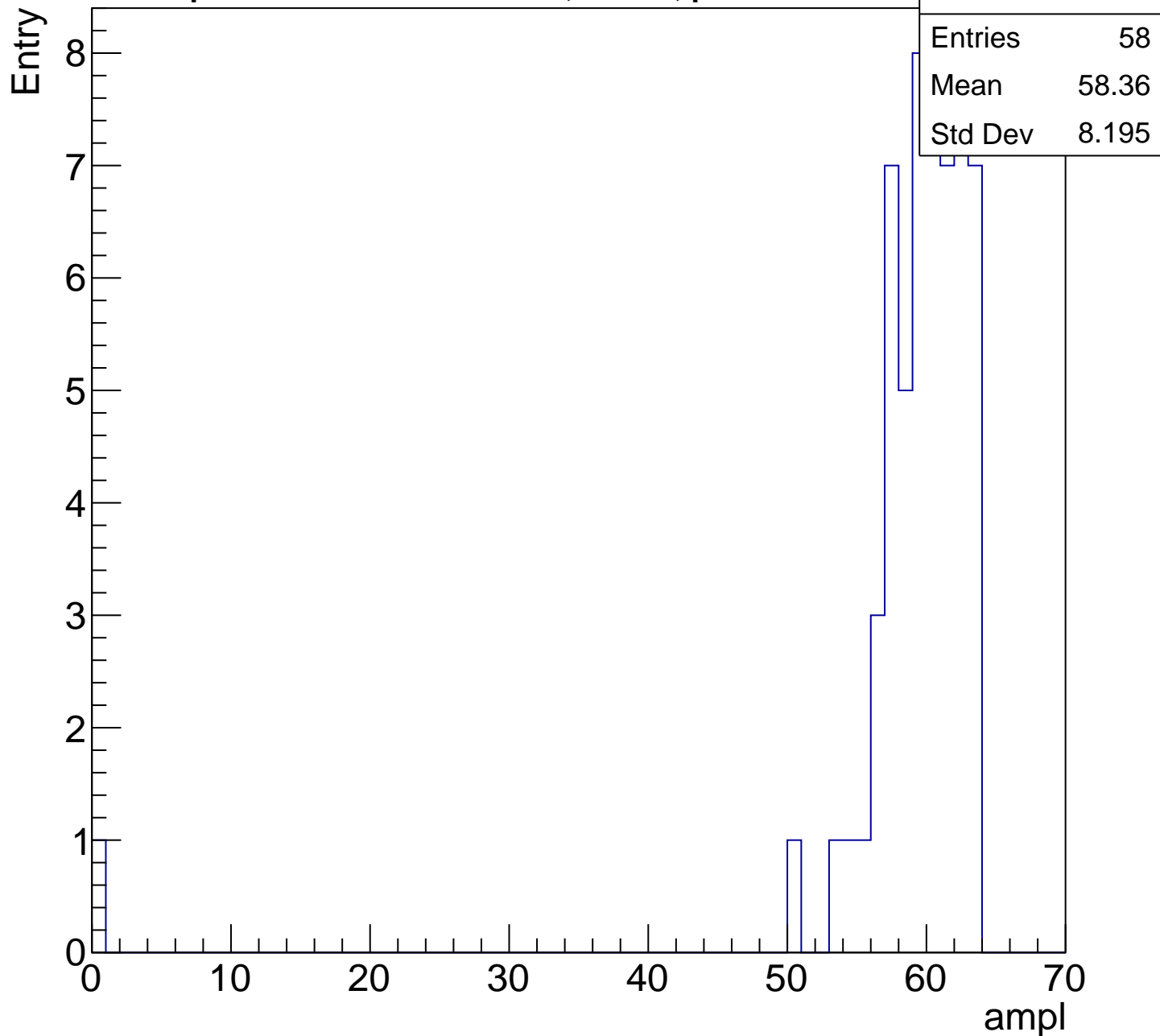
Entry

Entries	46
Mean	54.67
Std Dev	2.942



B1L003S, U21-ch58, adc5

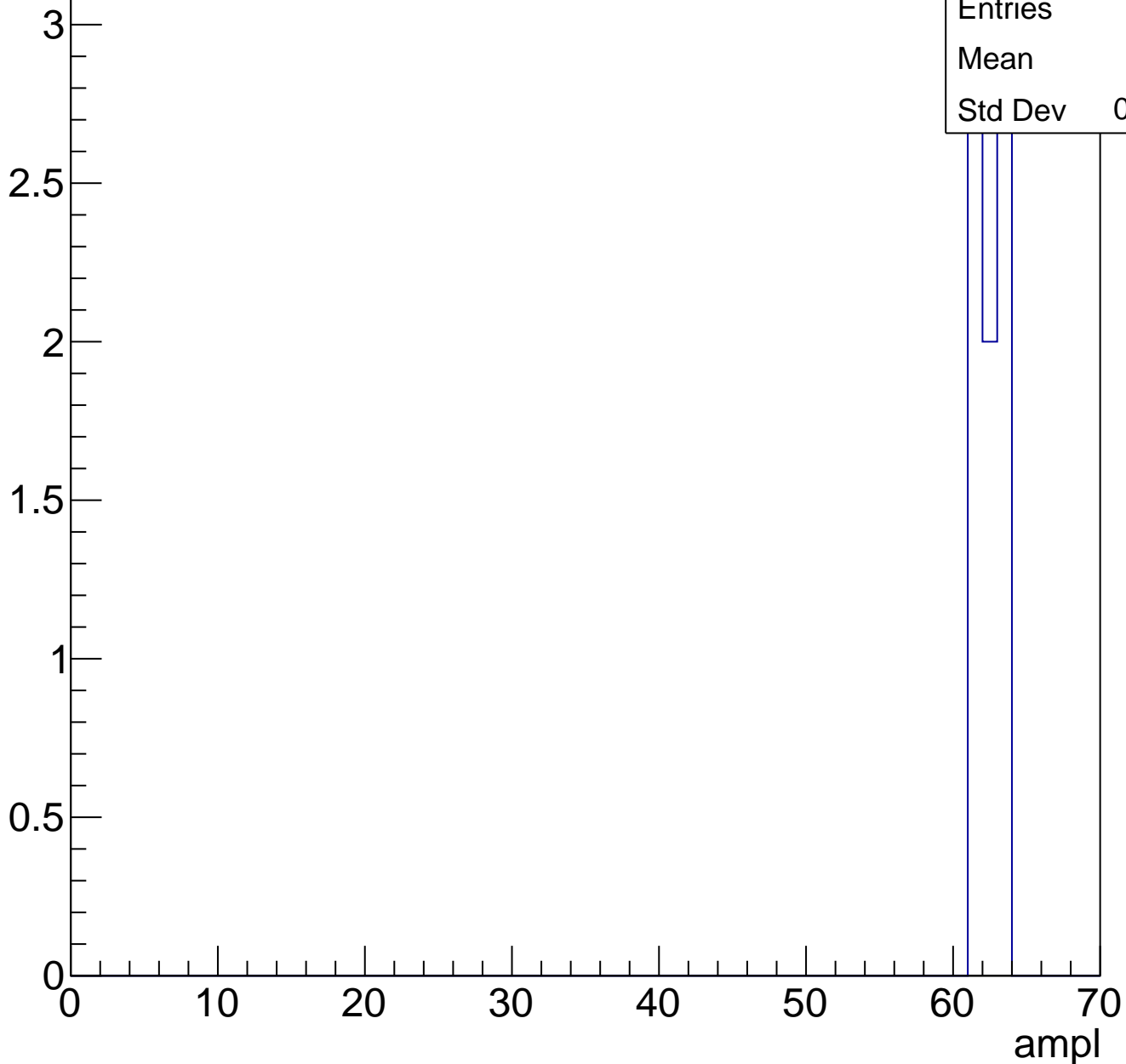
calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U21-ch58, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch58, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch59, adc0

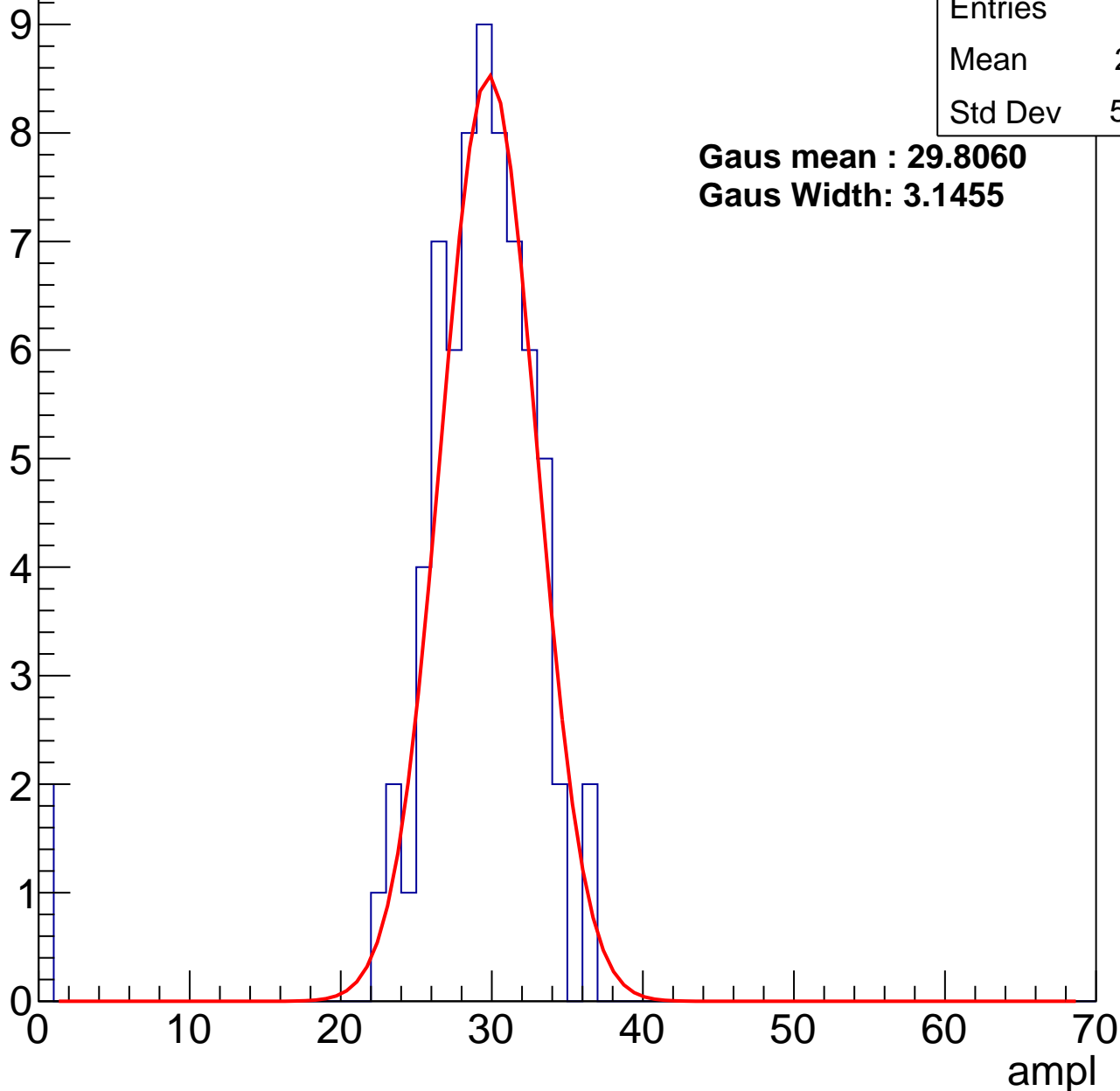
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	28.21
Std Dev	5.684

Gaus mean : 29.8060

Gaus Width: 3.1455



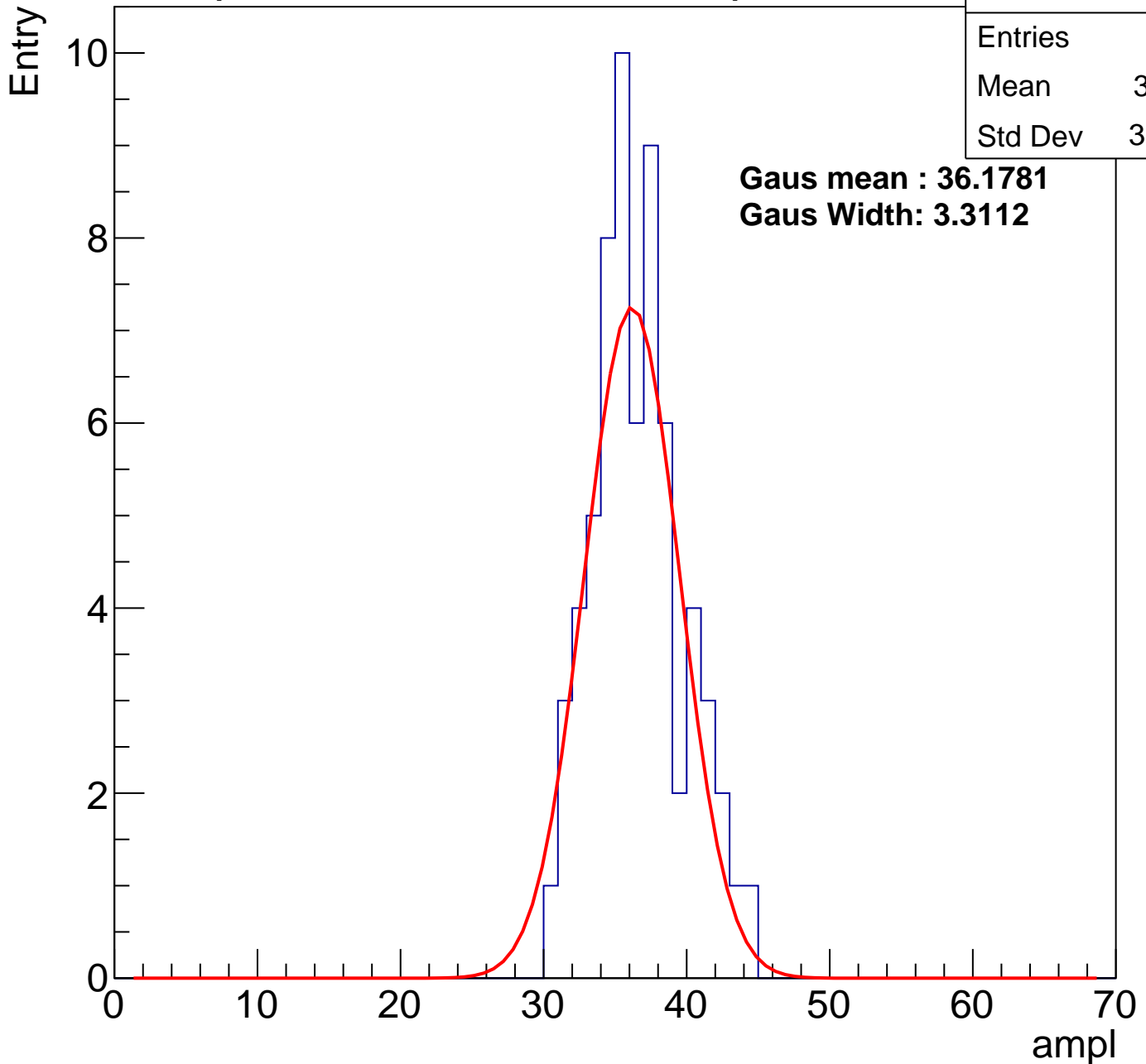
B1L003S, U21-ch59, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	36.11
Std Dev	3.129

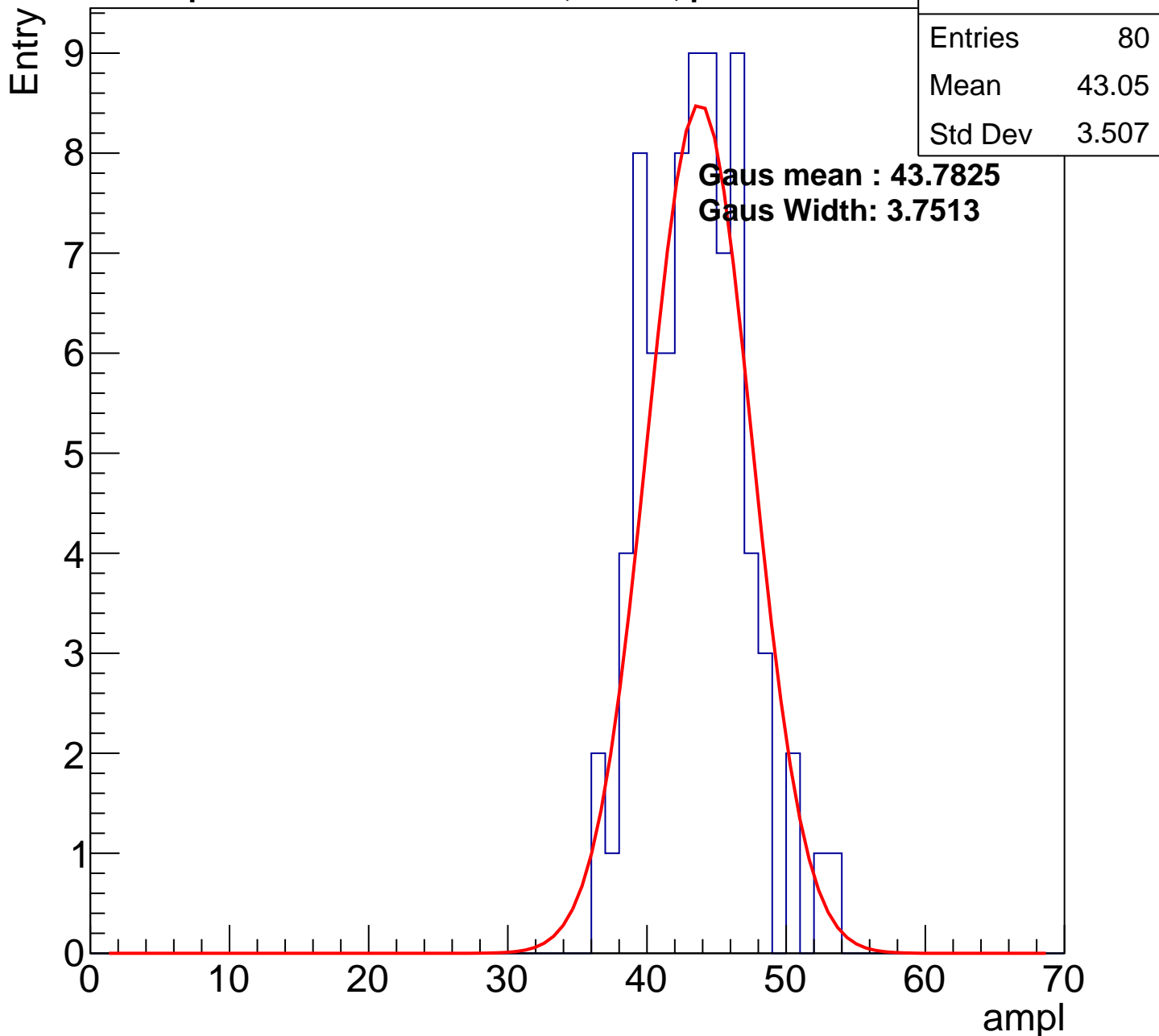
Gaus mean : 36.1781

Gaus Width: 3.3112



B1L003S, U21-ch59, adc2

calib_packv5_042523_0143.root, FC#13, port D2

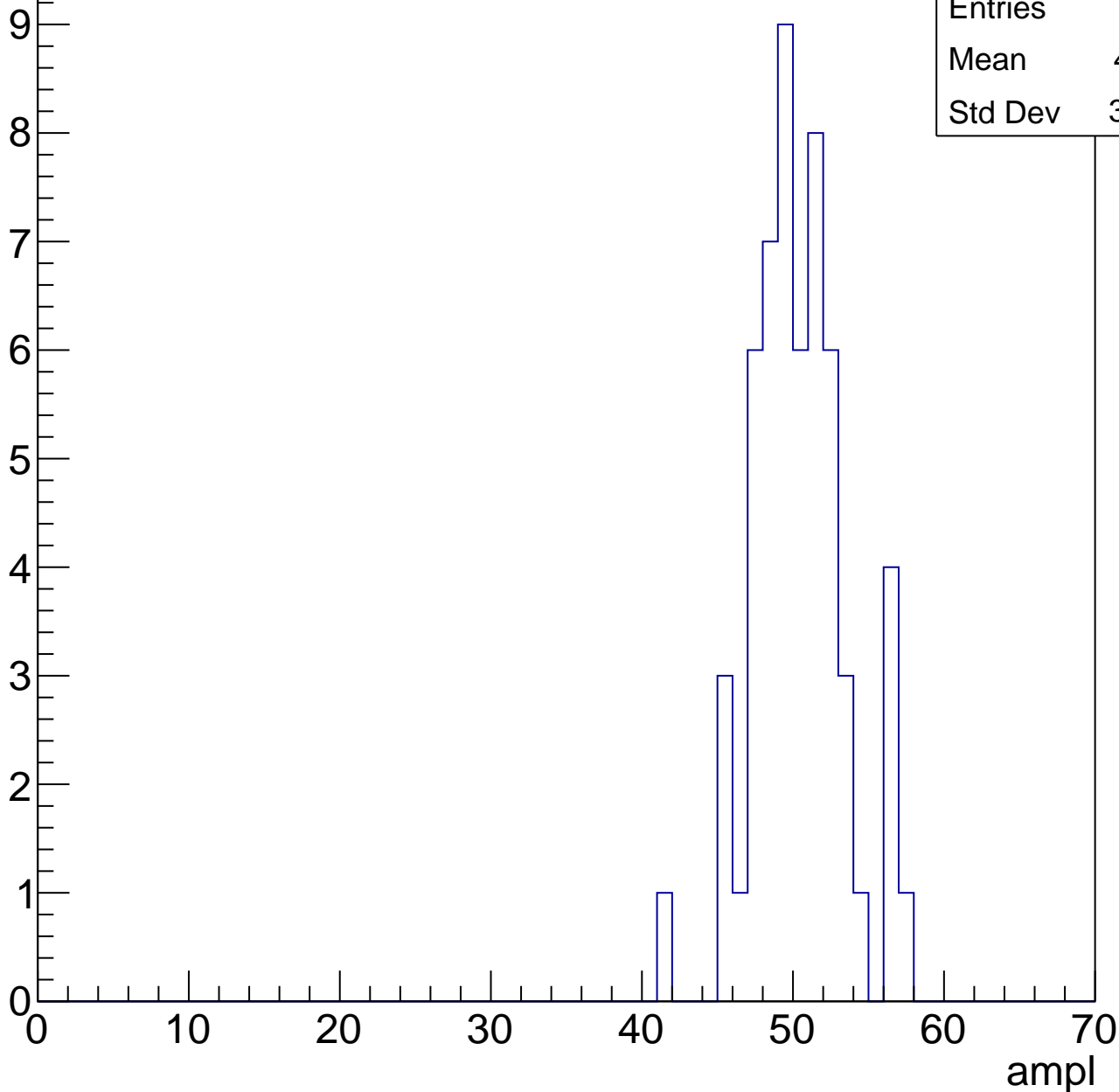


B1L003S, U21-ch59, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

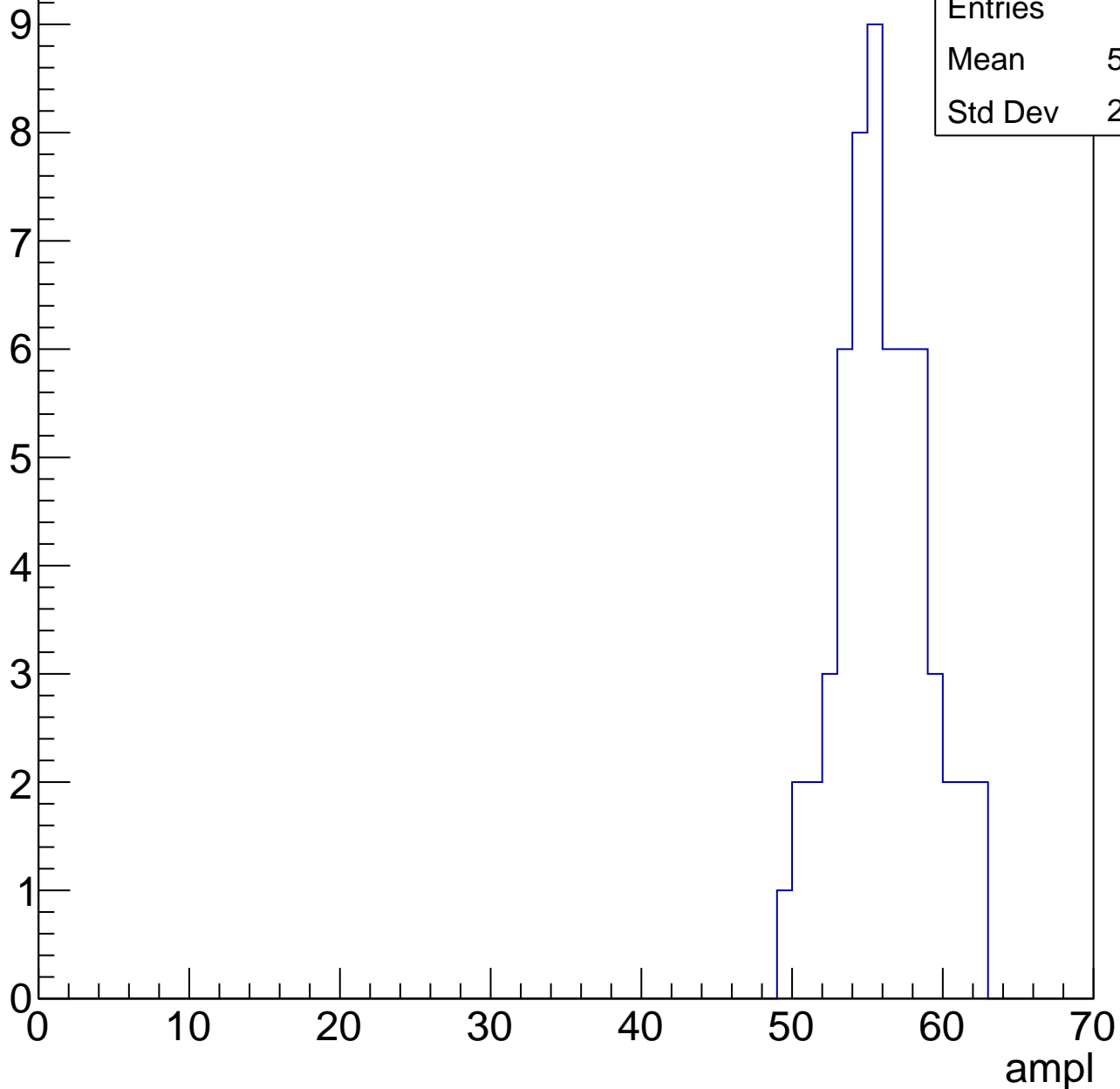
Entries	56
Mean	49.91
Std Dev	3.078



B1L003S, U21-ch59, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	58
Mean	55.53
Std Dev	2.972

B1L003S, U21-ch59, adc5

calib_packv5_042523_0143.root, FC#13, port D2

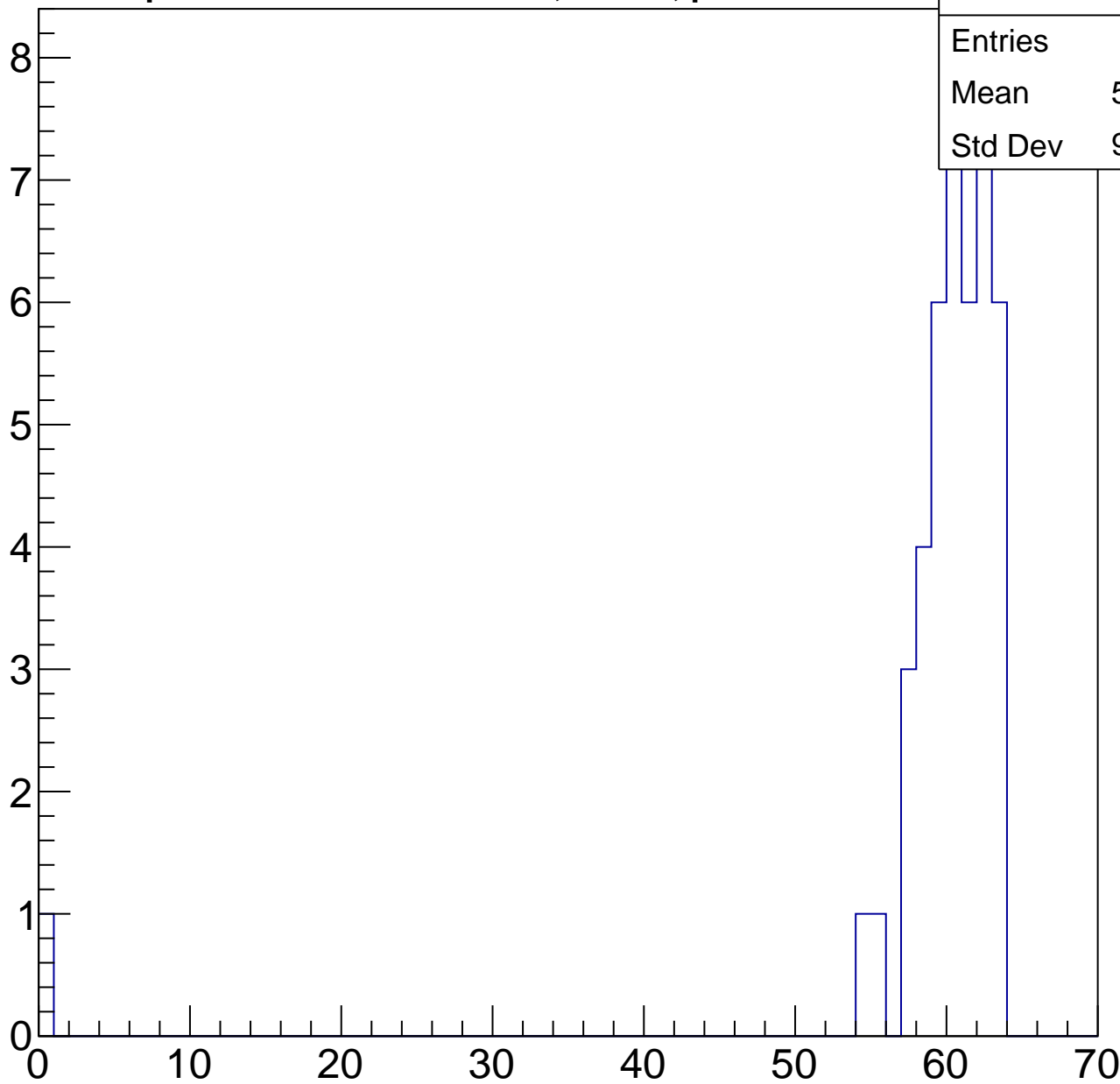
Entry

Entries	44
Mean	58.77
Std Dev	9.214

8
7
6
5
4
3
2
1
0

0 10 20 30 40 50 60 70

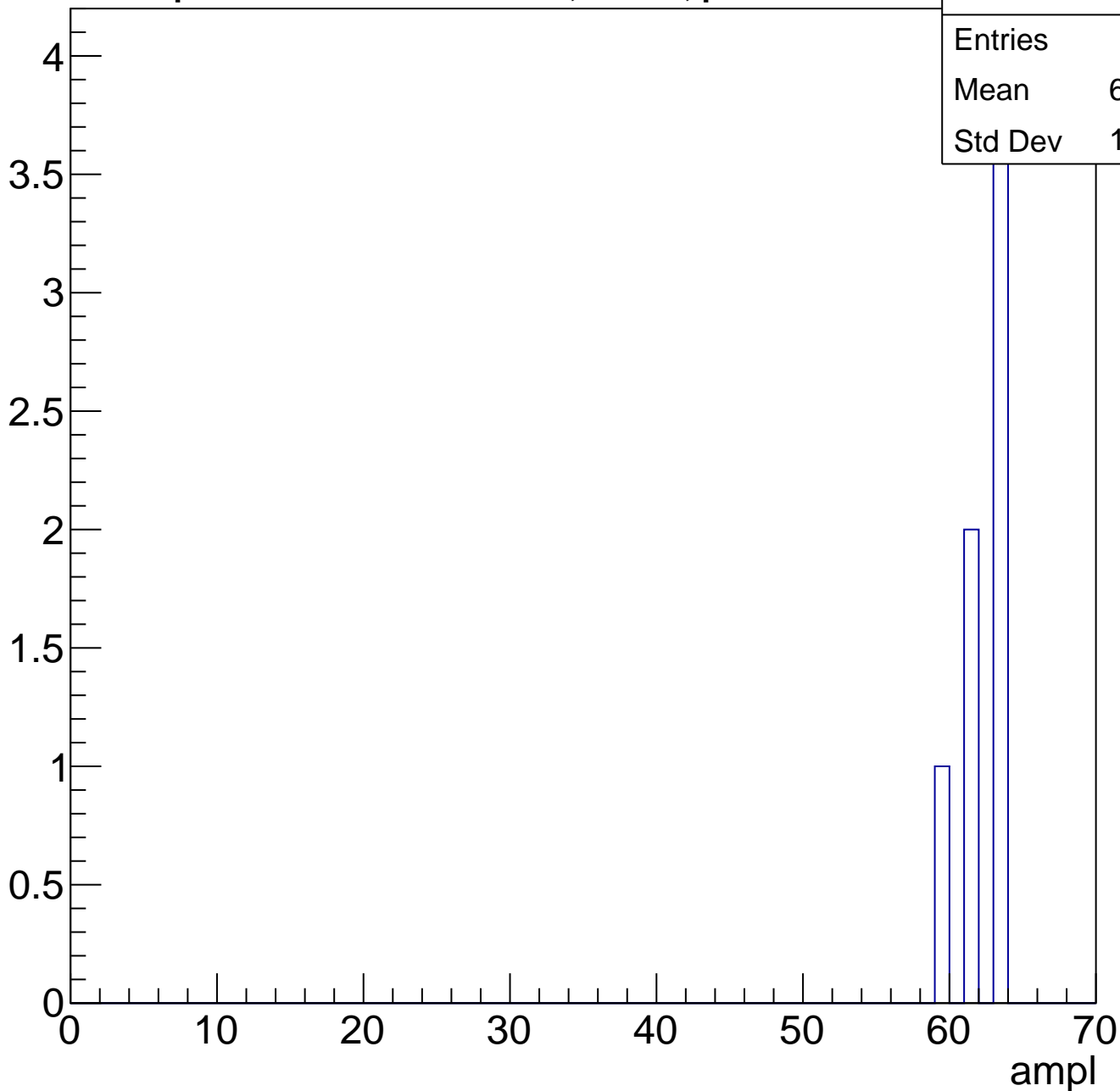
ampl



B1L003S, U21-ch59, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch59, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch60, adc0

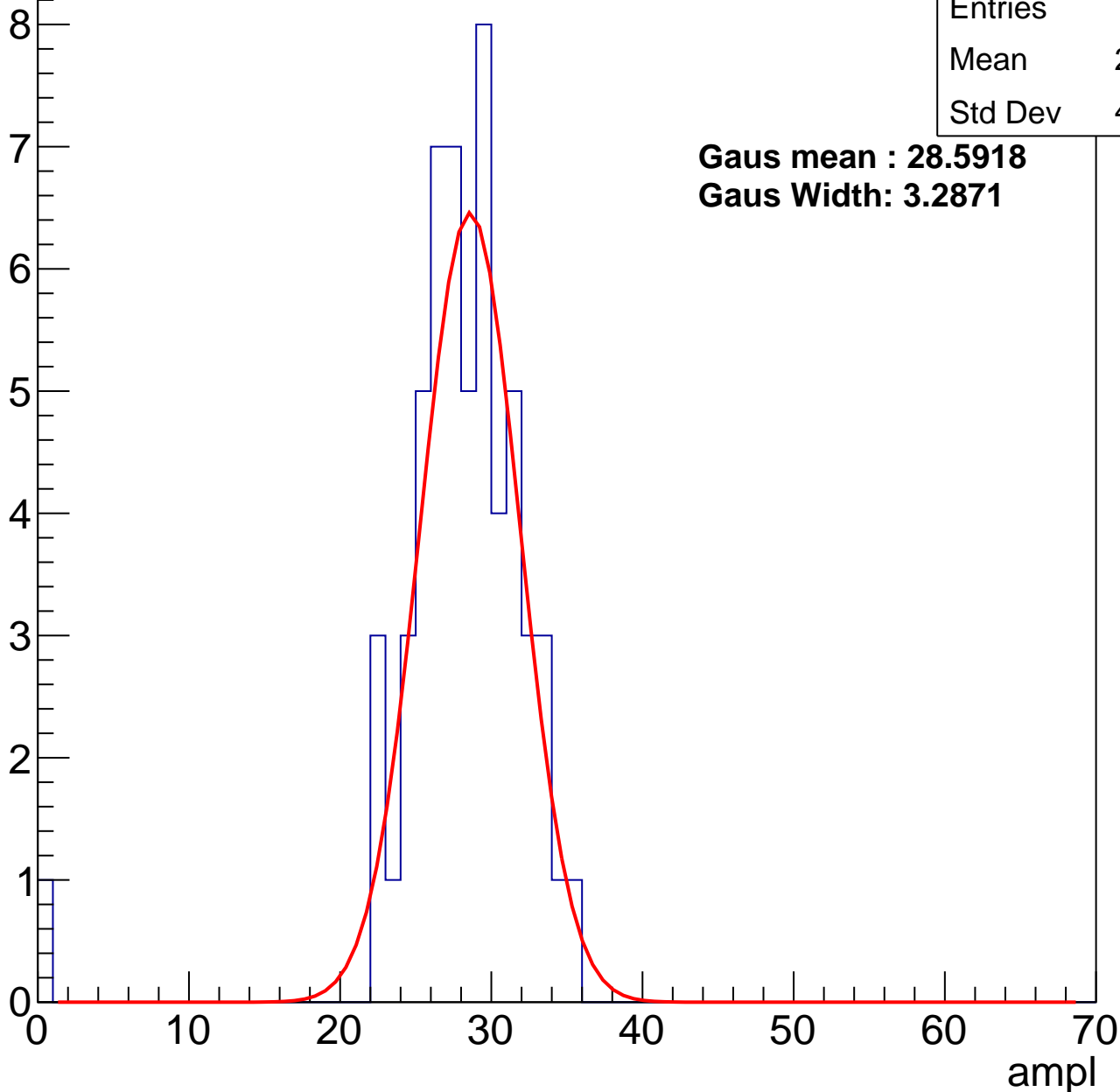
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	27.51
Std Dev	4.791

Gaus mean : 28.5918

Gaus Width: 3.2871



B1L003S, U21-ch60, adc1

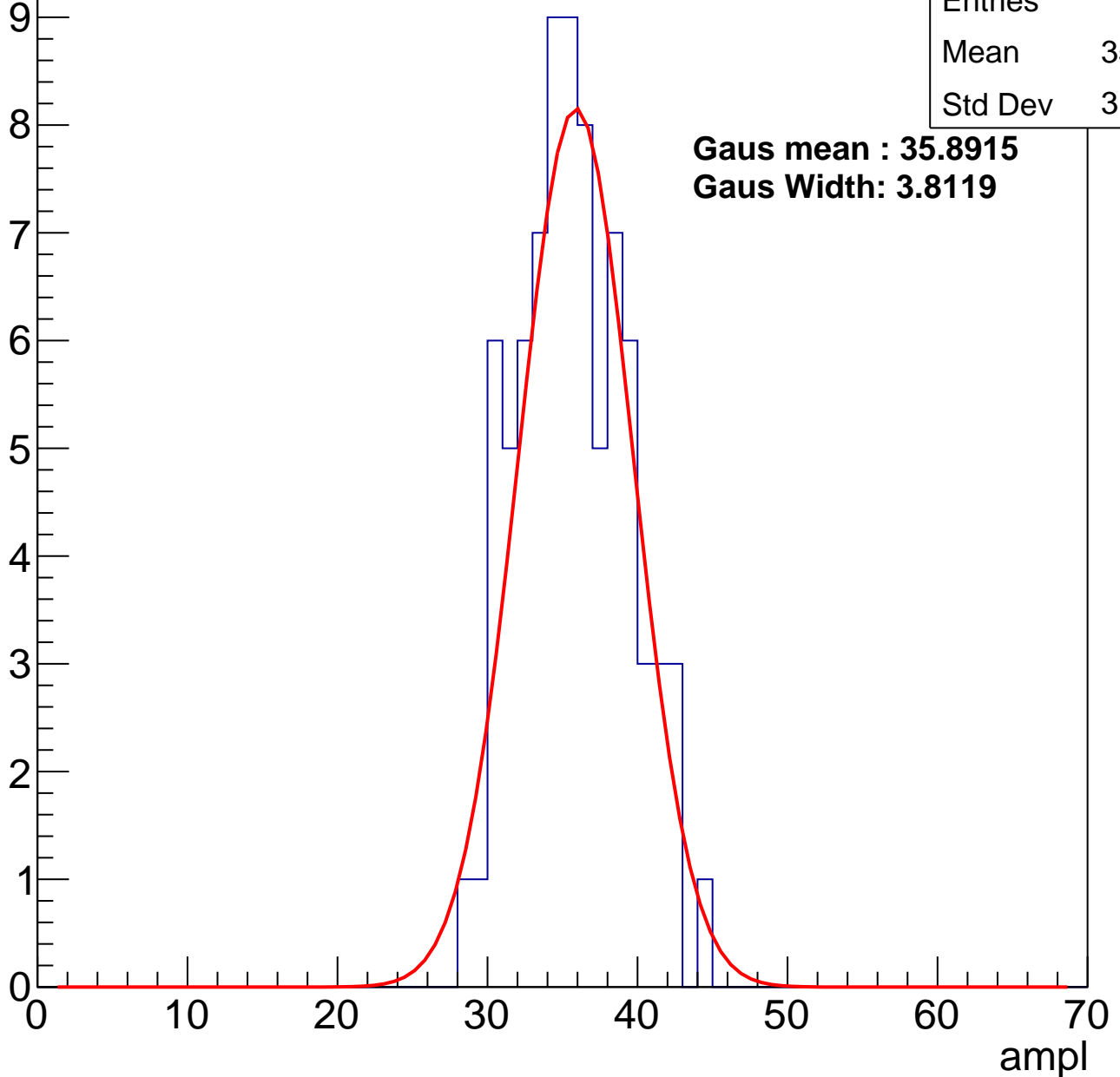
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	35.27
Std Dev	3.532

Gaus mean : 35.8915

Gaus Width: 3.8119



B1L003S, U21-ch60, adc2

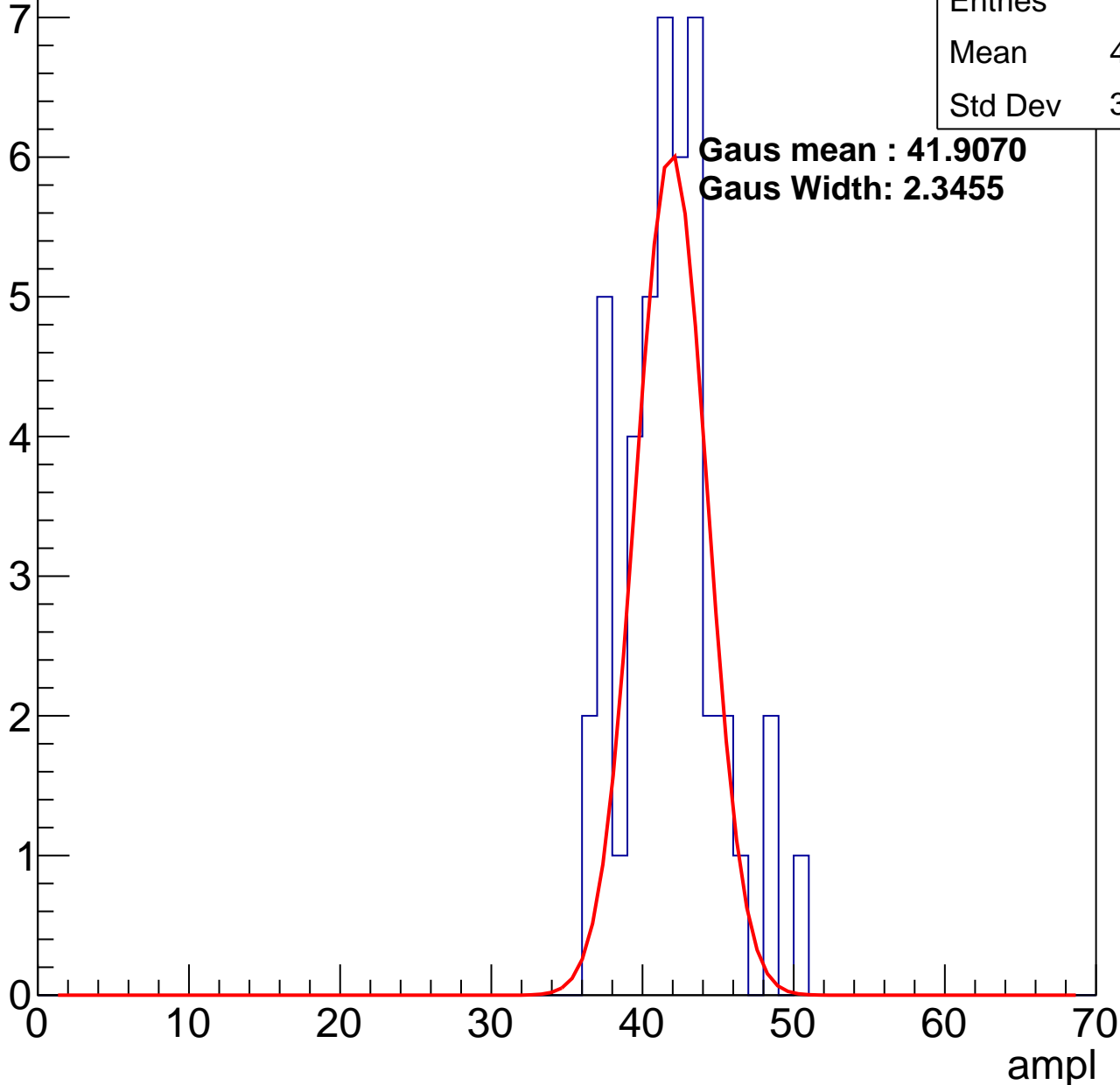
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	41.36
Std Dev	3.135

Gaus mean : 41.9070

Gaus Width: 2.3455

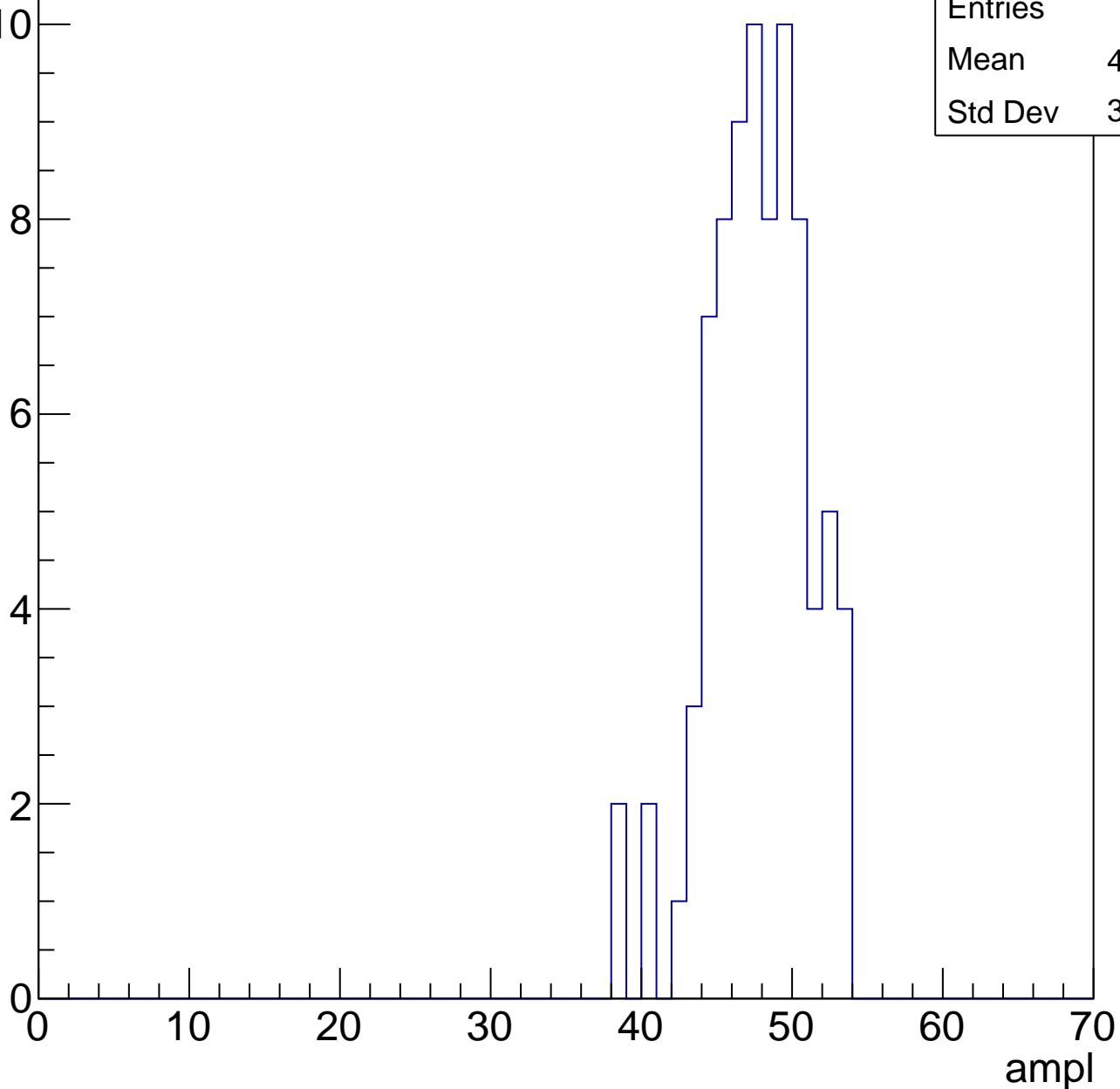


B1L003S, U21-ch60, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	47.27
Std Dev	3.304

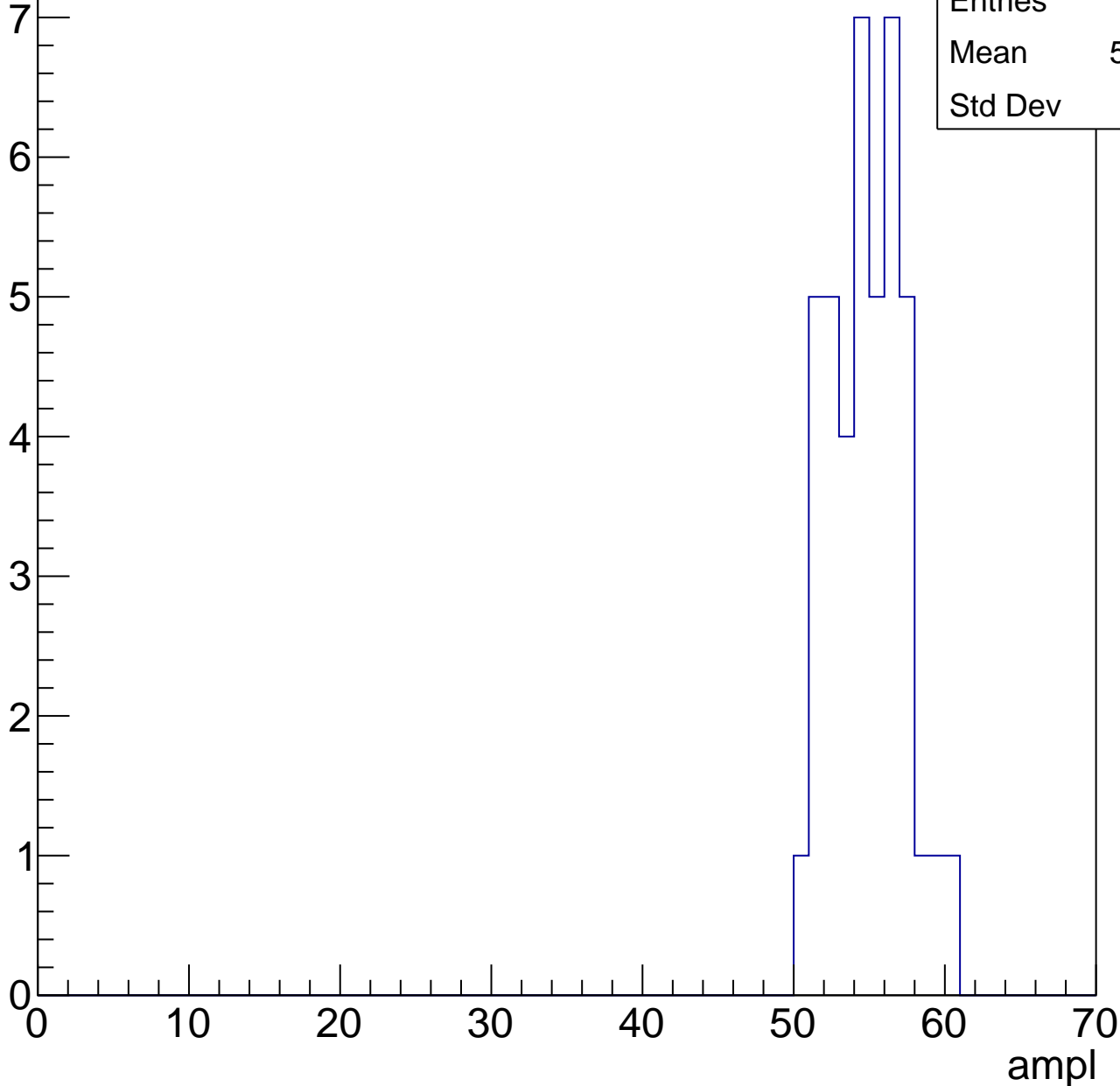


B1L003S, U21-ch60, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	54.38
Std Dev	2.36

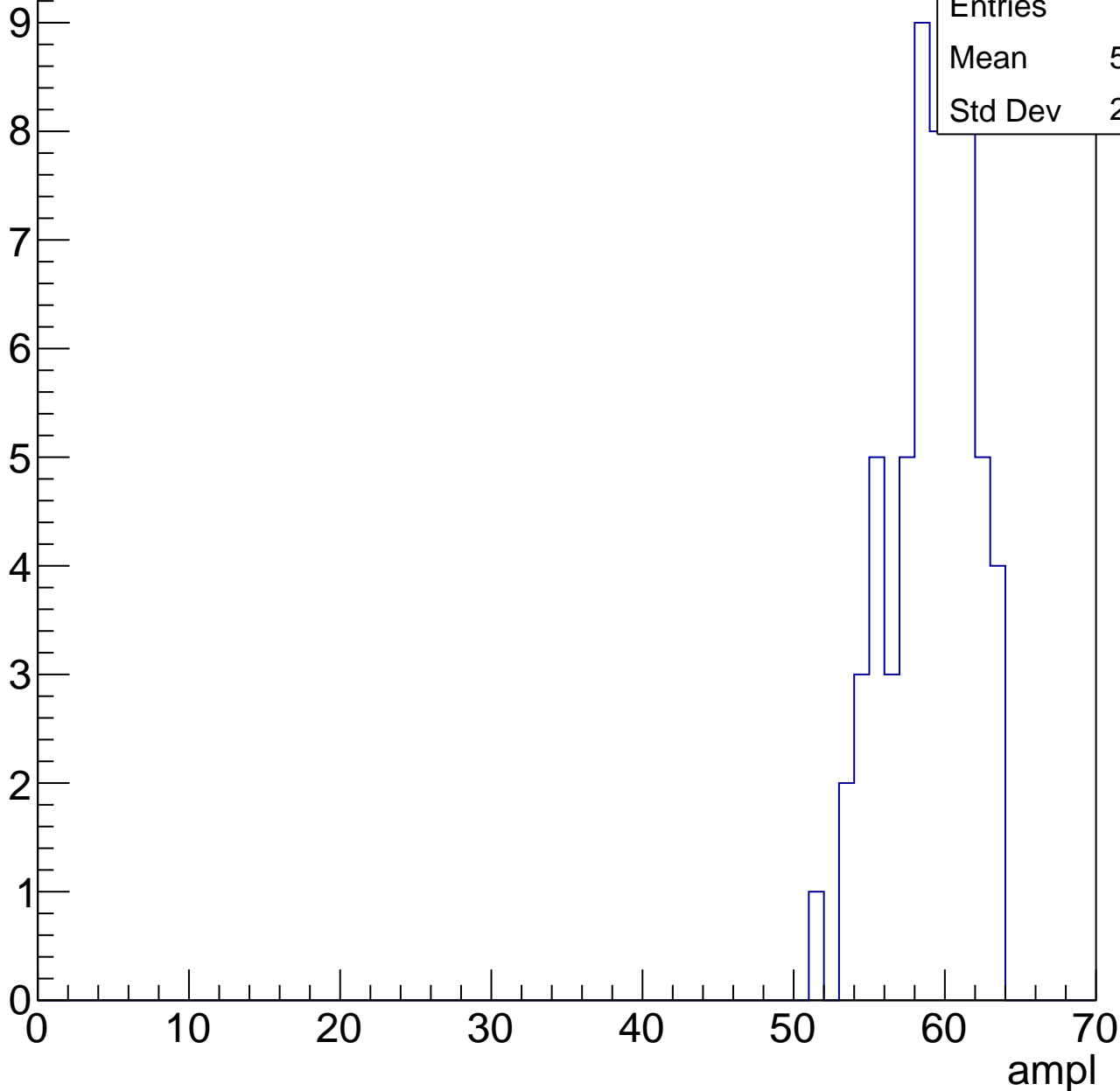


B1L003S, U21-ch60, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	58.58
Std Dev	2.809

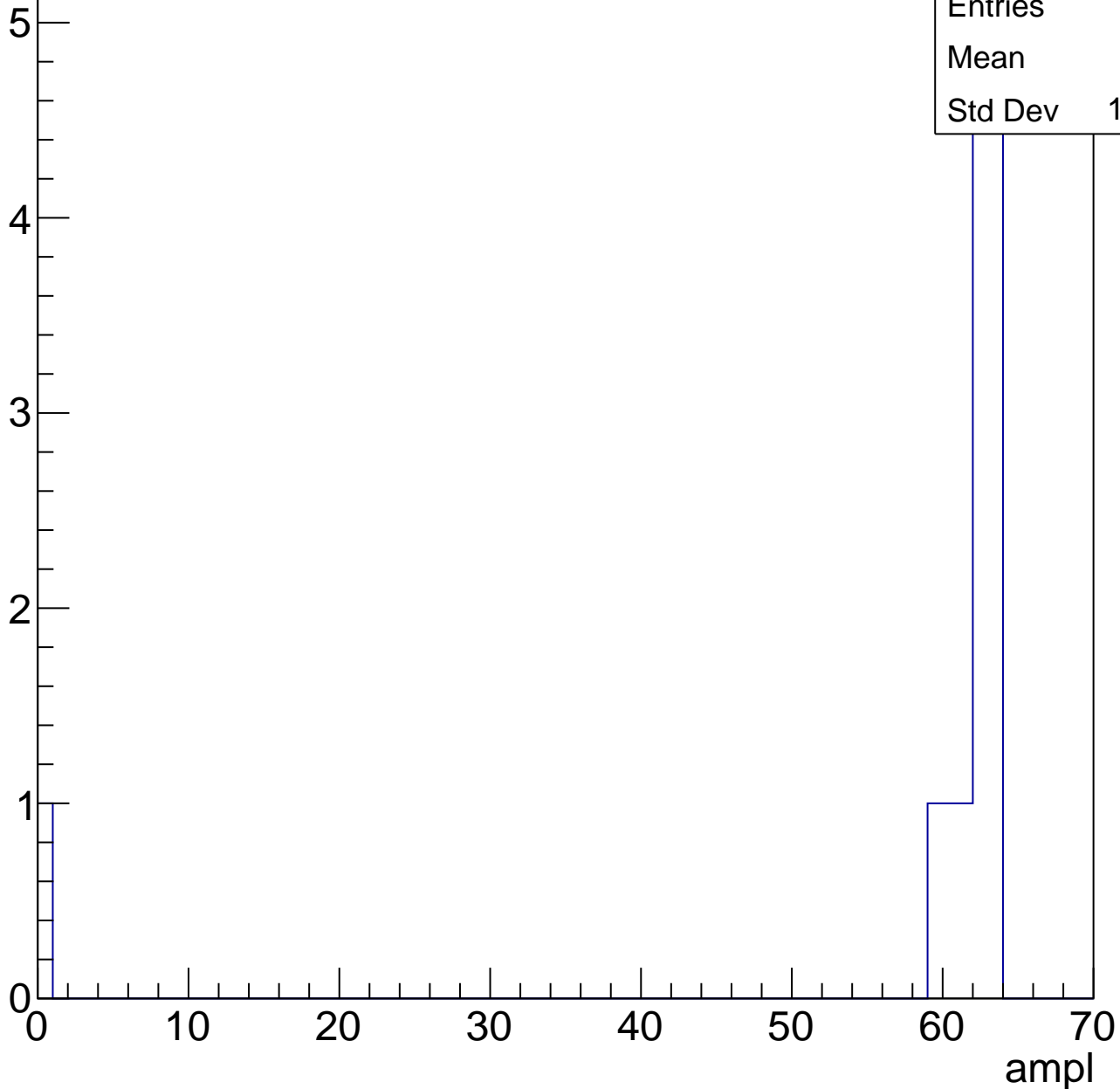


B1L003S, U21-ch60, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	57.5
Std Dev	15.99



B1L003S, U21-ch60, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L003S, U21-ch61, adc0

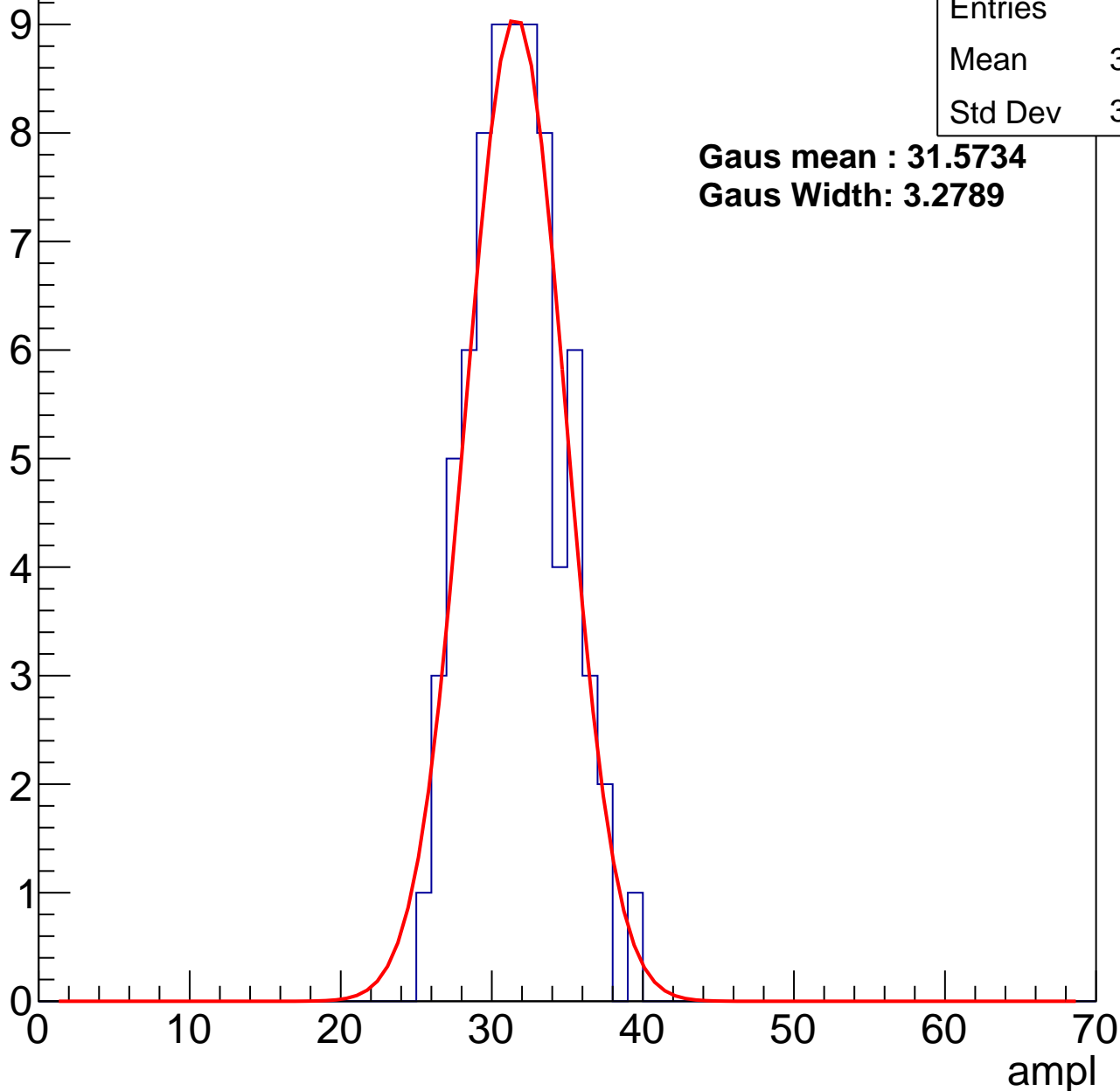
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	31.16
Std Dev	3.005

Gaus mean : 31.5734

Gaus Width: 3.2789



B1L003S, U21-ch61, adc1

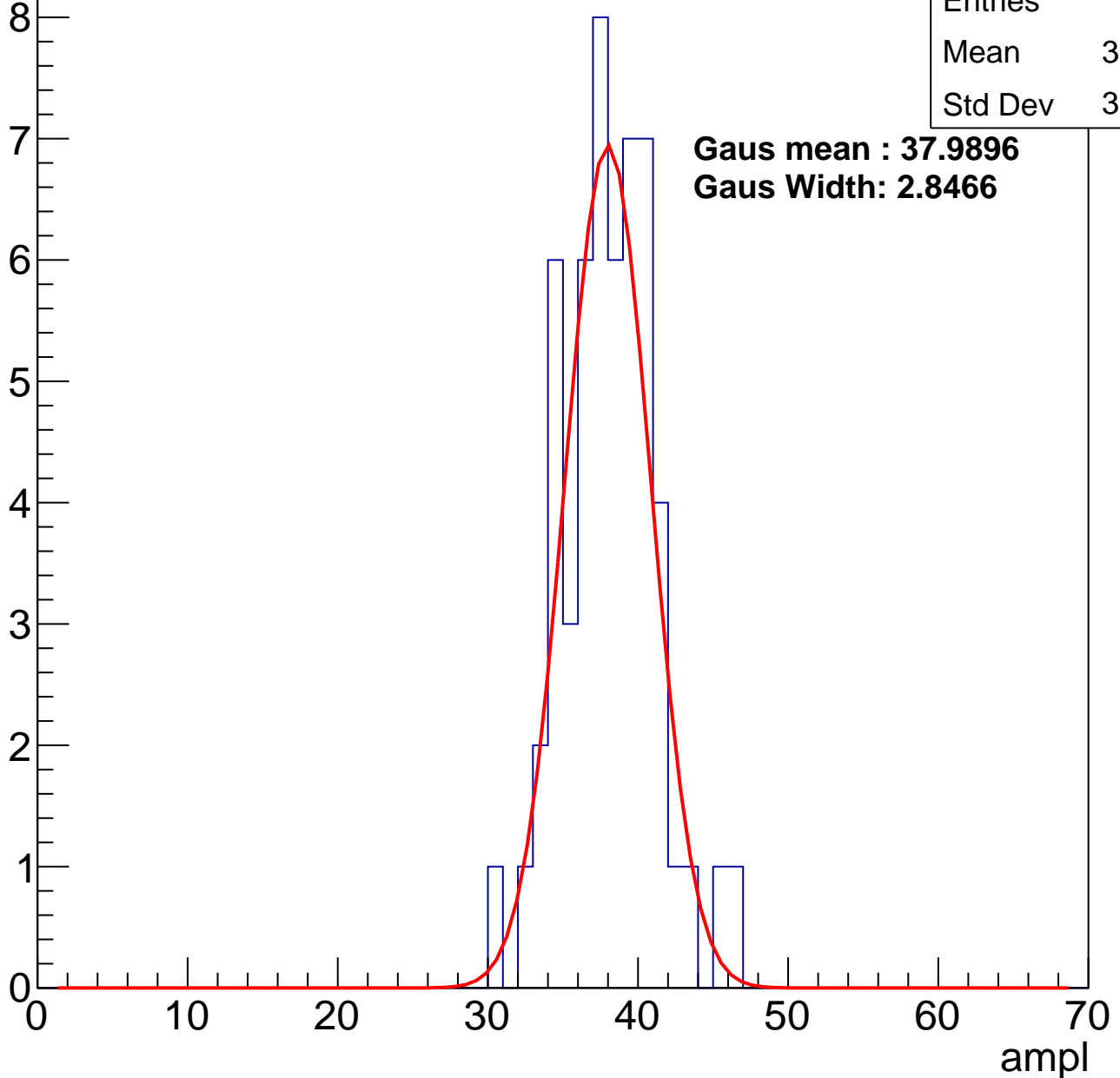
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	37.64
Std Dev	3.083

Gaus mean : 37.9896

Gaus Width: 2.8466



B1L003S, U21-ch61, adc2

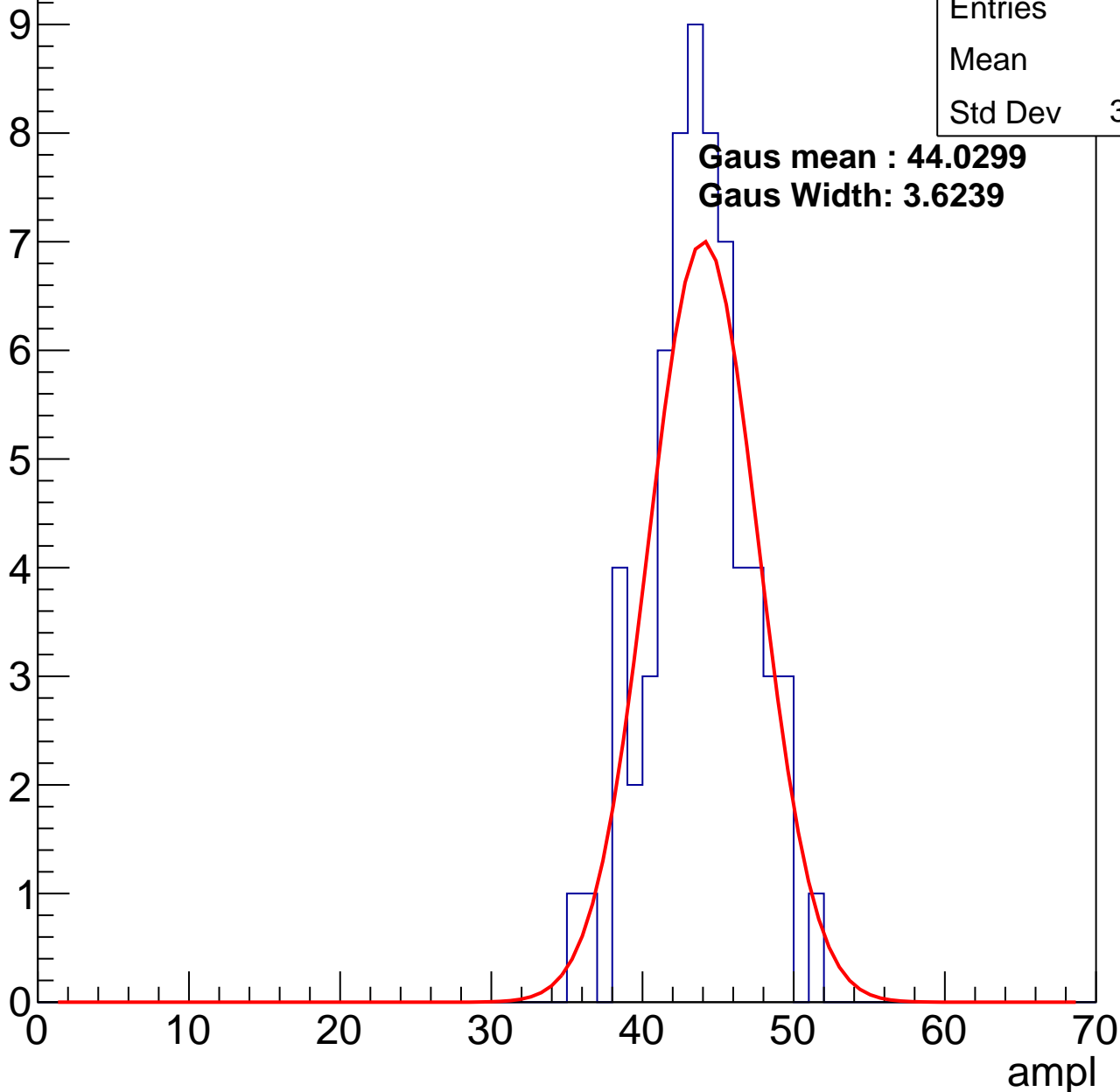
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.3
Std Dev	3.263

Gaus mean : 44.0299

Gaus Width: 3.6239

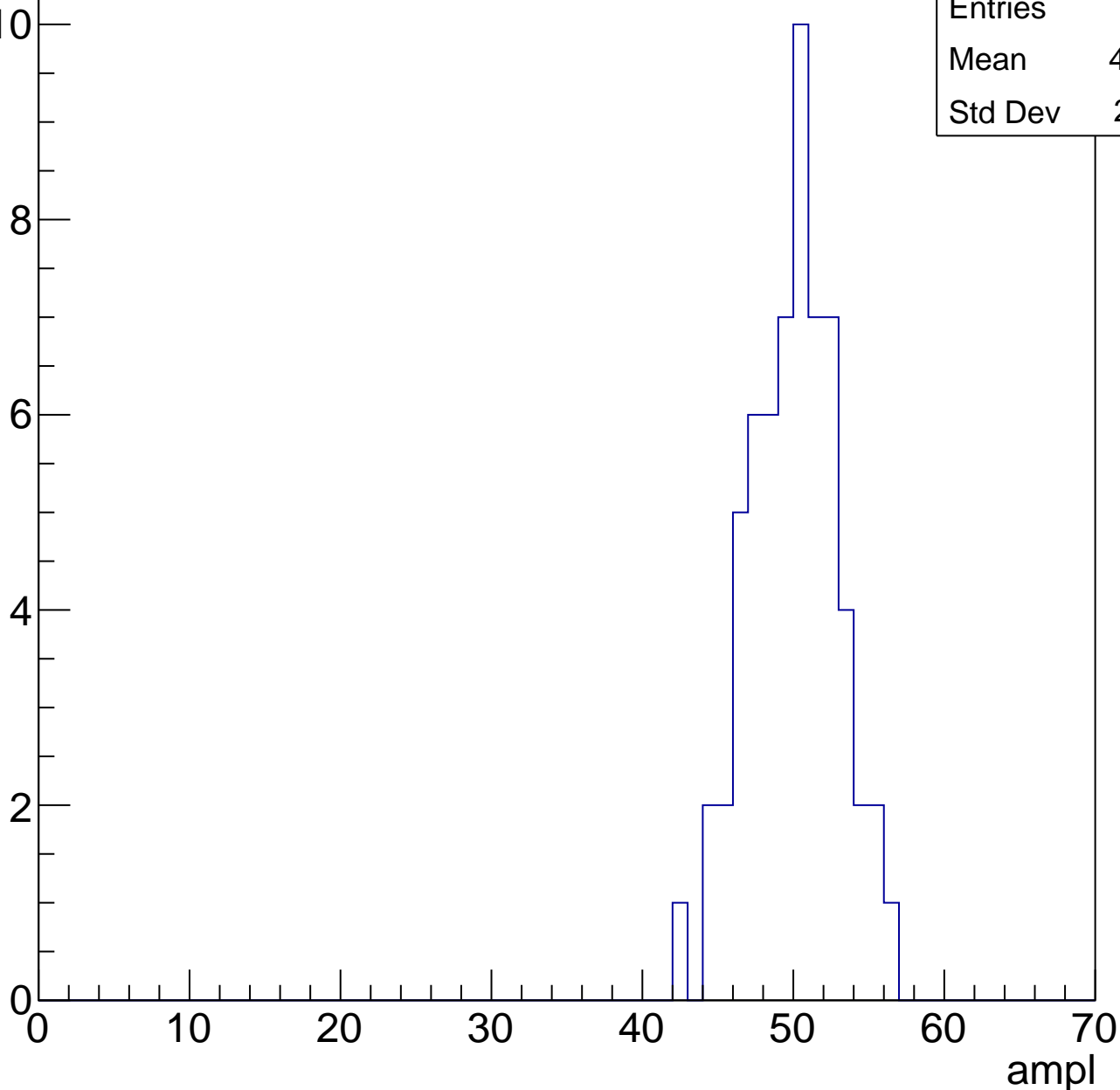


B1L003S, U21-ch61, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.52
Std Dev	2.911

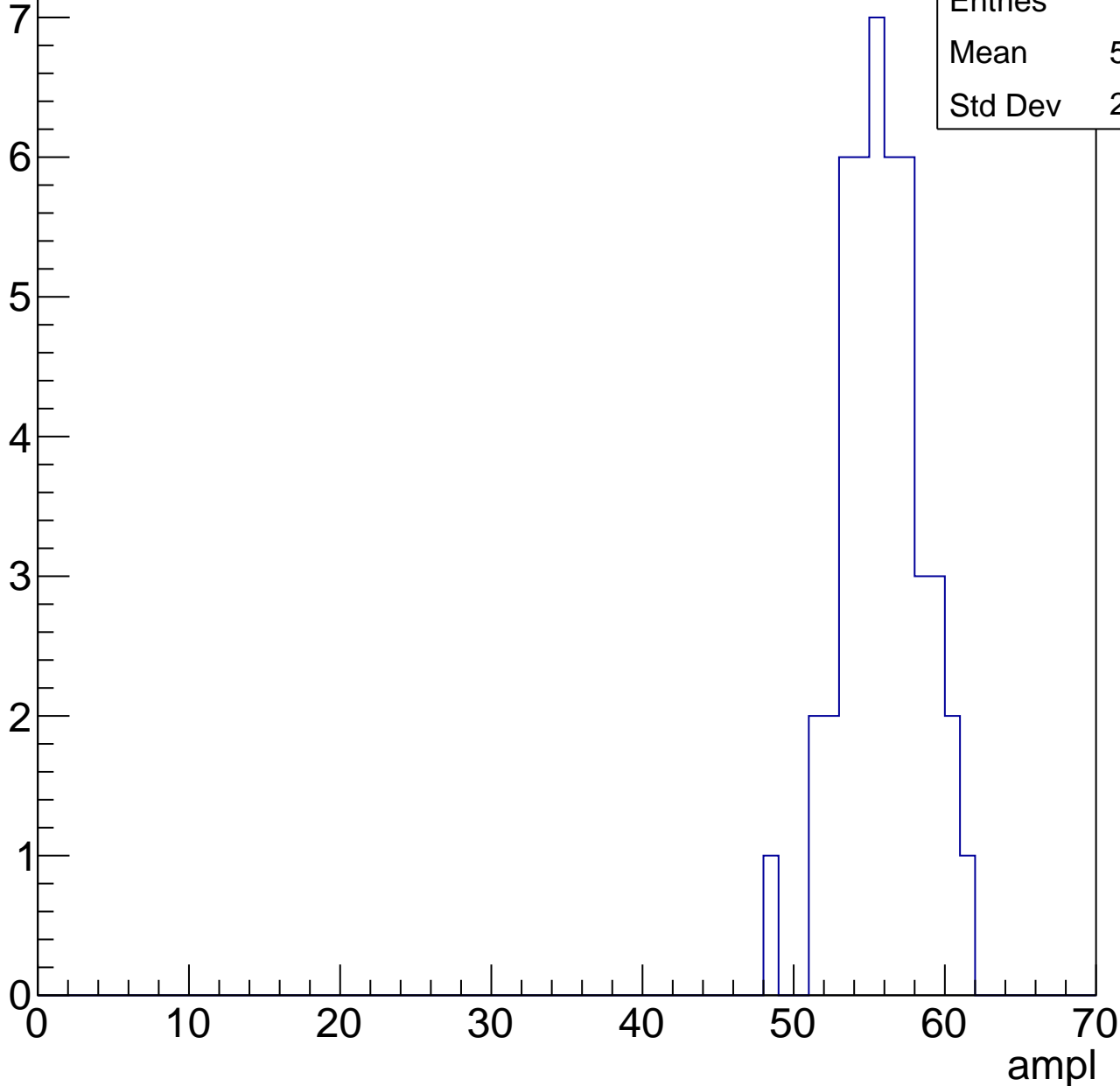


B1L003S, U21-ch61, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	55.36
Std Dev	2.643

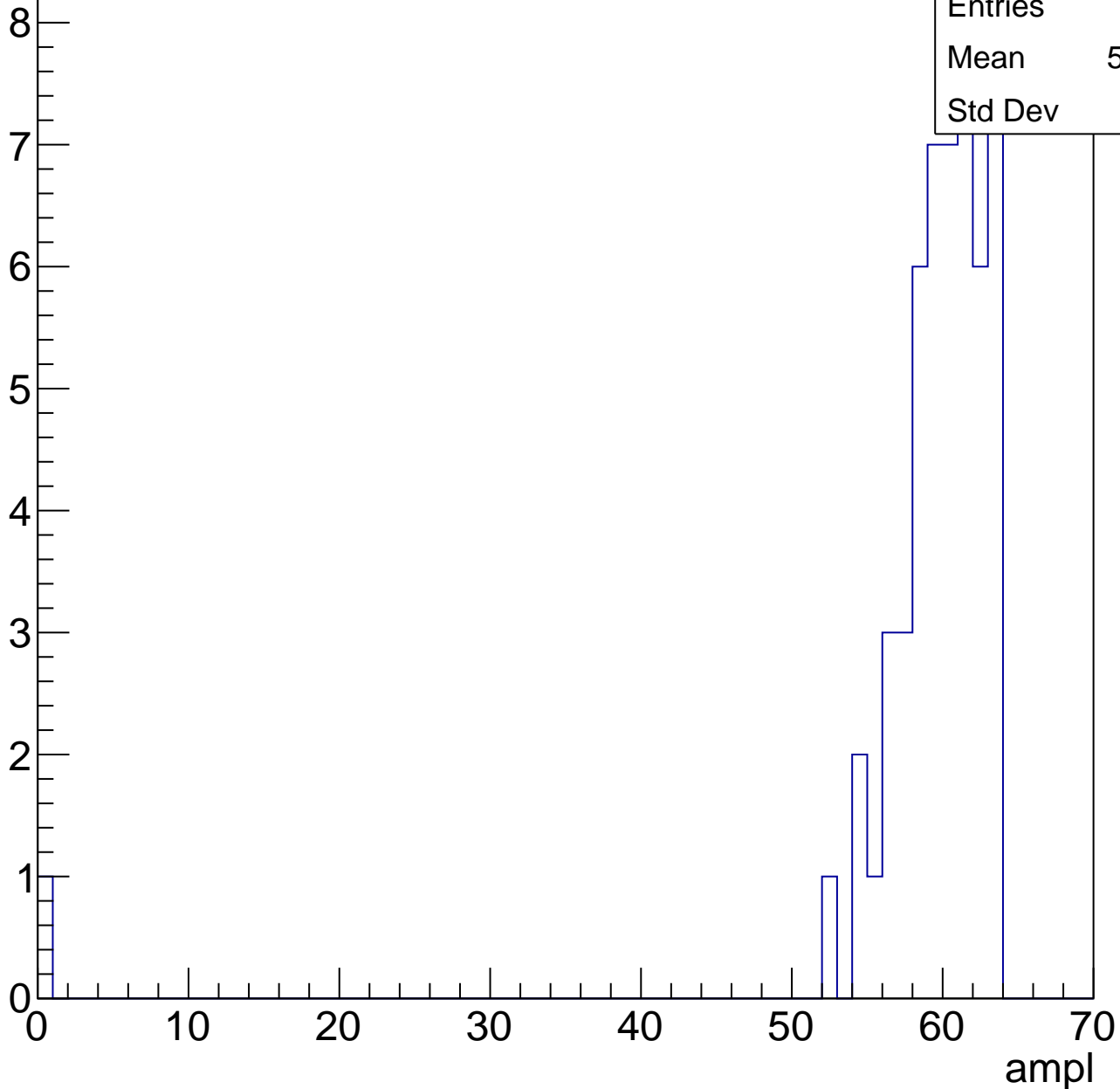


B1L003S, U21-ch61, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

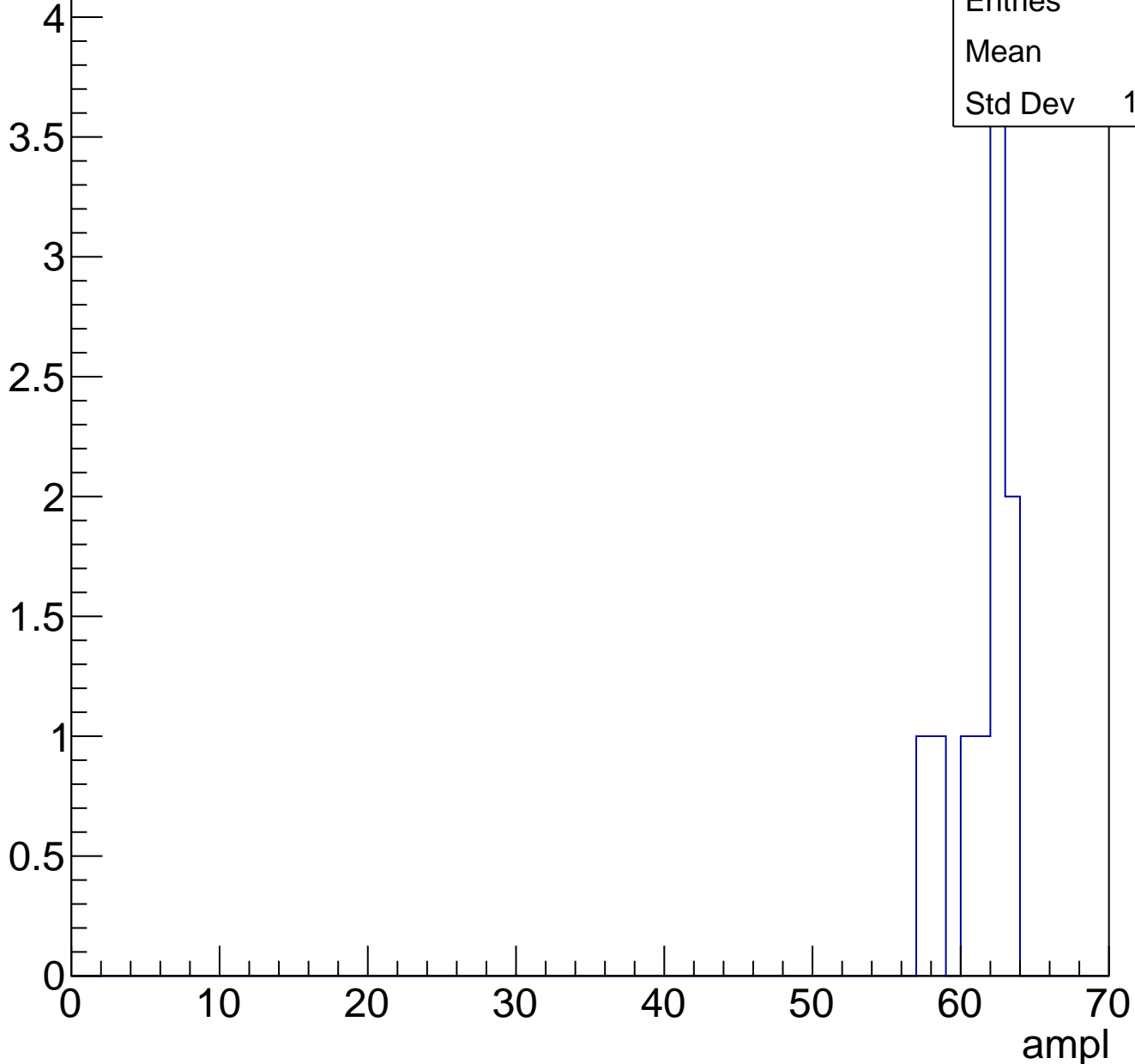
Entries	53
Mean	58.47
Std Dev	8.52



B1L003S, U21-ch61, adc6

calib_packv5_042523_0143.root, FC#13, port D2

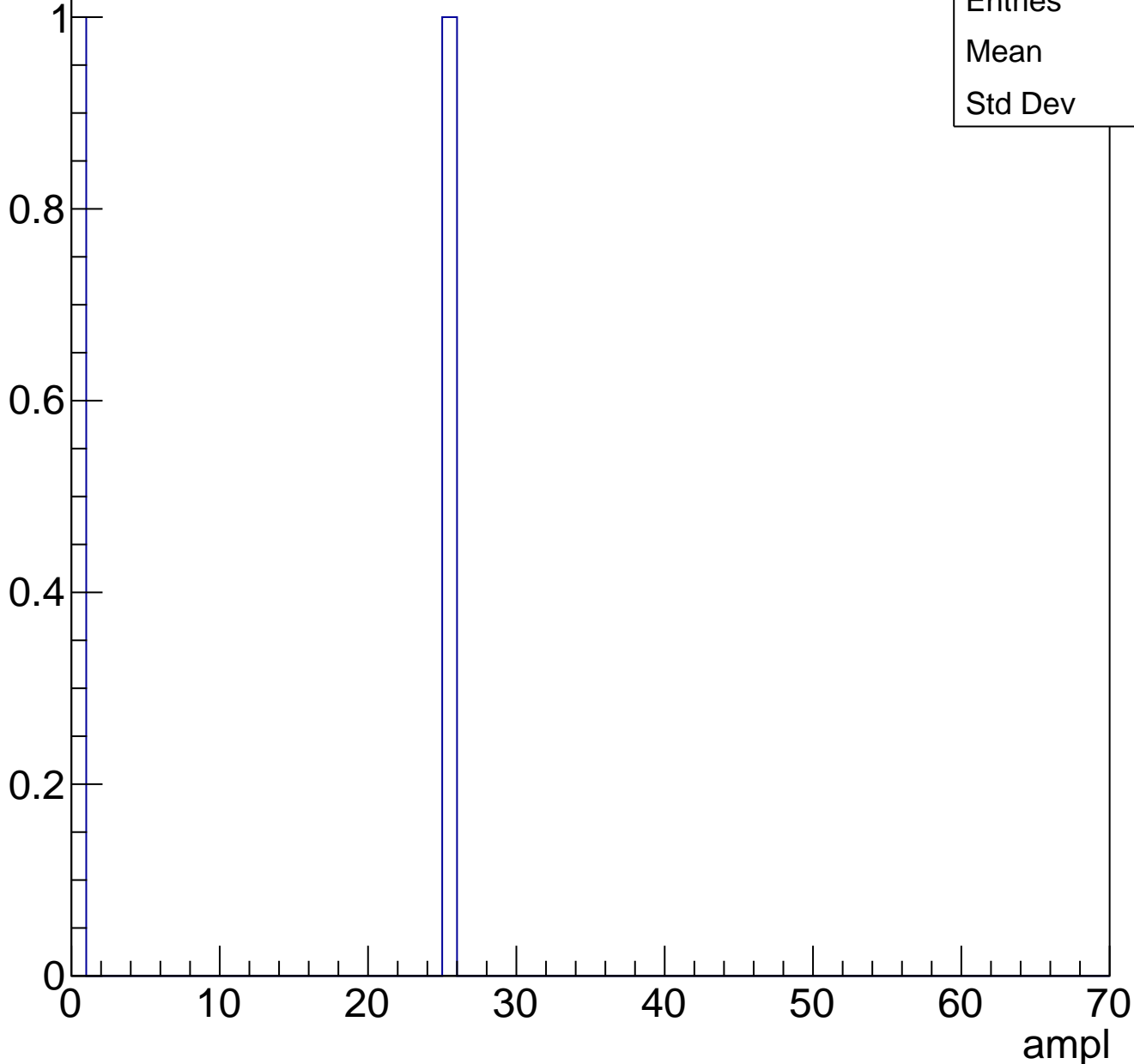
Entry



B1L003S, U21-ch61, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch62, adc0

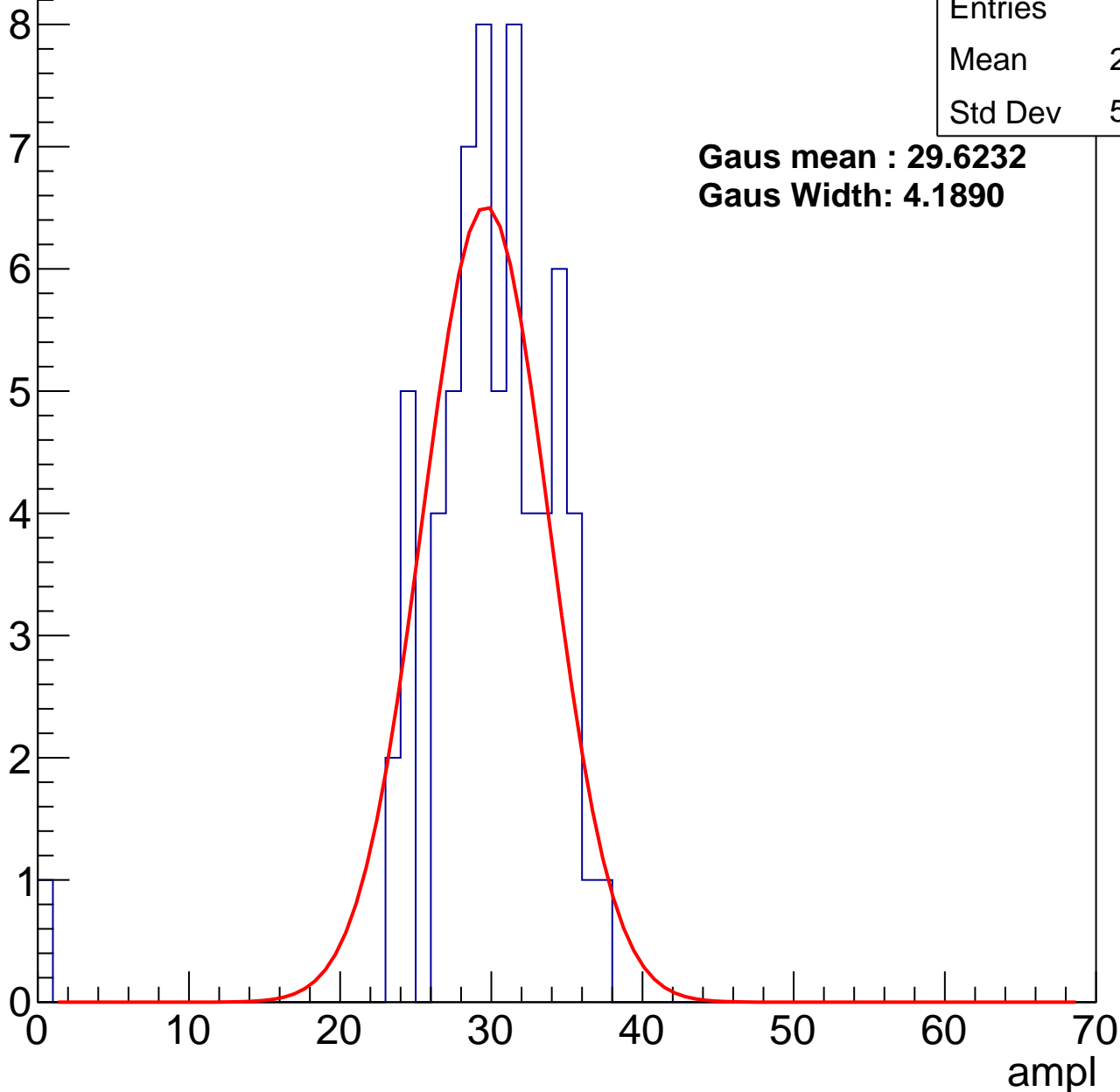
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.35
Std Dev	5.018

Gaus mean : 29.6232

Gaus Width: 4.1890



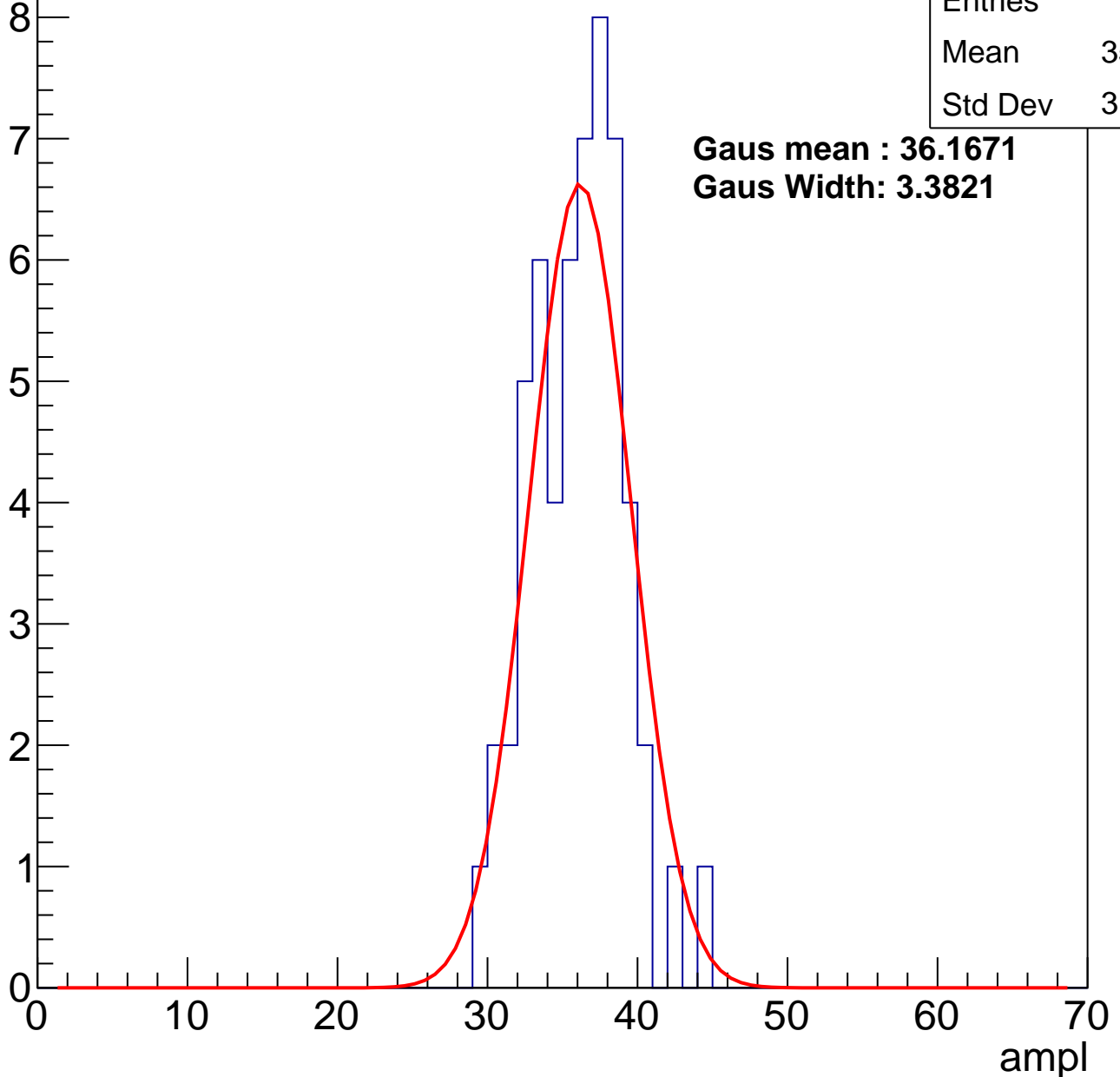
B1L003S, U21-ch62, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	35.55
Std Dev	3.047

Gaus mean : 36.1671
Gaus Width: 3.3821



B1L003S, U21-ch62, adc2

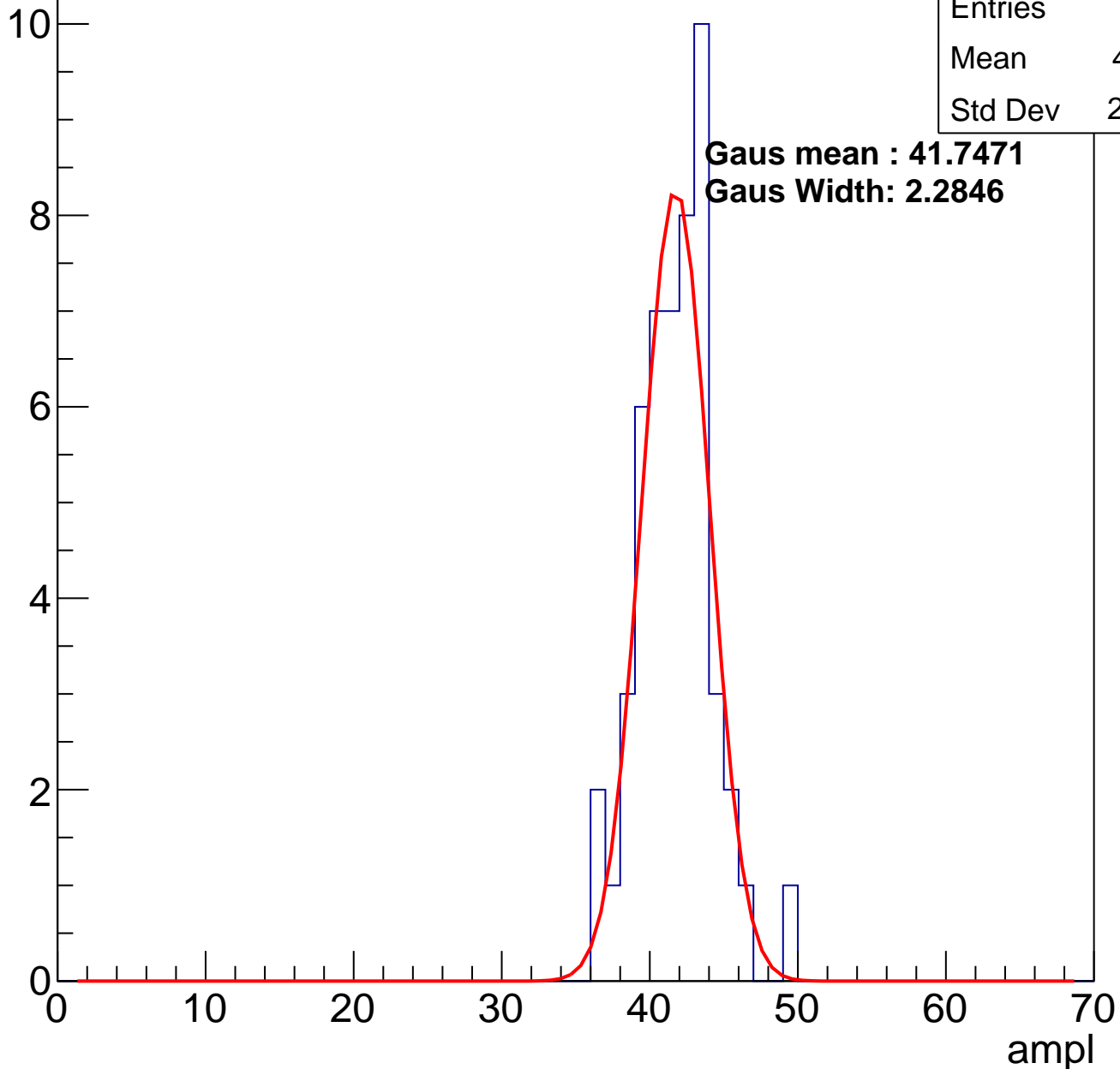
calib_packv5_042523_0143.root, FC#13, port D2

Entries	51
Mean	41.31
Std Dev	2.485

Gaus mean : 41.7471

Gaus Width: 2.2846

Entry

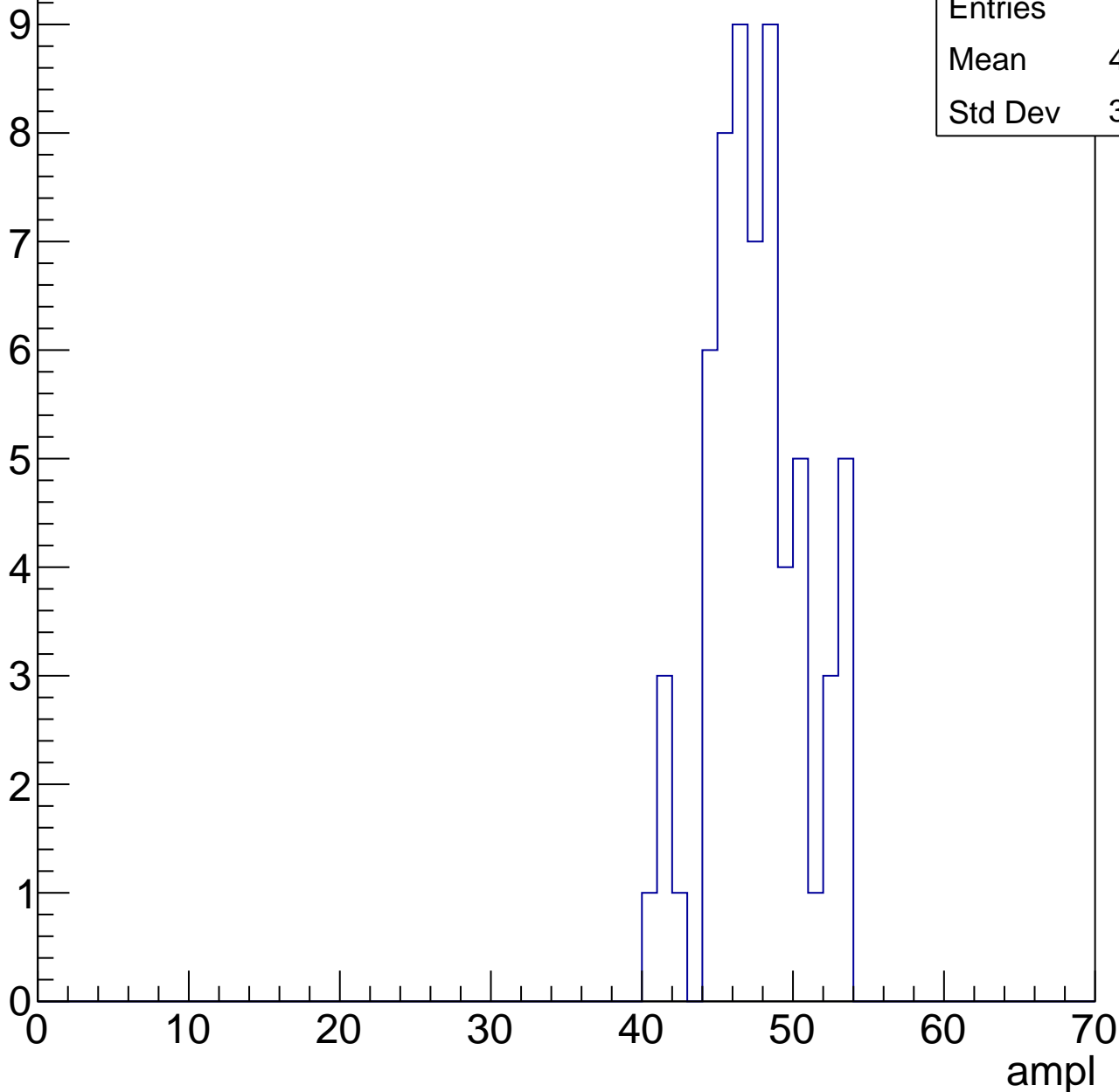


B1L003S, U21-ch62, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

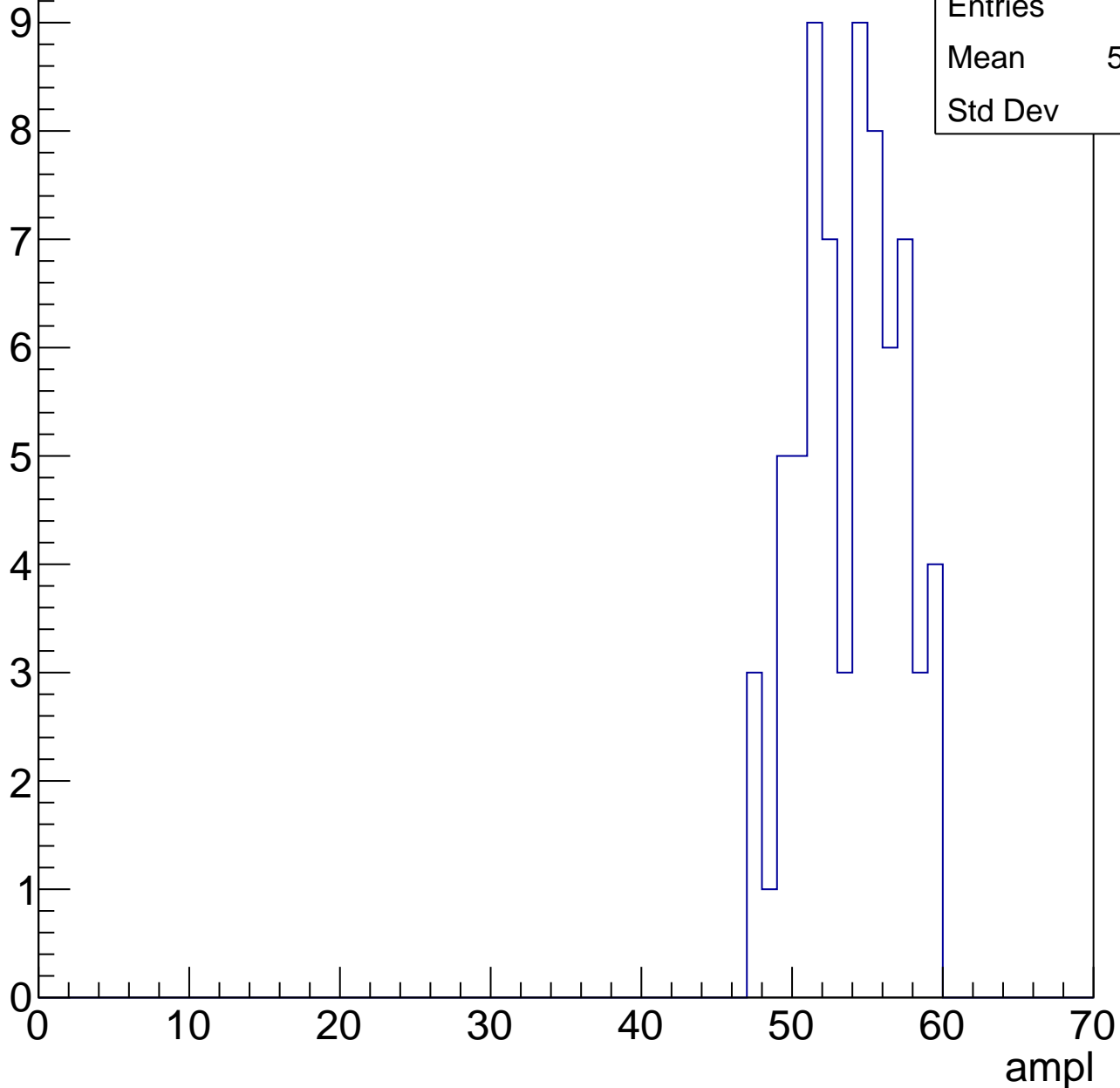
Entries	62
Mean	47.13
Std Dev	3.155



B1L003S, U21-ch62, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



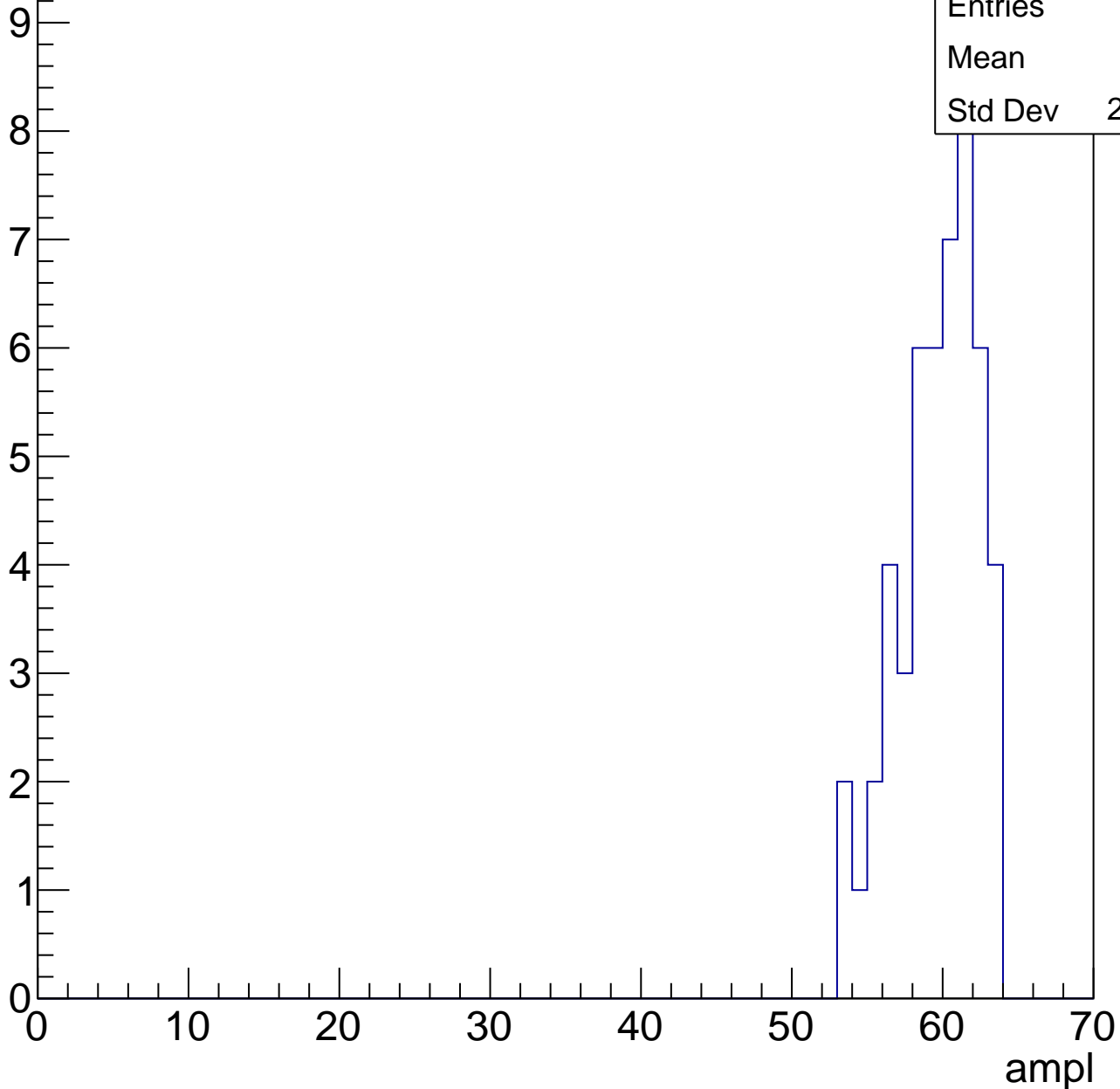
Entries	70
Mean	53.39
Std Dev	3.2

B1L003S, U21-ch62, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

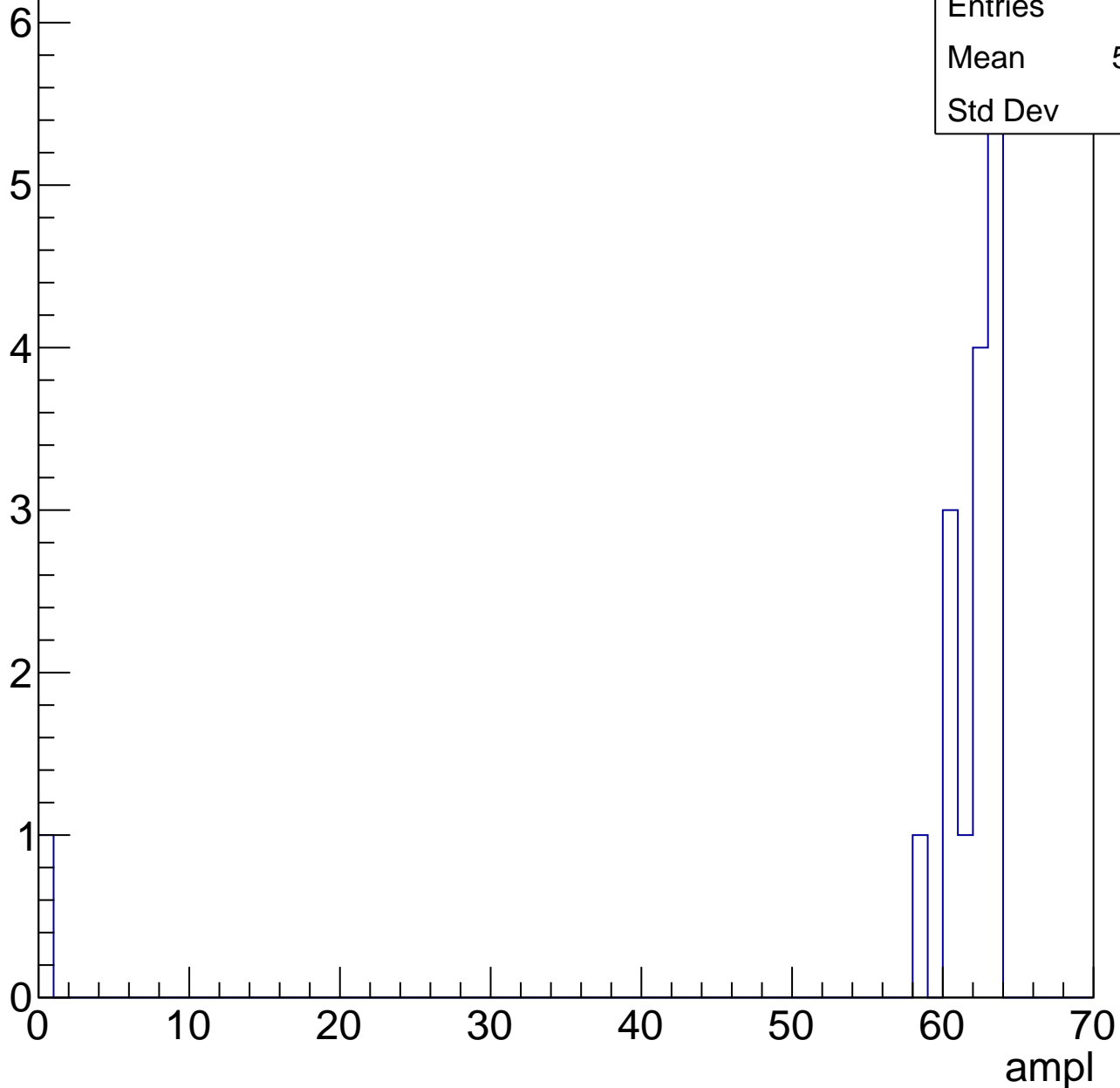
Entries	50
Mean	59.2
Std Dev	2.615



B1L003S, U21-ch62, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch62, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch63, adc0

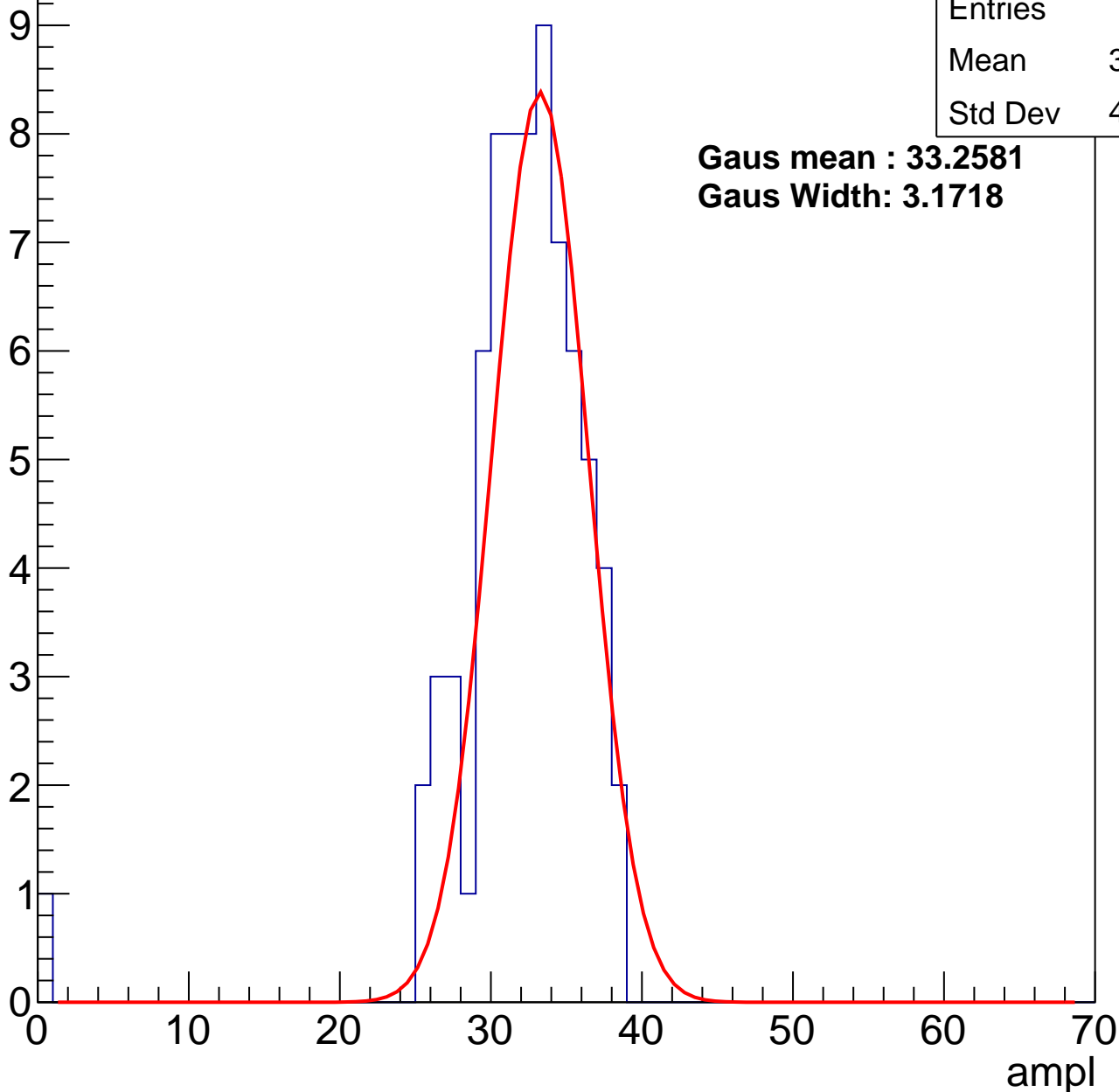
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	31.56
Std Dev	4.888

Gaus mean : 33.2581

Gaus Width: 3.1718



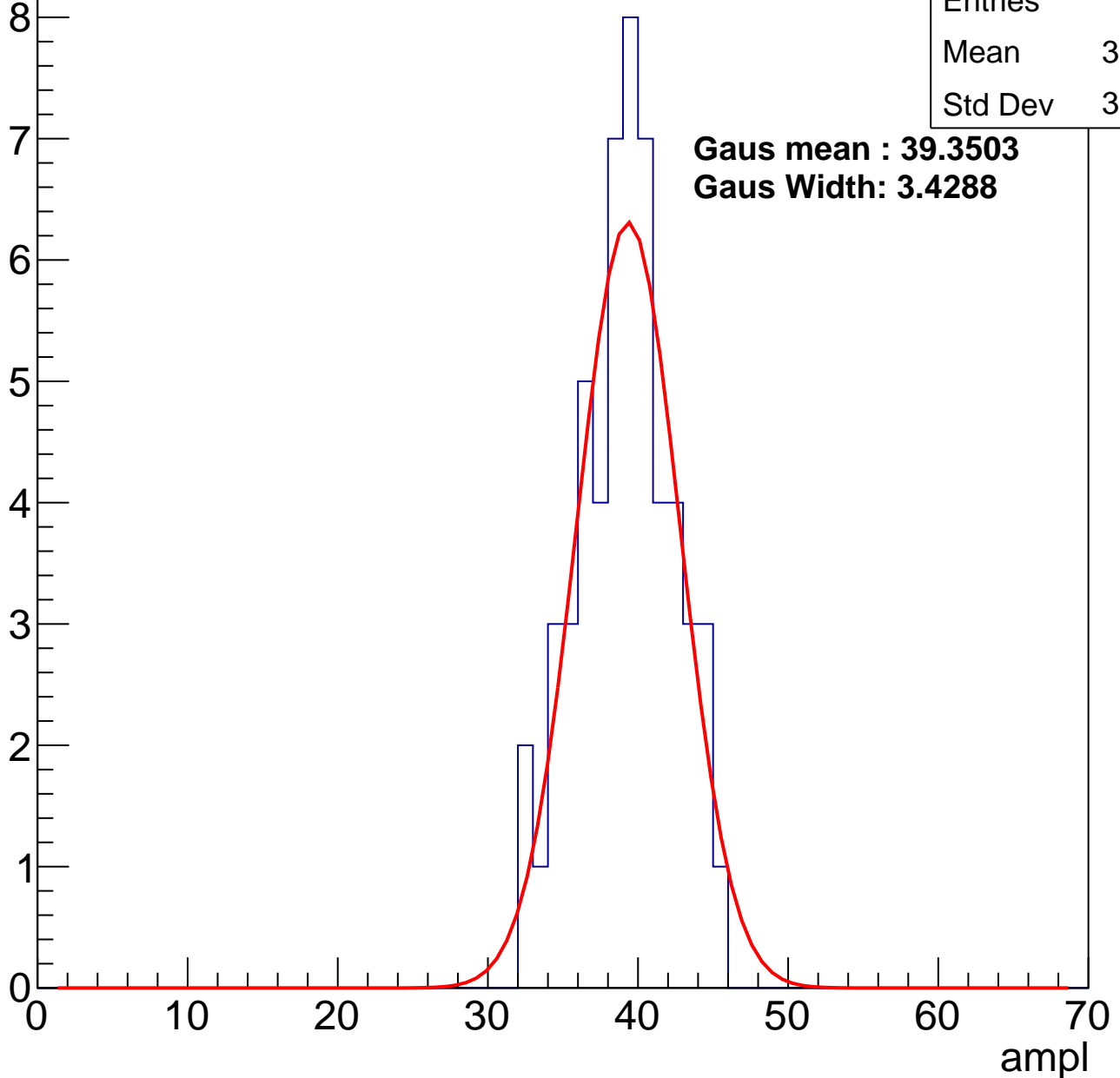
B1L003S, U21-ch63, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	38.69
Std Dev	3.127

Gaus mean : 39.3503
Gaus Width: 3.4288



B1L003S, U21-ch63, adc2

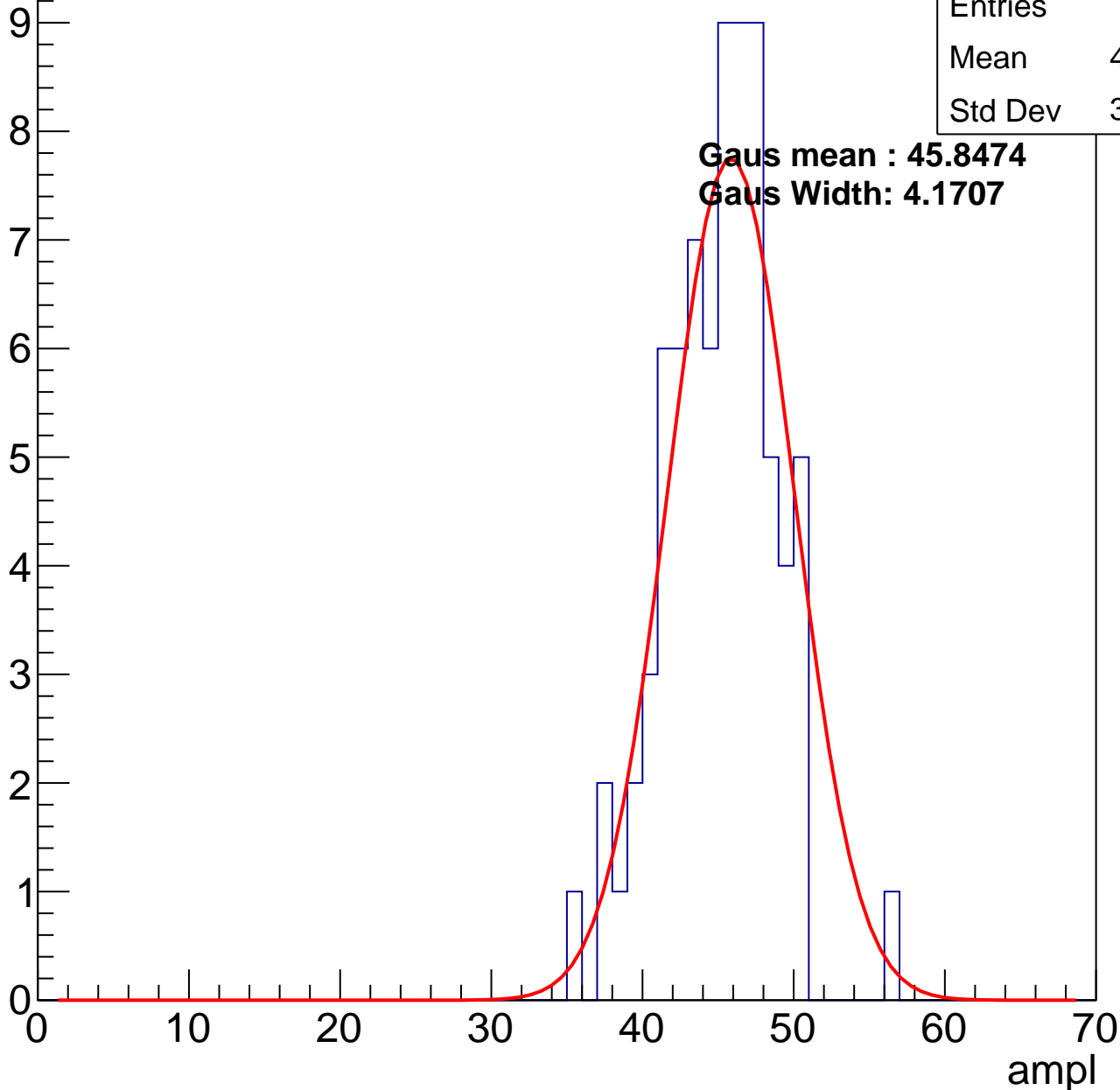
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	44.63
Std Dev	3.623

Gaus mean : 45.8474

Gaus Width: 4.1707

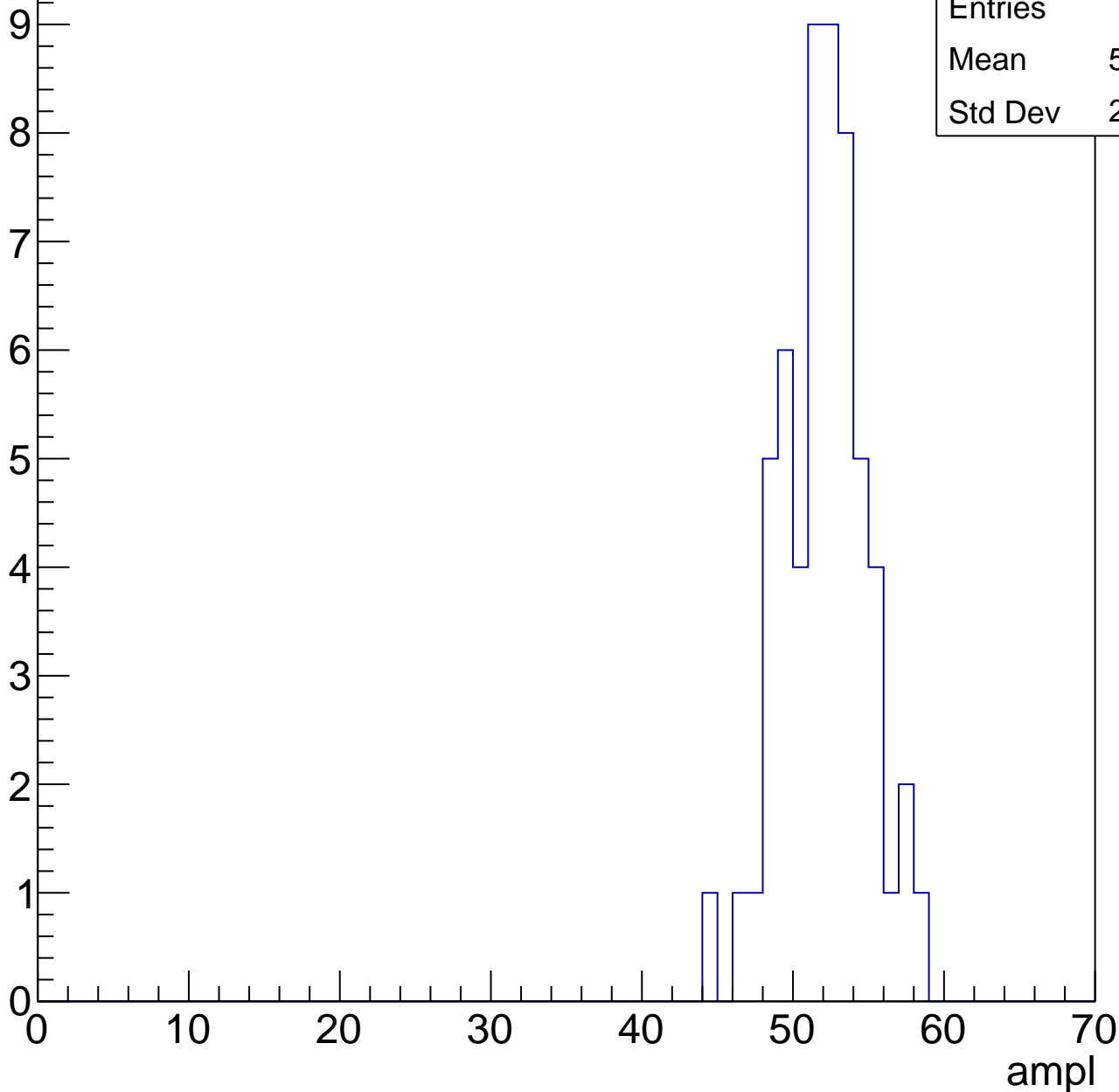


B1L003S, U21-ch63, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	51.58
Std Dev	2.778

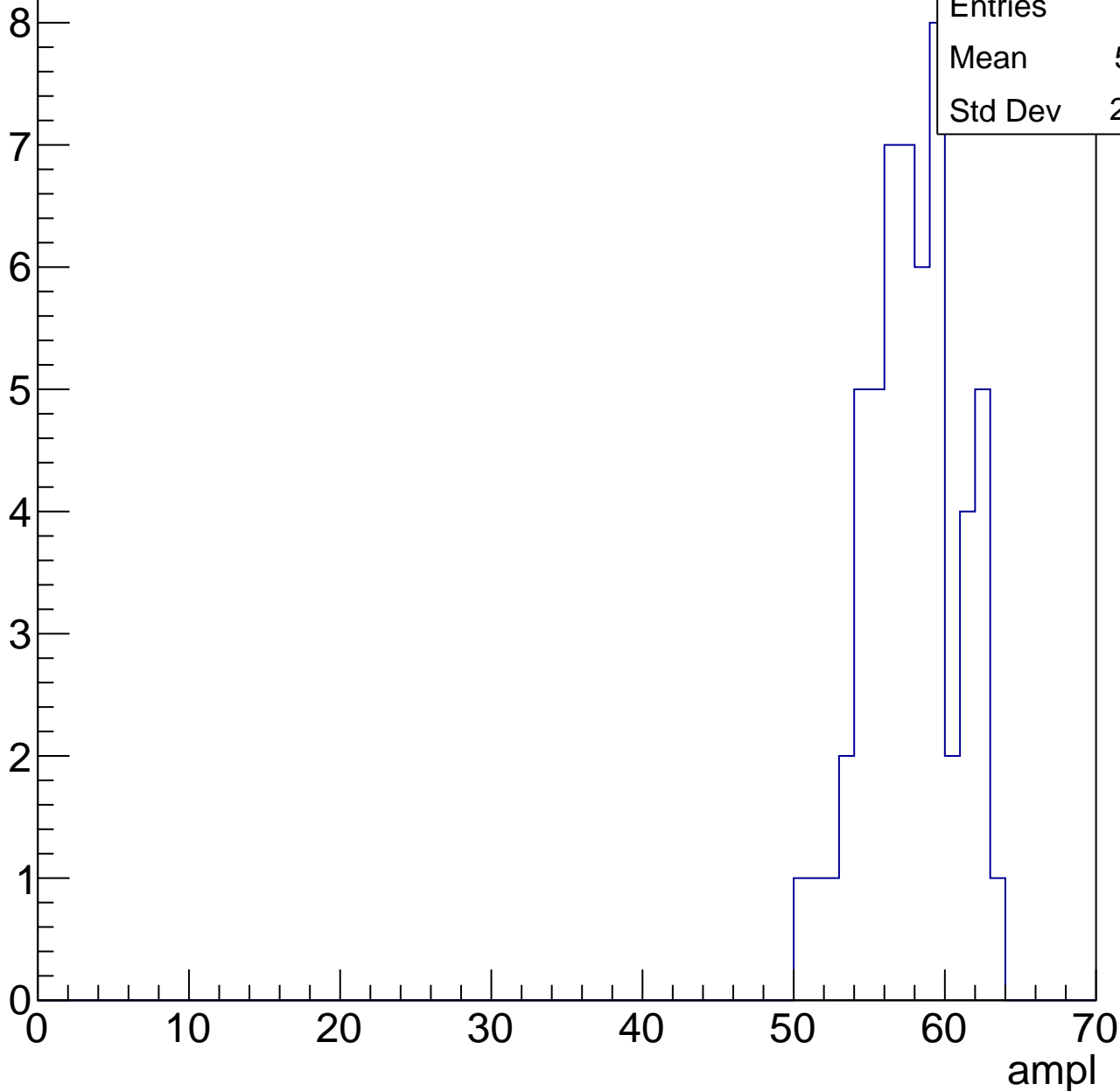


B1L003S, U21-ch63, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

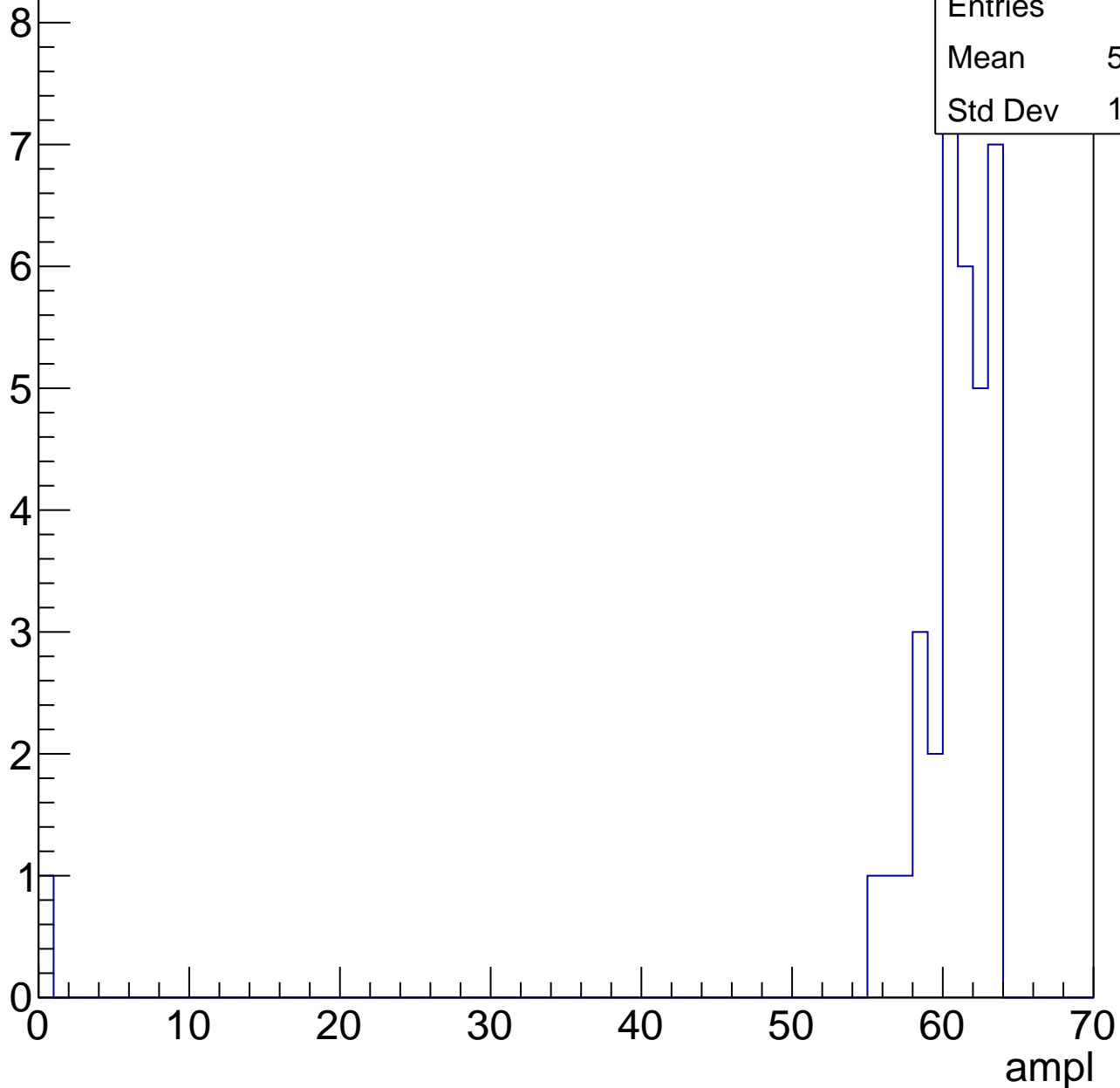
Entries	55
Mean	57.31
Std Dev	2.984



B1L003S, U21-ch63, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch63, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L003S, U21-ch63, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B1L003S, U21-ch64, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	27.48
Std Dev	6.611

Gaus mean : 28.9126

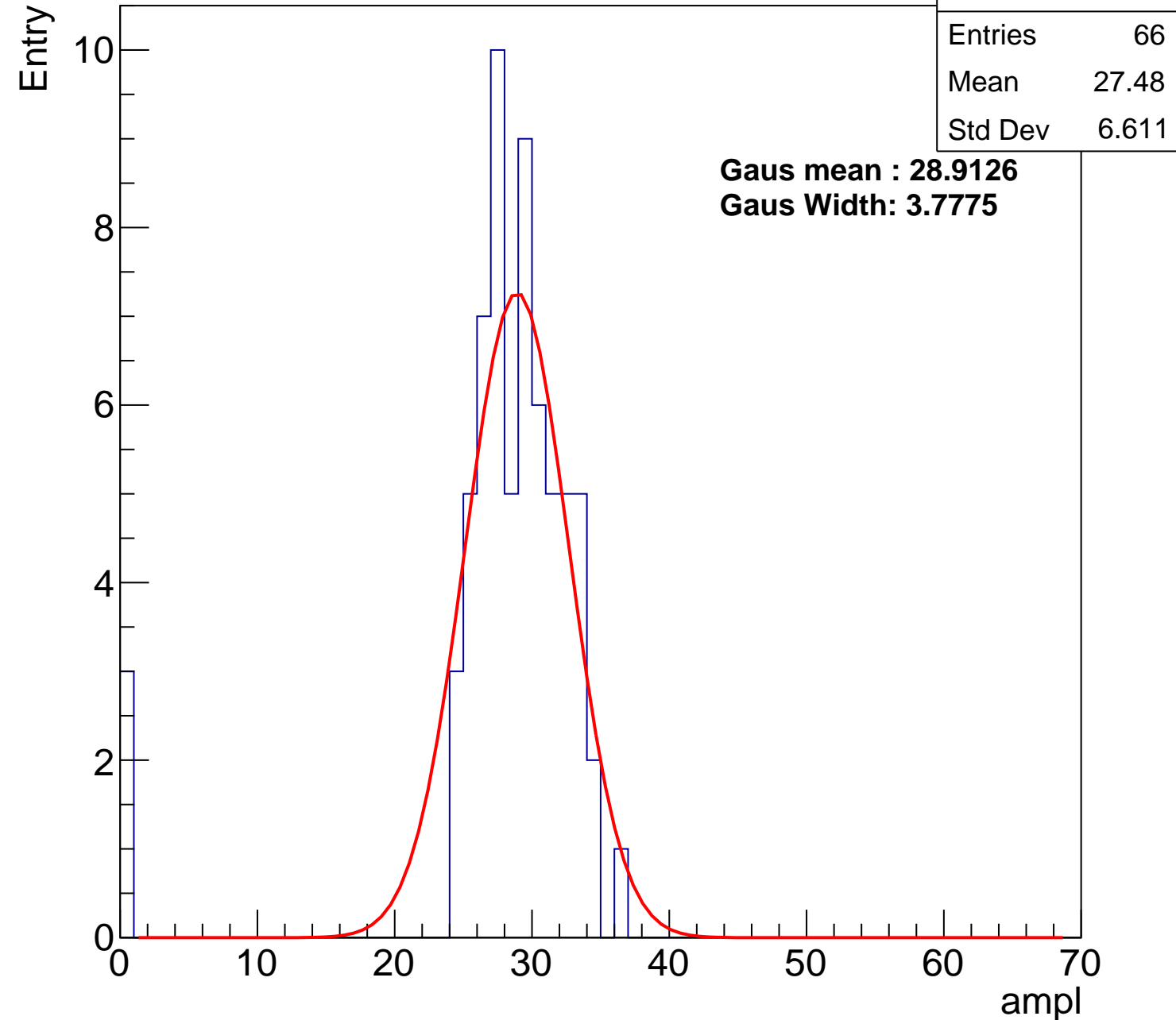
Gaus Width: 3.7775

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch64, adc1

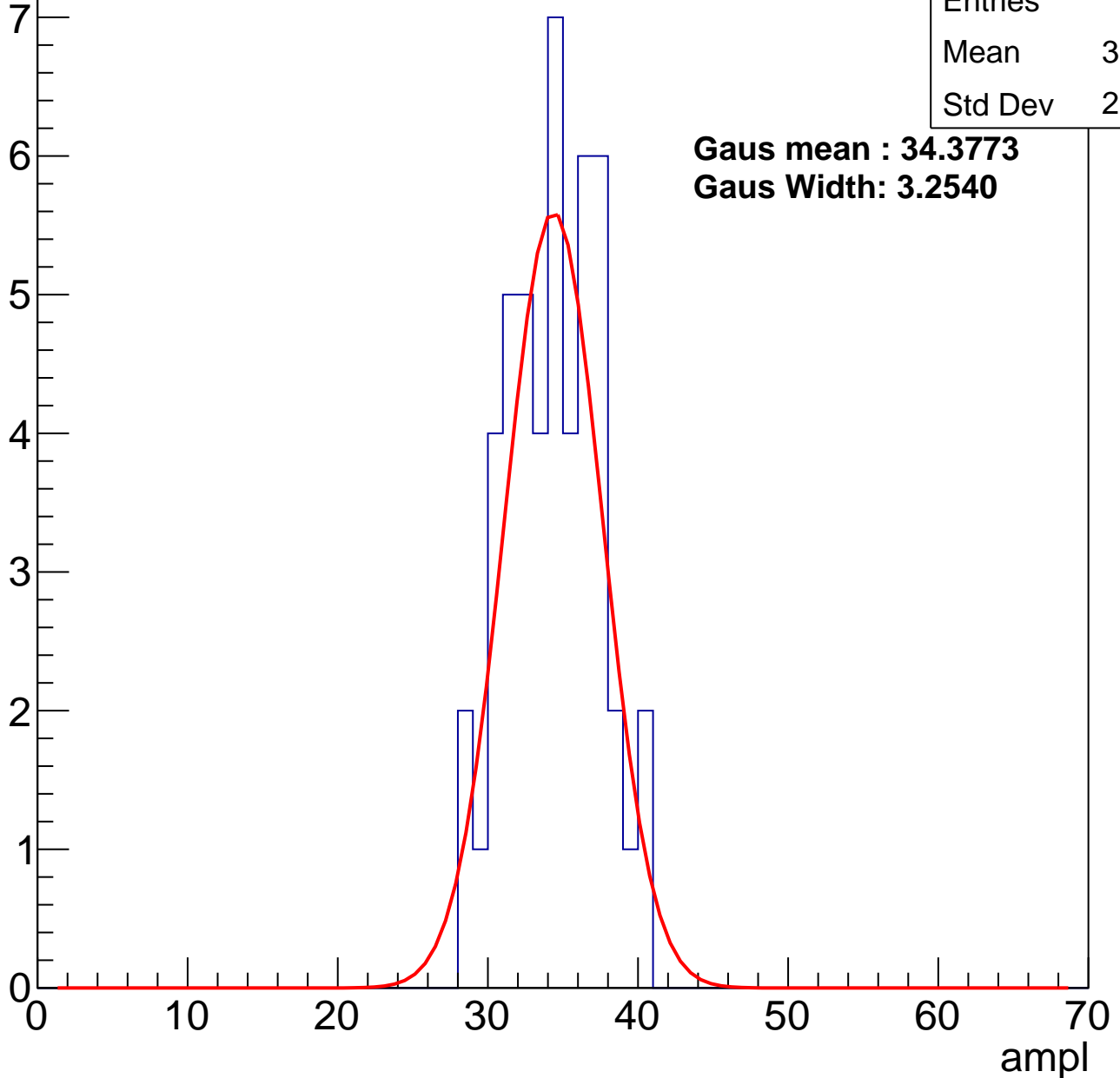
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	33.94
Std Dev	2.999

Gaus mean : 34.3773

Gaus Width: 3.2540



B1L003S, U21-ch64, adc2

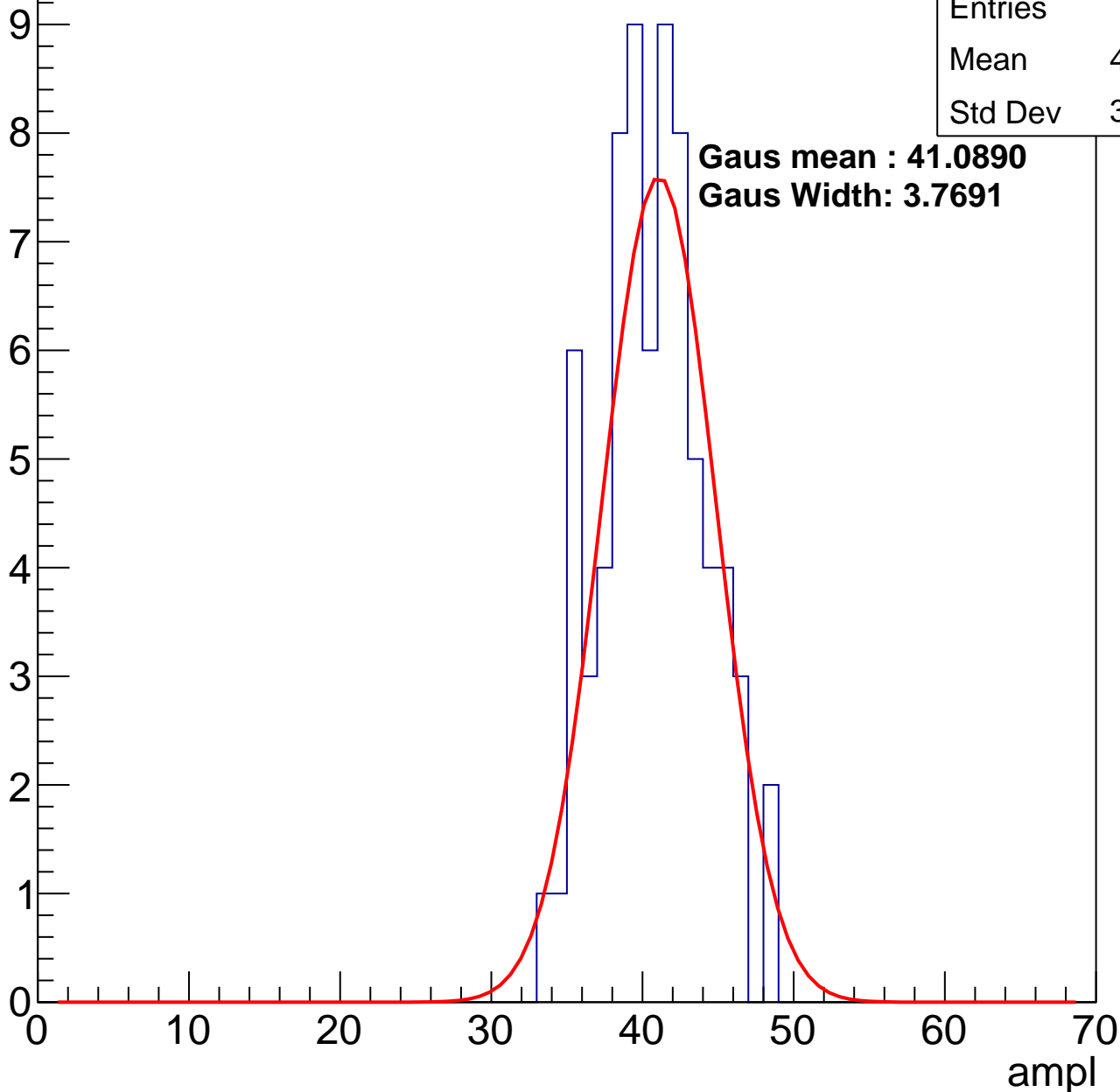
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	40.25
Std Dev	3.395

Gaus mean : 41.0890

Gaus Width: 3.7691

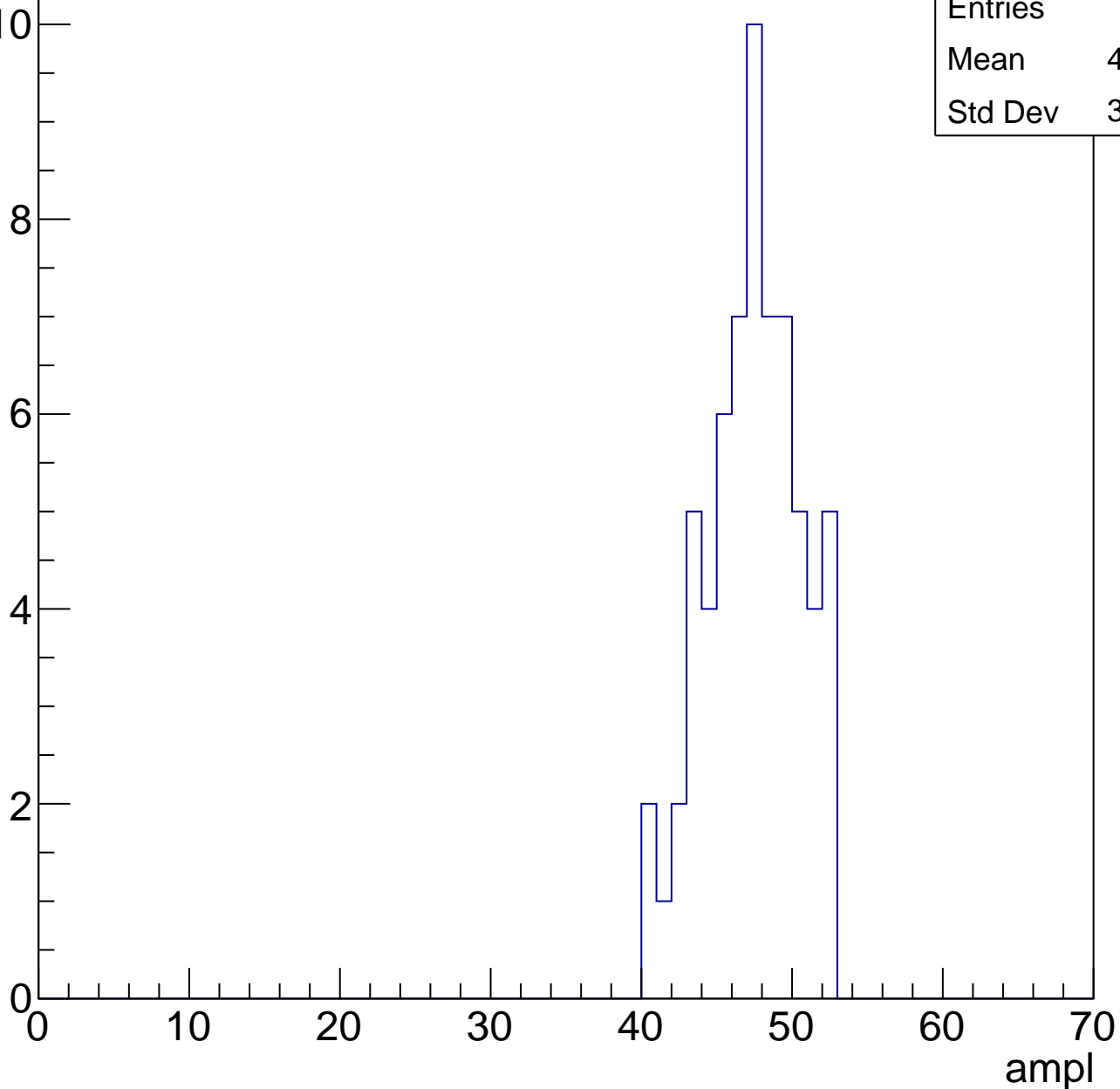


B1L003S, U21-ch64, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	46.94
Std Dev	3.038

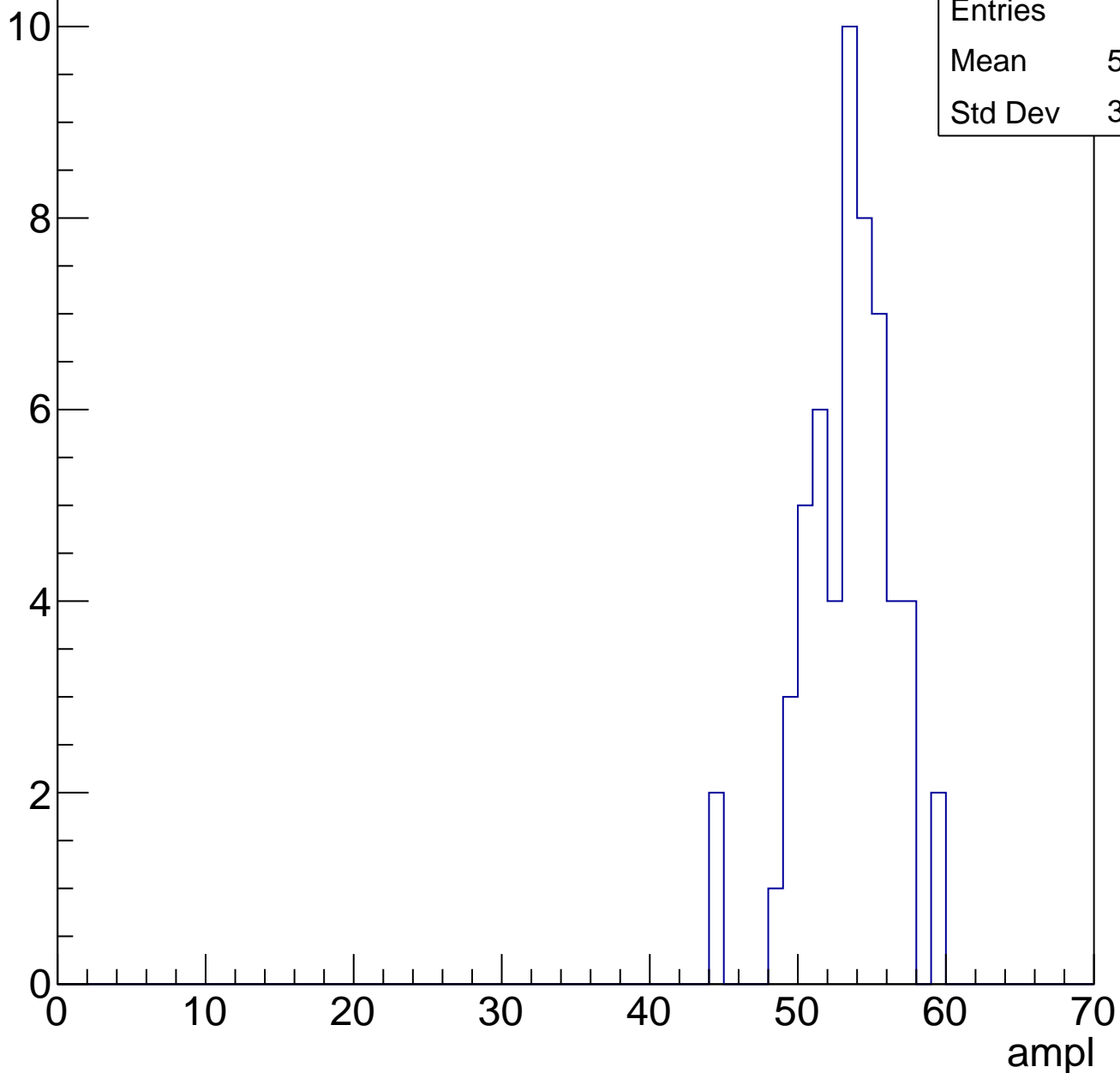


B1L003S, U21-ch64, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

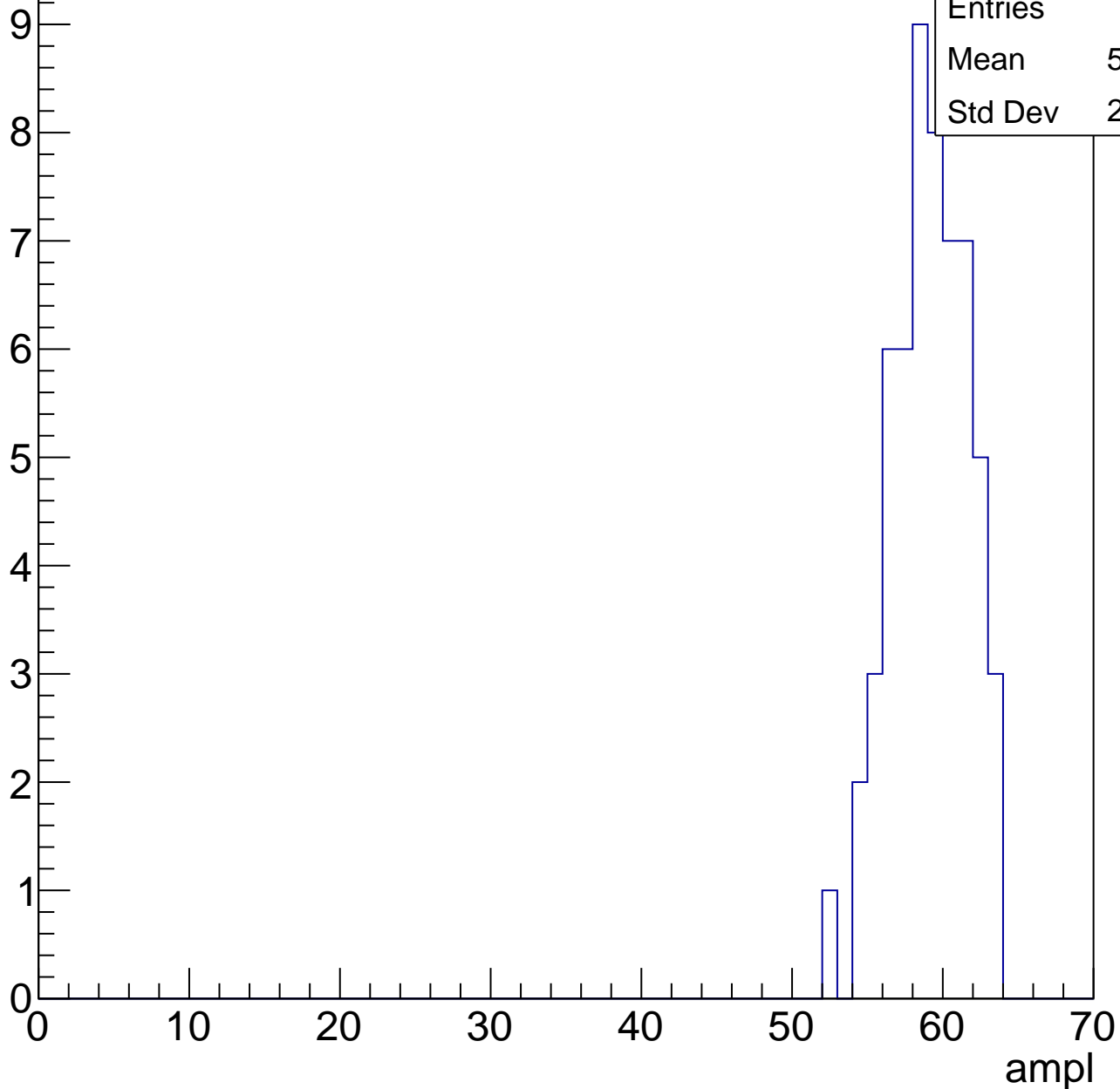
Entries	56
Mean	52.93
Std Dev	3.035



B1L003S, U21-ch64, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

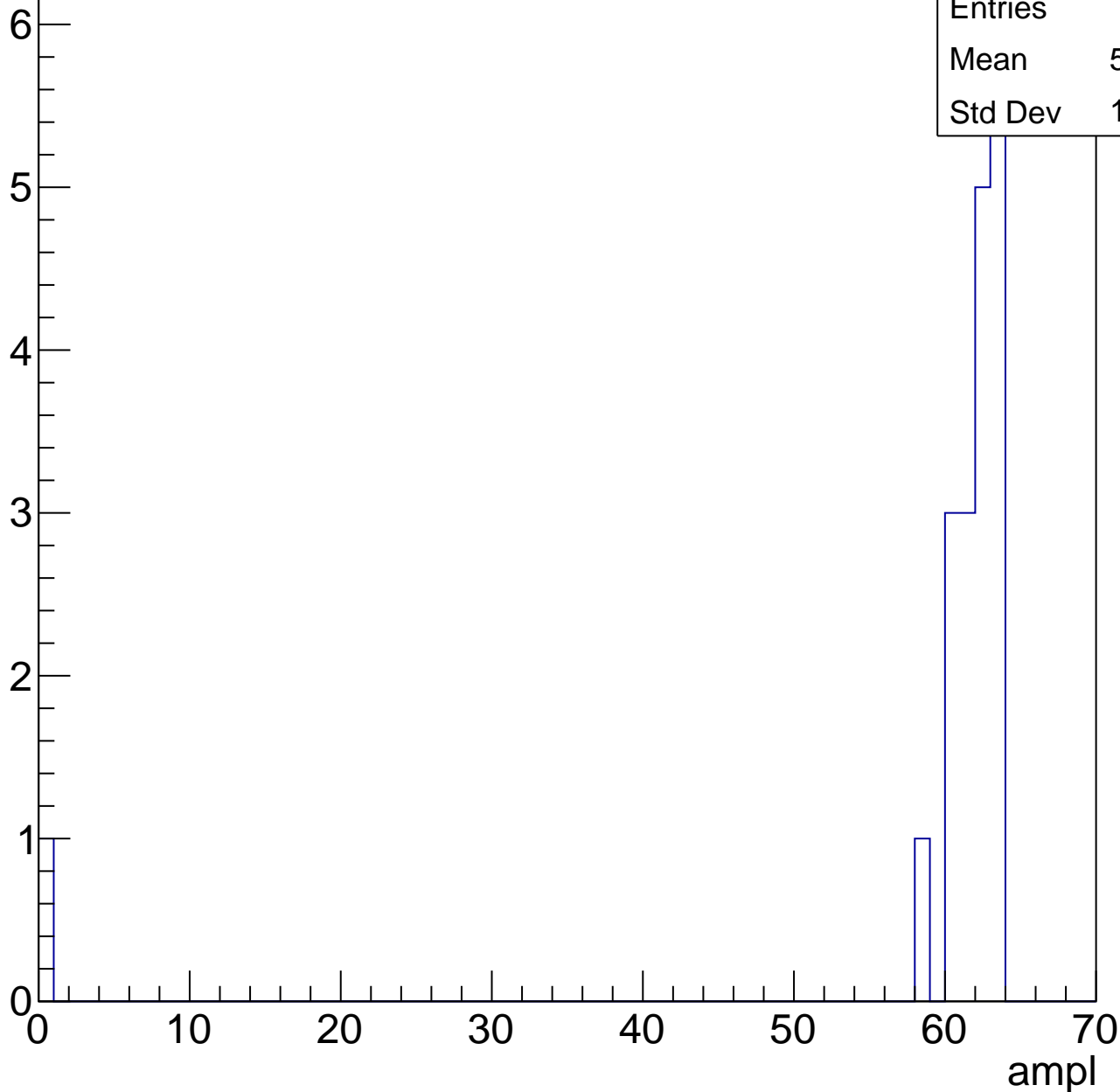


B1L003S, U21-ch64, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

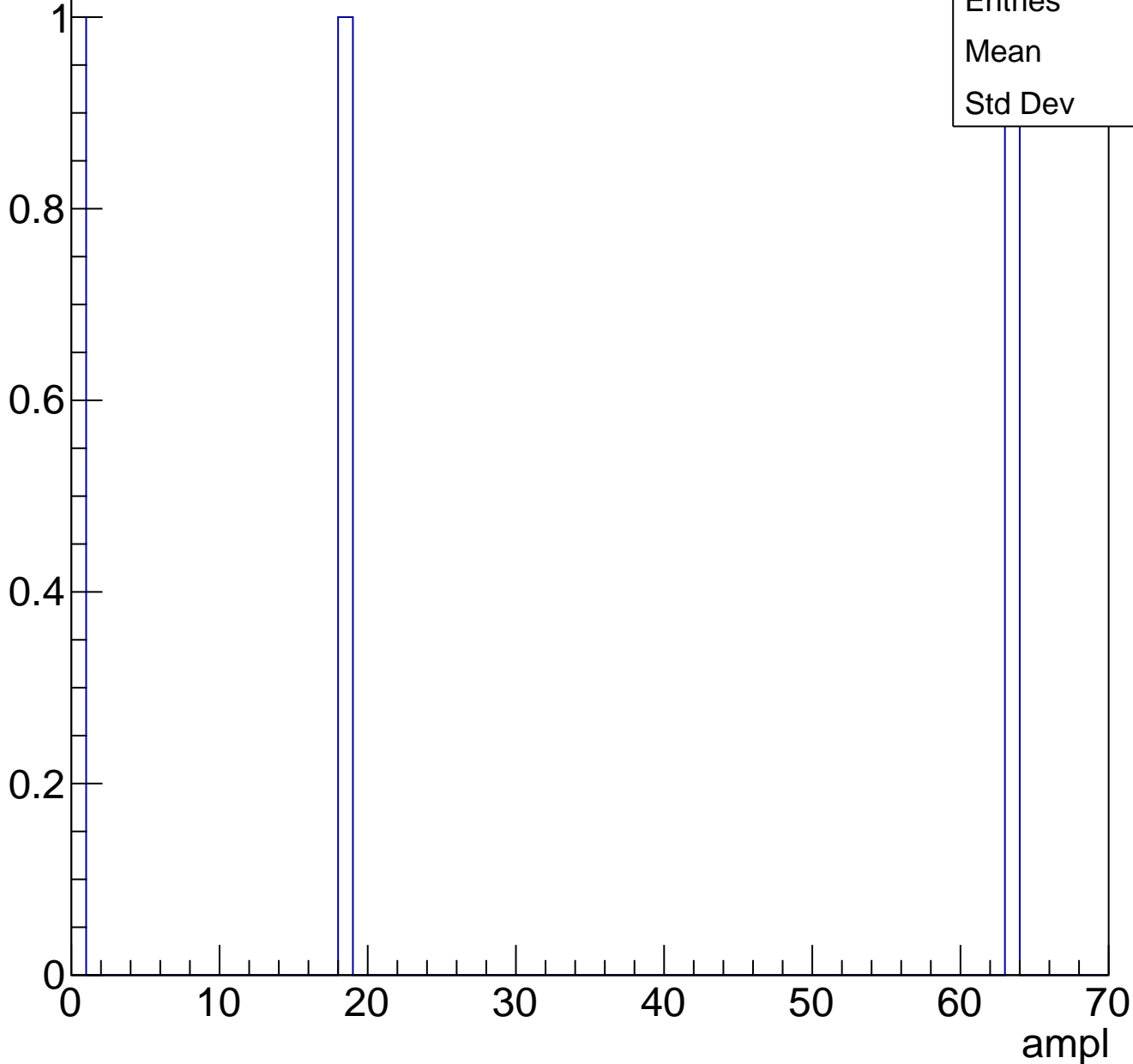
Entries	19
Mean	58.37
Std Dev	13.82



B1L003S, U21-ch64, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	27
Std Dev	26.5

B1L003S, U21-ch65, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	53
Mean	28.98
Std Dev	3.406

Gaus mean : 30.2752

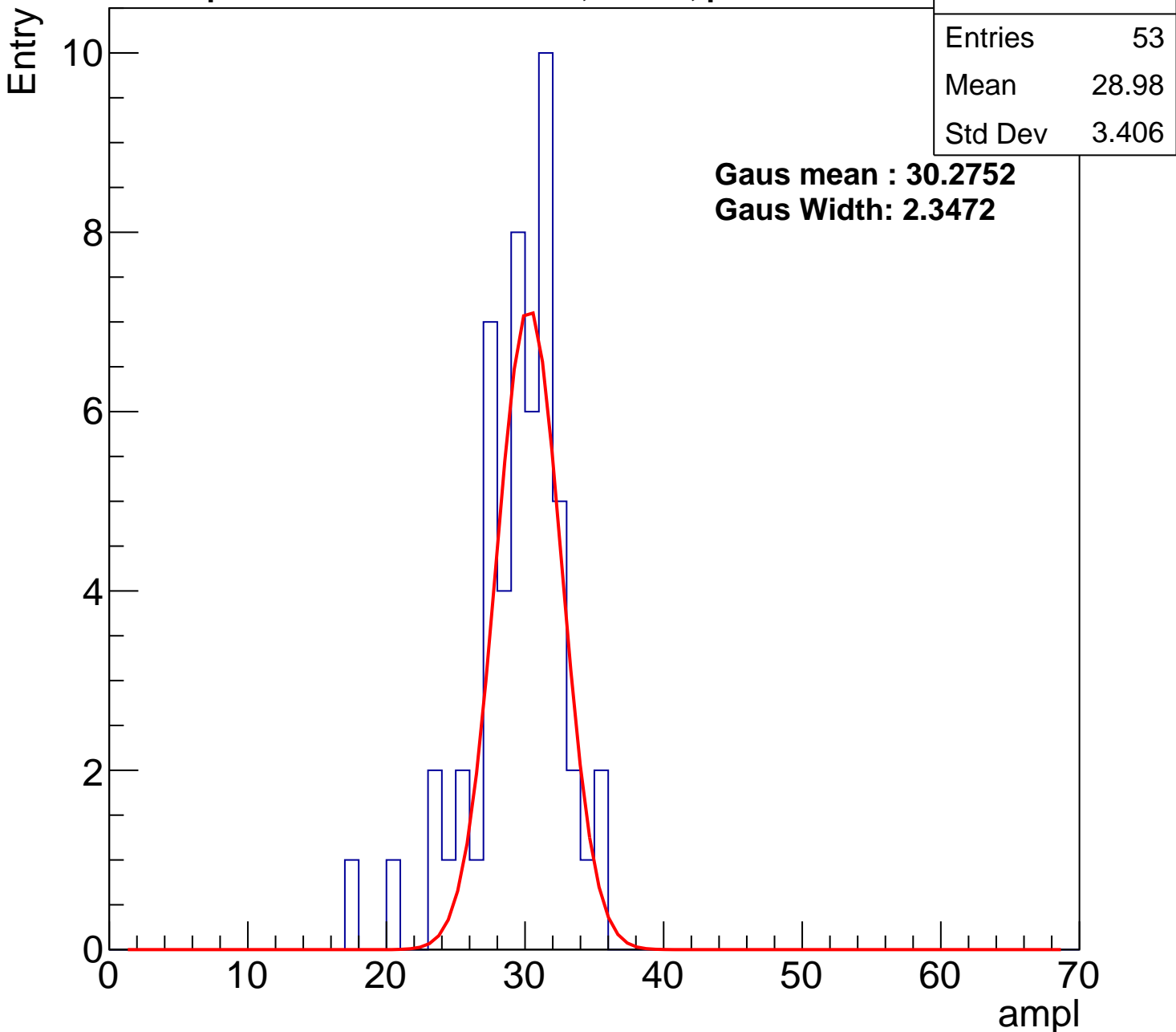
Gaus Width: 2.3472

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch65, adc1

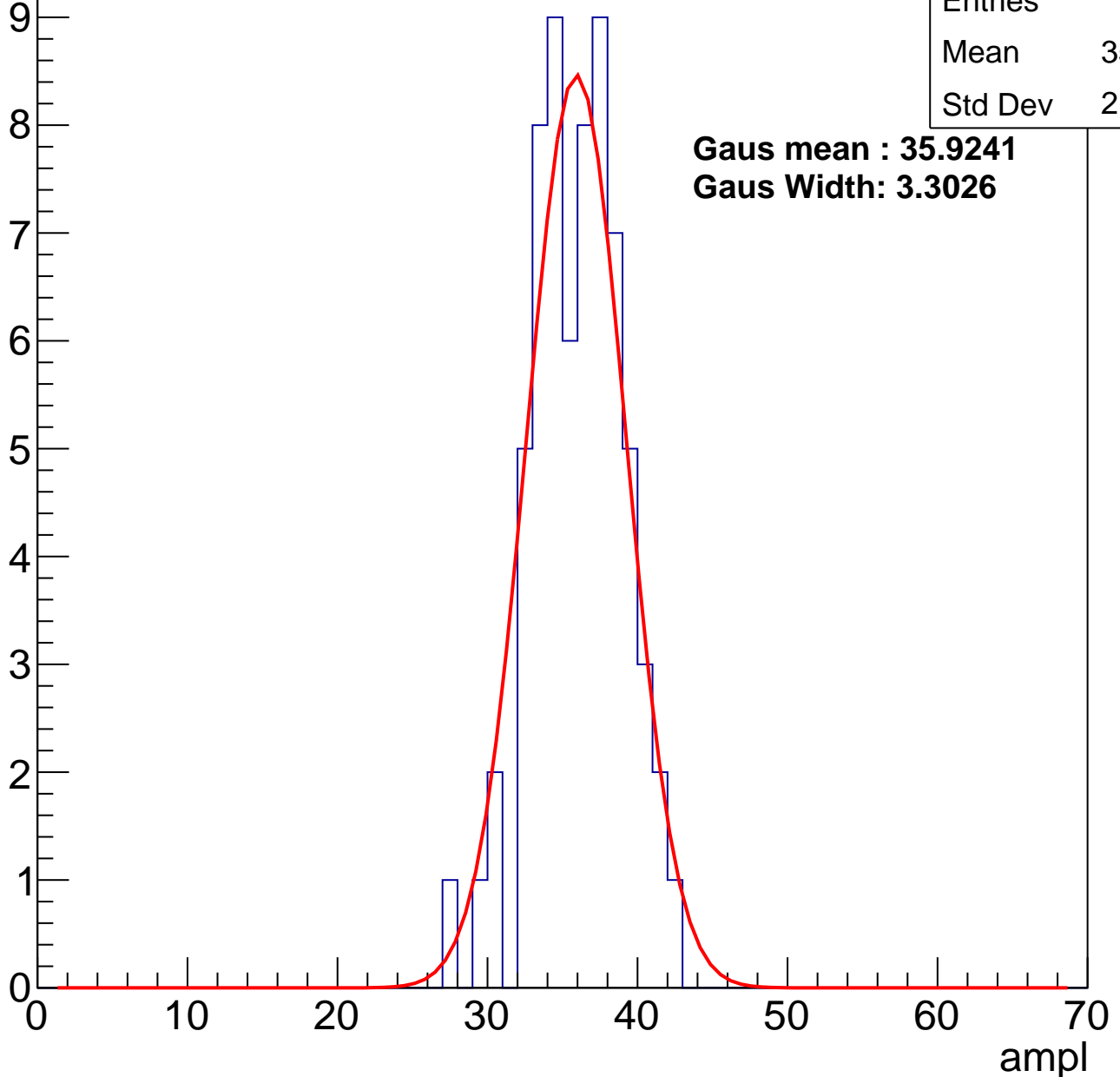
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	35.55
Std Dev	2.989

Gaus mean : 35.9241

Gaus Width: 3.3026



B1L003S, U21-ch65, adc2

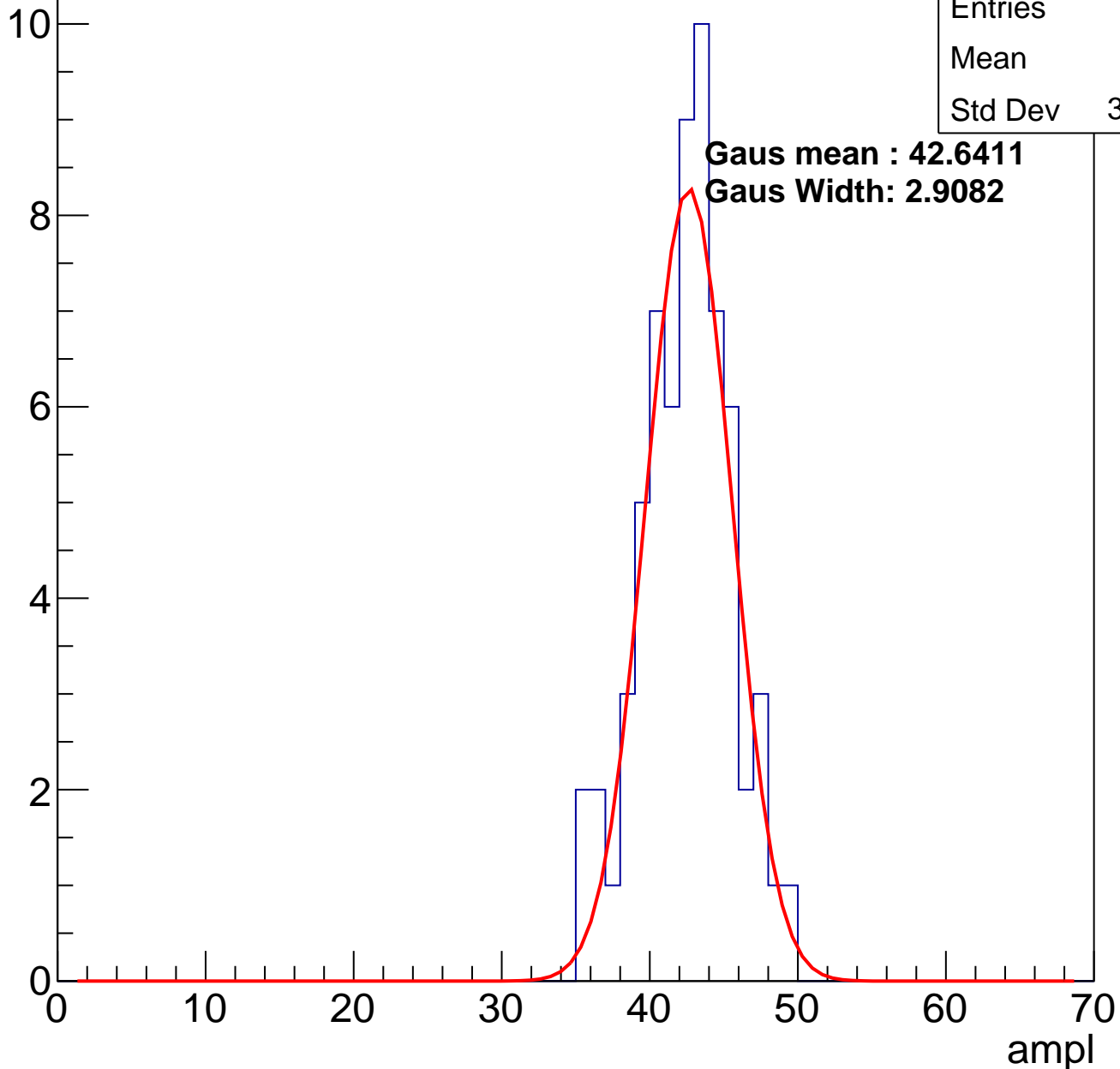
calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	42
Std Dev	3.053

Gaus mean : 42.6411

Gaus Width: 2.9082

Entry

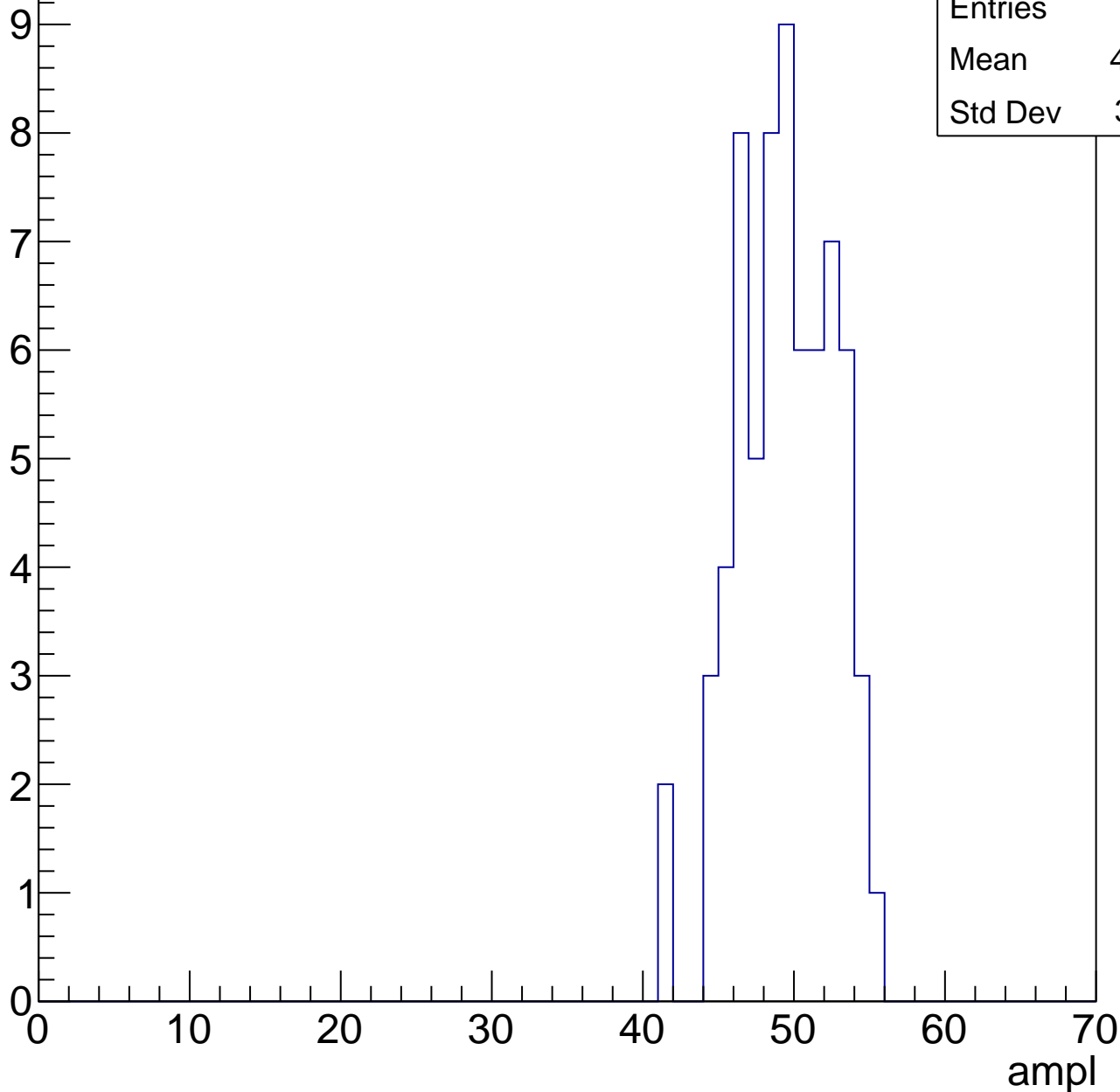


B1L003S, U21-ch65, adc3

calib_packv5_042523_0143.root, FC#13, port D2

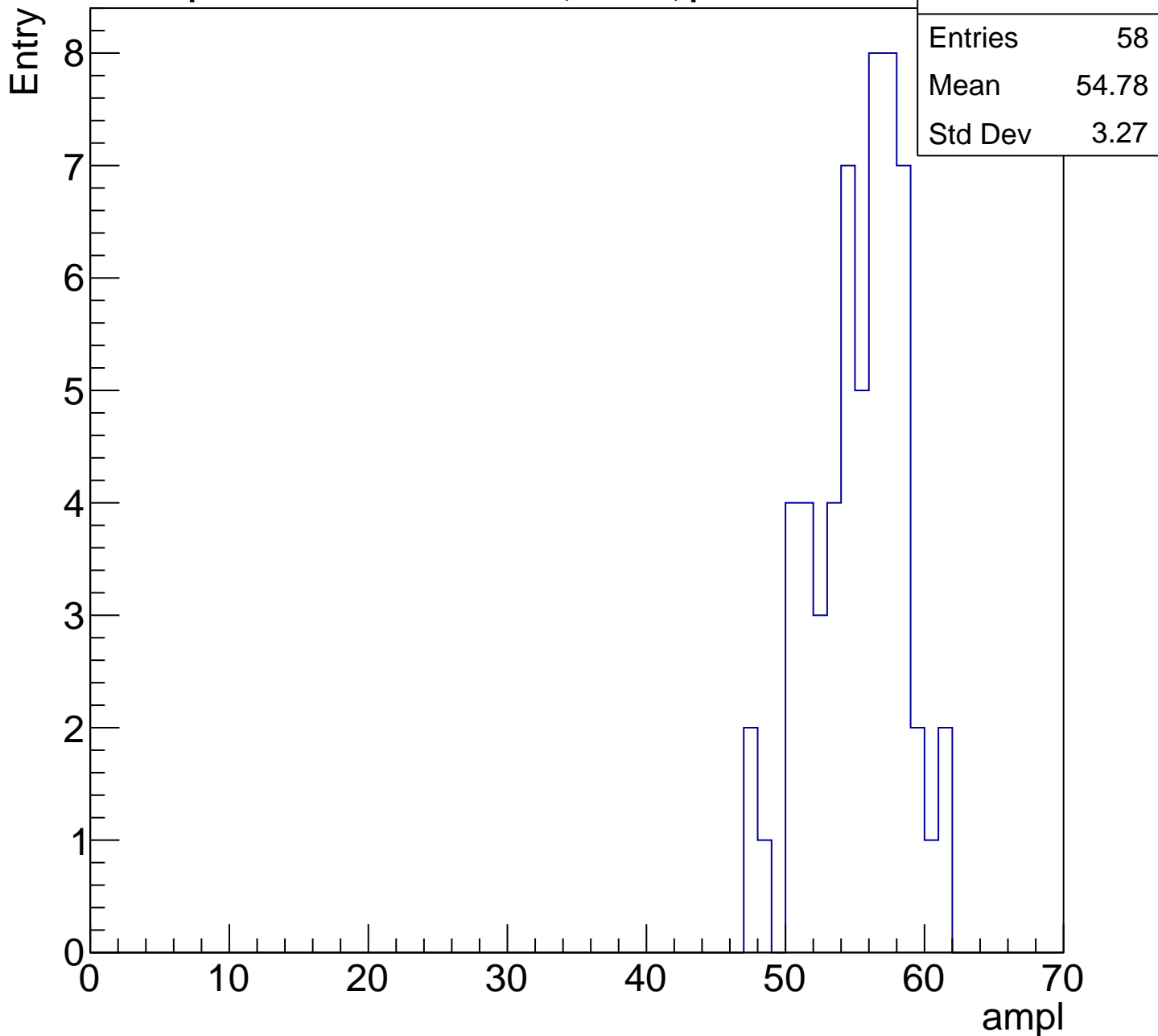
Entry

Entries	68
Mean	48.93
Std Dev	3.131



B1L003S, U21-ch65, adc4

calib_packv5_042523_0143.root, FC#13, port D2



B1L003S, U21-ch65, adc5

calib_packv5_042523_0143.root, FC#13, port D2

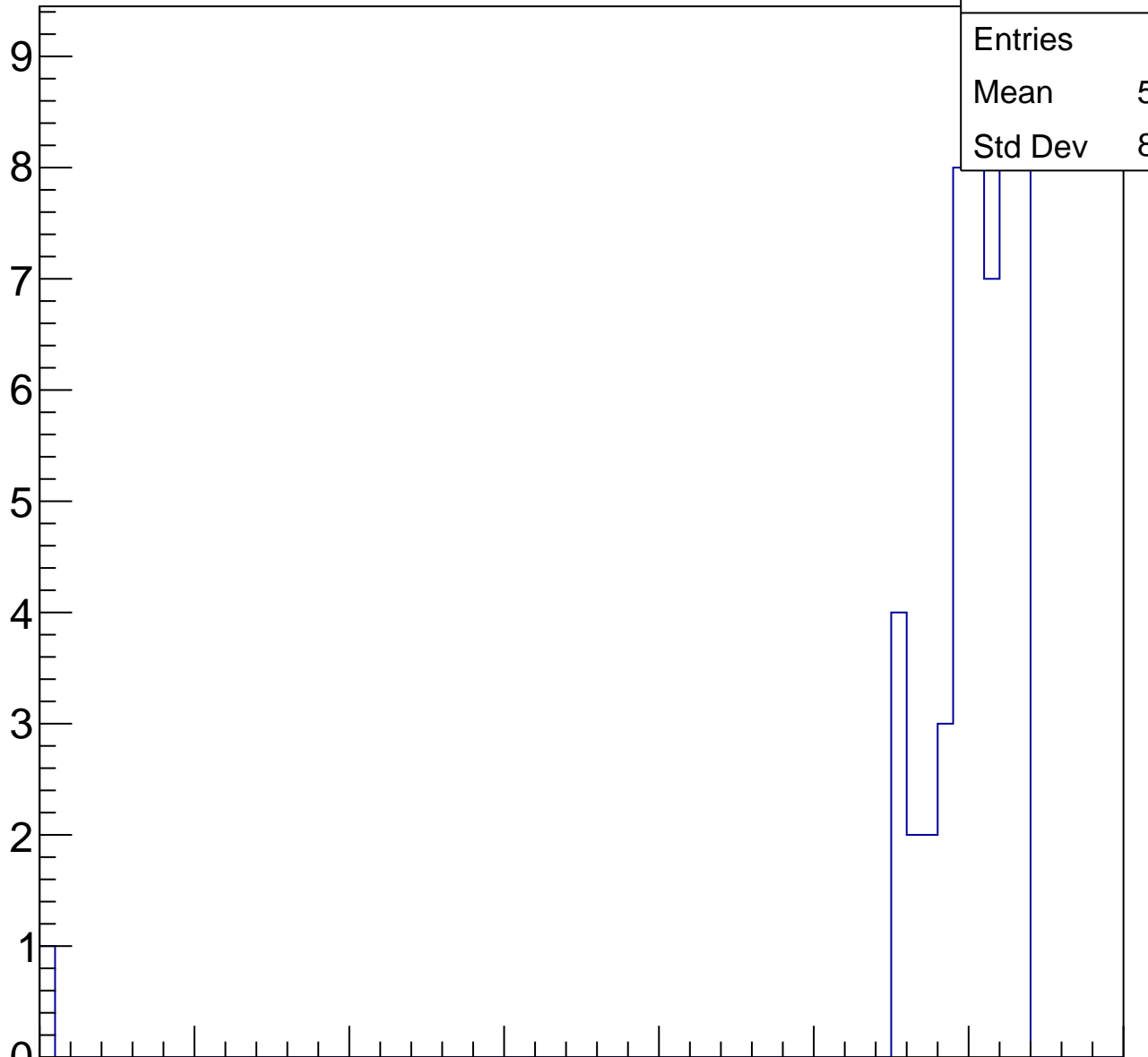
Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.83
Std Dev	8.559

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch65, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch65, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch66, adc0

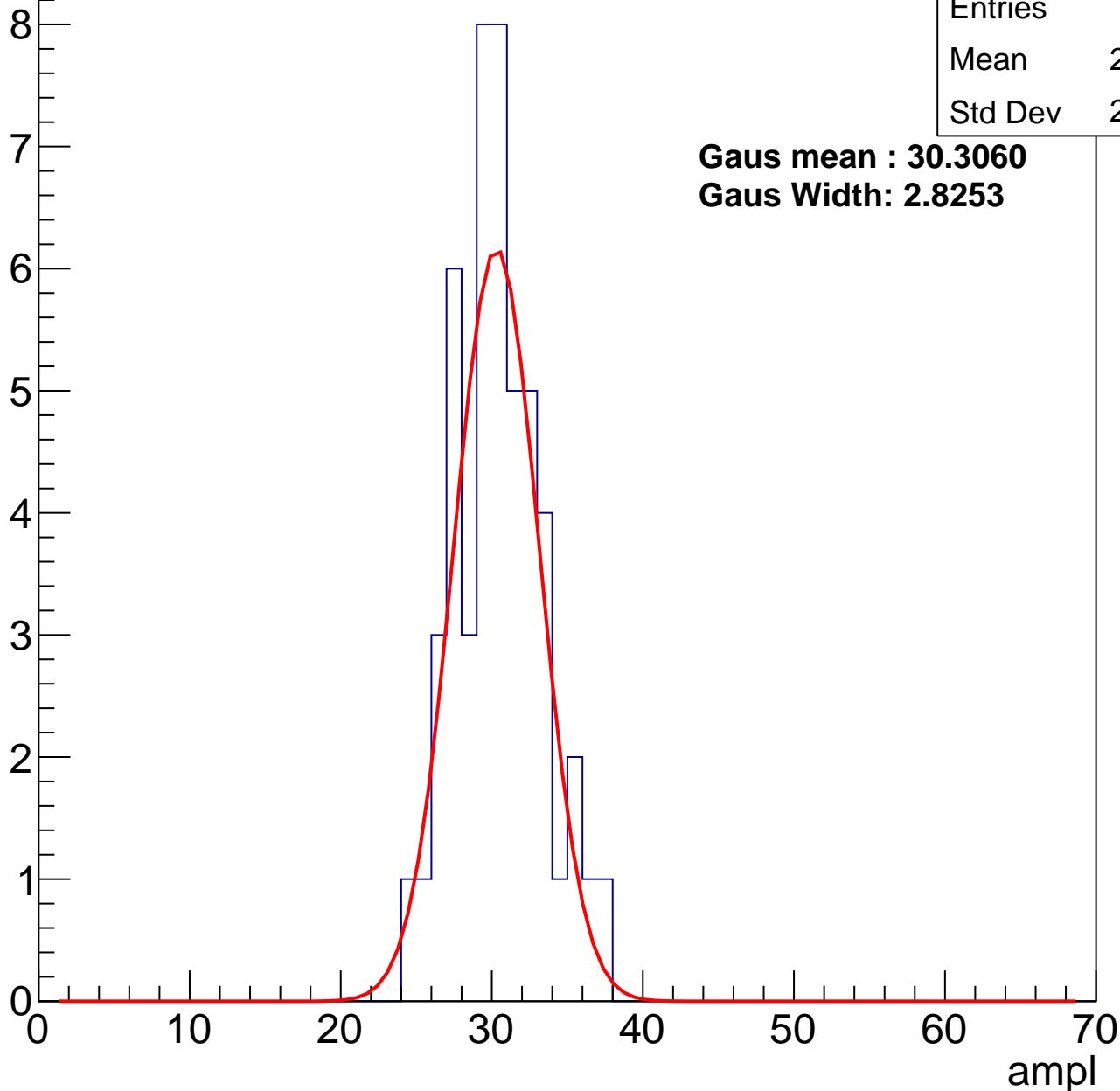
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	29.98
Std Dev	2.839

Gaus mean : 30.3060

Gaus Width: 2.8253



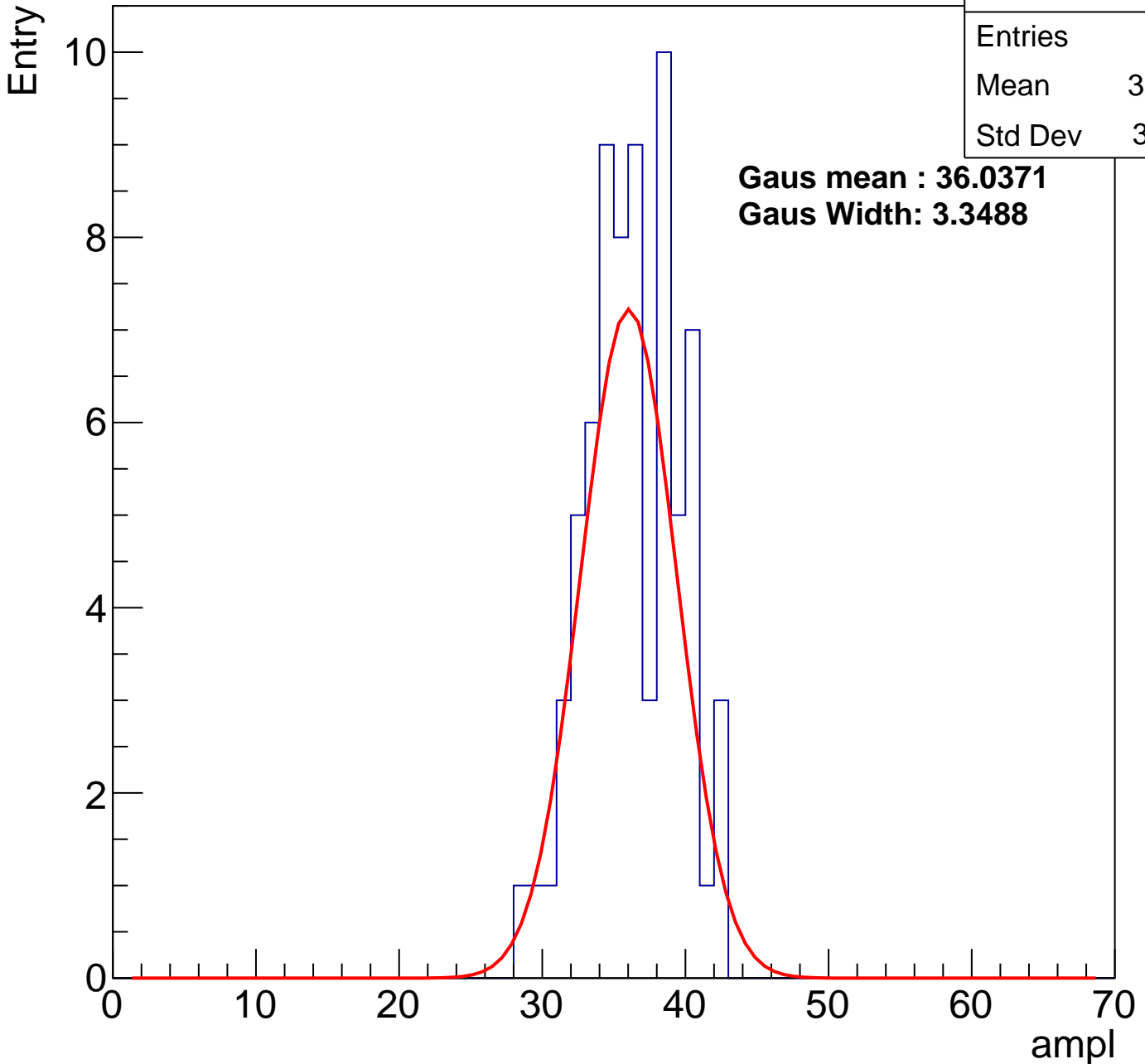
B1L003S, U21-ch66, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	35.85
Std Dev	3.191

Gaus mean : 36.0371

Gaus Width: 3.3488



B1L003S, U21-ch66, adc2

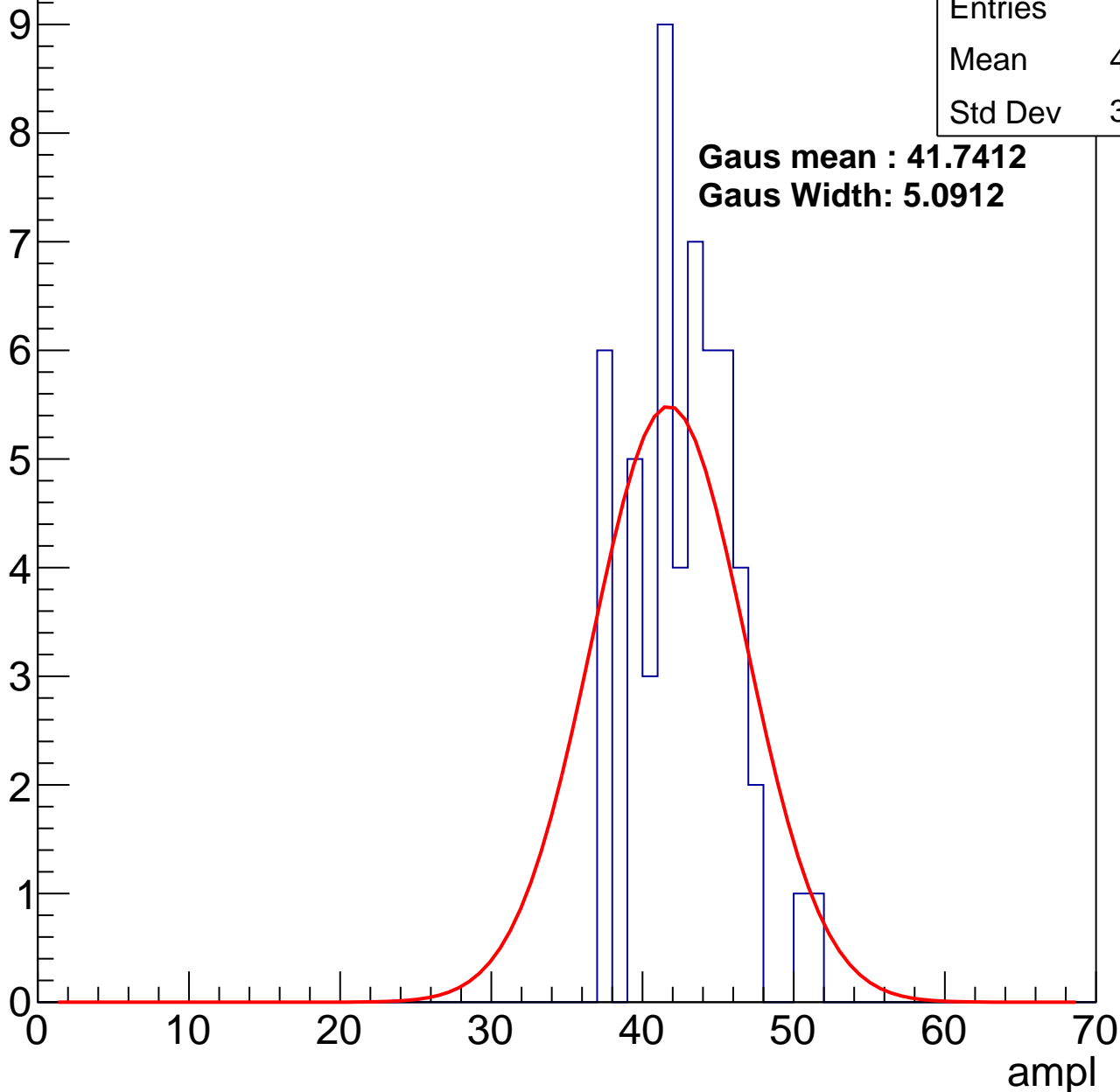
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.37
Std Dev	3.199

Gaus mean : 41.7412

Gaus Width: 5.0912



B1L003S, U21-ch66, adc3

calib_packv5_042523_0143.root, FC#13, port D2

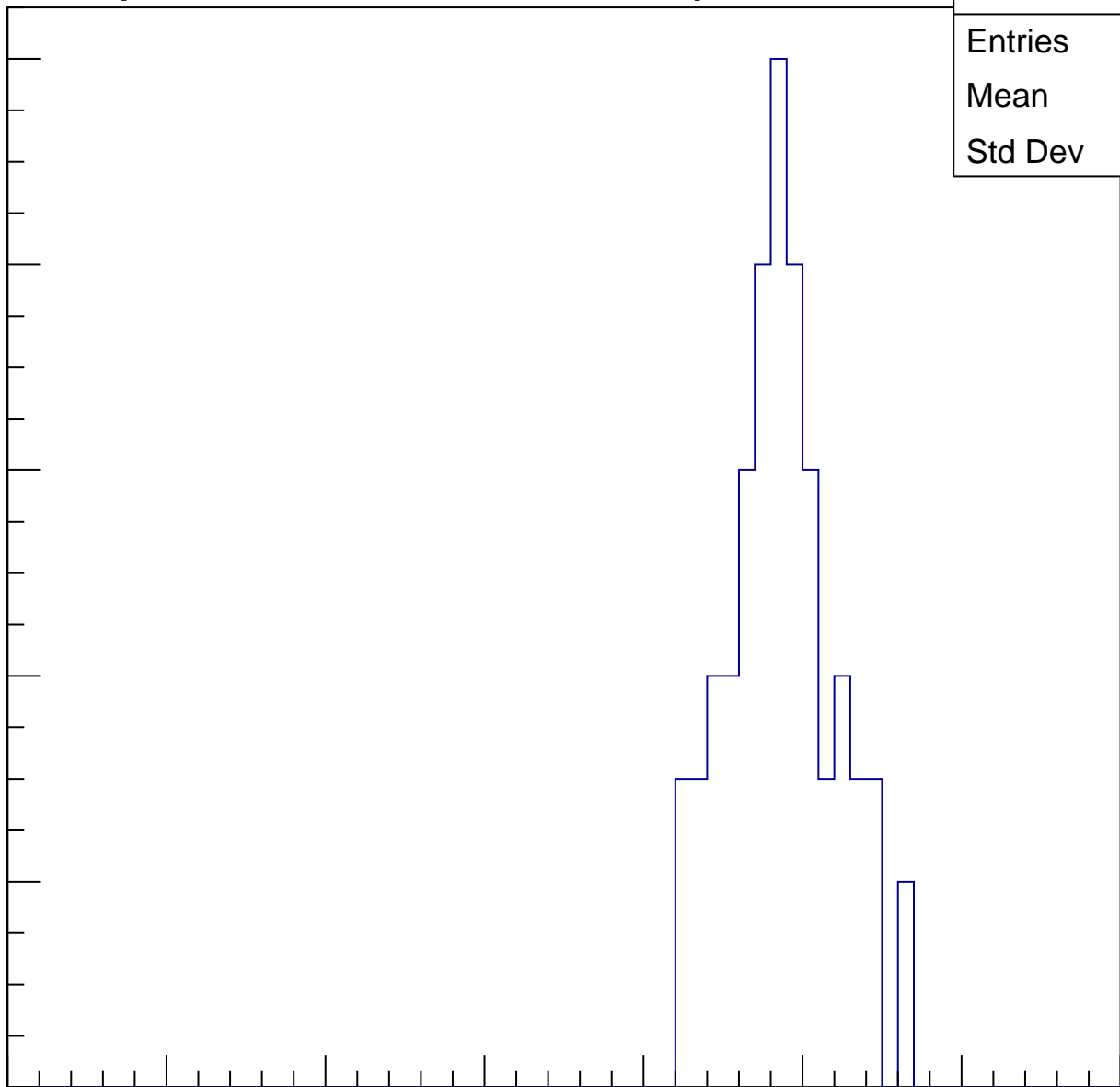
Entries	67
Mean	48.19
Std Dev	3.338

Entry

10
8
6
4
2
0

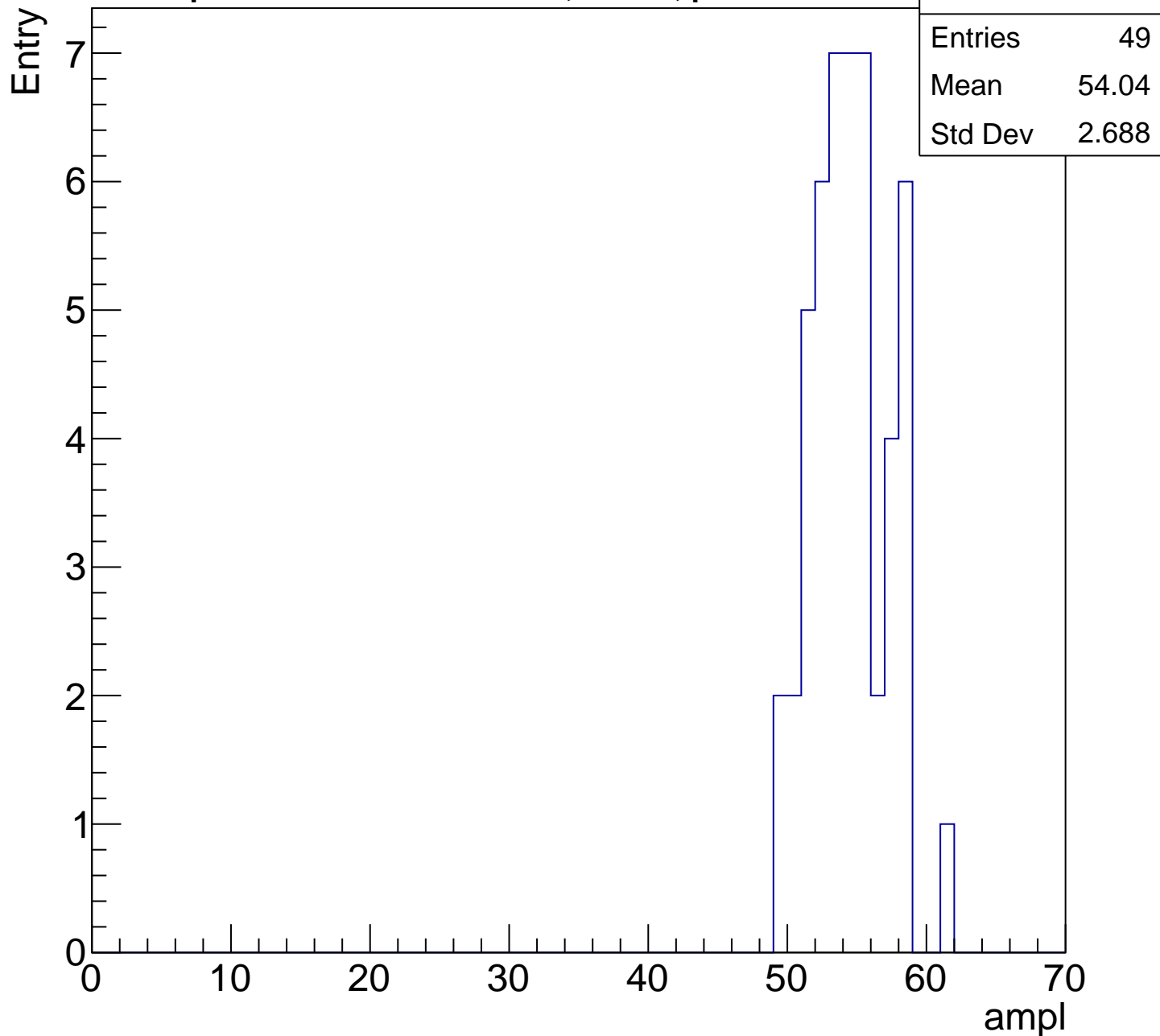
0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch66, adc4

calib_packv5_042523_0143.root, FC#13, port D2

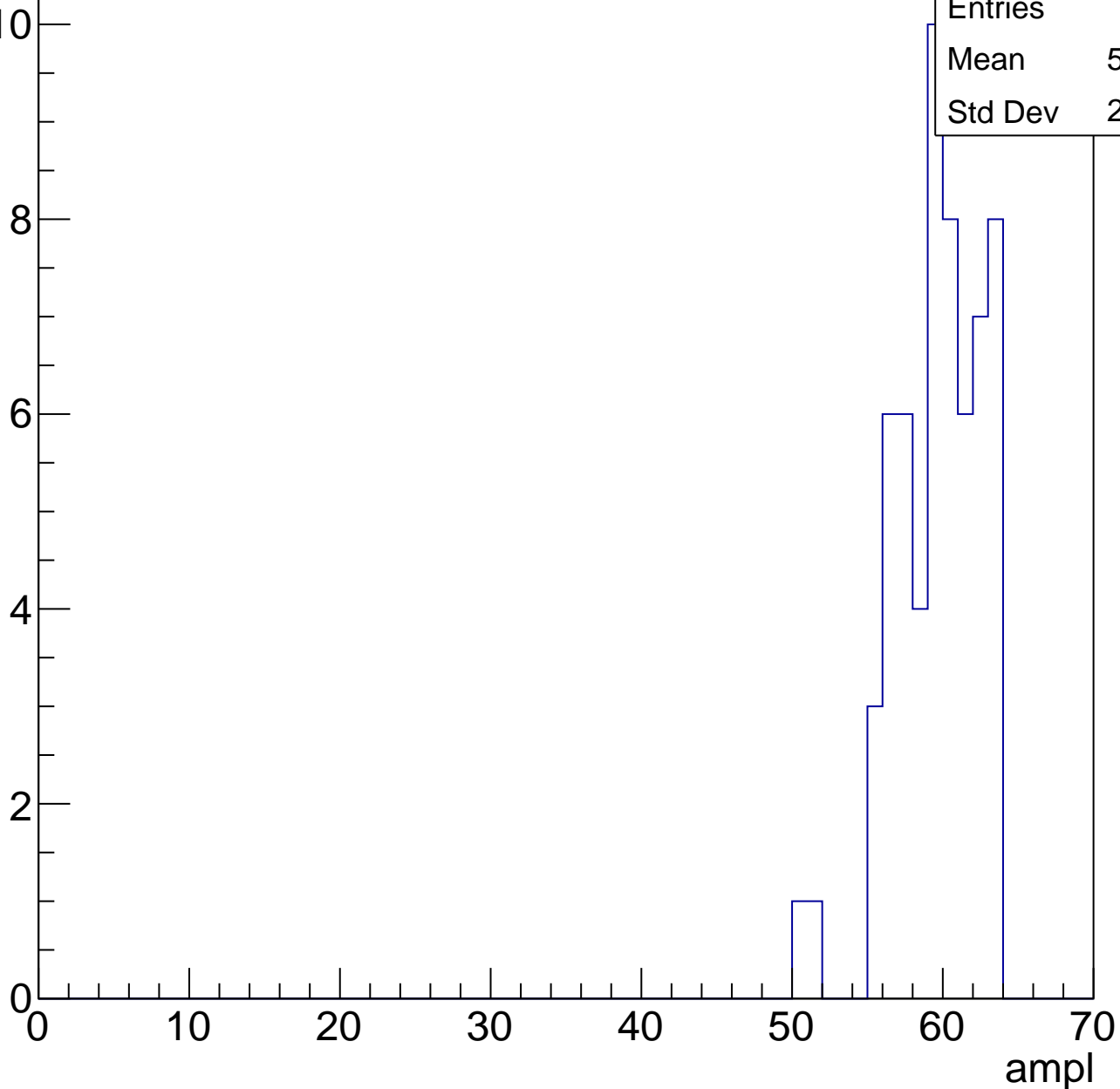


B1L003S, U21-ch66, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

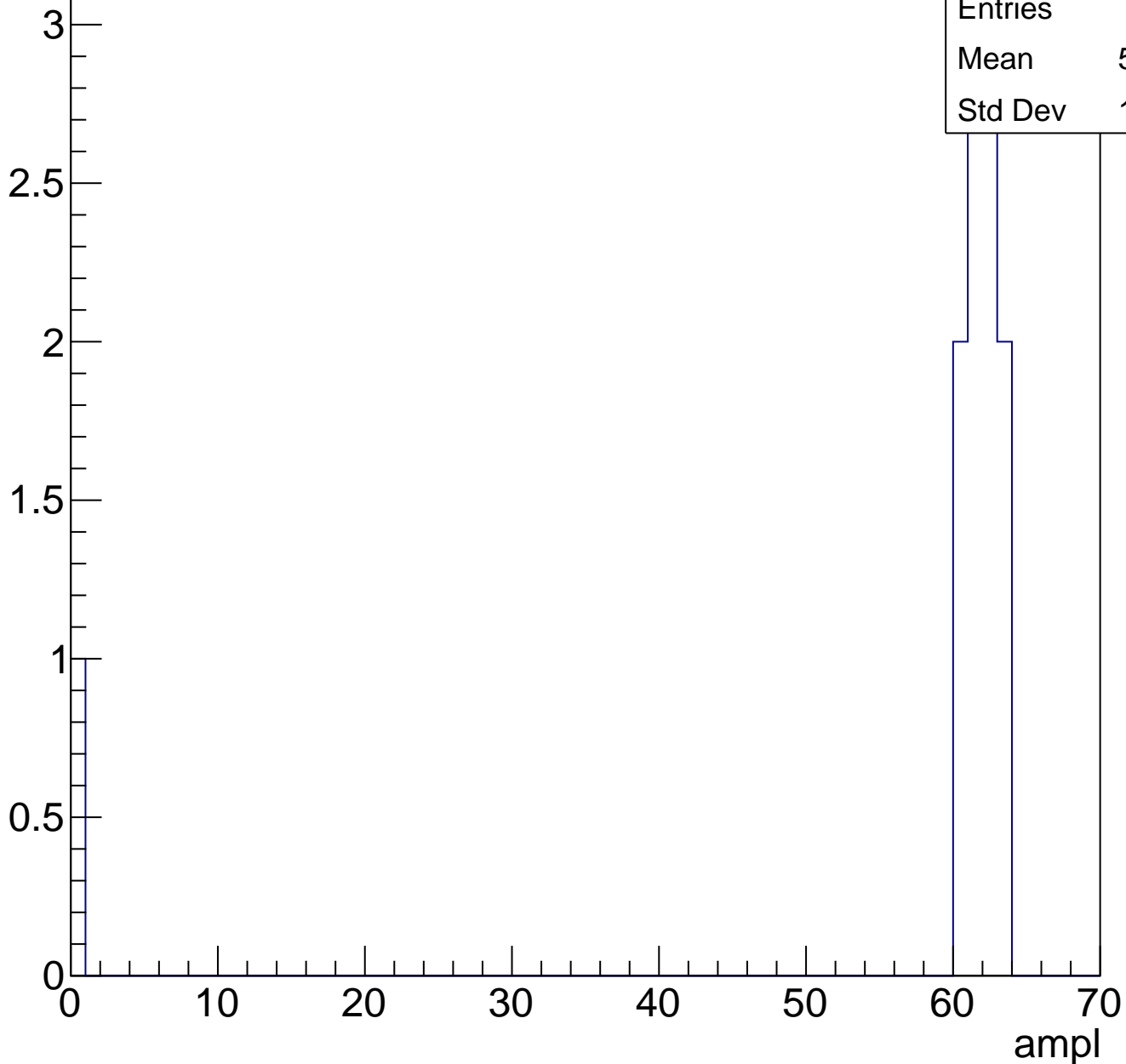
Entries	60
Mean	59.17
Std Dev	2.876



B1L003S, U21-ch66, adc6

calib_packv5_042523_0143.root, FC#13, port D2

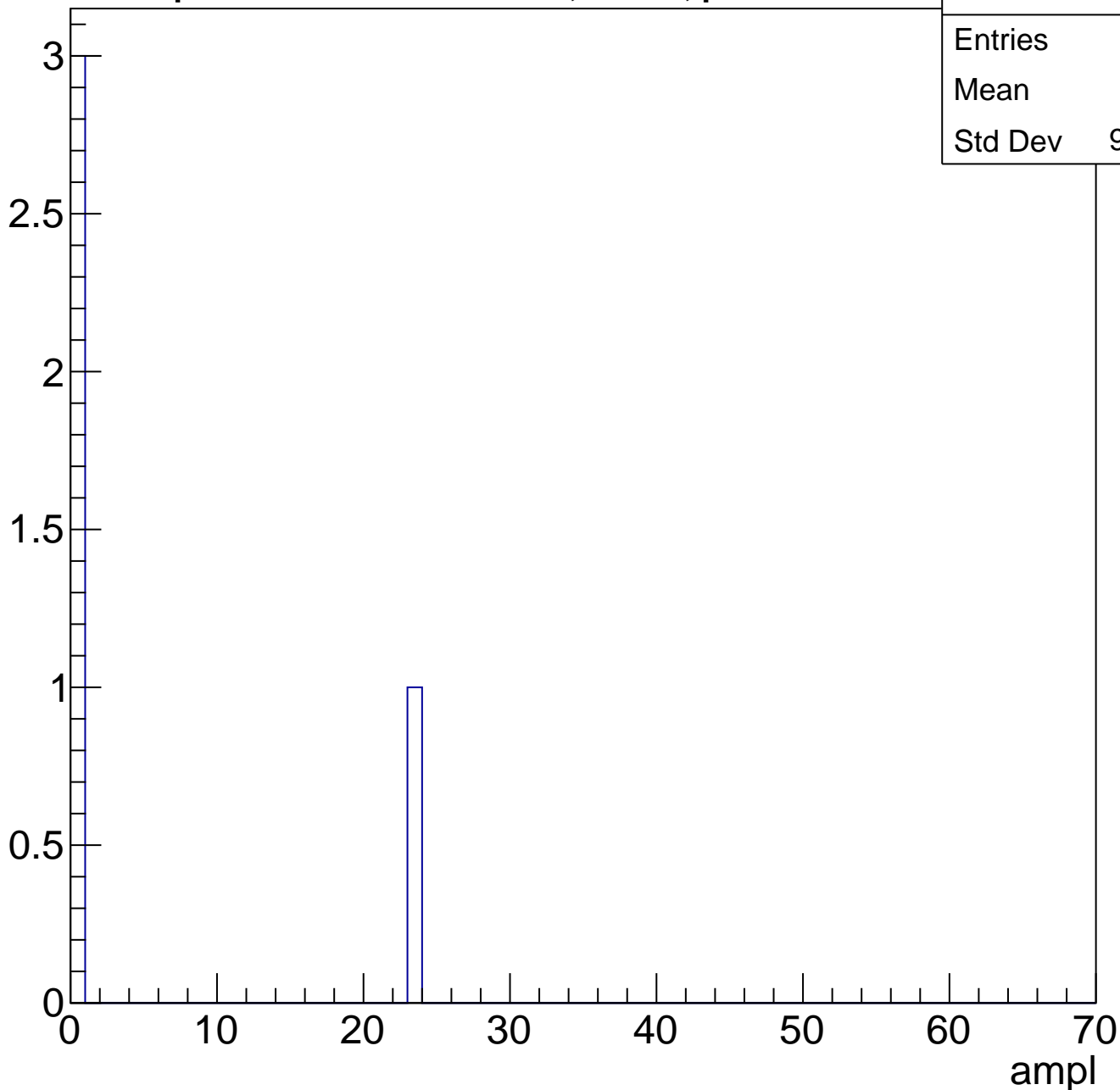
Entry



B1L003S, U21-ch66, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch67, adc0

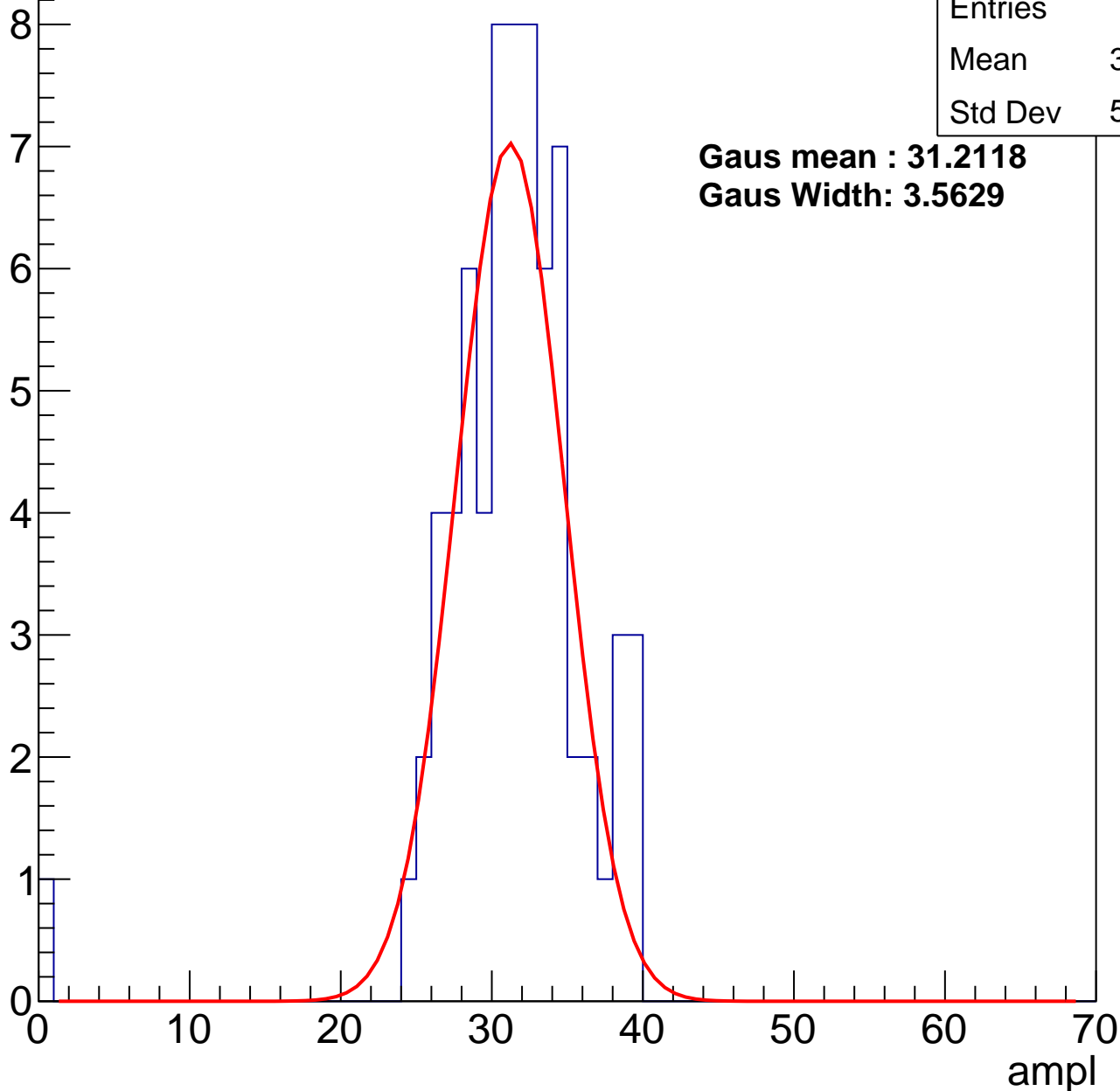
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	30.86
Std Dev	5.175

Gaus mean : 31.2118

Gaus Width: 3.5629



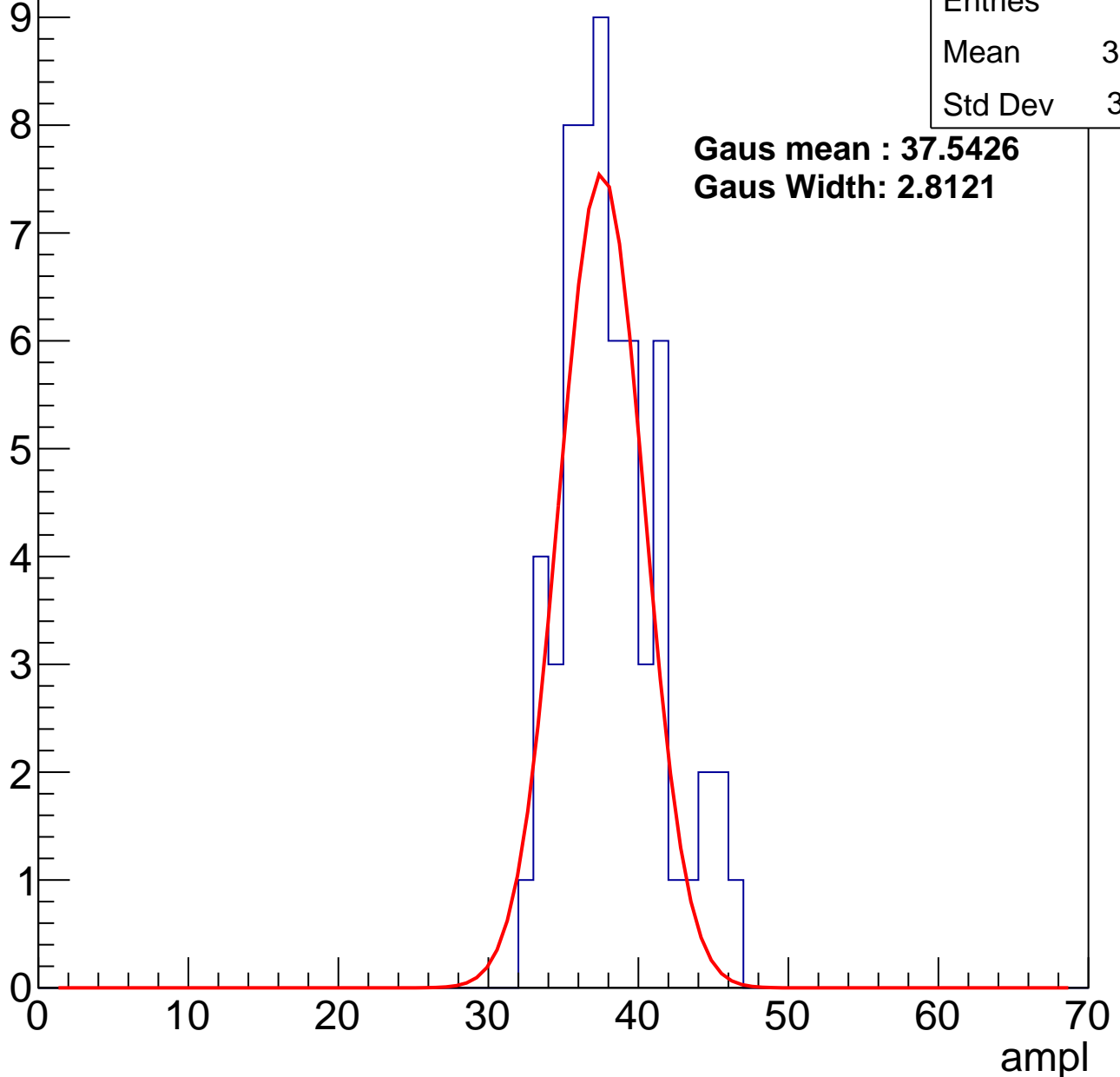
B1L003S, U21-ch67, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	37.77
Std Dev	3.241

Gaus mean : 37.5426
Gaus Width: 2.8121



B1L003S, U21-ch67, adc2

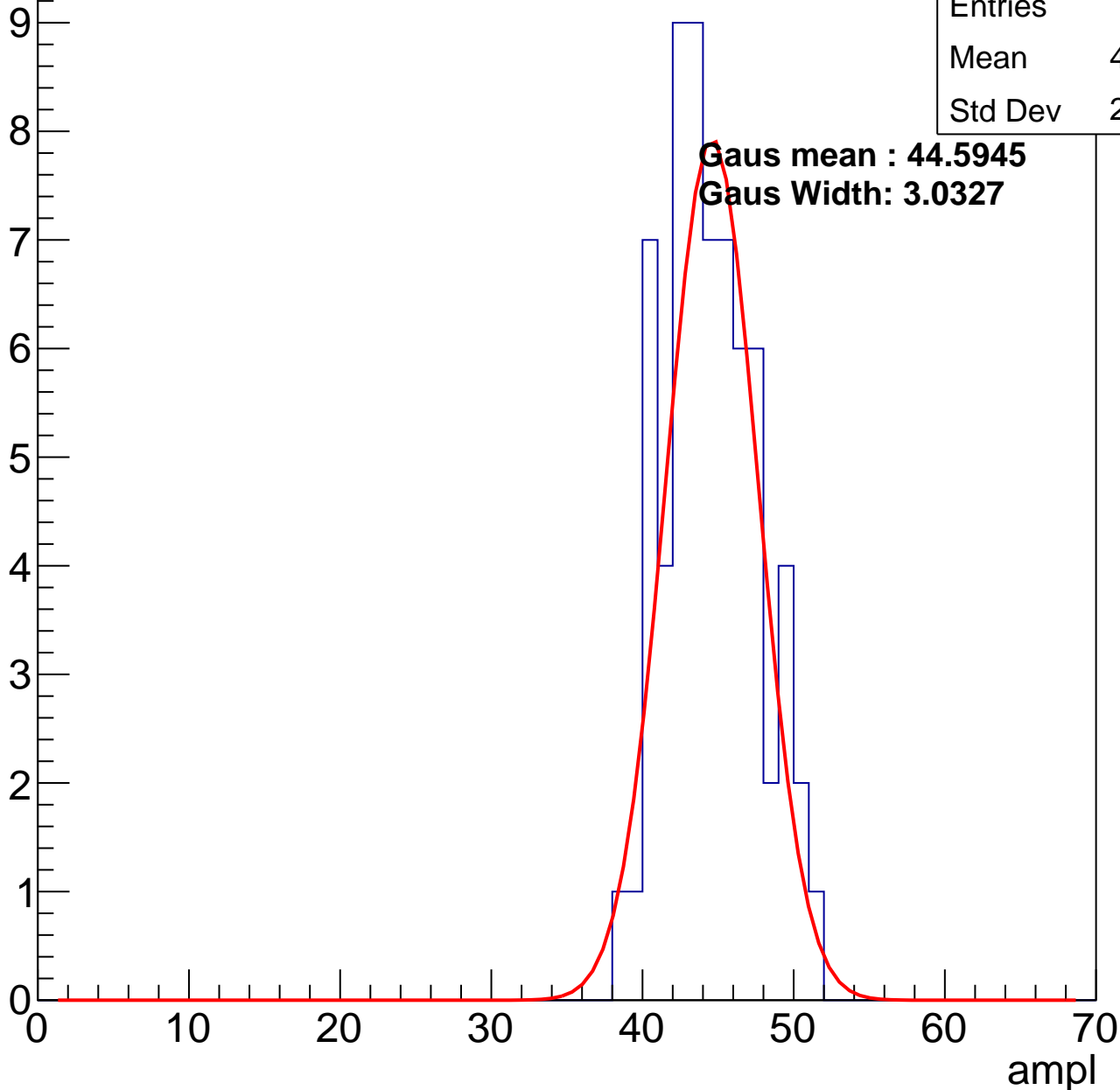
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	44.09
Std Dev	2.994

Gaus mean : 44.5945

Gaus Width: 3.0327

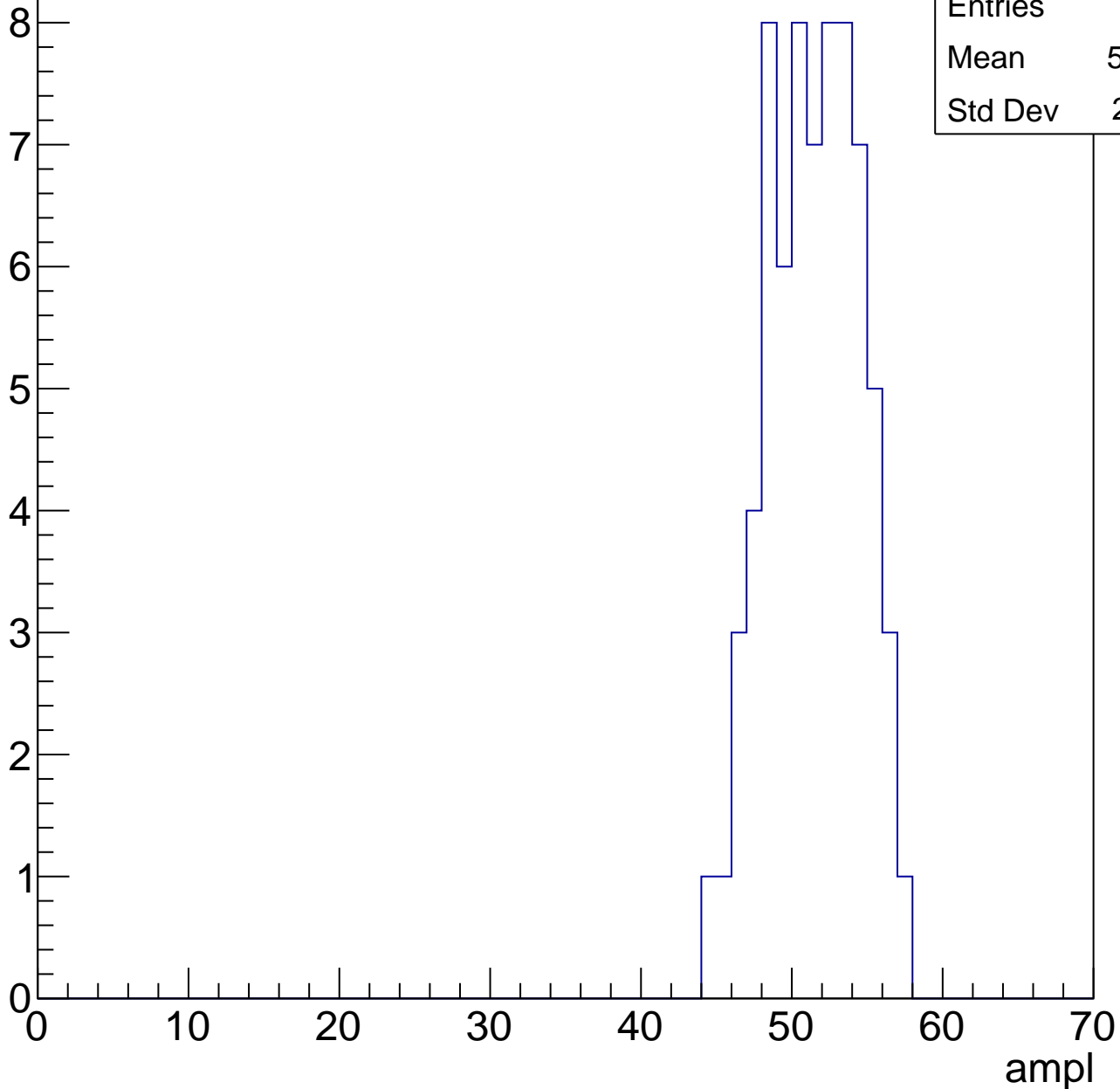


B1L003S, U21-ch67, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	50.97
Std Dev	2.981



B1L003S, U21-ch67, adc4

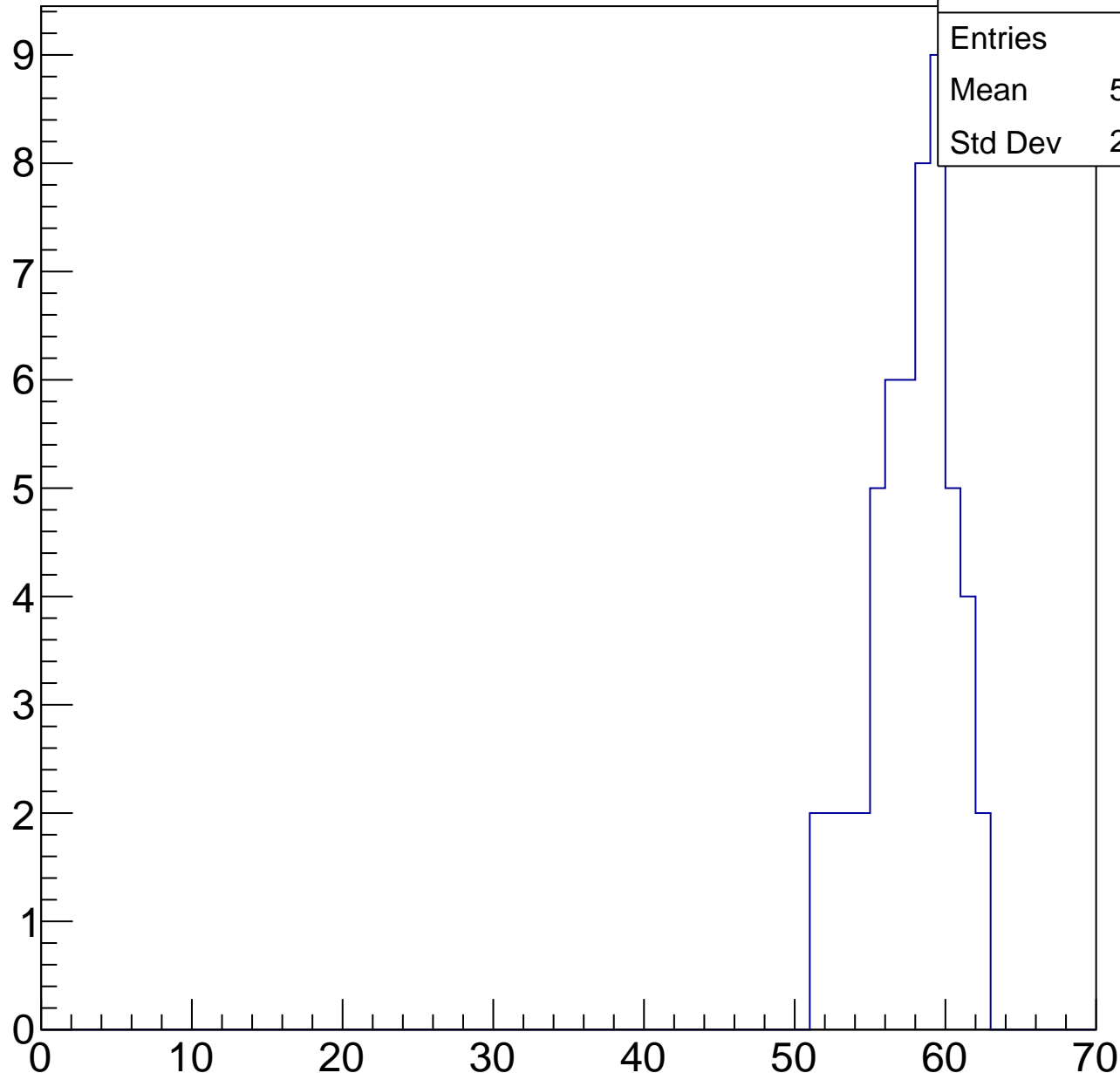
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	57.28
Std Dev	2.736

ampl

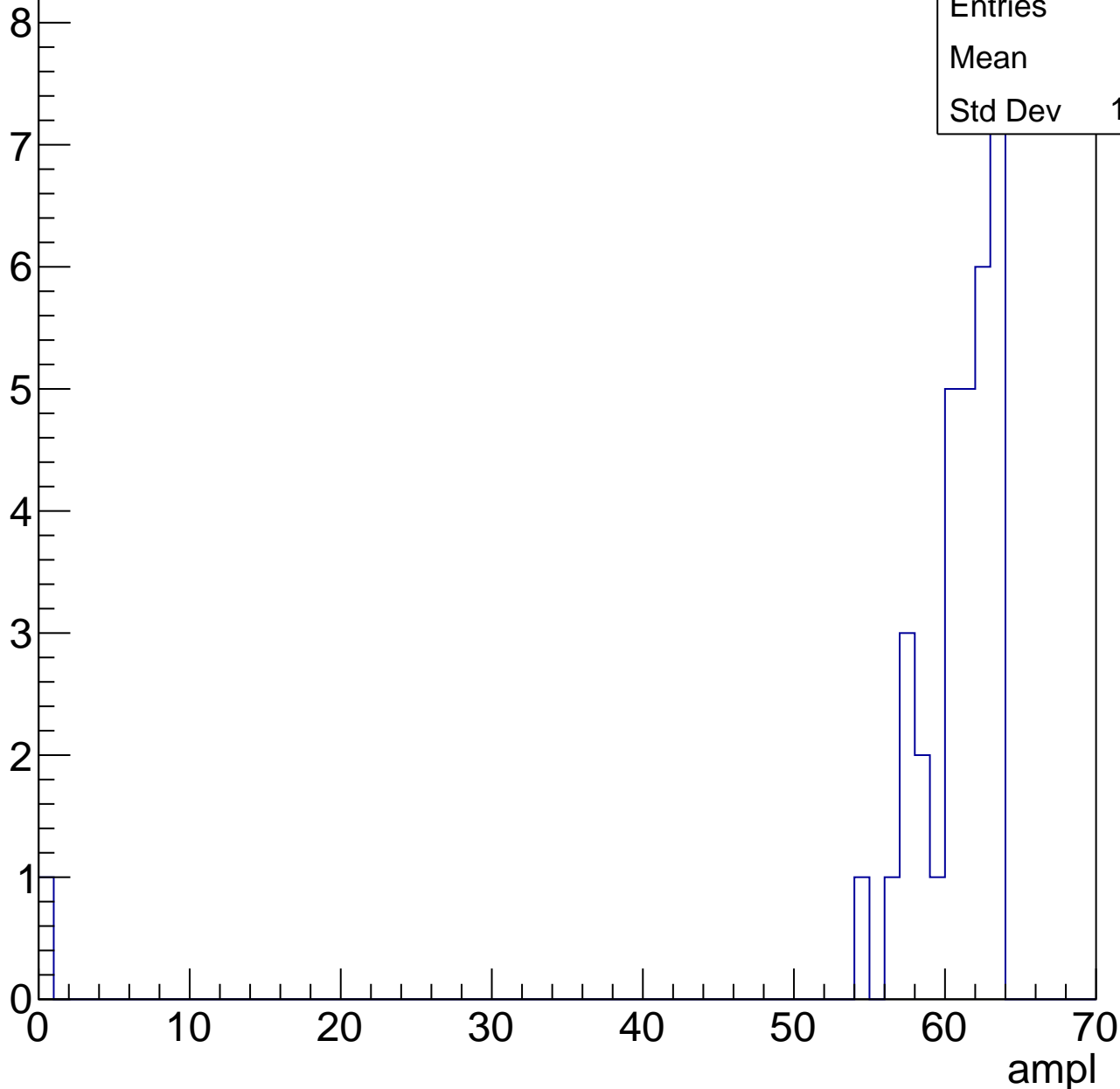


B1L003S, U21-ch67, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	33
Mean	58.7
Std Dev	10.64



B1L003S, U21-ch67, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

ampl

0 10 20 30 40 50 60 70

B1L003S, U21-ch67, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch68, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	28.72
Std Dev	4.697

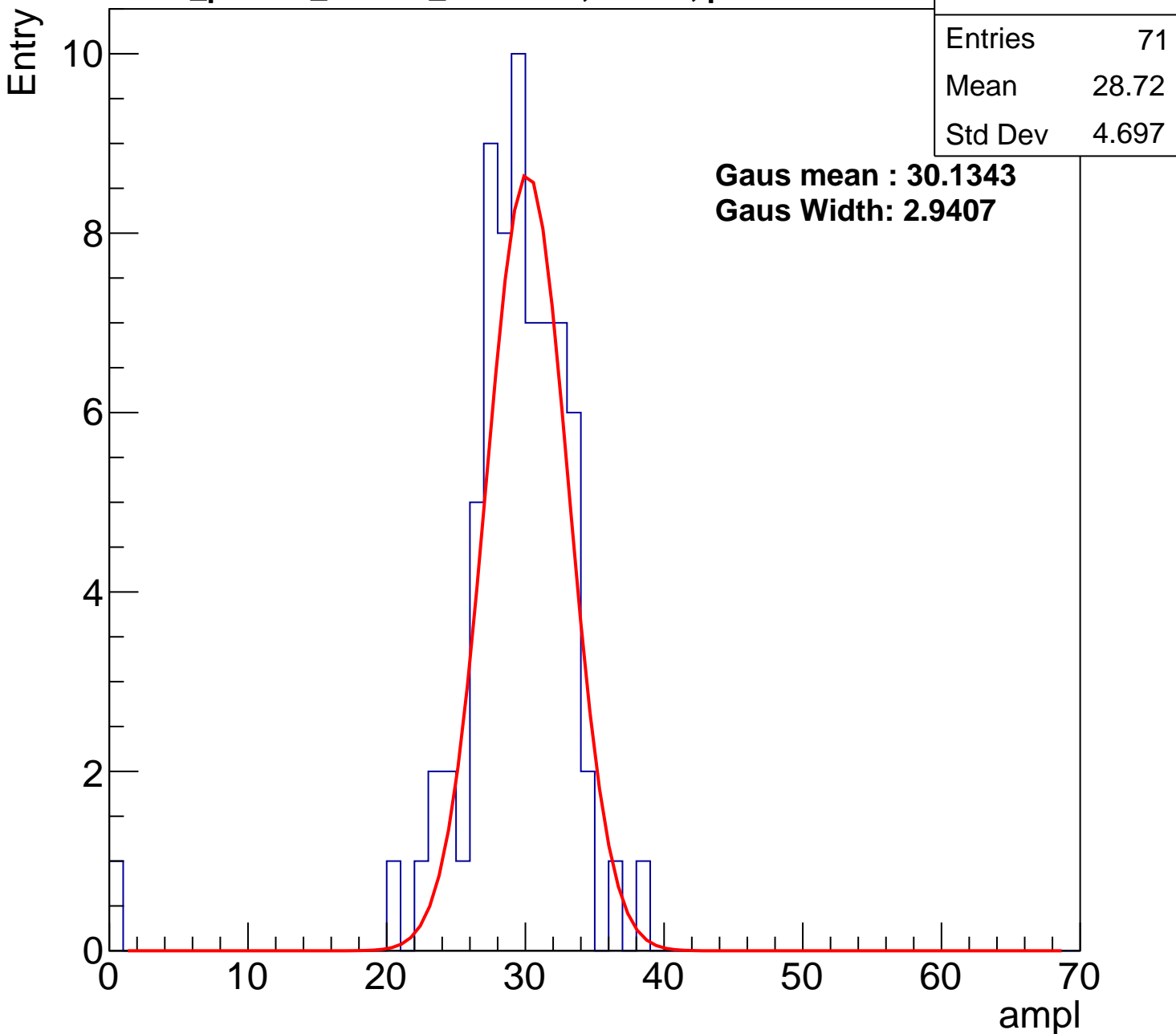
Gaus mean : 30.1343
Gaus Width: 2.9407

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch68, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	36.32
Std Dev	3.054

Gaus mean : 36.6969

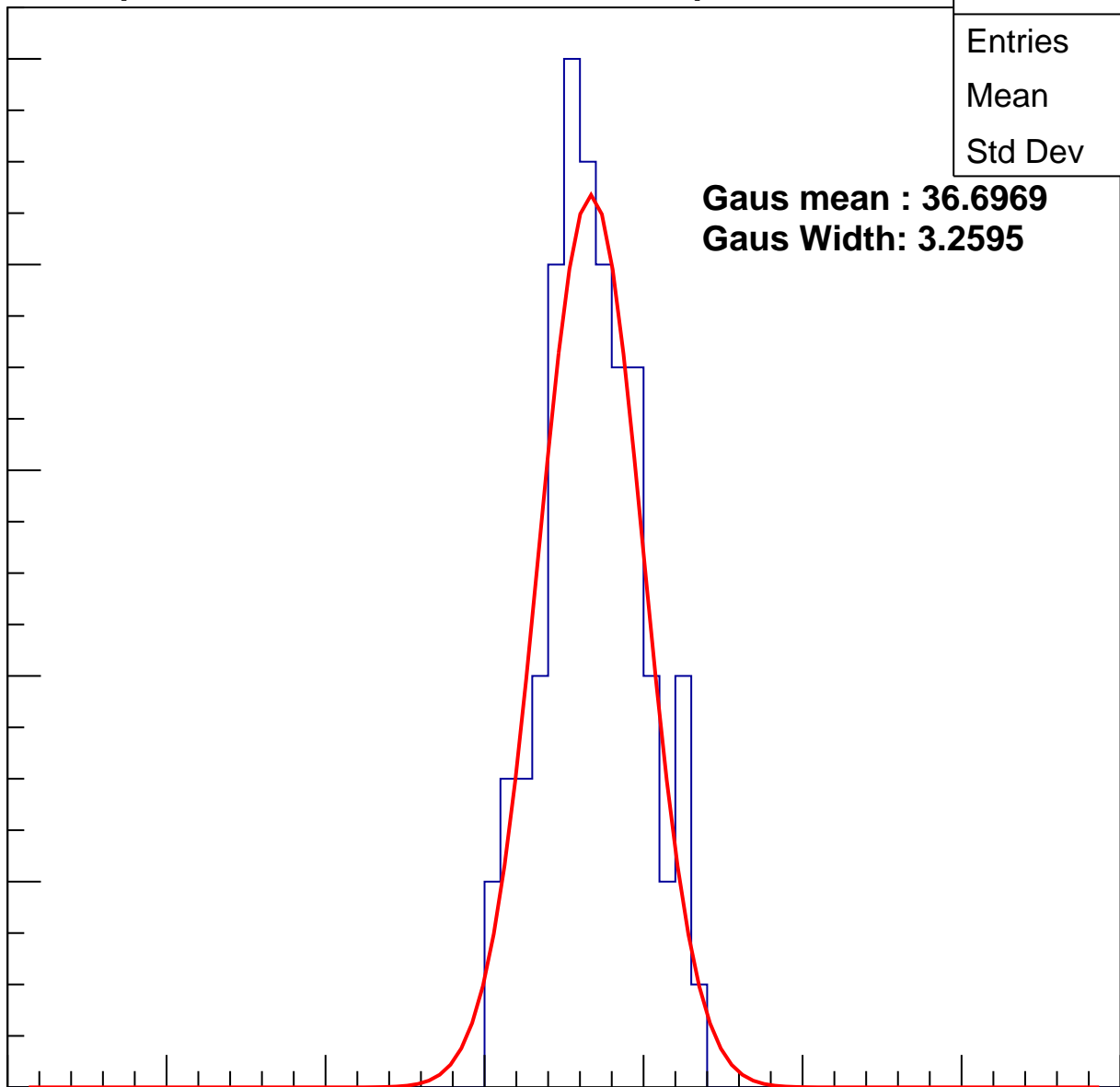
Gaus Width: 3.2595

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch68, adc2

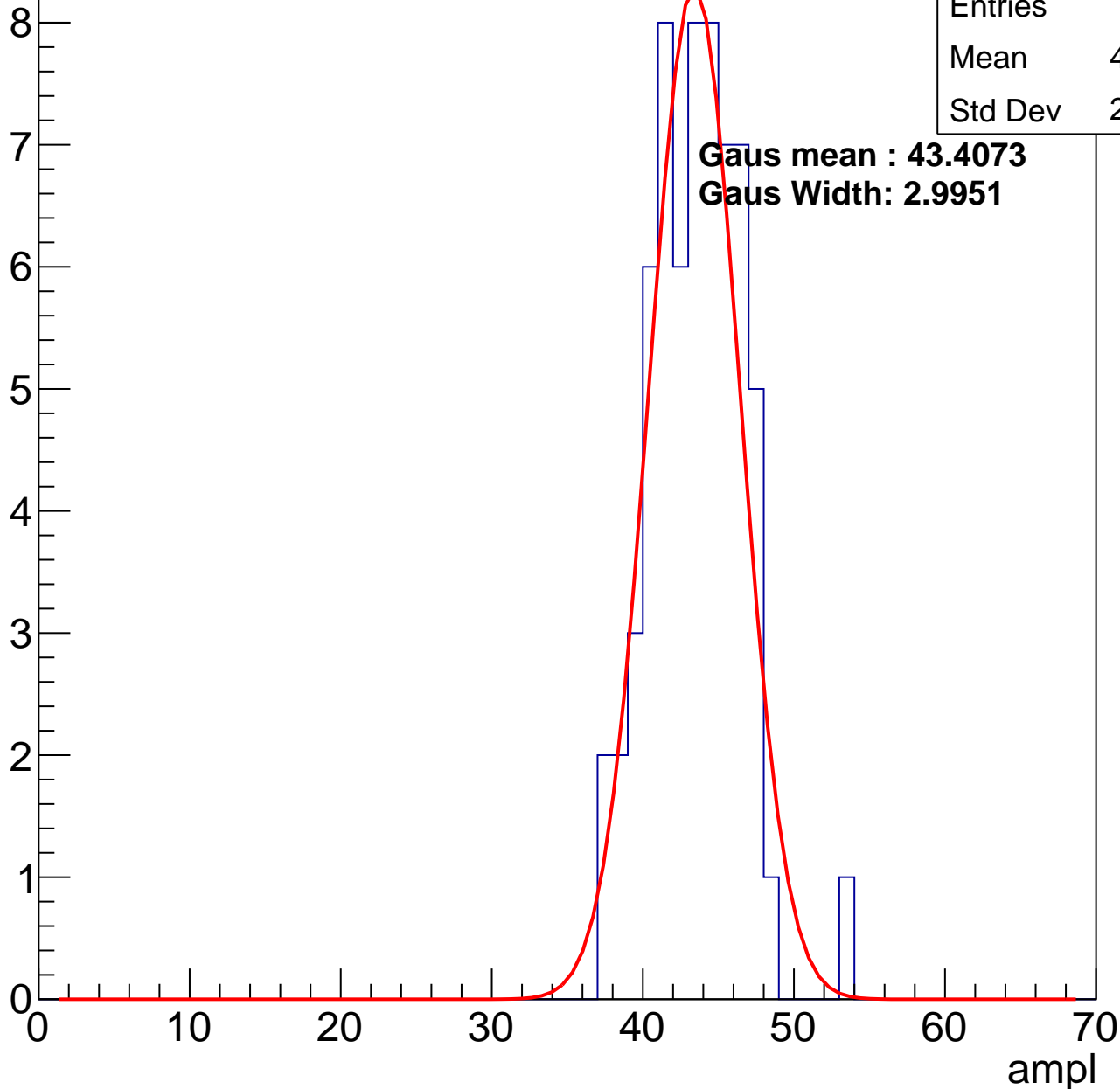
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	43.06
Std Dev	2.973

Gaus mean : 43.4073

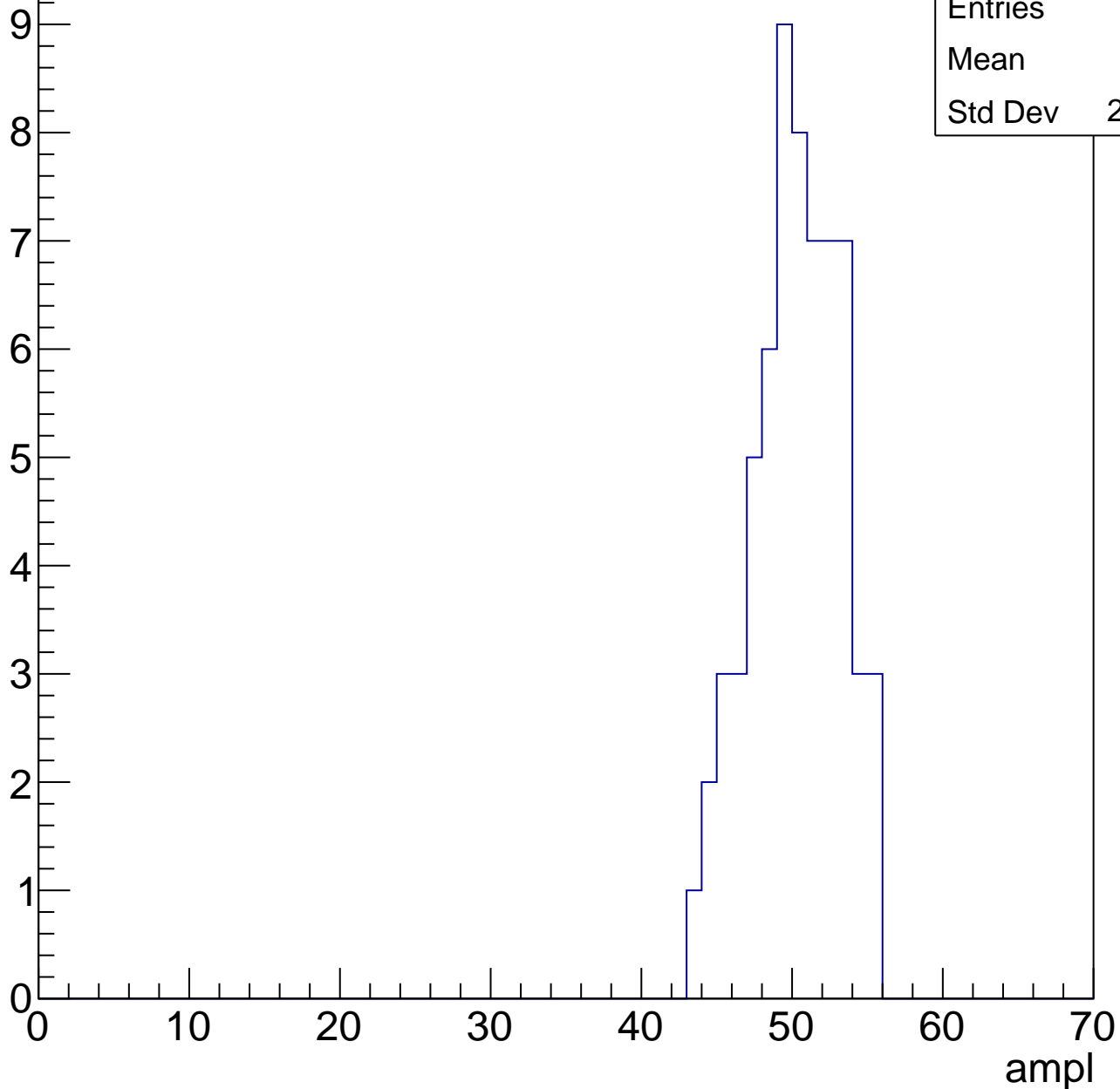
Gaus Width: 2.9951



B1L003S, U21-ch68, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry



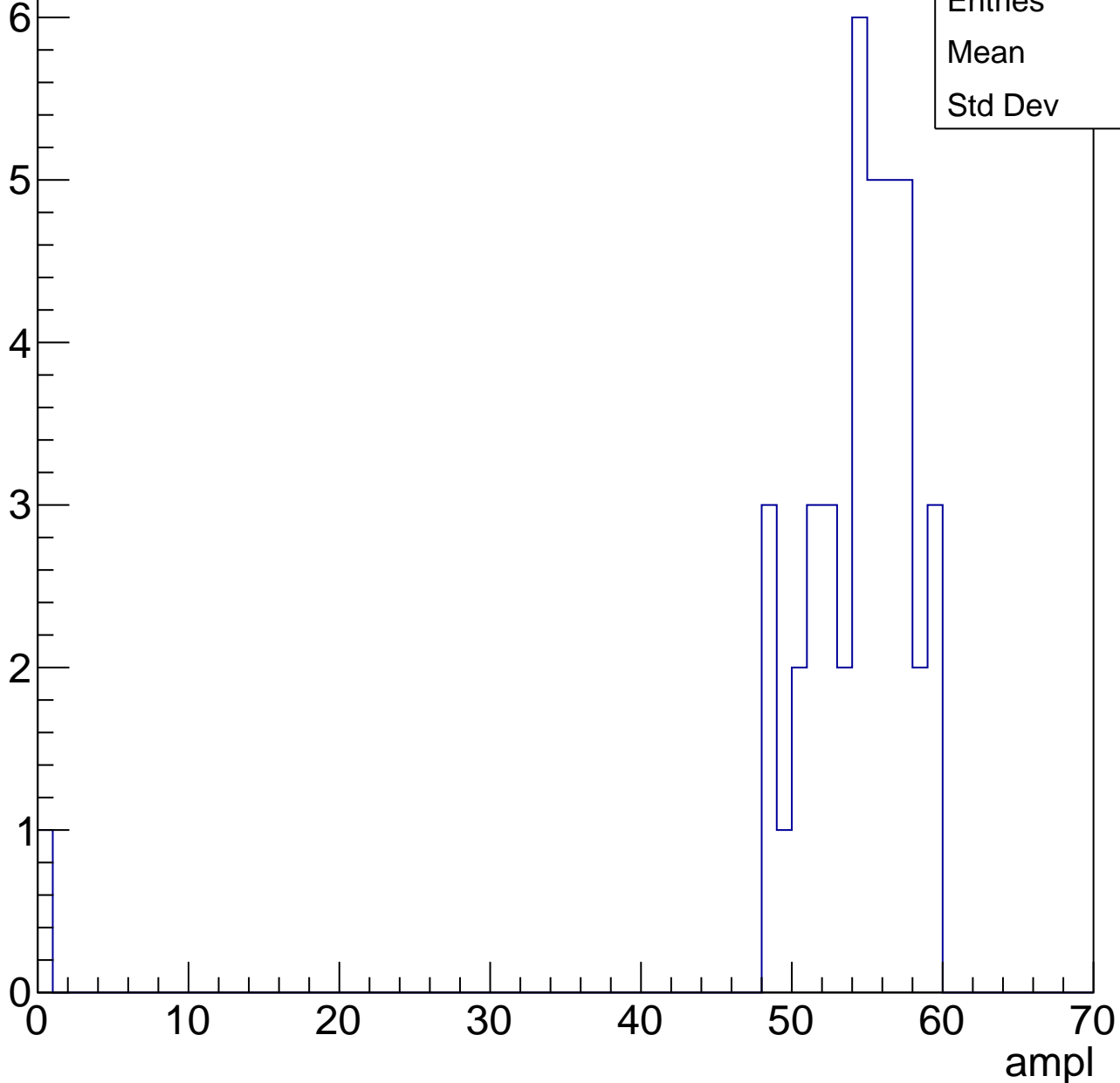
Entries	64
Mean	49.8
Std Dev	2.906

B1L003S, U21-ch68, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	52.8
Std Dev	8.89



B1L003S, U21-ch68, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

Entries 63

Mean 59.6

Std Dev 2.367

8

6

4

2

0

0

10

20

30

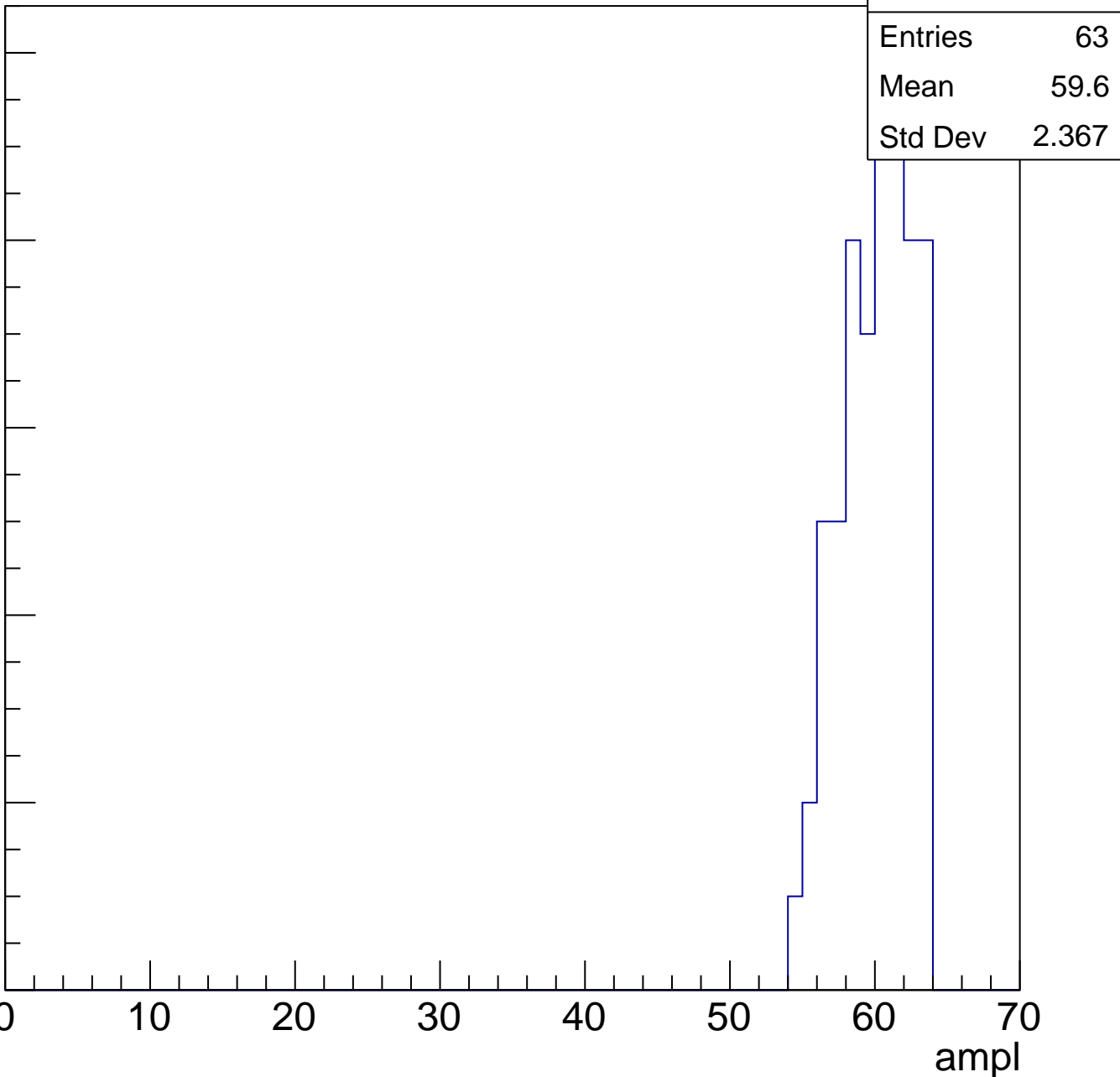
40

50

60

70

ampl



B1L003S, U21-ch68, adc6

calib_packv5_042523_0143.root, FC#13, port D2

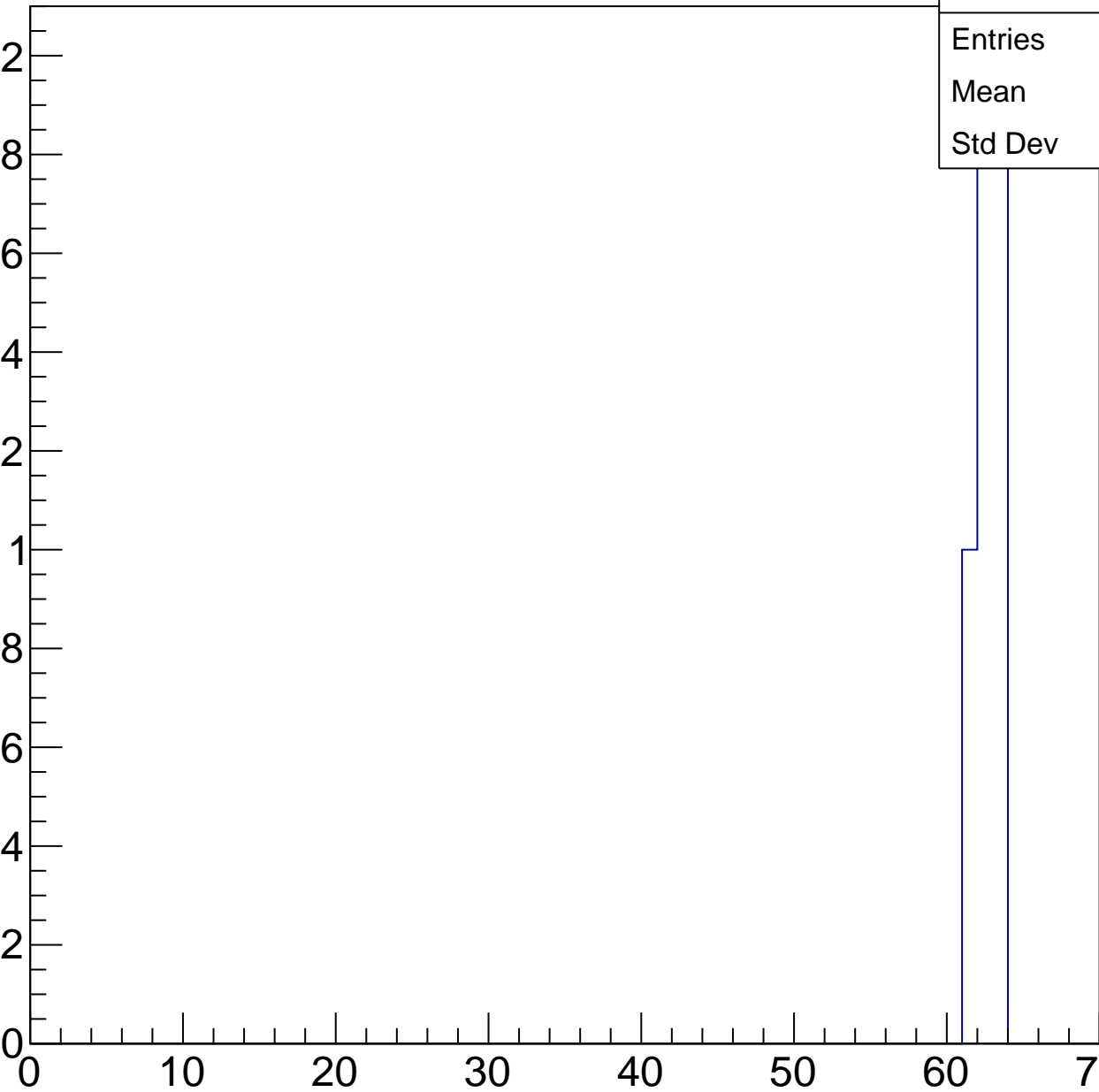
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch68, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch69, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	30.73
Std Dev	3.282

Gaus mean : 30.5888

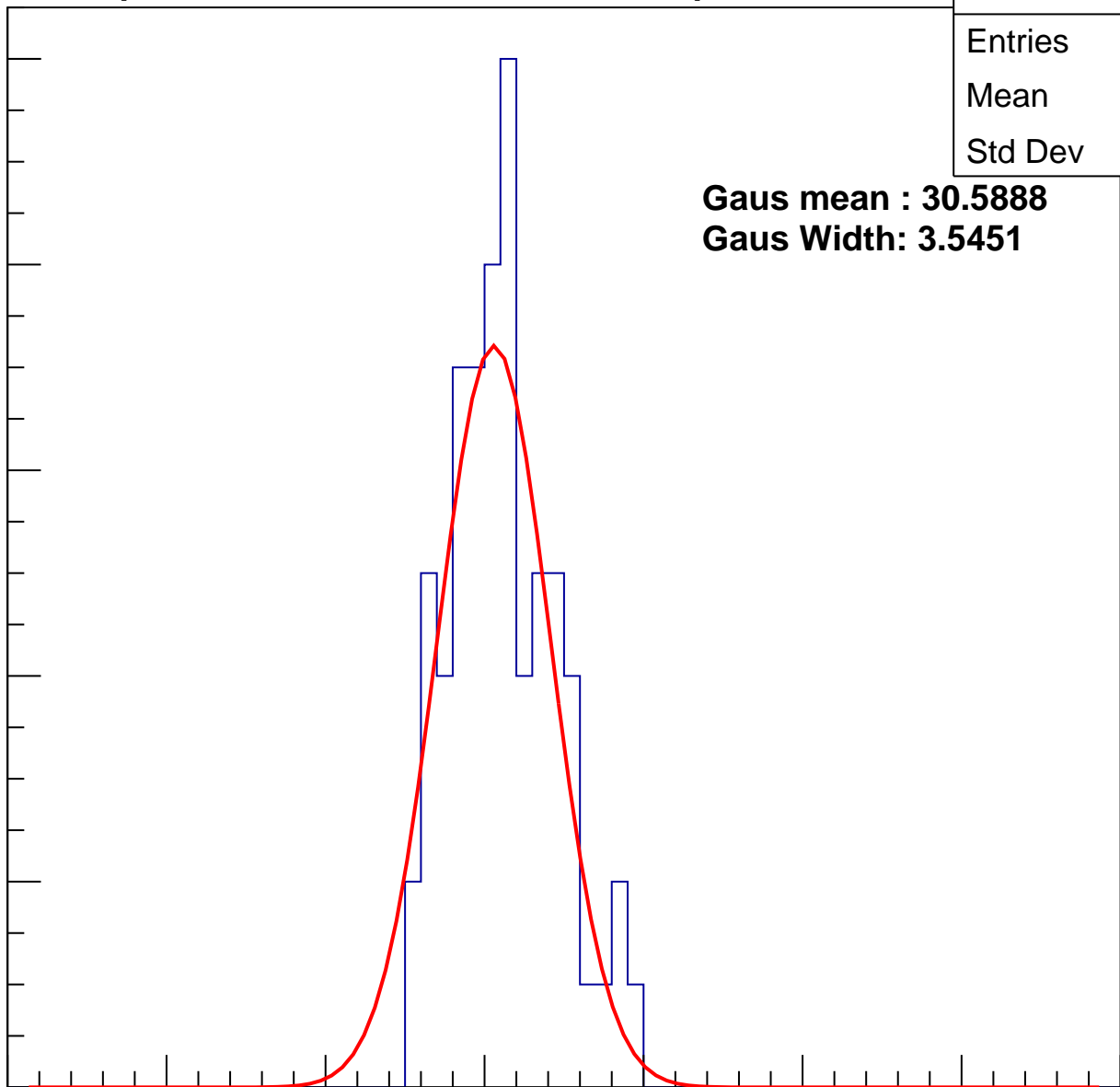
Gaus Width: 3.5451

Entry

10
8
6
4
2
0

ampl

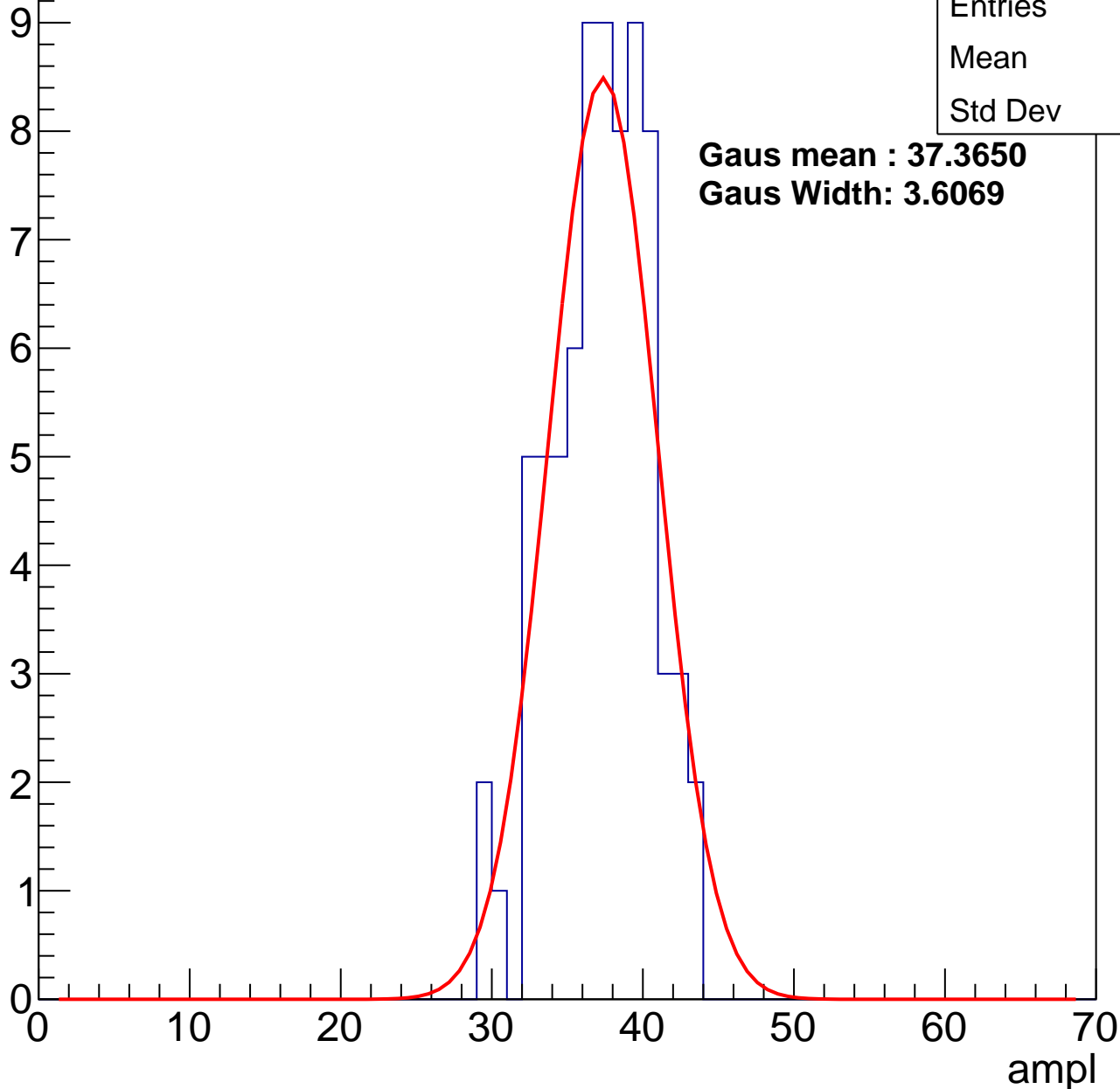
0 10 20 30 40 50 60 70



B1L003S, U21-ch69, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch69, adc2

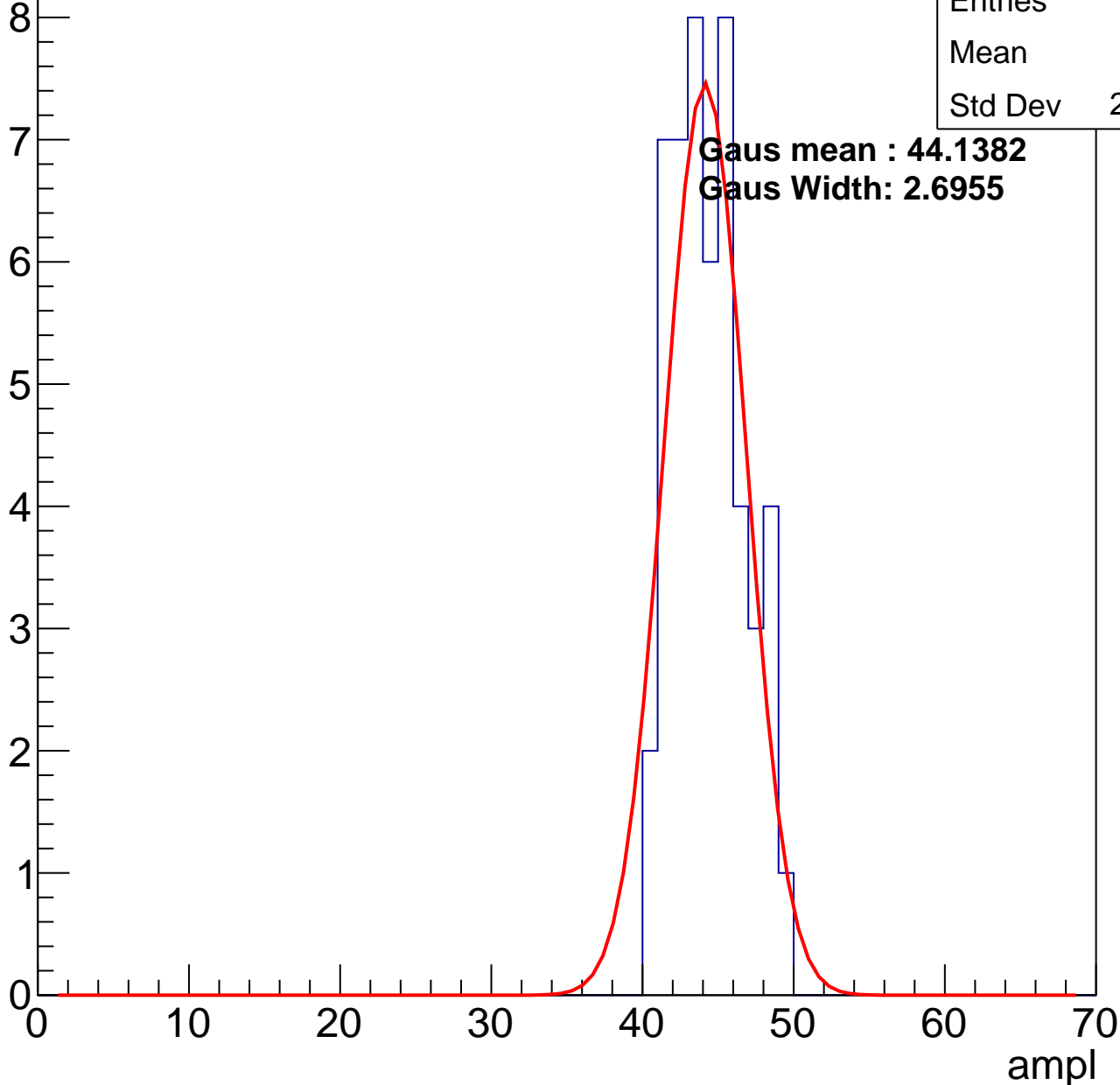
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.9
Std Dev	2.326

Gaus mean : 44.1382

Gaus Width: 2.6955

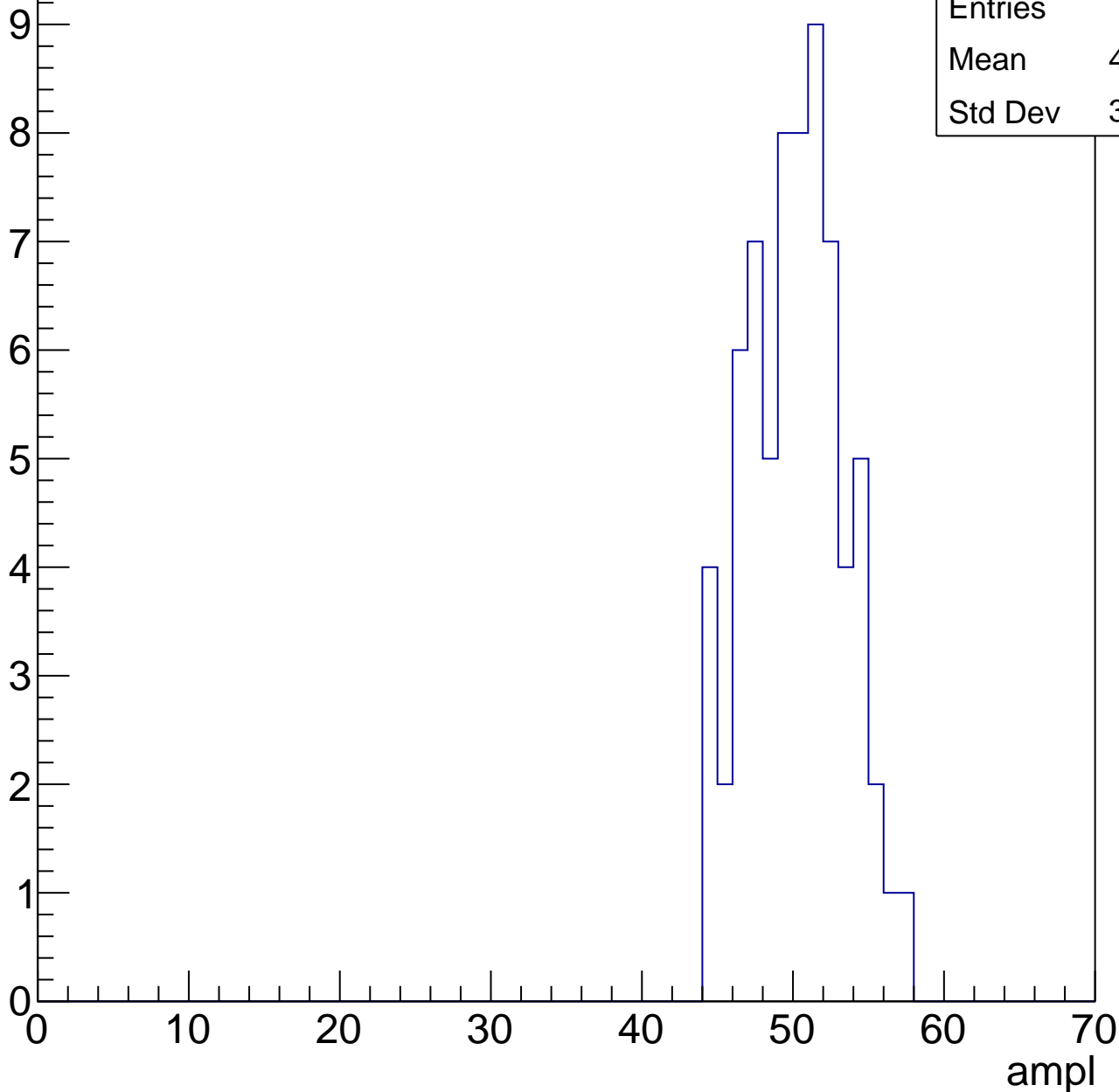


B1L003S, U21-ch69, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

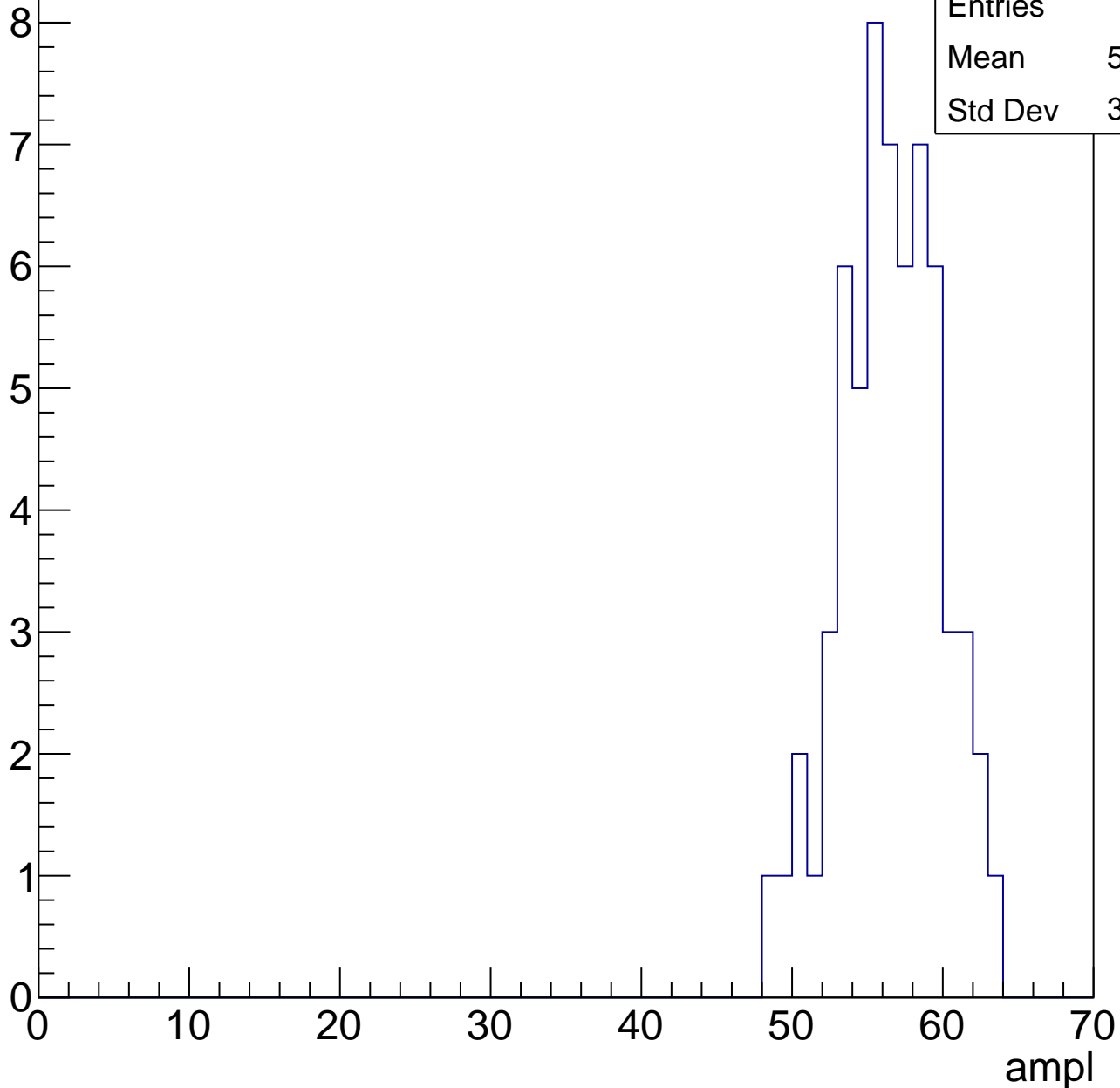
Entries	69
Mean	49.72
Std Dev	3.102



B1L003S, U21-ch69, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

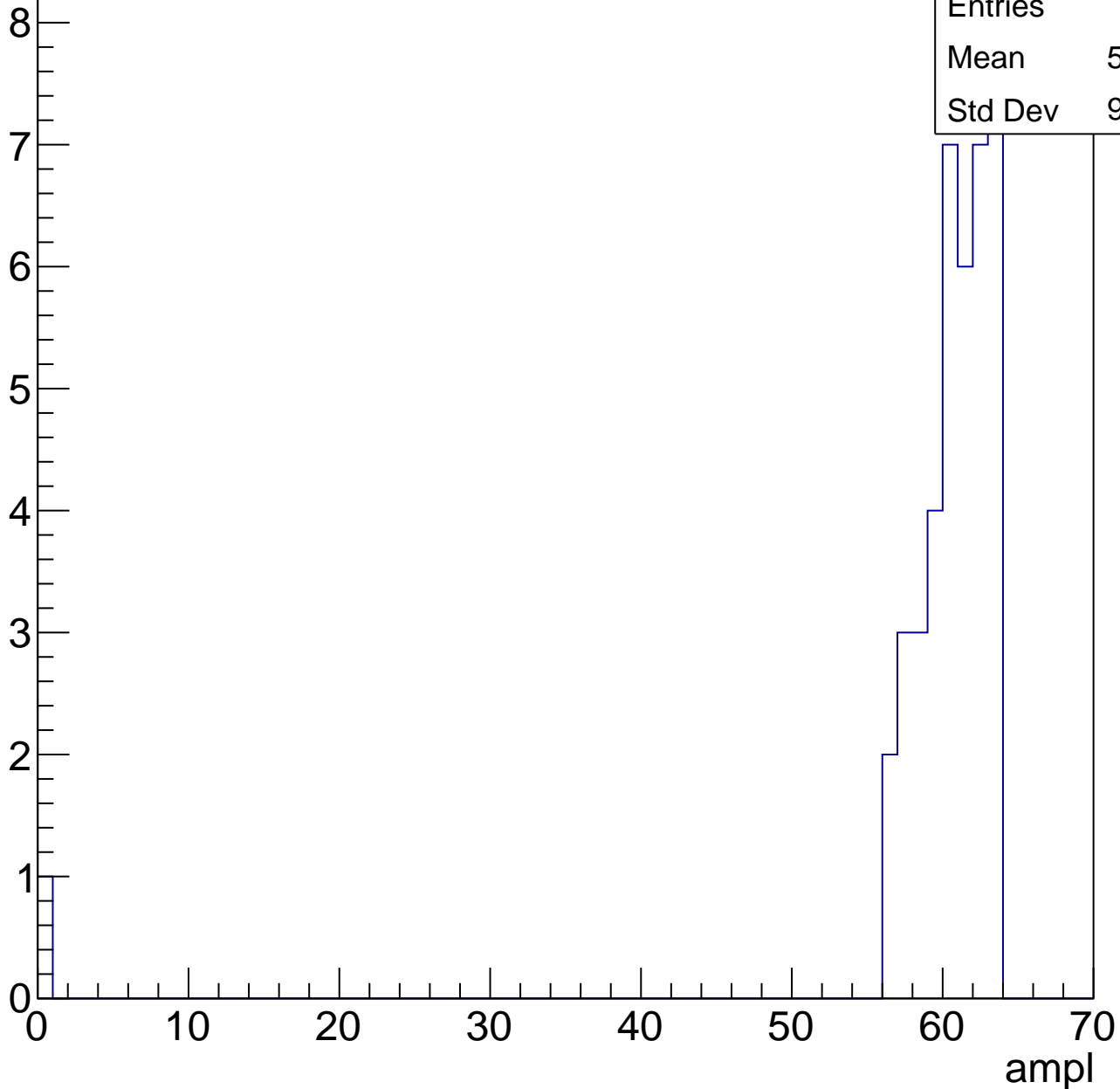


B1L003S, U21-ch69, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	58.95
Std Dev	9.546



B1L003S, U21-ch69, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch69, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch70, adc0

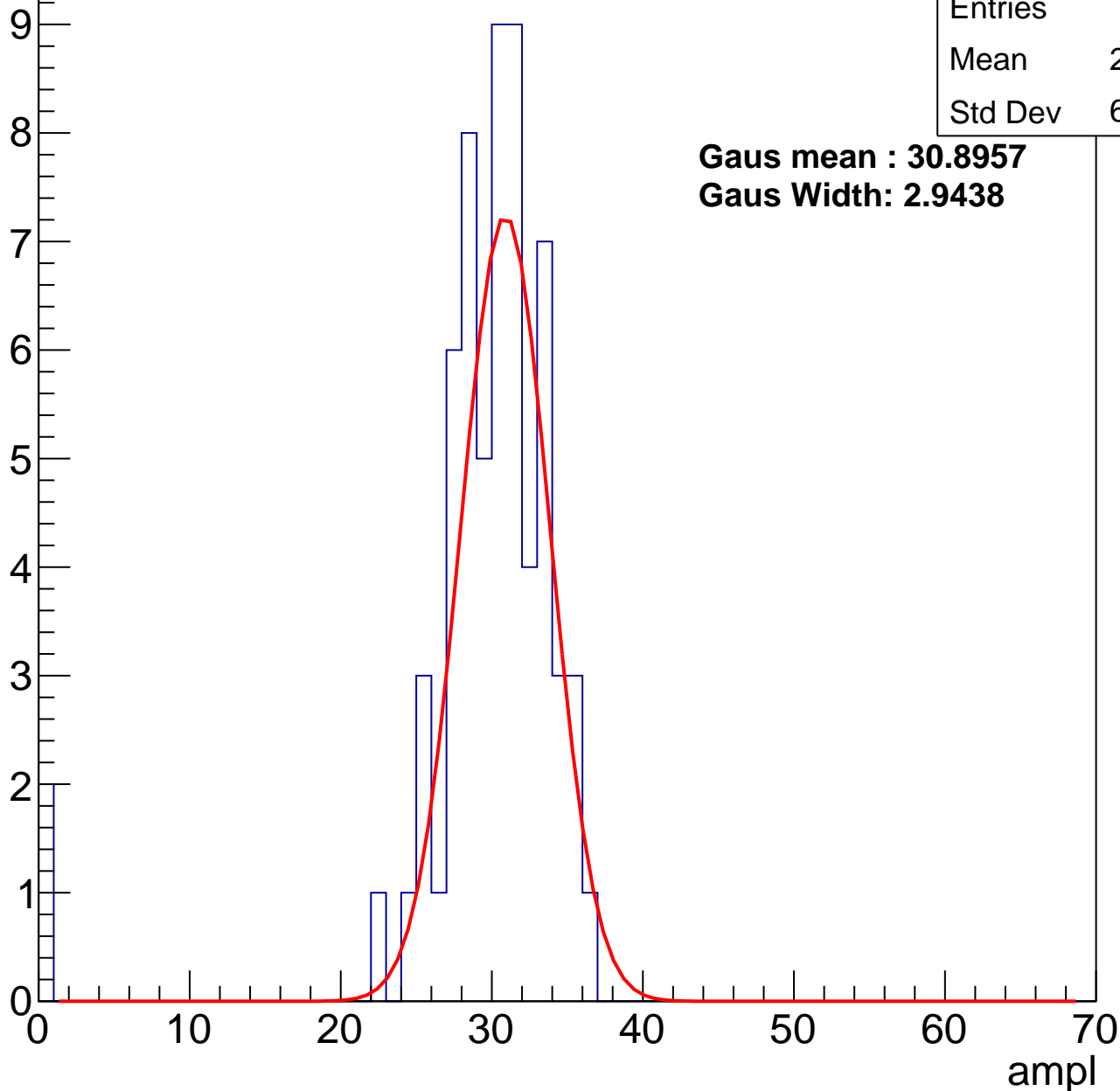
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.03
Std Dev	6.003

Gaus mean : 30.8957

Gaus Width: 2.9438



B1L003S, U21-ch70, adc1

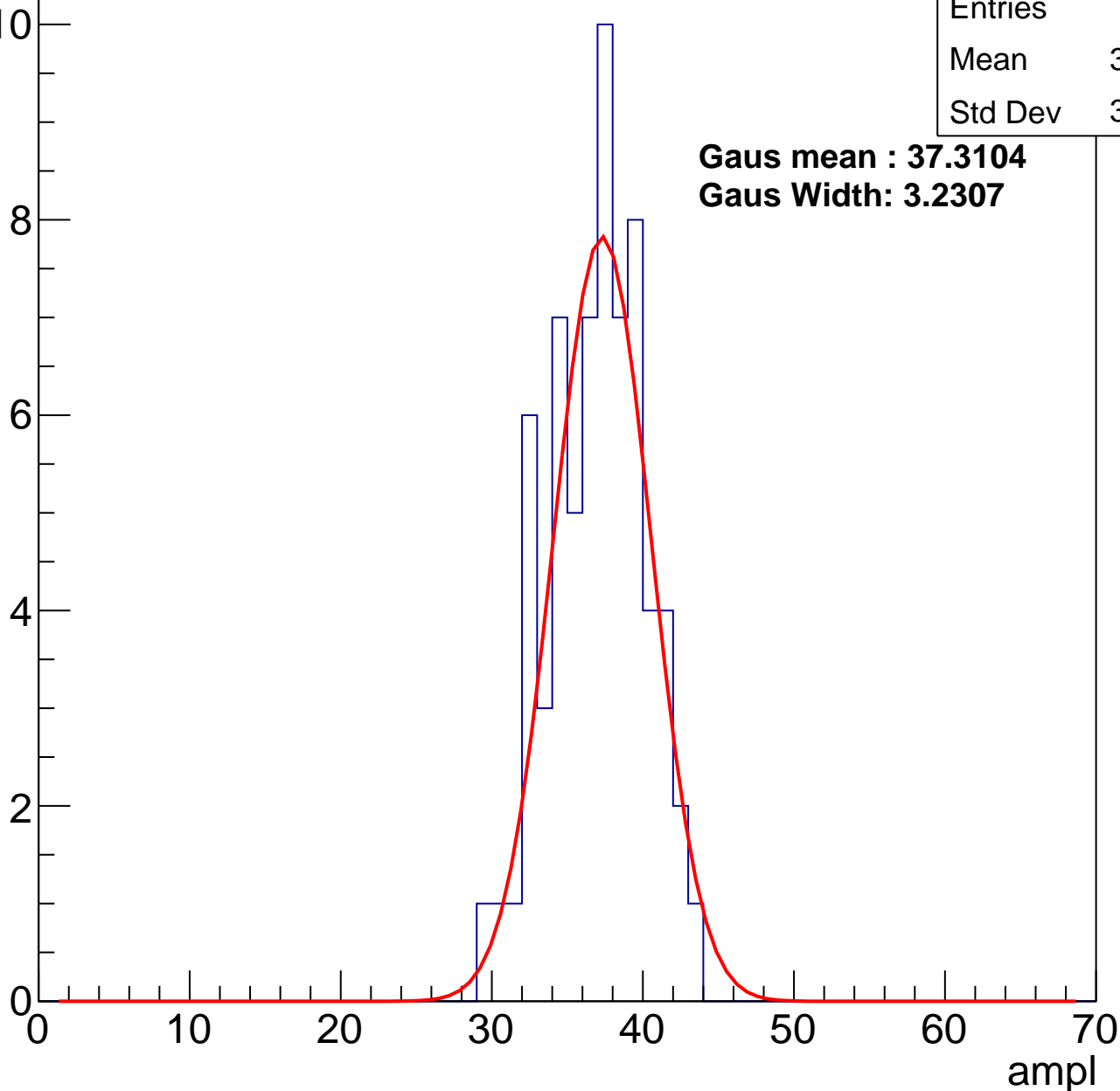
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.49
Std Dev	3.102

Gaus mean : 37.3104

Gaus Width: 3.2307



B1L003S, U21-ch70, adc2

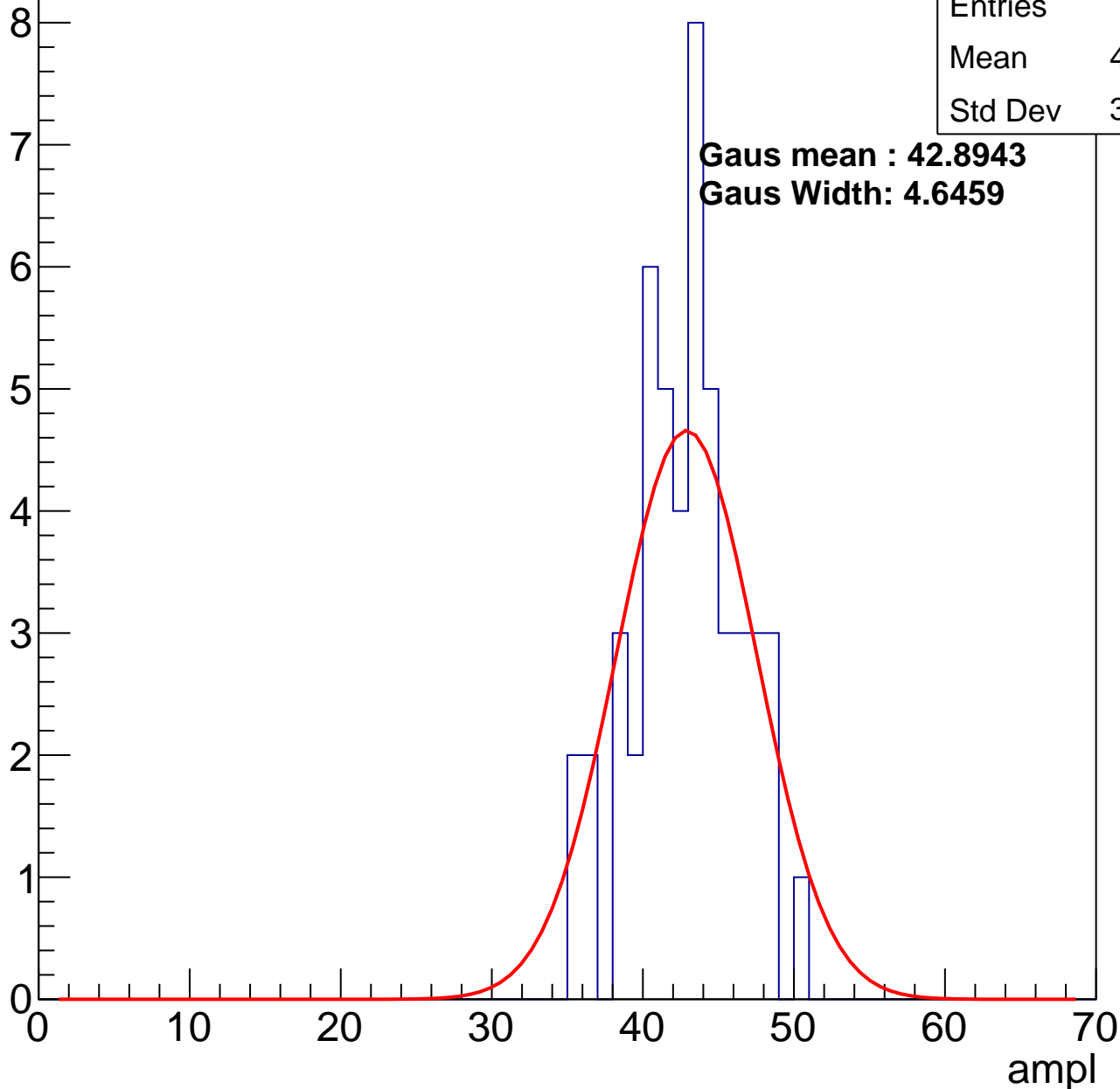
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	42.38
Std Dev	3.486

Gaus mean : 42.8943

Gaus Width: 4.6459

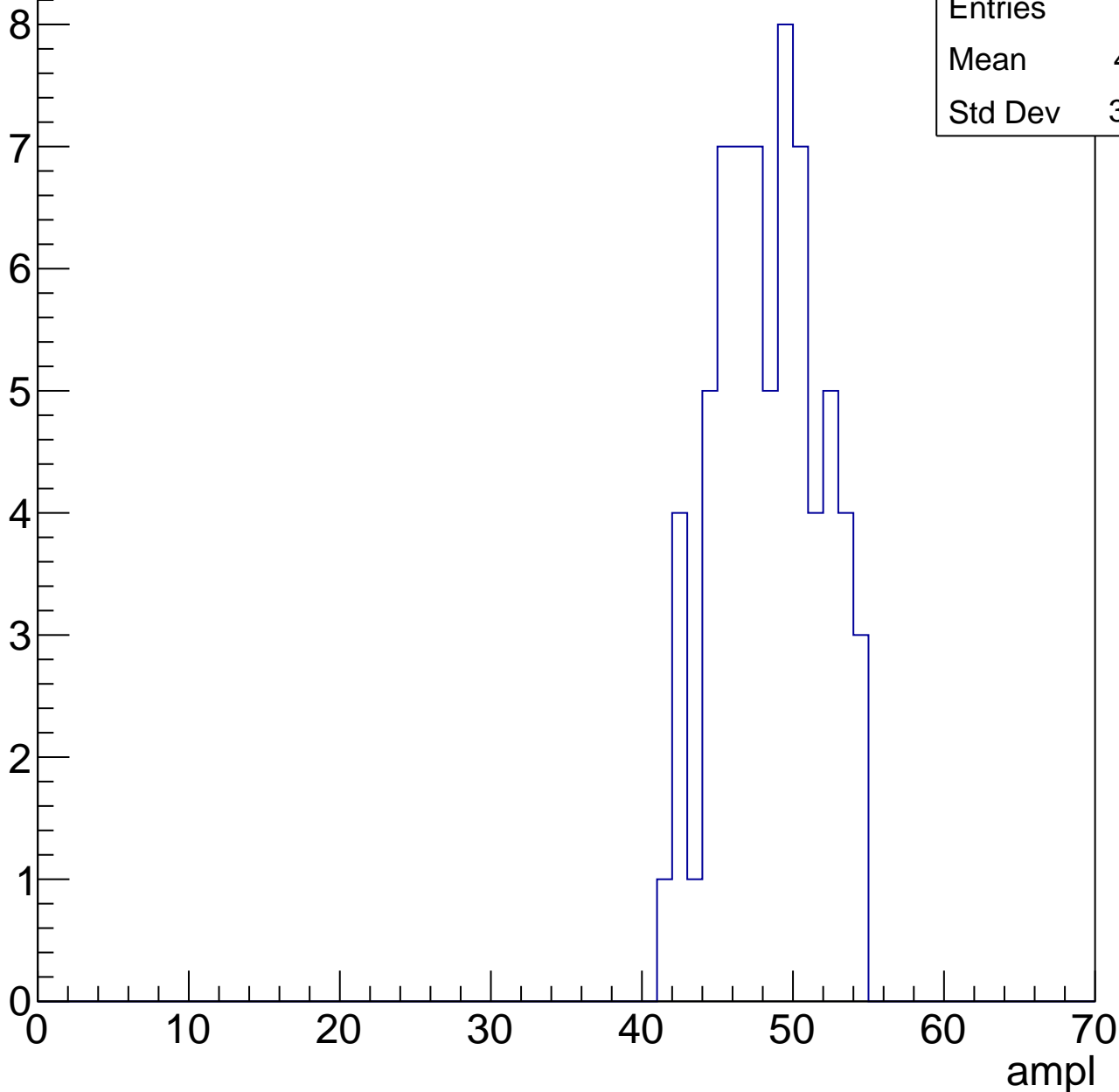


B1L003S, U21-ch70, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	47.91
Std Dev	3.333

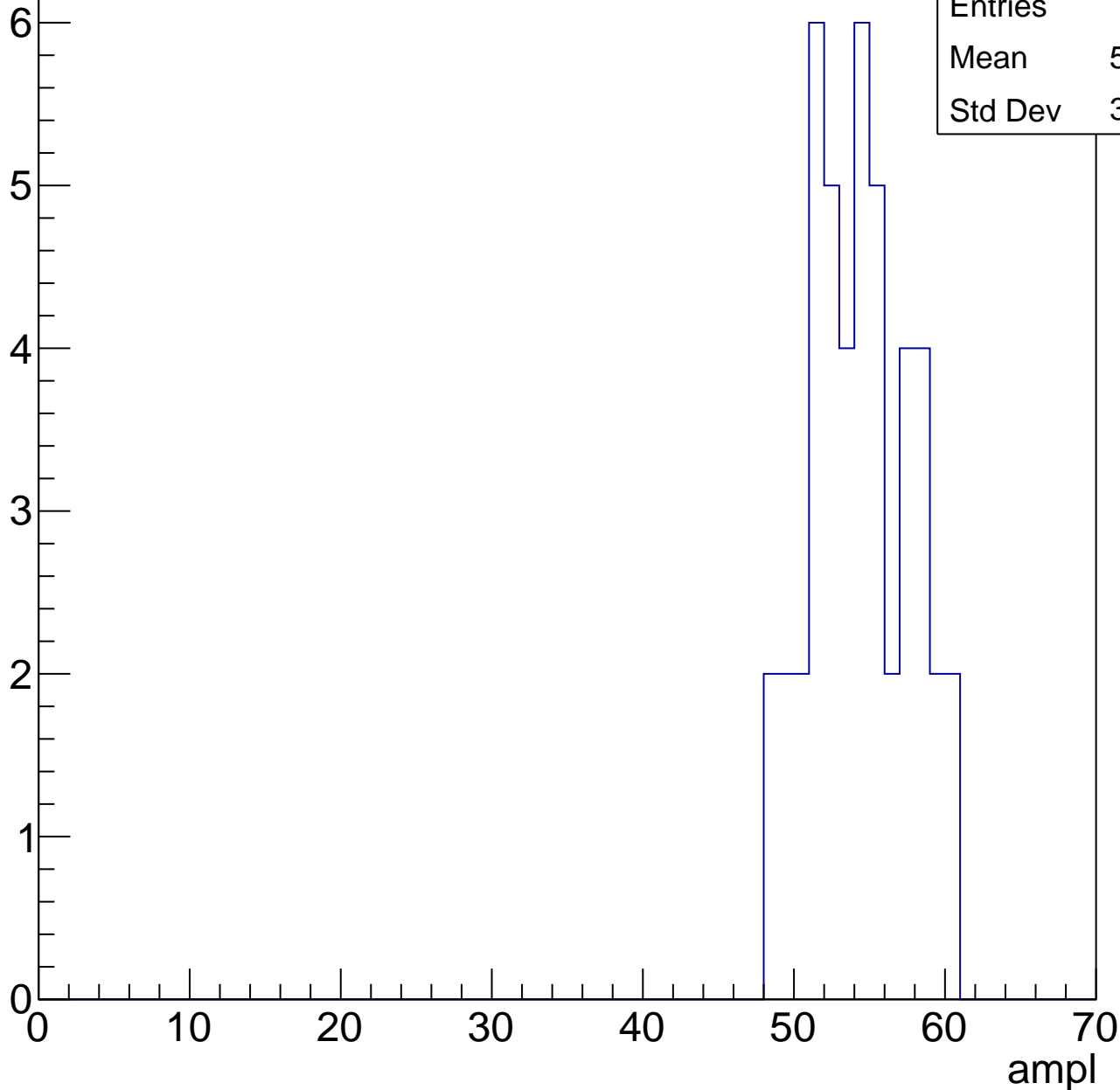


B1L003S, U21-ch70, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

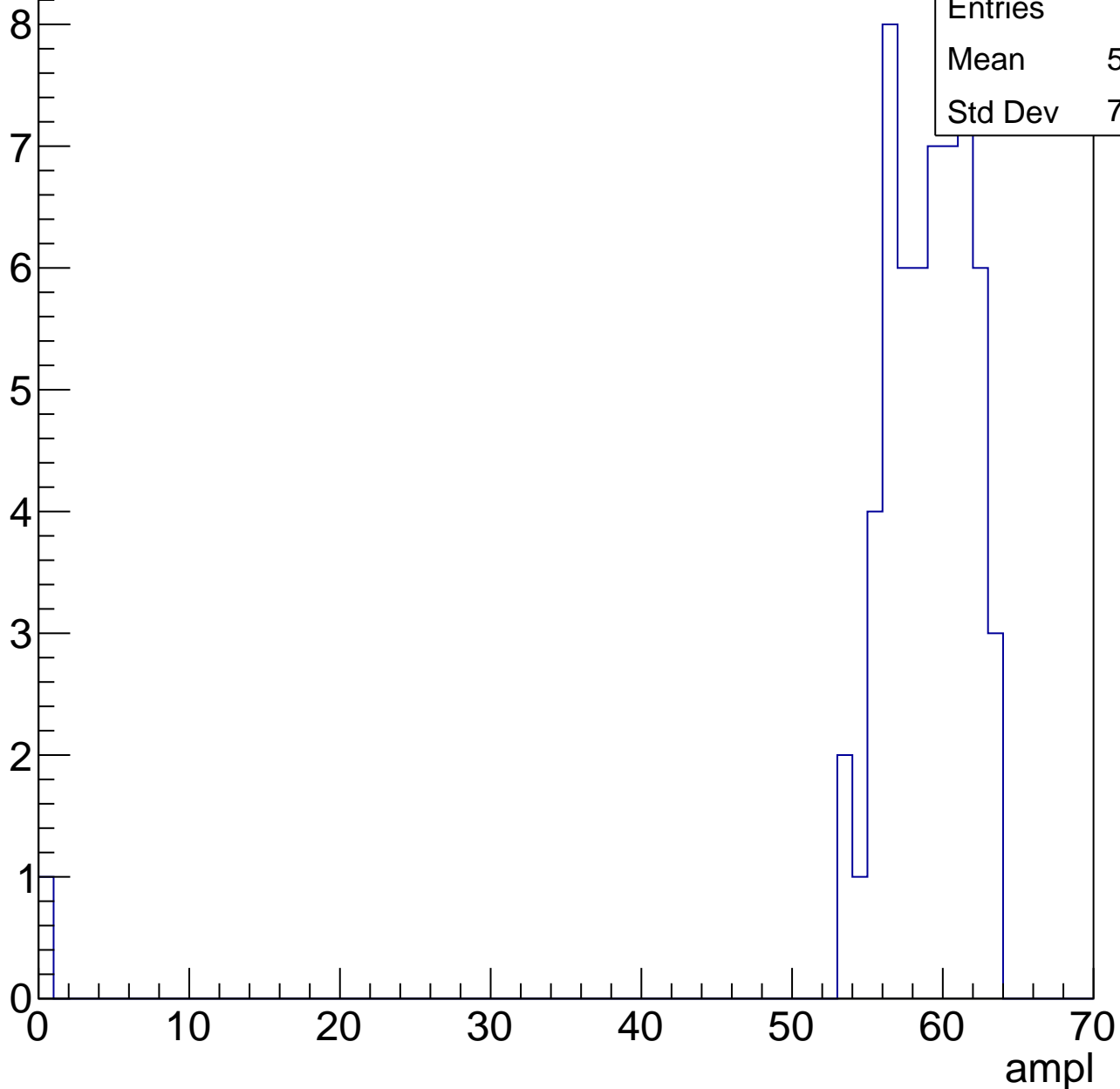
Entries	46
Mean	53.93
Std Dev	3.186



B1L003S, U21-ch70, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

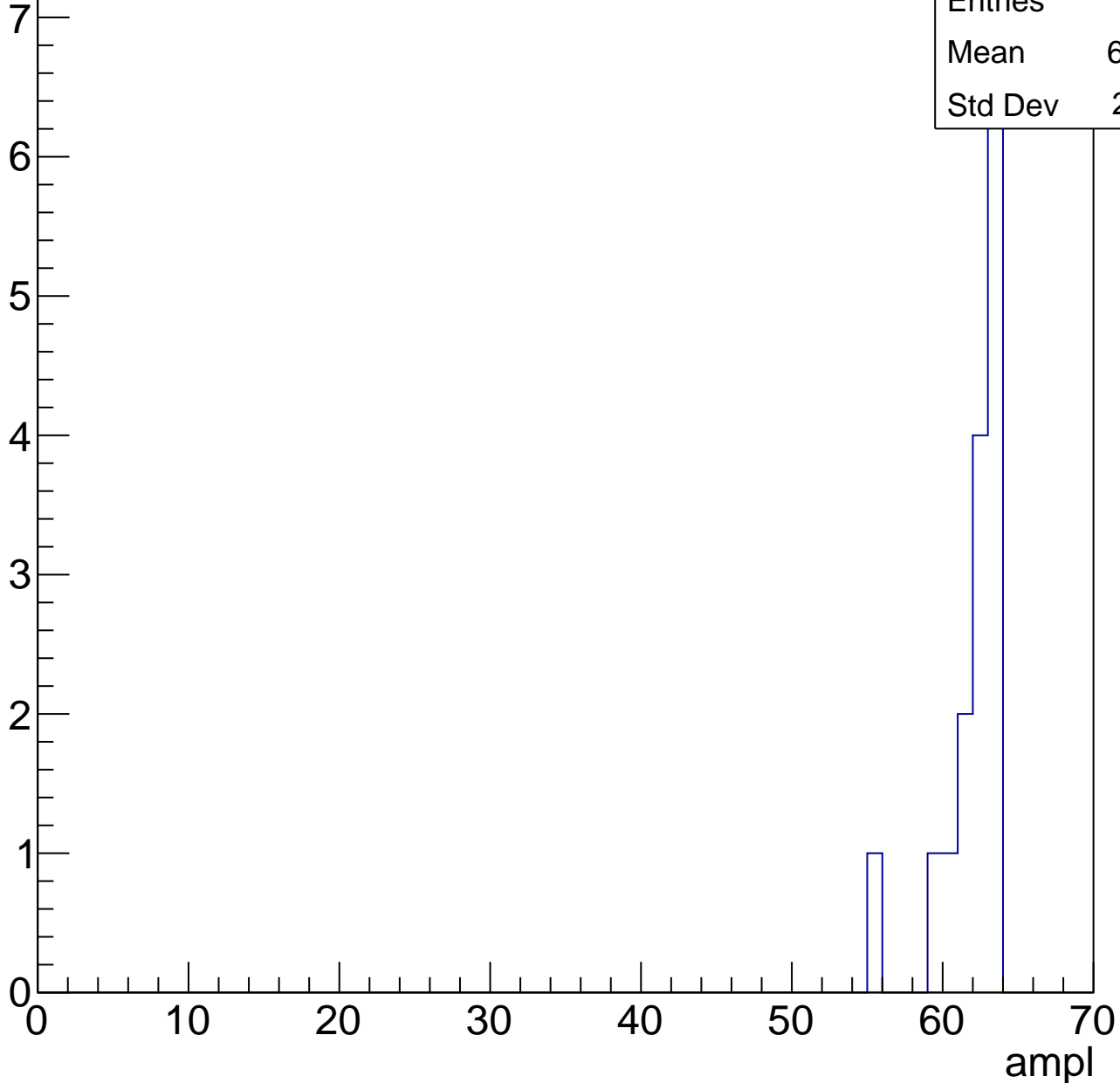


B1L003S, U21-ch70, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	61.56
Std Dev	2.061



B1L003S, U21-ch70, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch71, adc0

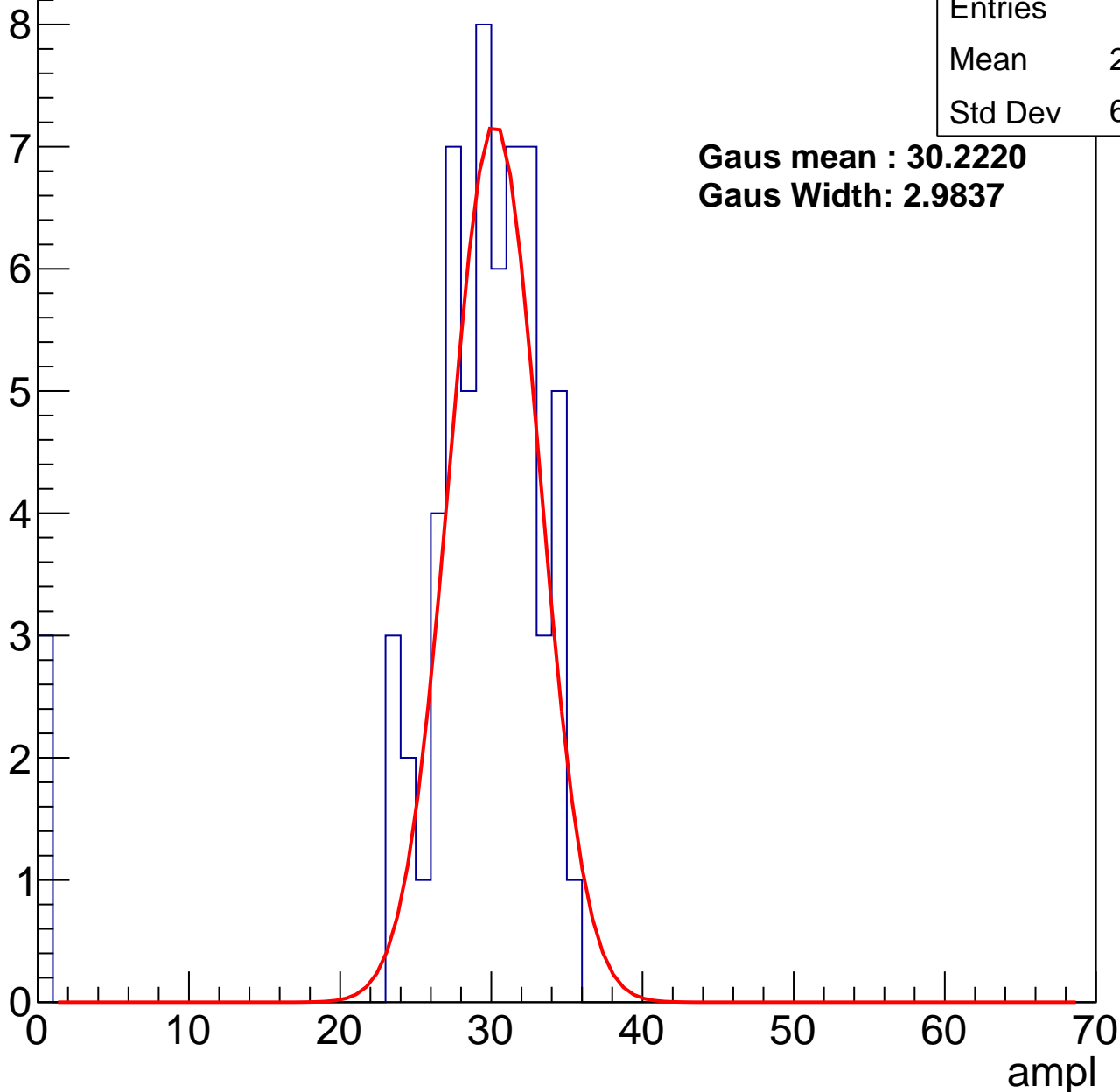
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	27.94
Std Dev	6.958

Gaus mean : 30.2220

Gaus Width: 2.9837



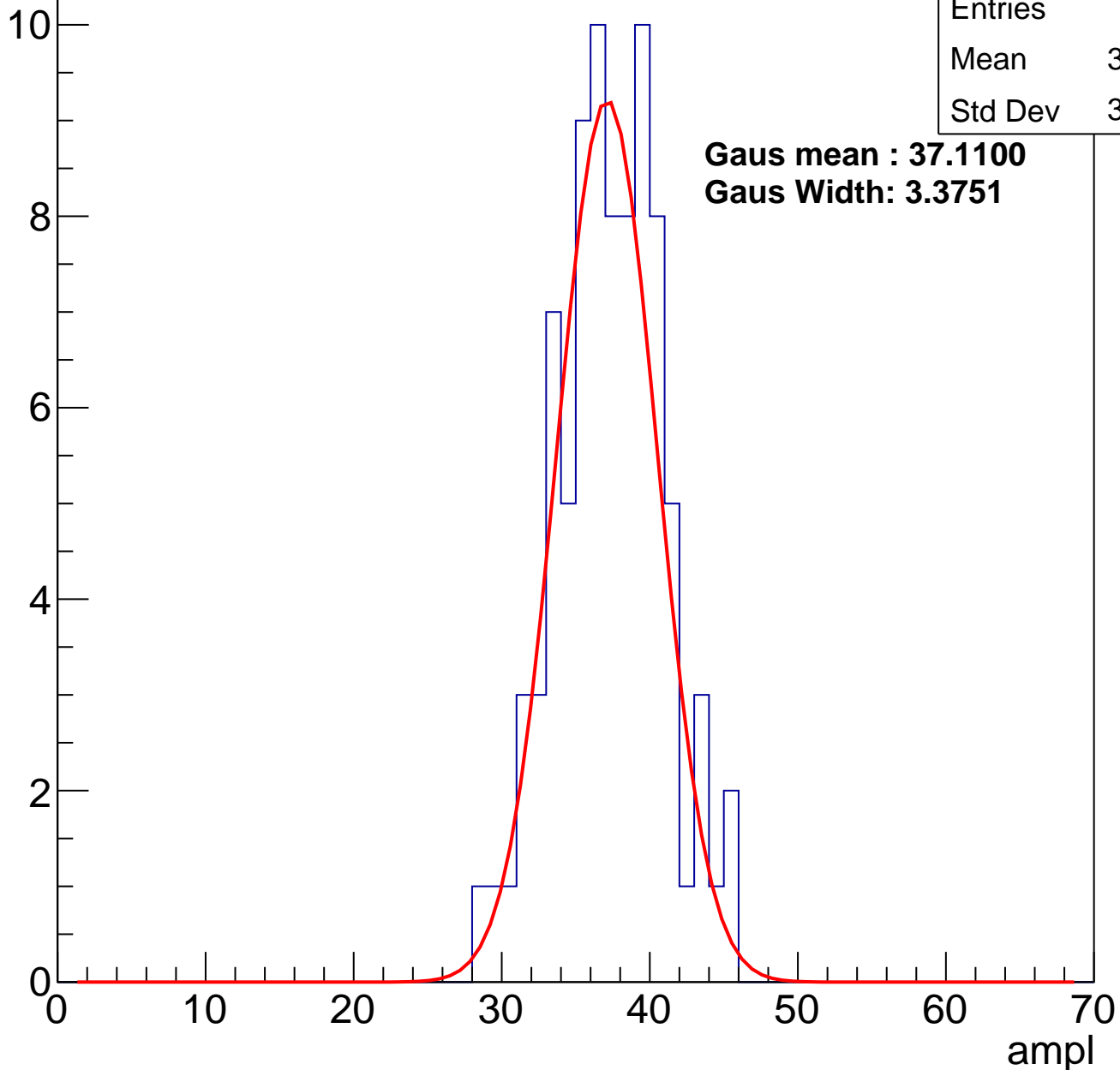
B1L003S, U21-ch71, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	36.88
Std Dev	3.558

Gaus mean : 37.1100
Gaus Width: 3.3751

Entry



B1L003S, U21-ch71, adc2

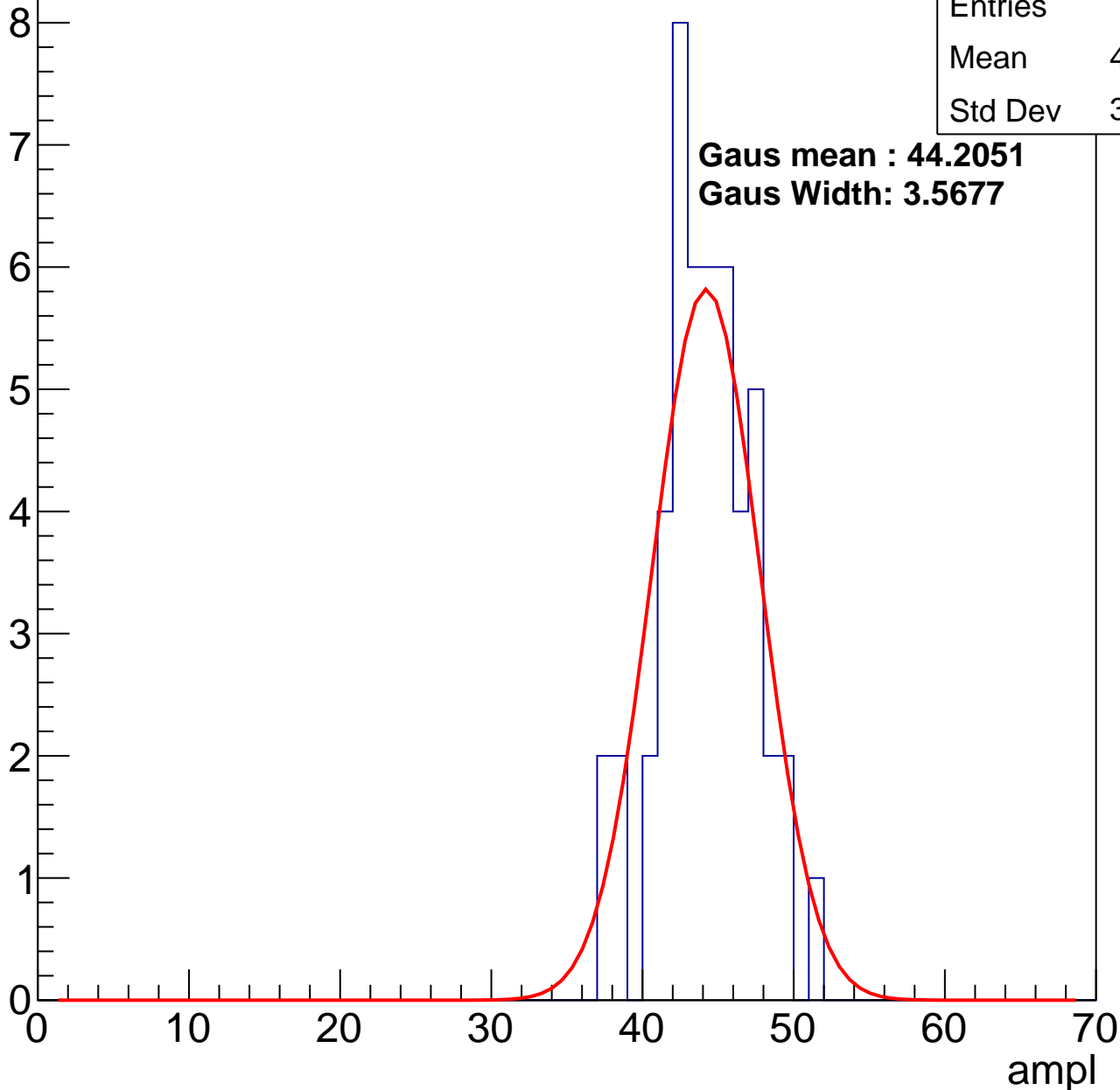
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	43.72
Std Dev	3.066

Gaus mean : 44.2051

Gaus Width: 3.5677

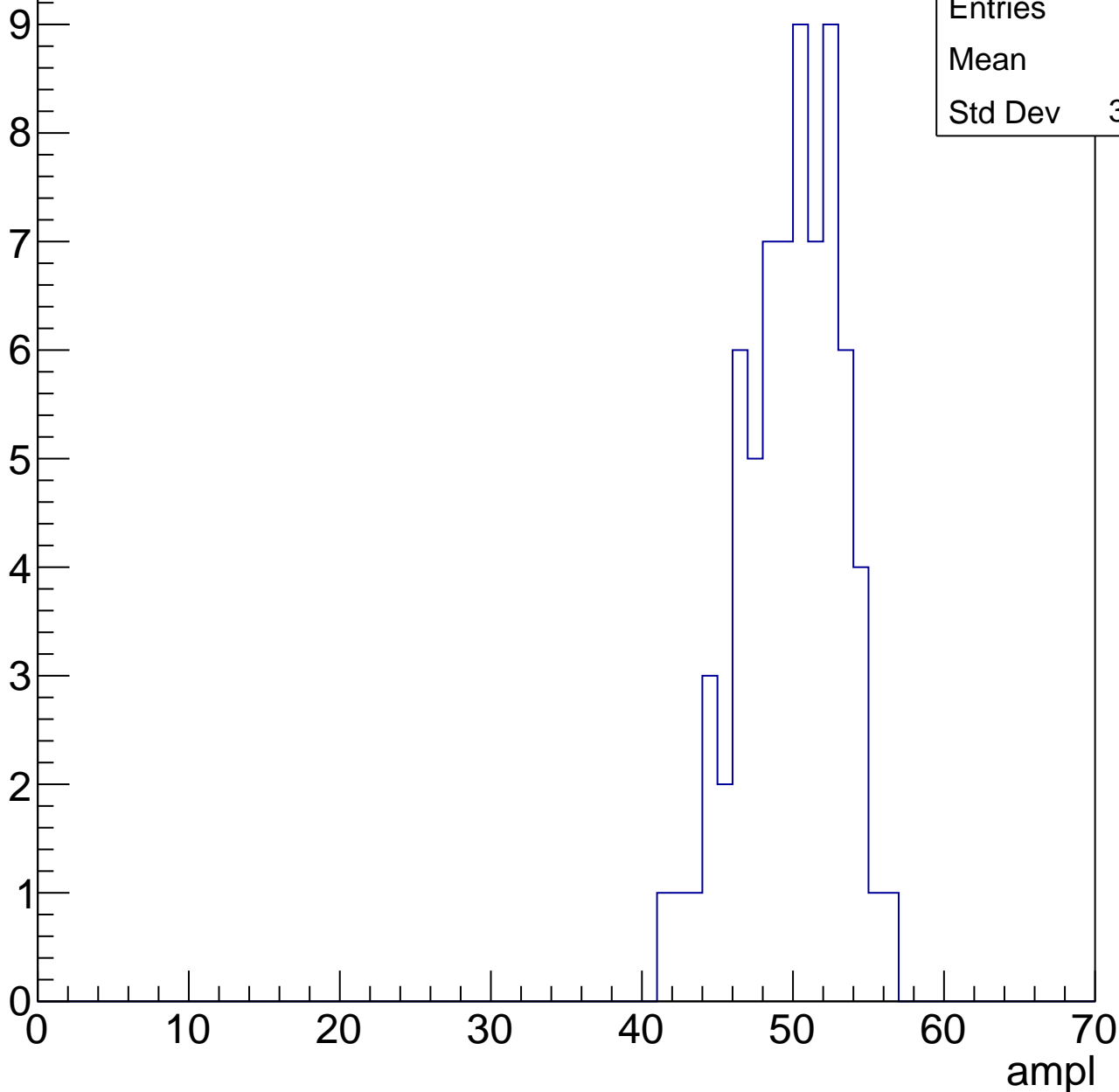


B1L003S, U21-ch71, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	49.4
Std Dev	3.218

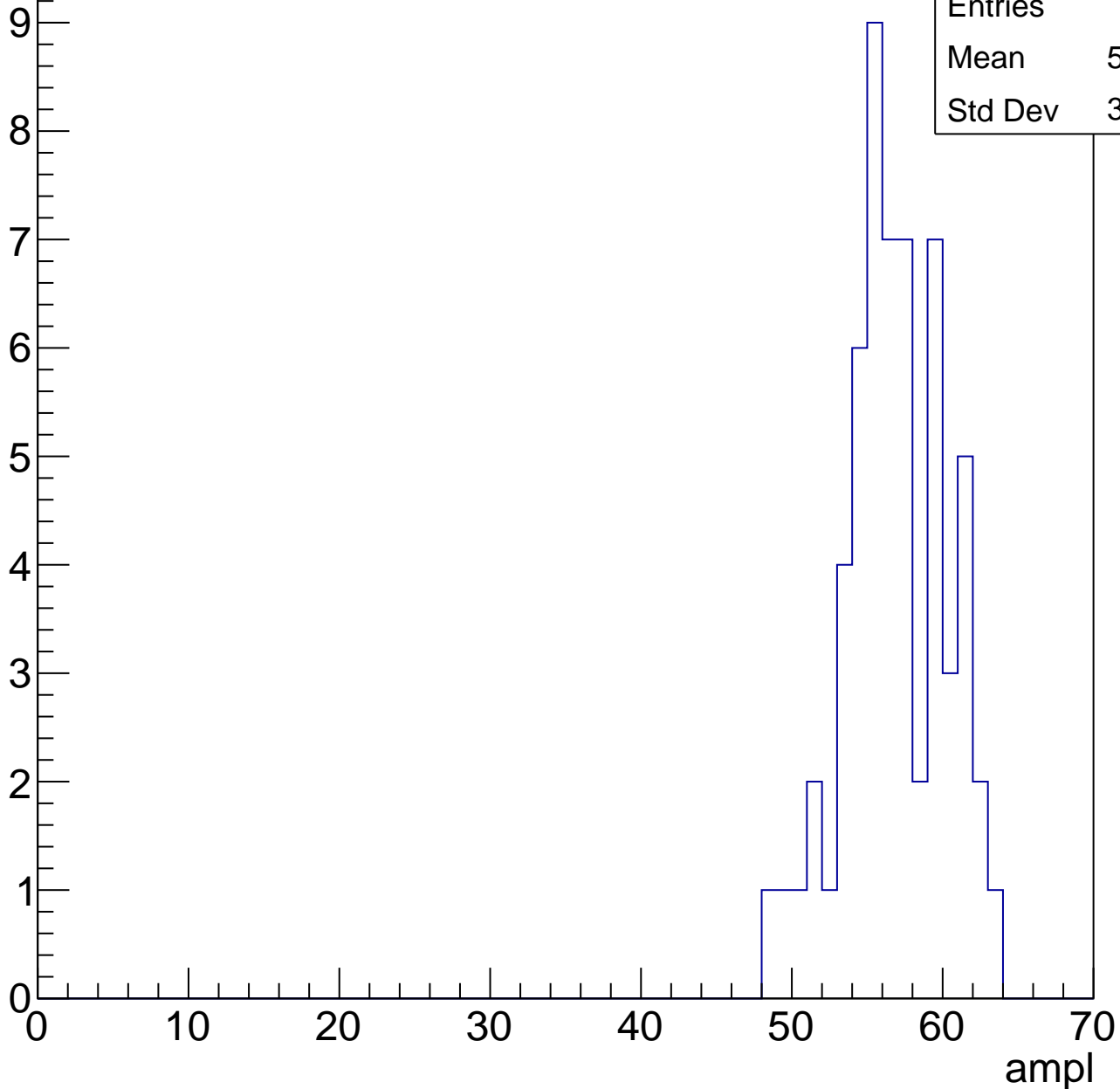


B1L003S, U21-ch71, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

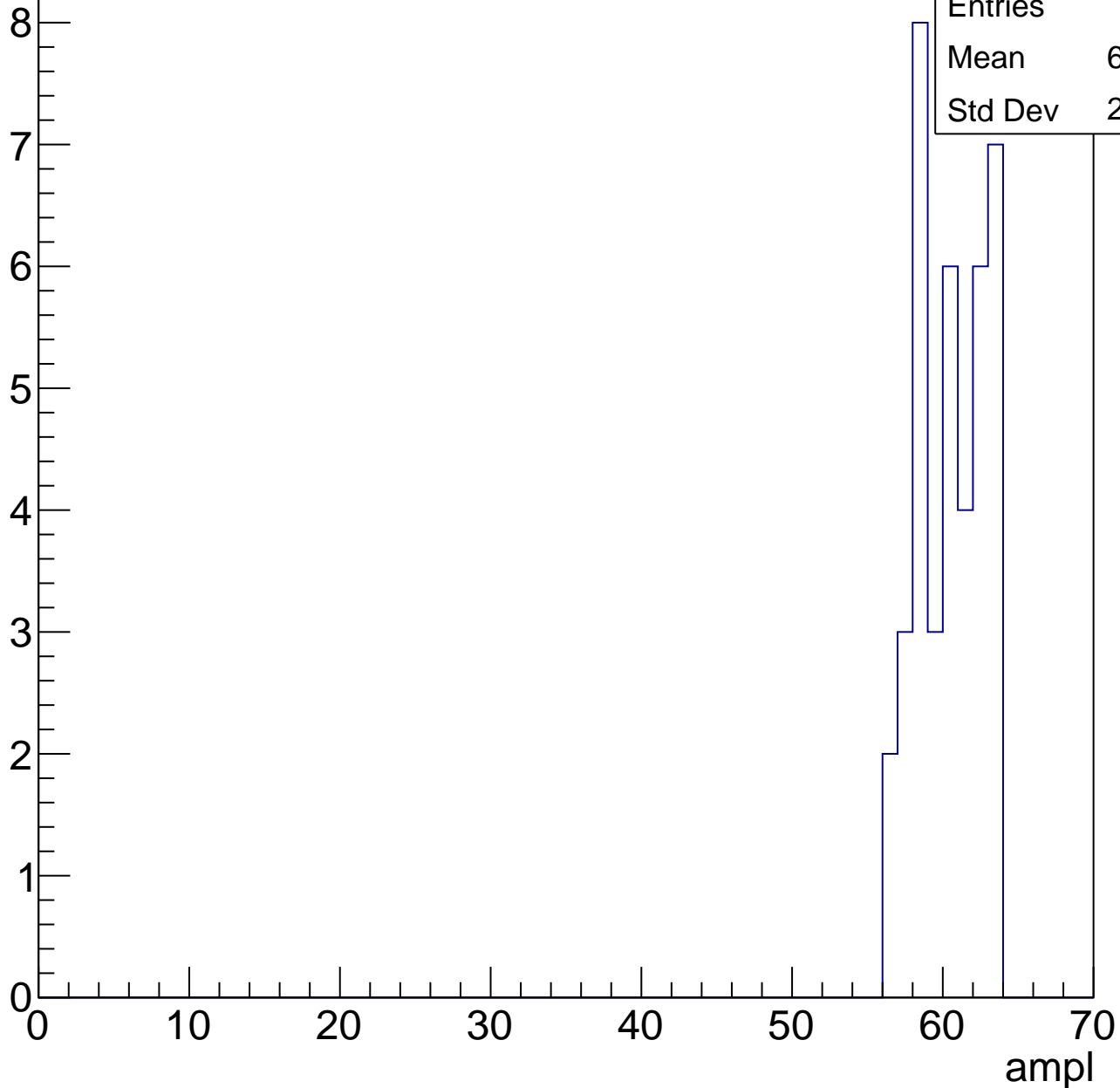
Entries	59
Mean	56.34
Std Dev	3.317



B1L003S, U21-ch71, adc5

calib_packv5_042523_0143.root, FC#13, port D2

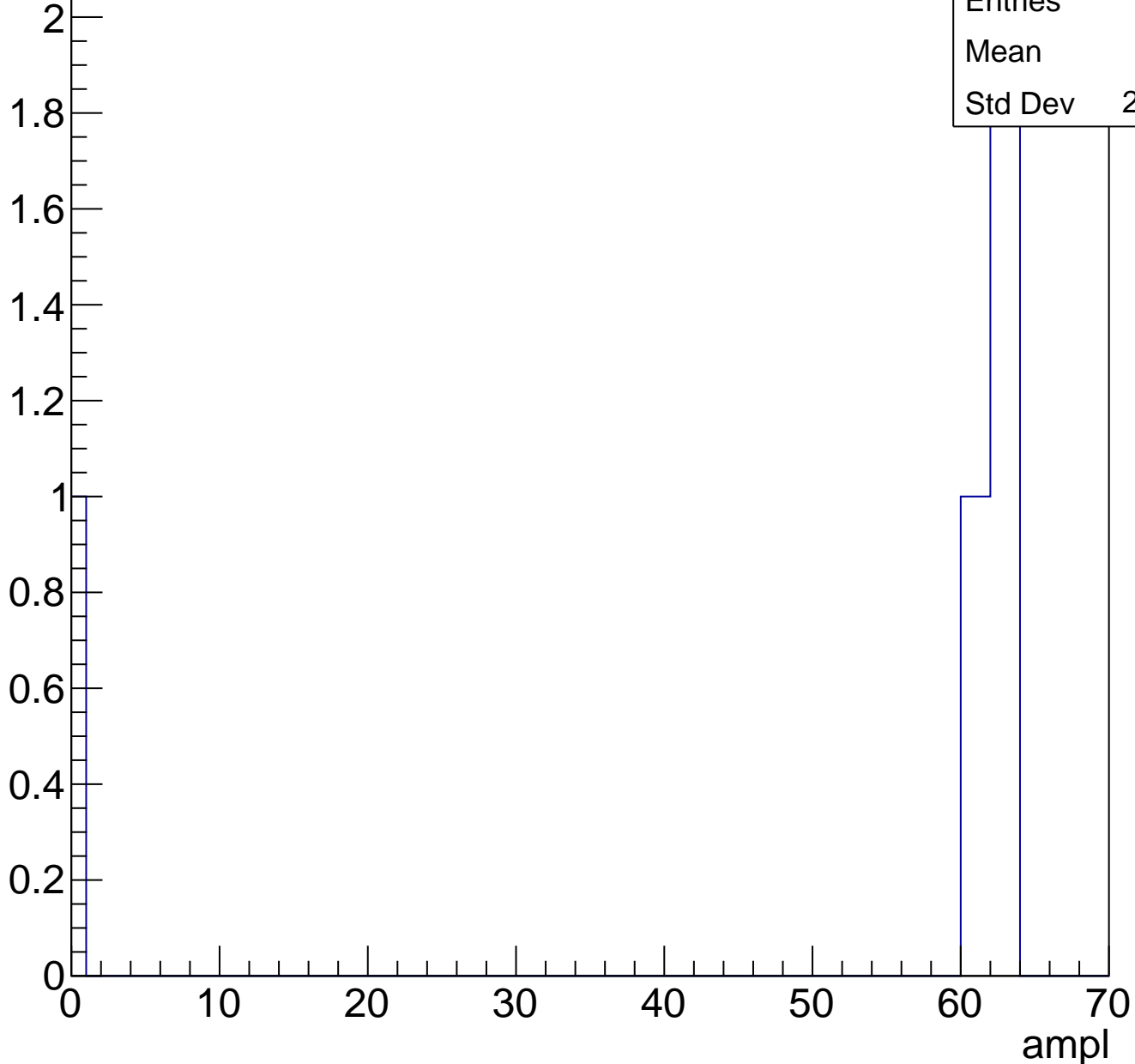
Entry



B1L003S, U21-ch71, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch71, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch72, adc0

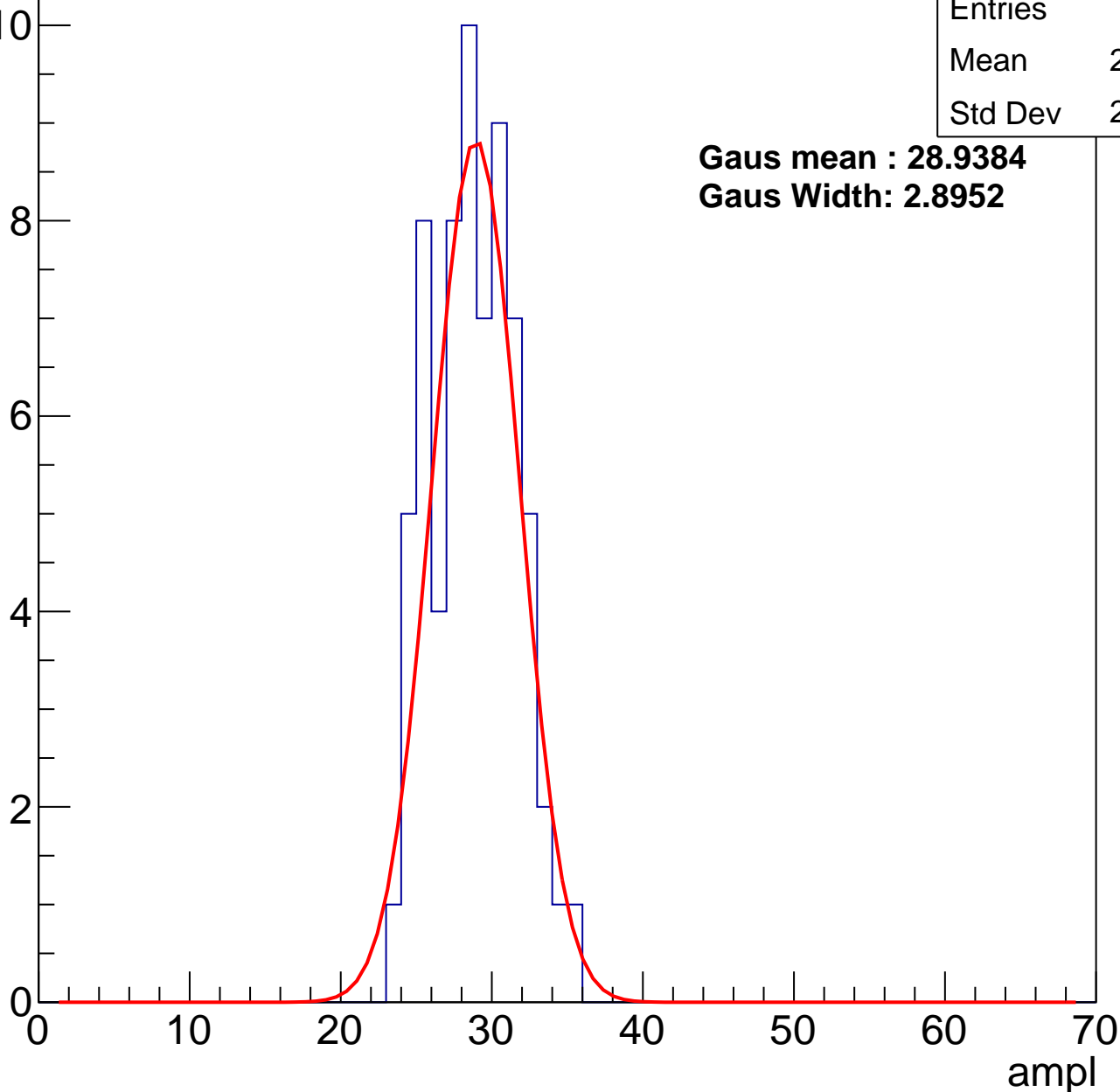
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.35
Std Dev	2.748

Gaus mean : 28.9384

Gaus Width: 2.8952



B1L003S, U21-ch72, adc1

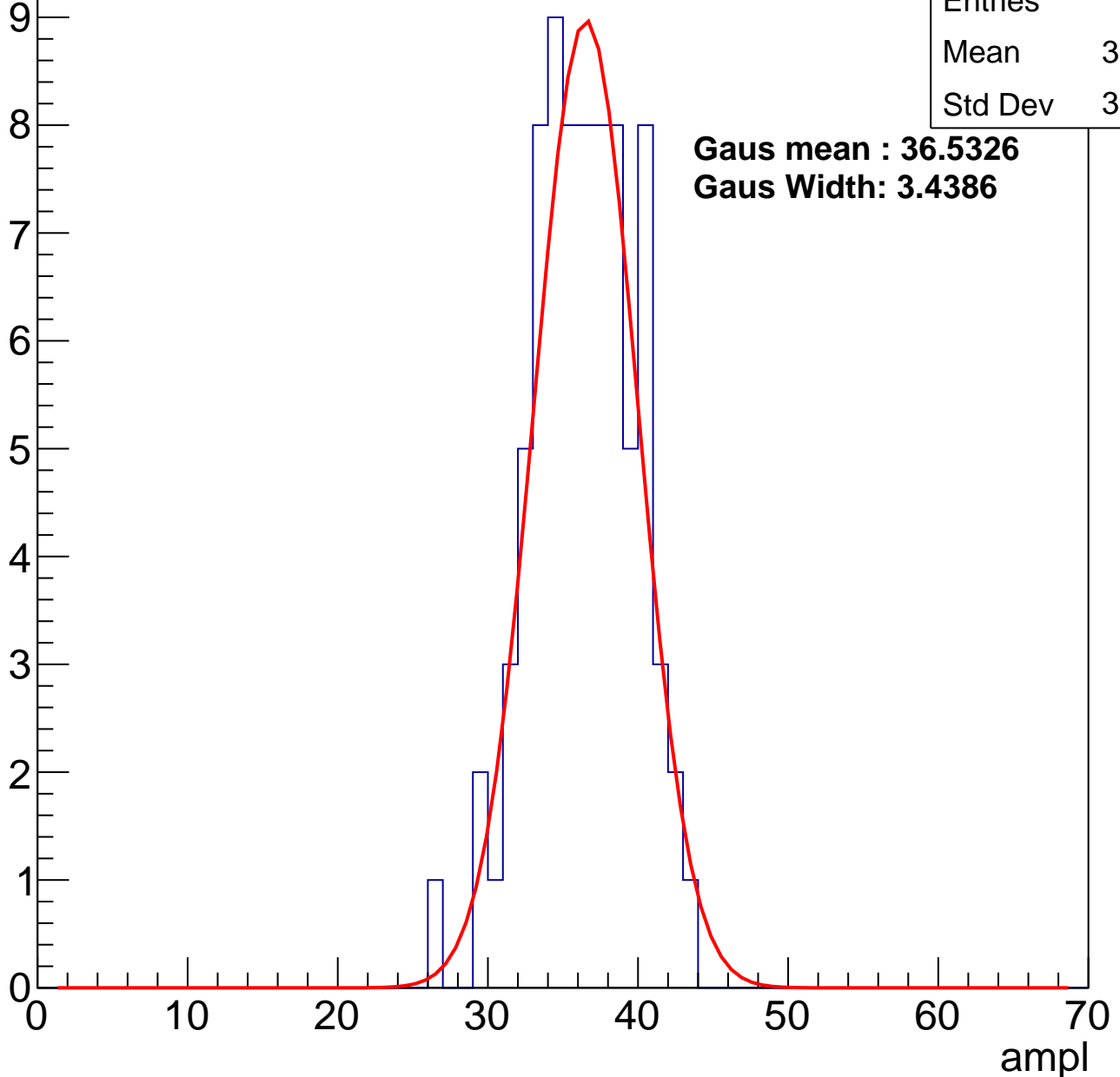
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	35.88
Std Dev	3.378

Gaus mean : 36.5326

Gaus Width: 3.4386



B1L003S, U21-ch72, adc2

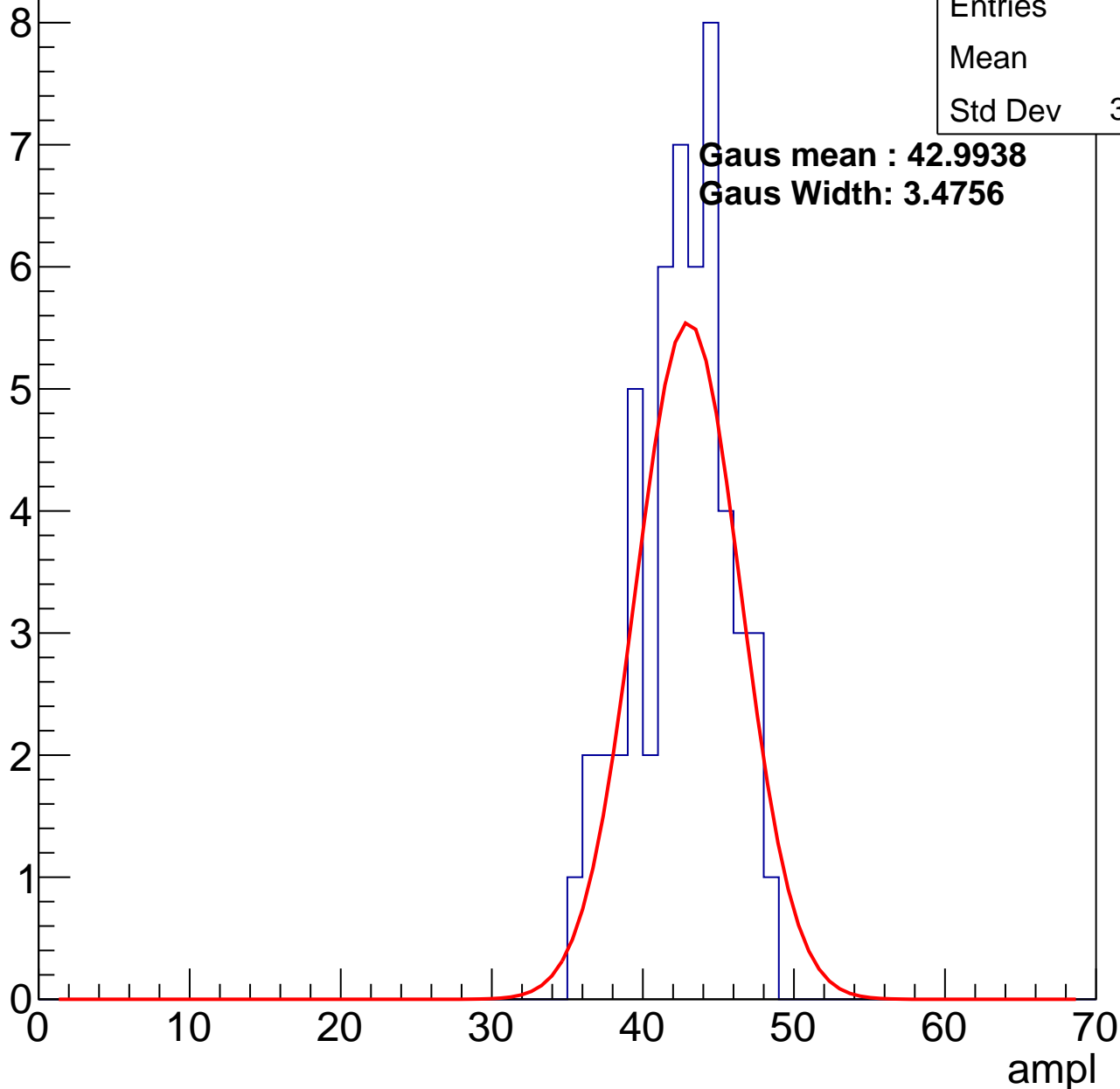
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.1
Std Dev	3.084

Gaus mean : 42.9938

Gaus Width: 3.4756

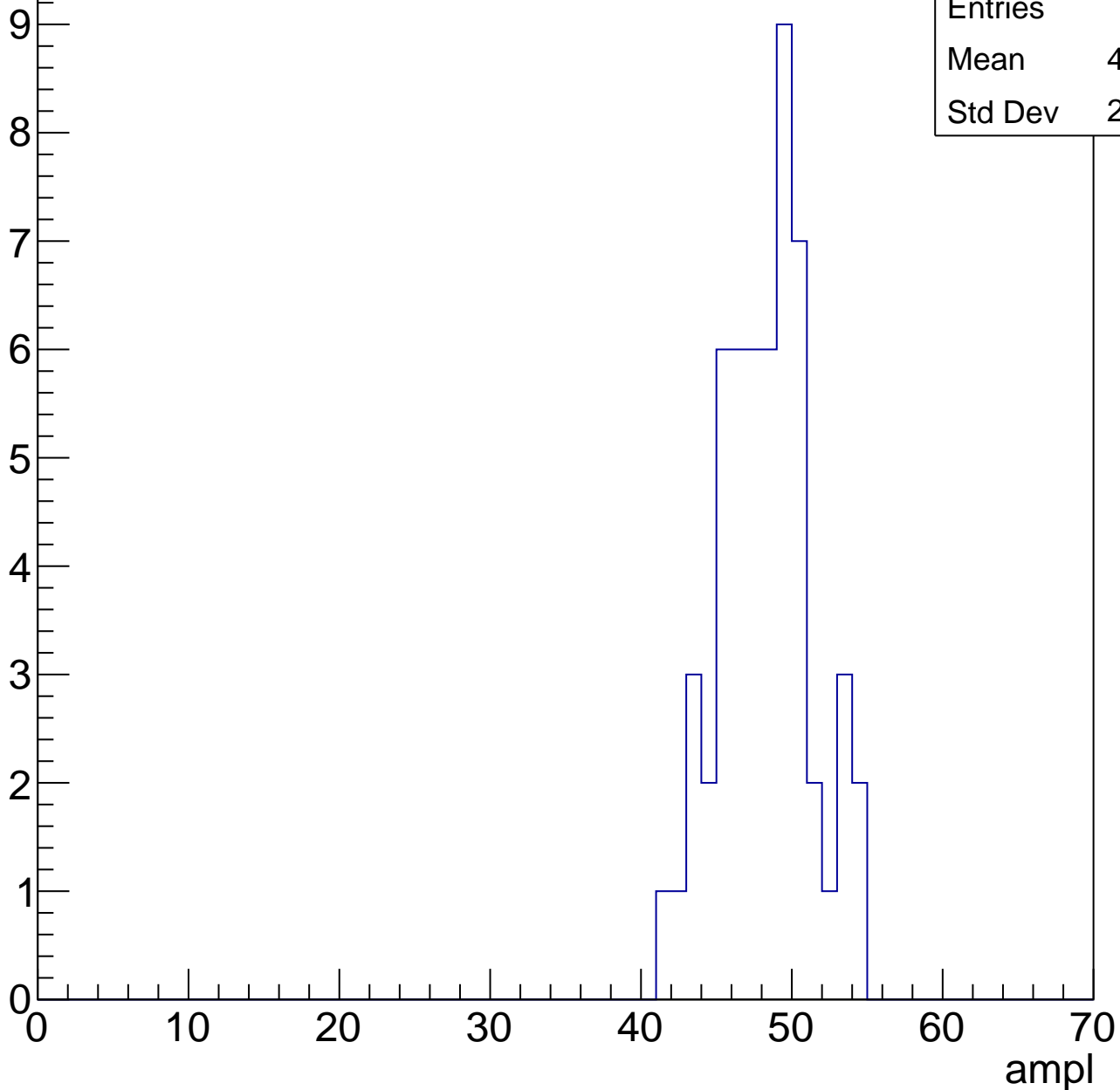


B1L003S, U21-ch72, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	47.78
Std Dev	2.989

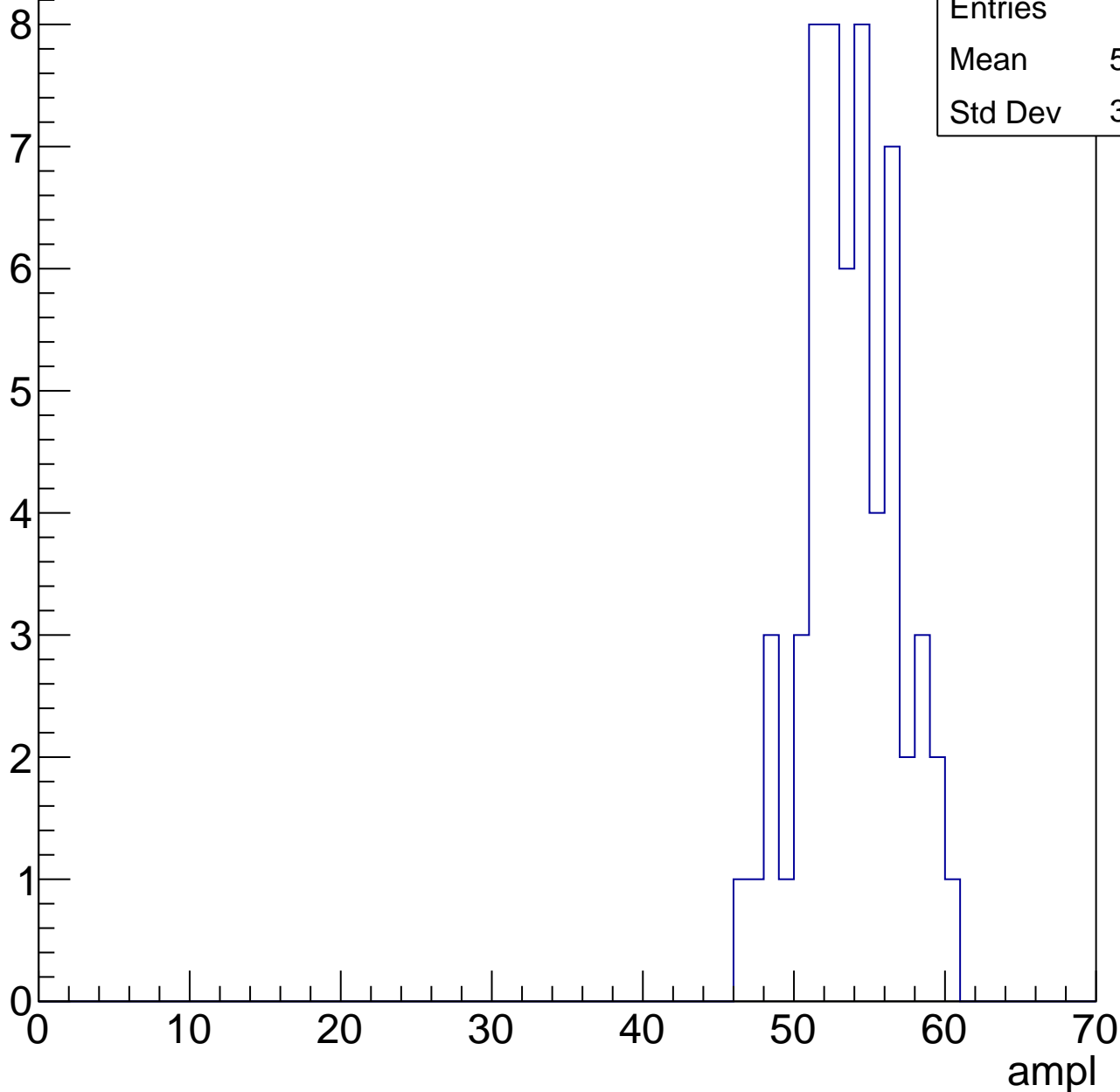


B1L003S, U21-ch72, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.24
Std Dev	3.092

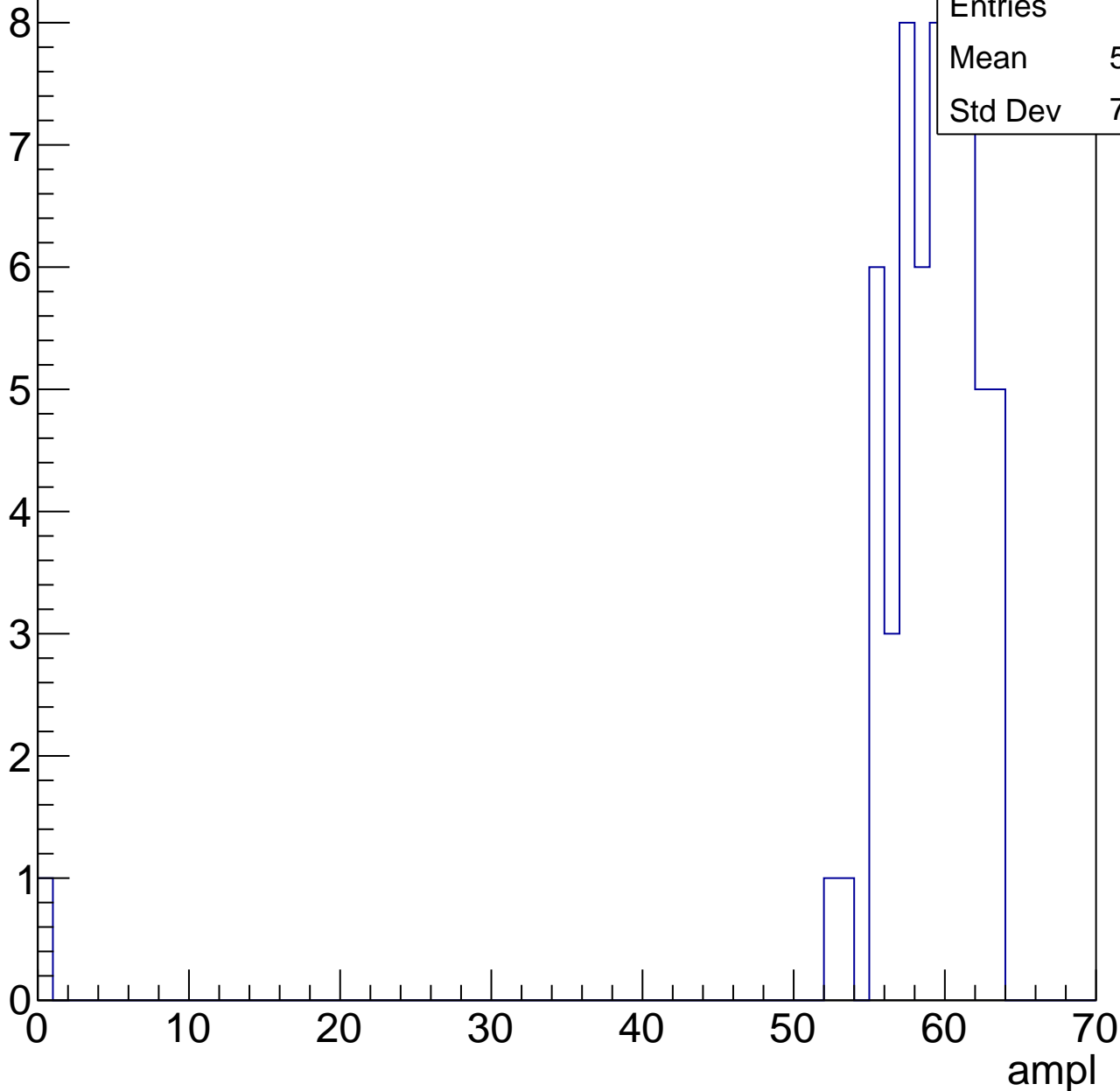


B1L003S, U21-ch72, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	57.87
Std Dev	7.974

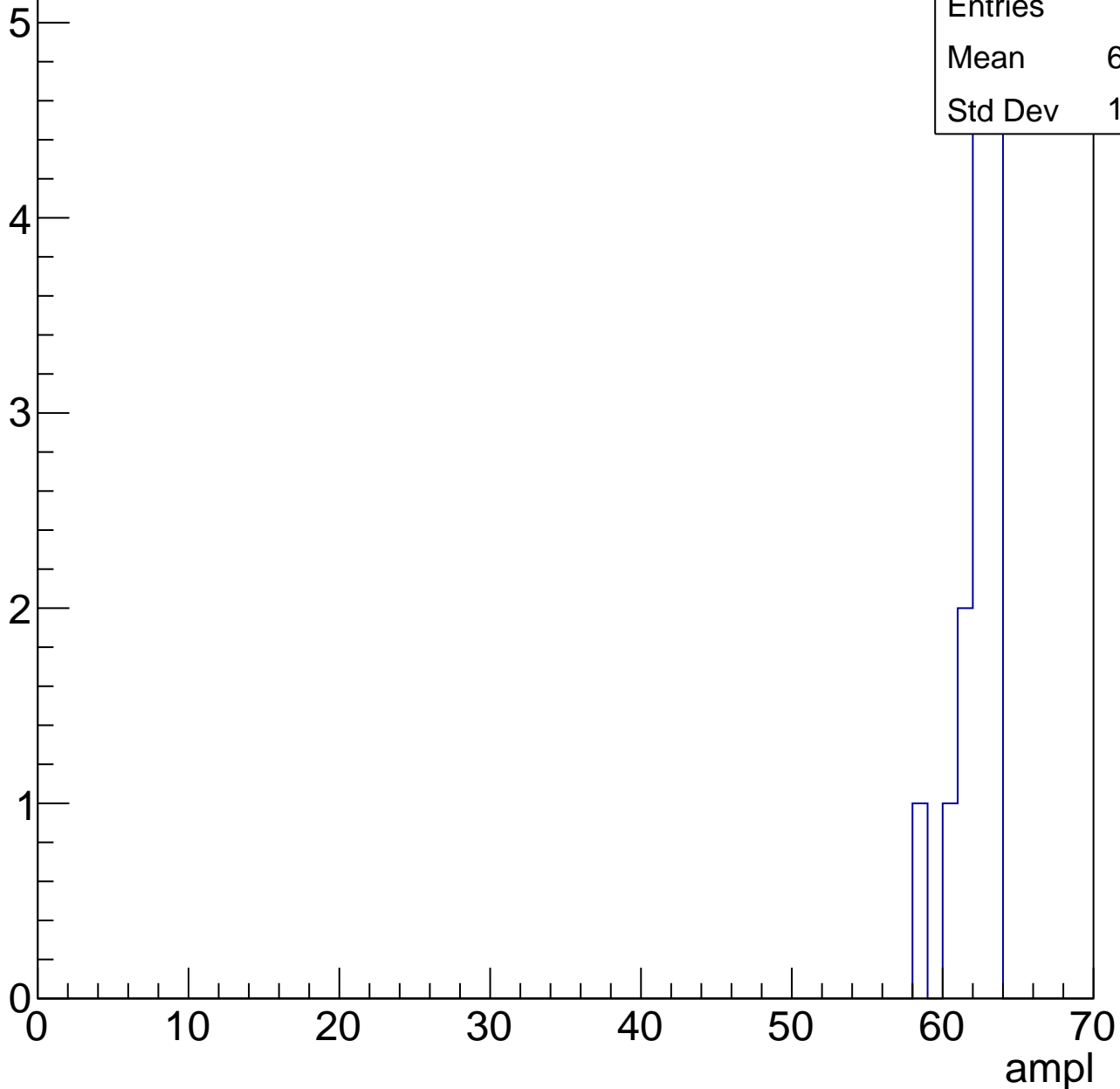


B1L003S, U21-ch72, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	61.79
Std Dev	1.372



B1L003S, U21-ch72, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch73, adc0

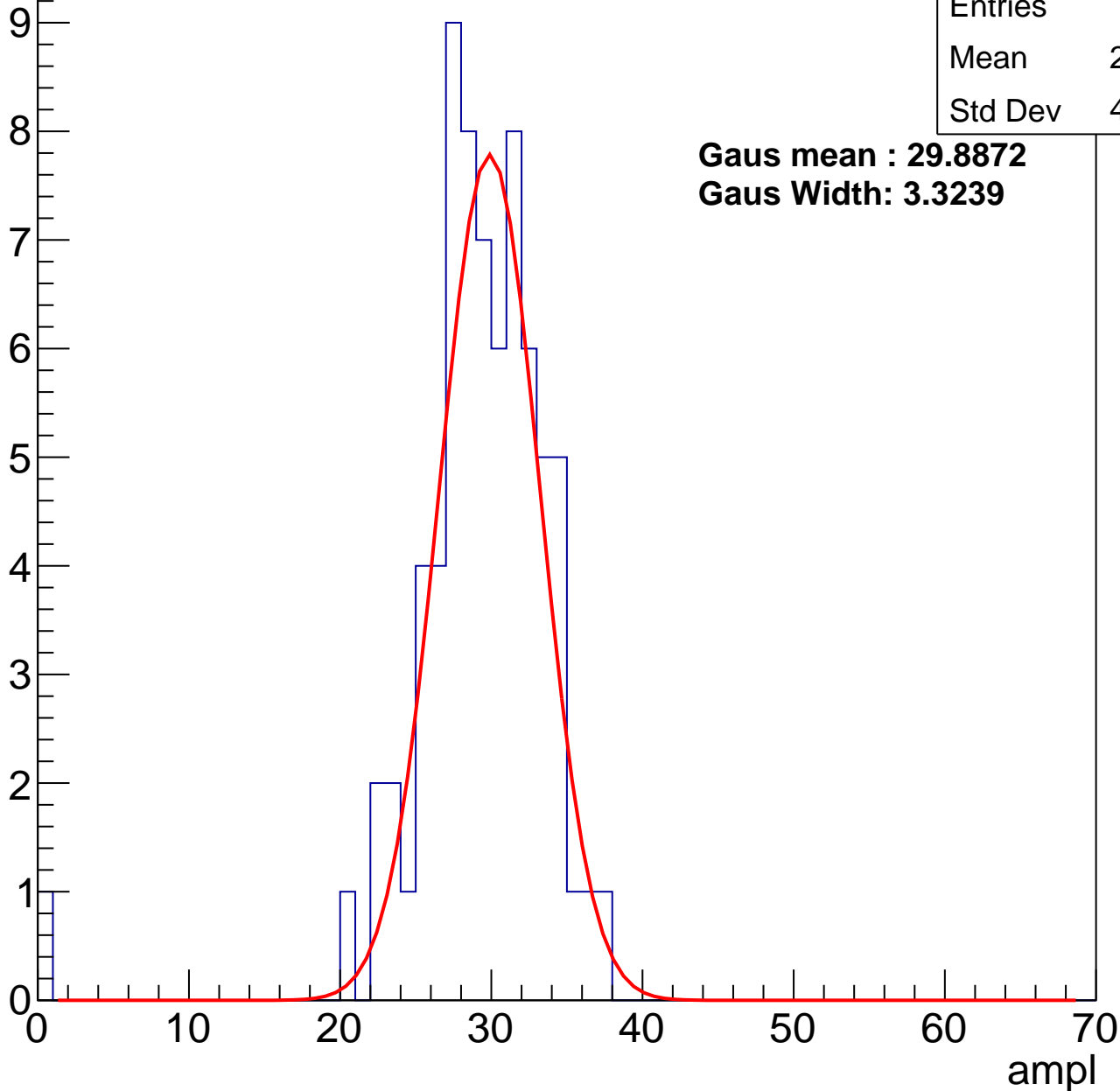
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	28.76
Std Dev	4.863

Gaus mean : 29.8872

Gaus Width: 3.3239



B1L003S, U21-ch73, adc1

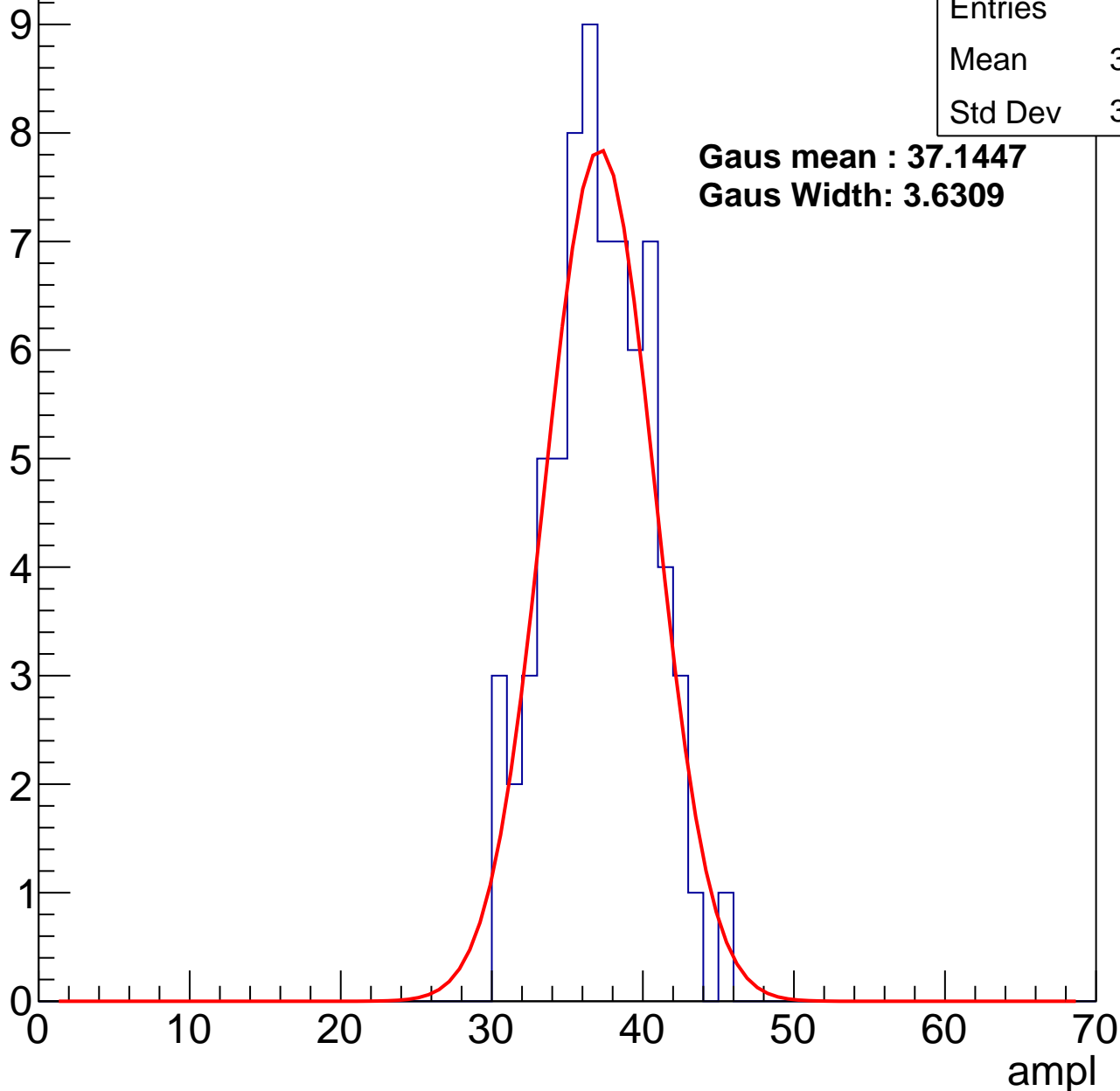
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.68
Std Dev	3.326

Gaus mean : 37.1447

Gaus Width: 3.6309



B1L003S, U21-ch73, adc2

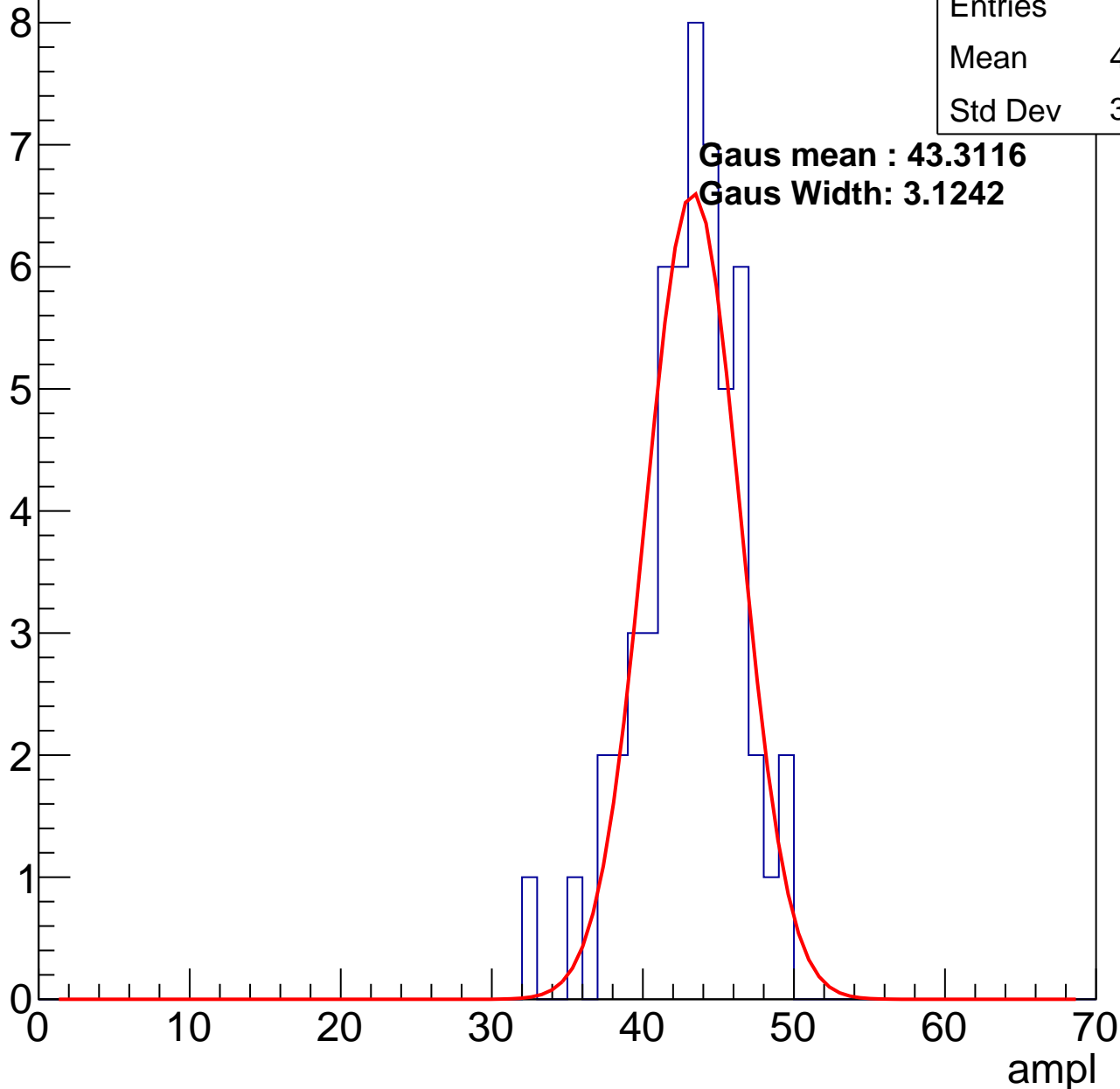
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.64
Std Dev	3.343

Gaus mean : 43.3116

Gaus Width: 3.1242

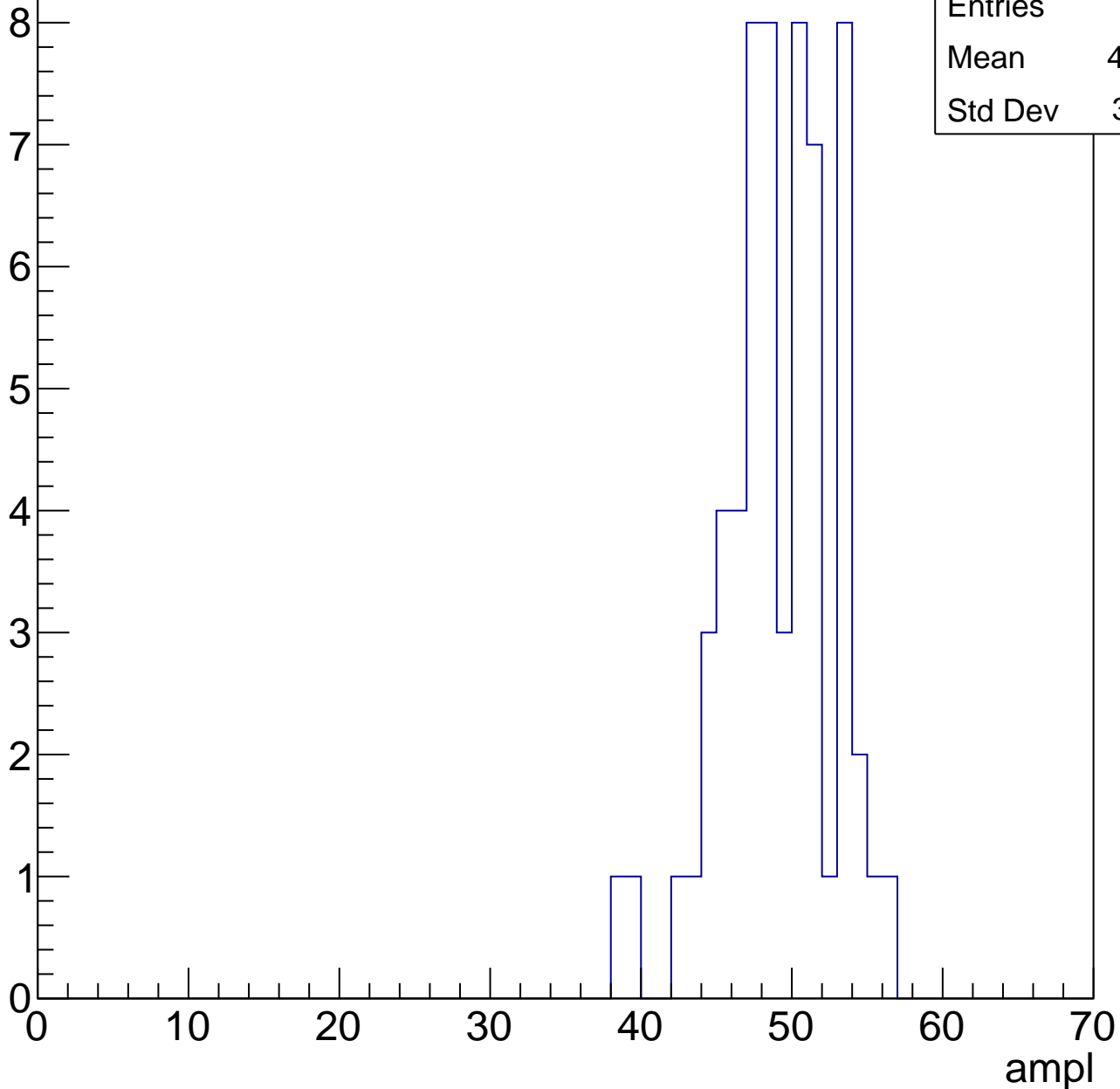


B1L003S, U21-ch73, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.66
Std Dev	3.641

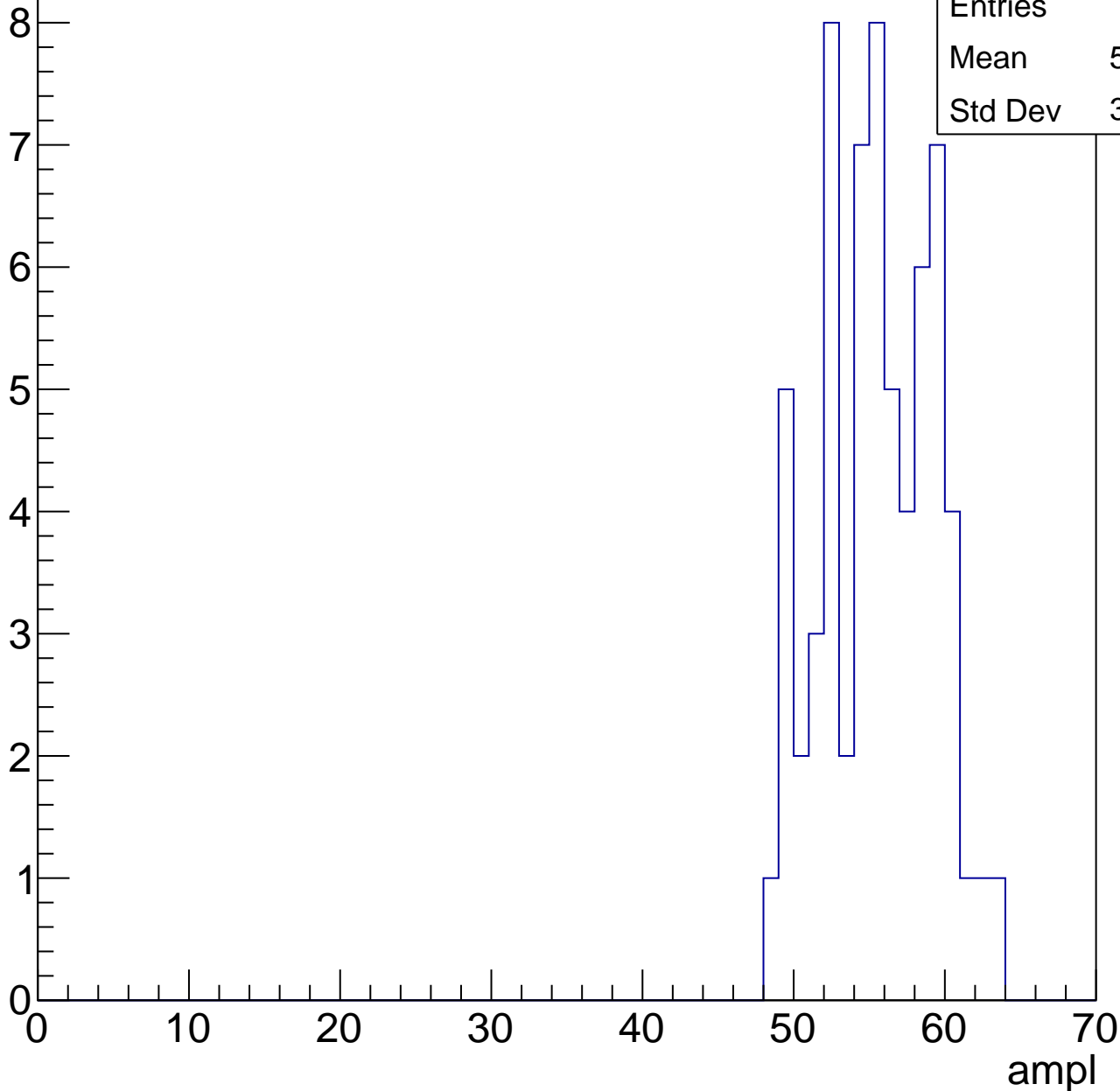


B1L003S, U21-ch73, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.09
Std Dev	3.615

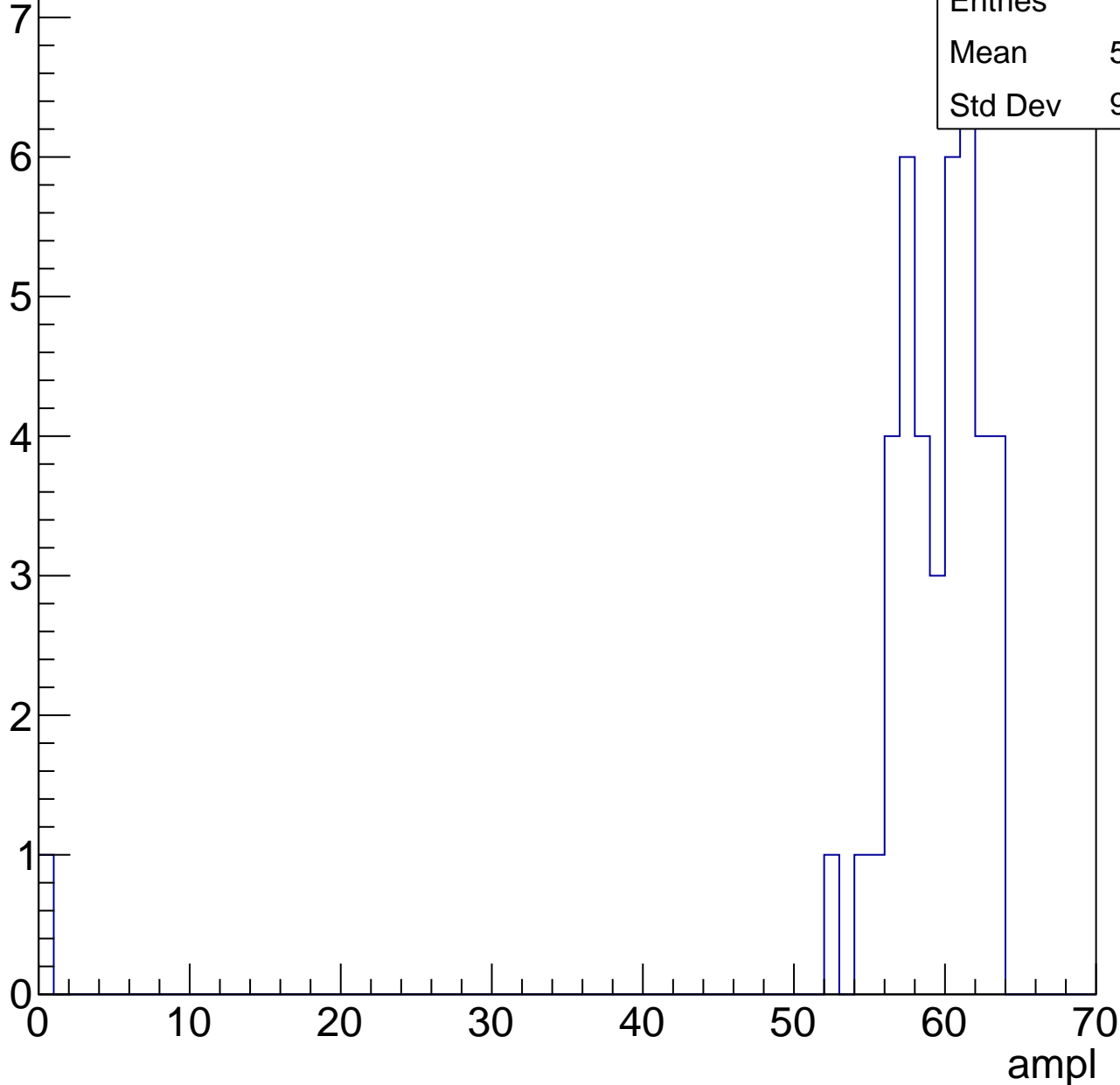


B1L003S, U21-ch73, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	57.69
Std Dev	9.382

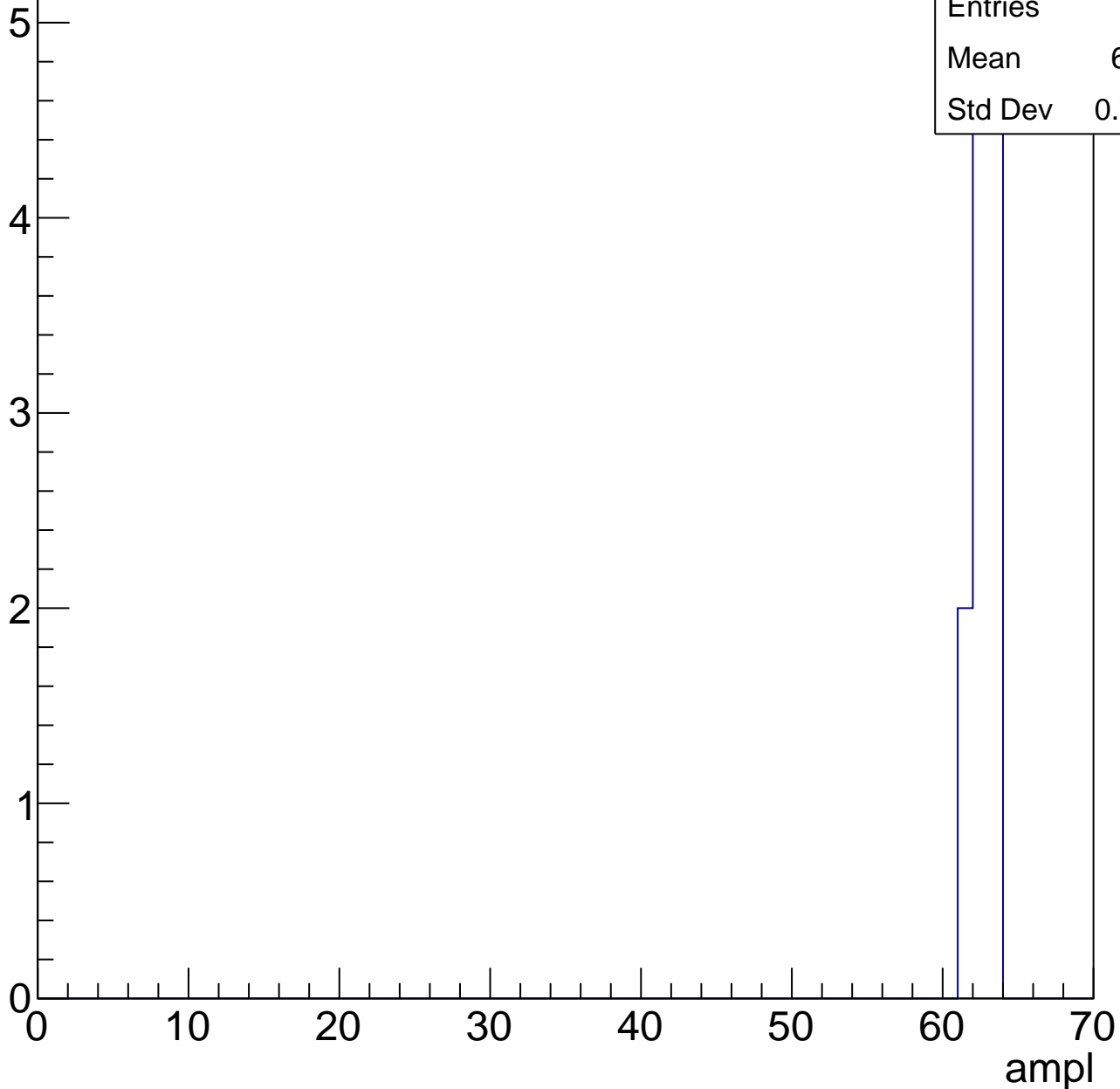


B1L003S, U21-ch73, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	12
Mean	62.25
Std Dev	0.7217



B1L003S, U21-ch73, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch74, adc0

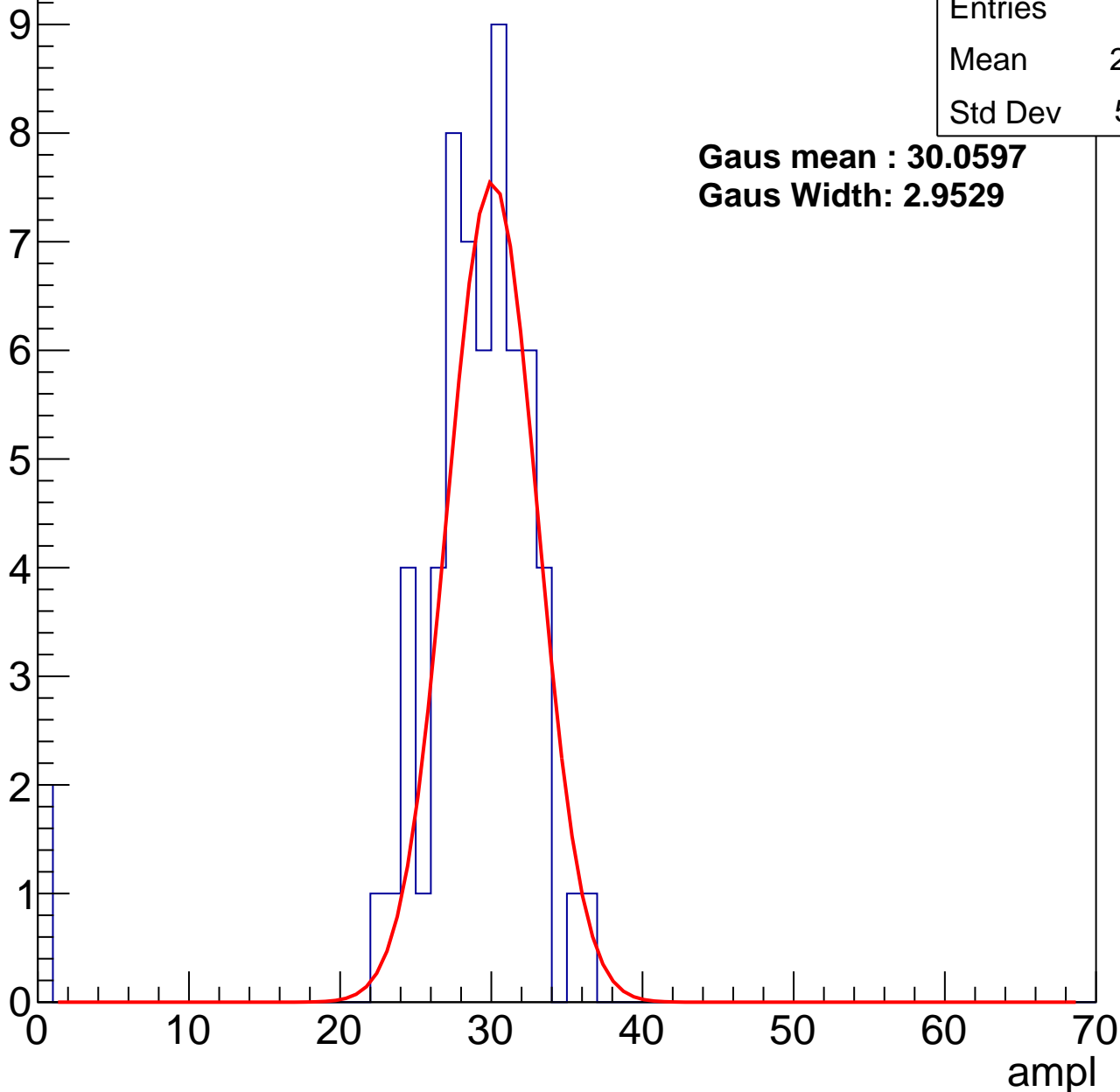
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	27.98
Std Dev	5.911

Gaus mean : 30.0597

Gaus Width: 2.9529

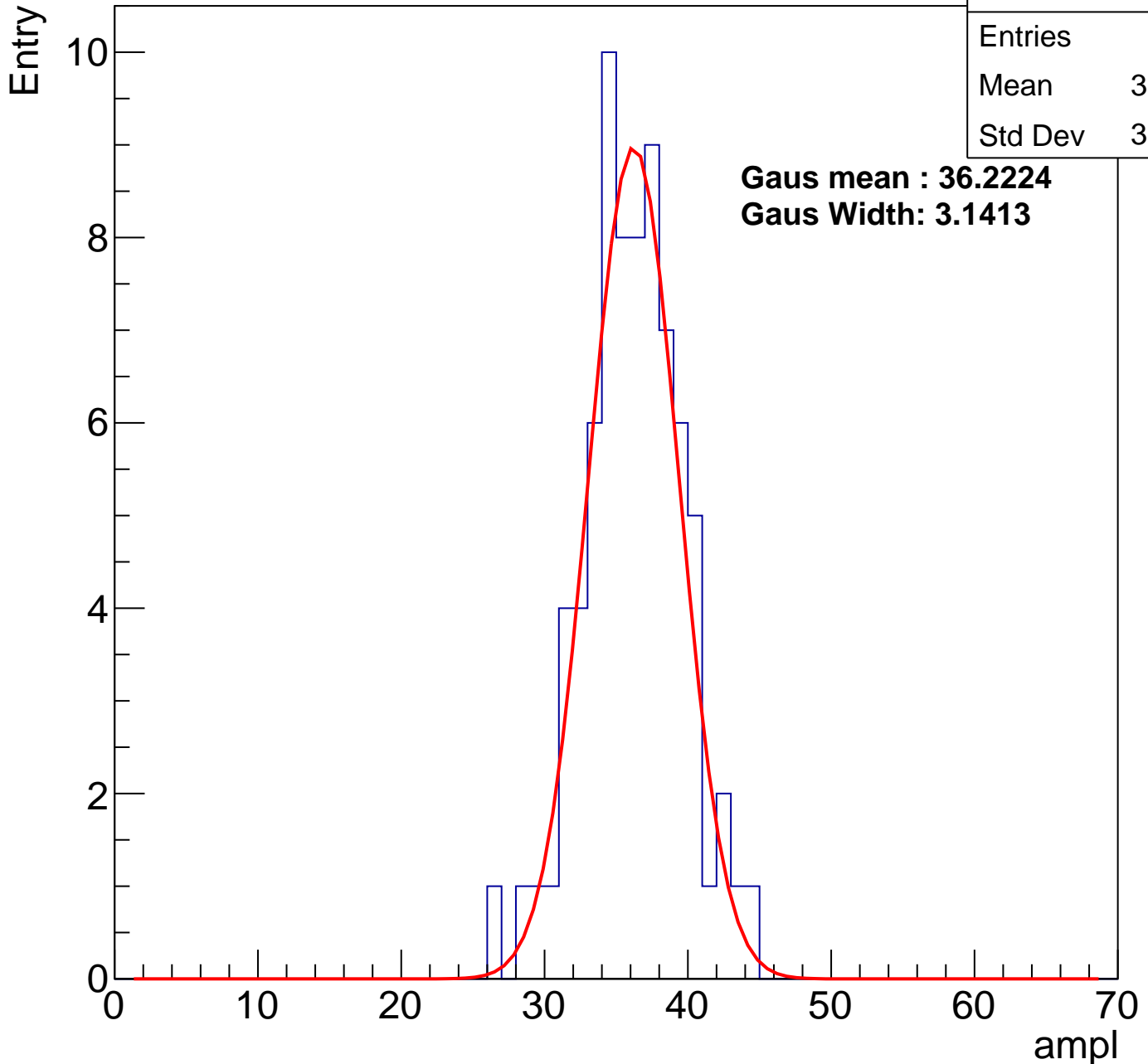


B1L003S, U21-ch74, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	35.74
Std Dev	3.423

Gaus mean : 36.2224
Gaus Width: 3.1413



B1L003S, U21-ch74, adc2

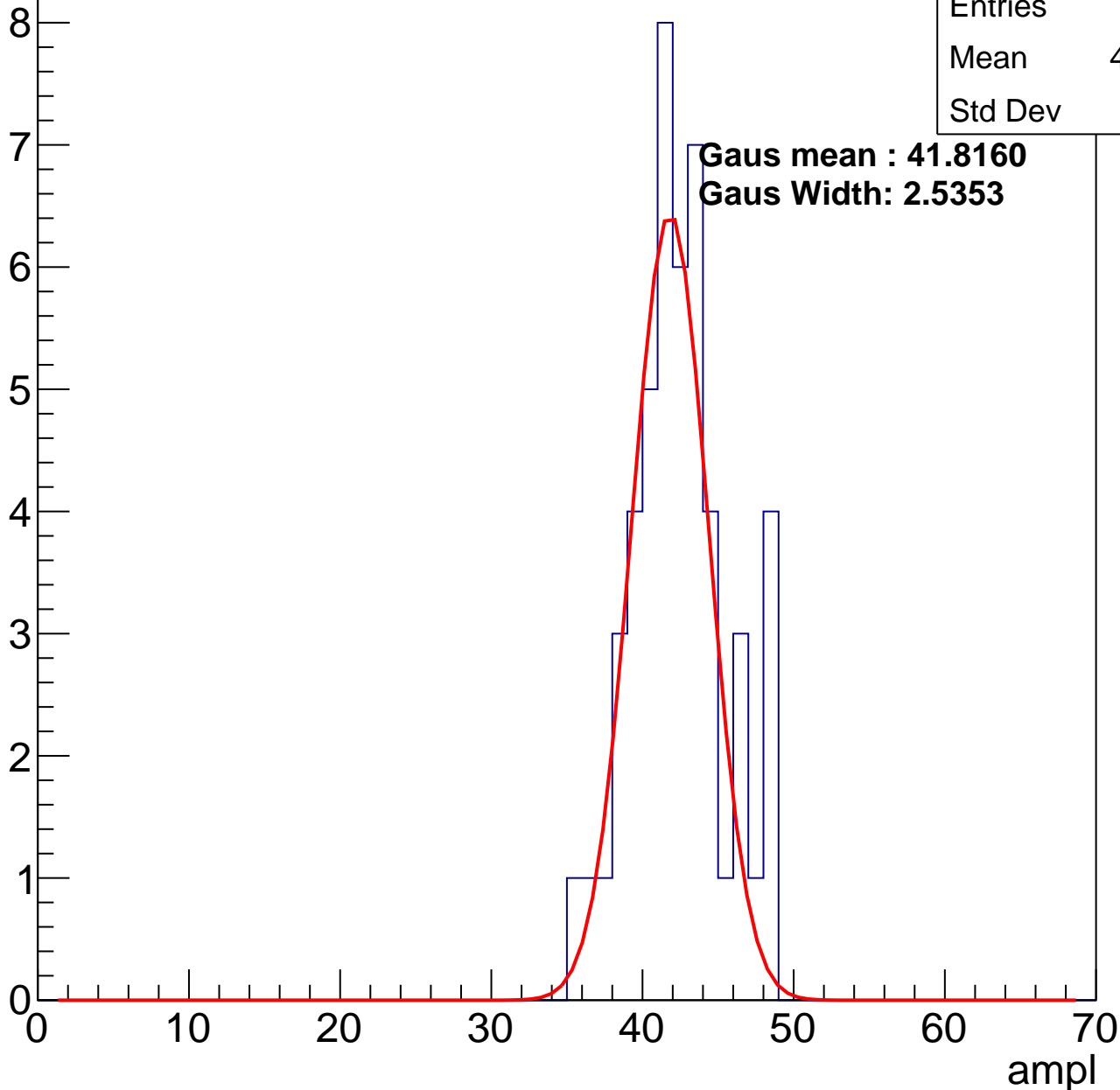
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	41.98
Std Dev	3.1

Gaus mean : 41.8160

Gaus Width: 2.5353



B1L003S, U21-ch74, adc3

calib_packv5_042523_0143.root, FC#13, port D2

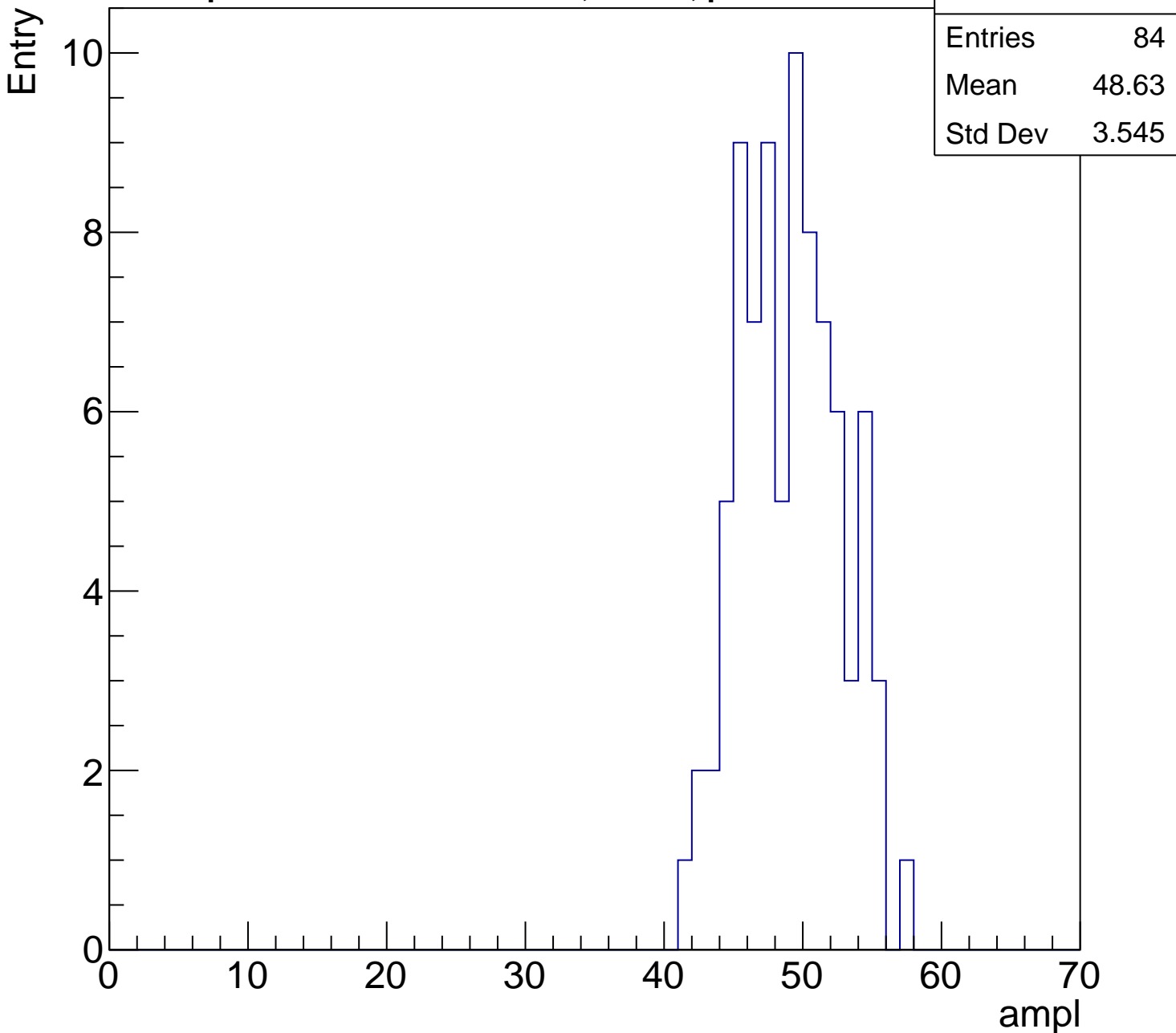
Entries	84
Mean	48.63
Std Dev	3.545

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

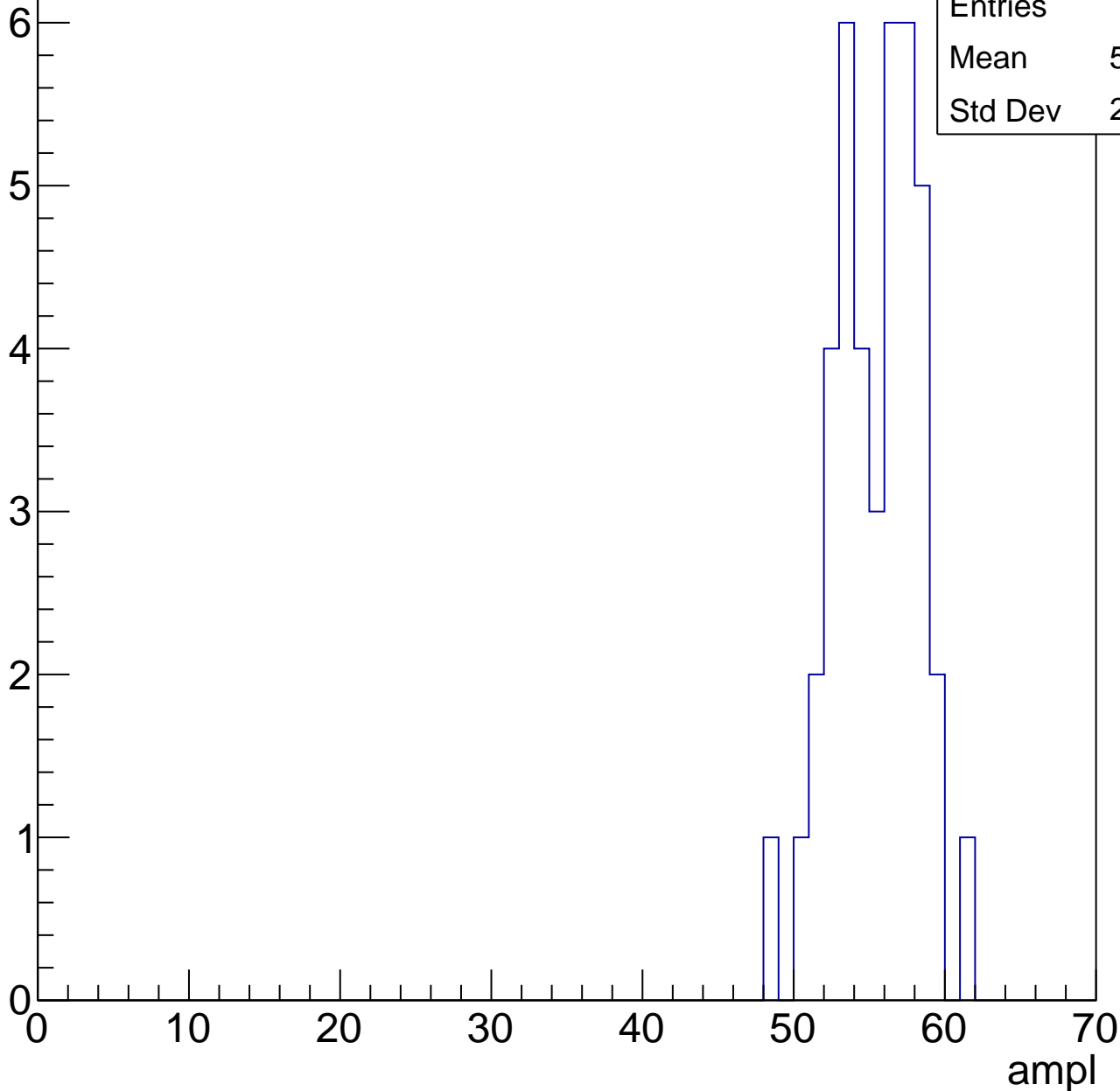


B1L003S, U21-ch74, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	41
Mean	54.98
Std Dev	2.763

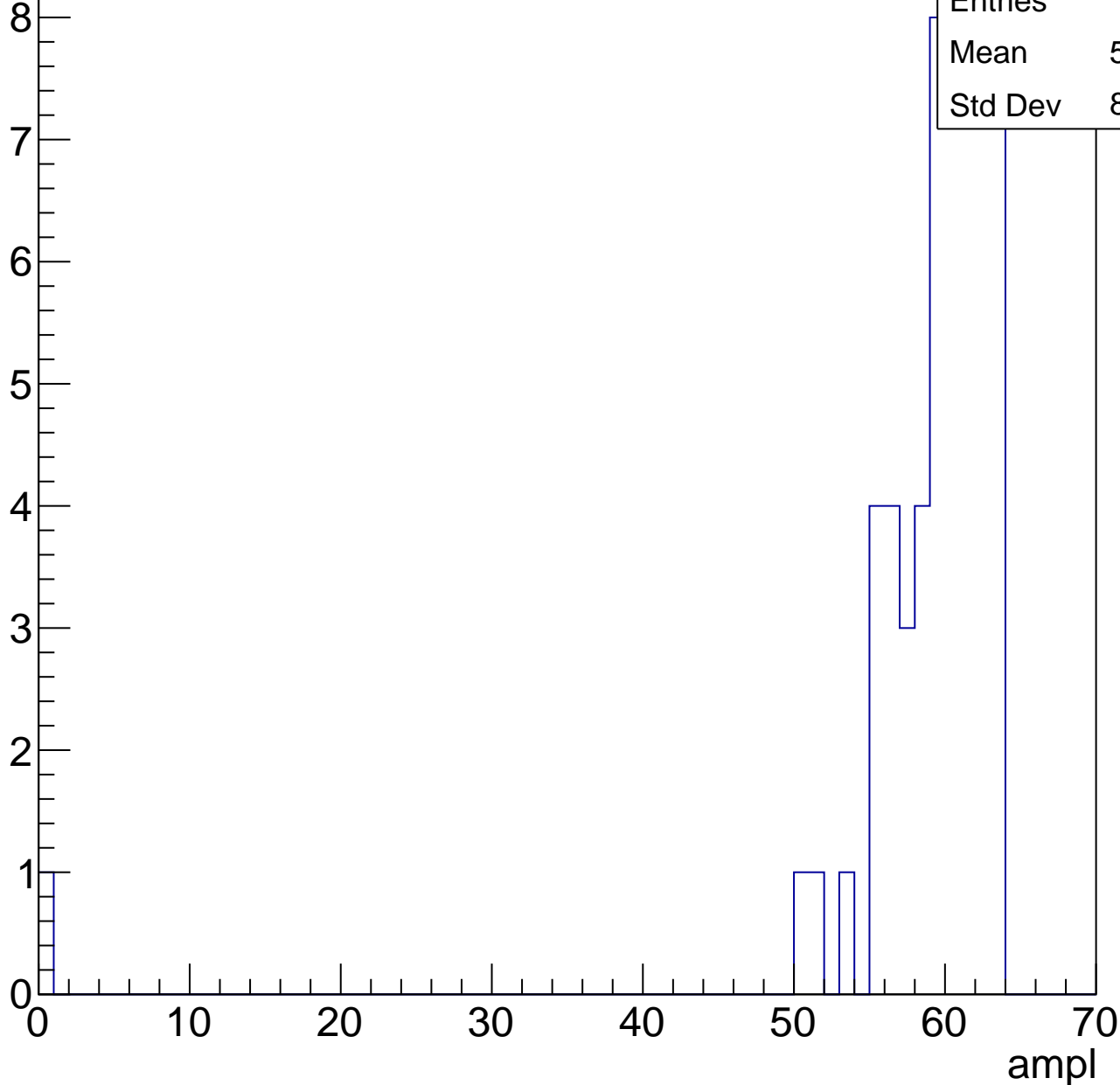


B1L003S, U21-ch74, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	58.32
Std Dev	8.224



B1L003S, U21-ch74, adc6

calib_packv5_042523_0143.root, FC#13, port D2

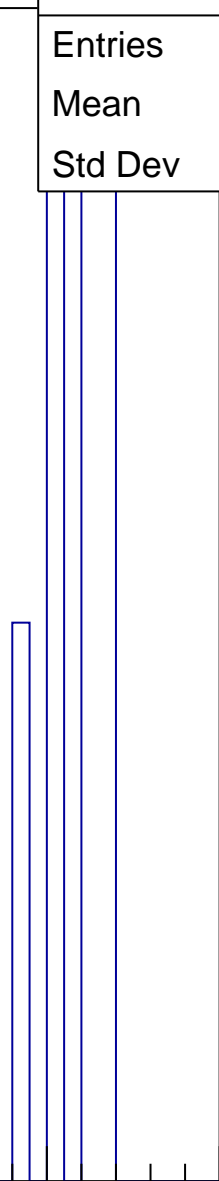
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	61.14
Std Dev	1.726

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch74, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch75, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	28.53
Std Dev	4.666

Gaus mean : 29.2433

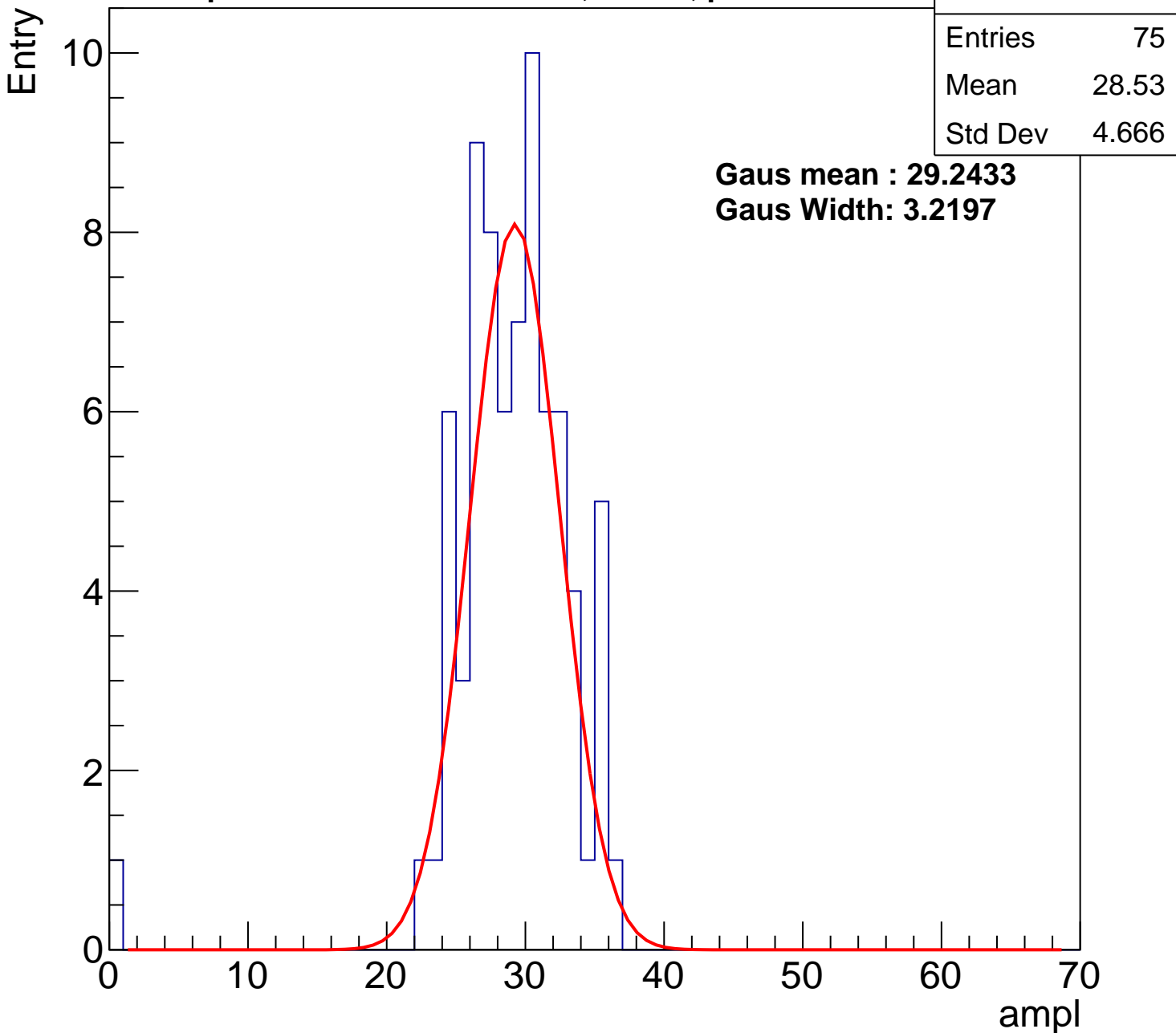
Gaus Width: 3.2197

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch75, adc1

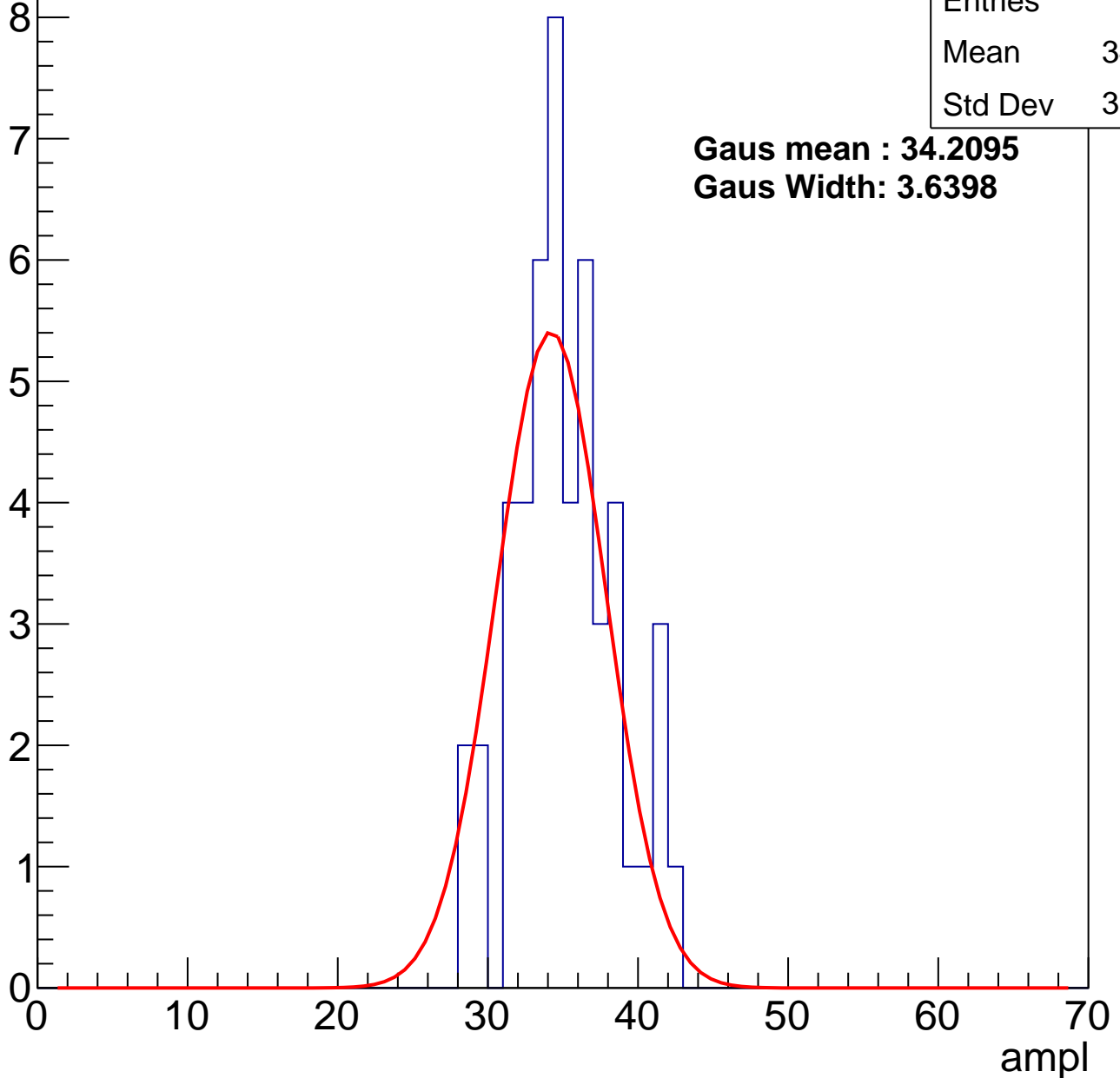
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	34.67
Std Dev	3.347

Gaus mean : 34.2095

Gaus Width: 3.6398



B1L003S, U21-ch75, adc2

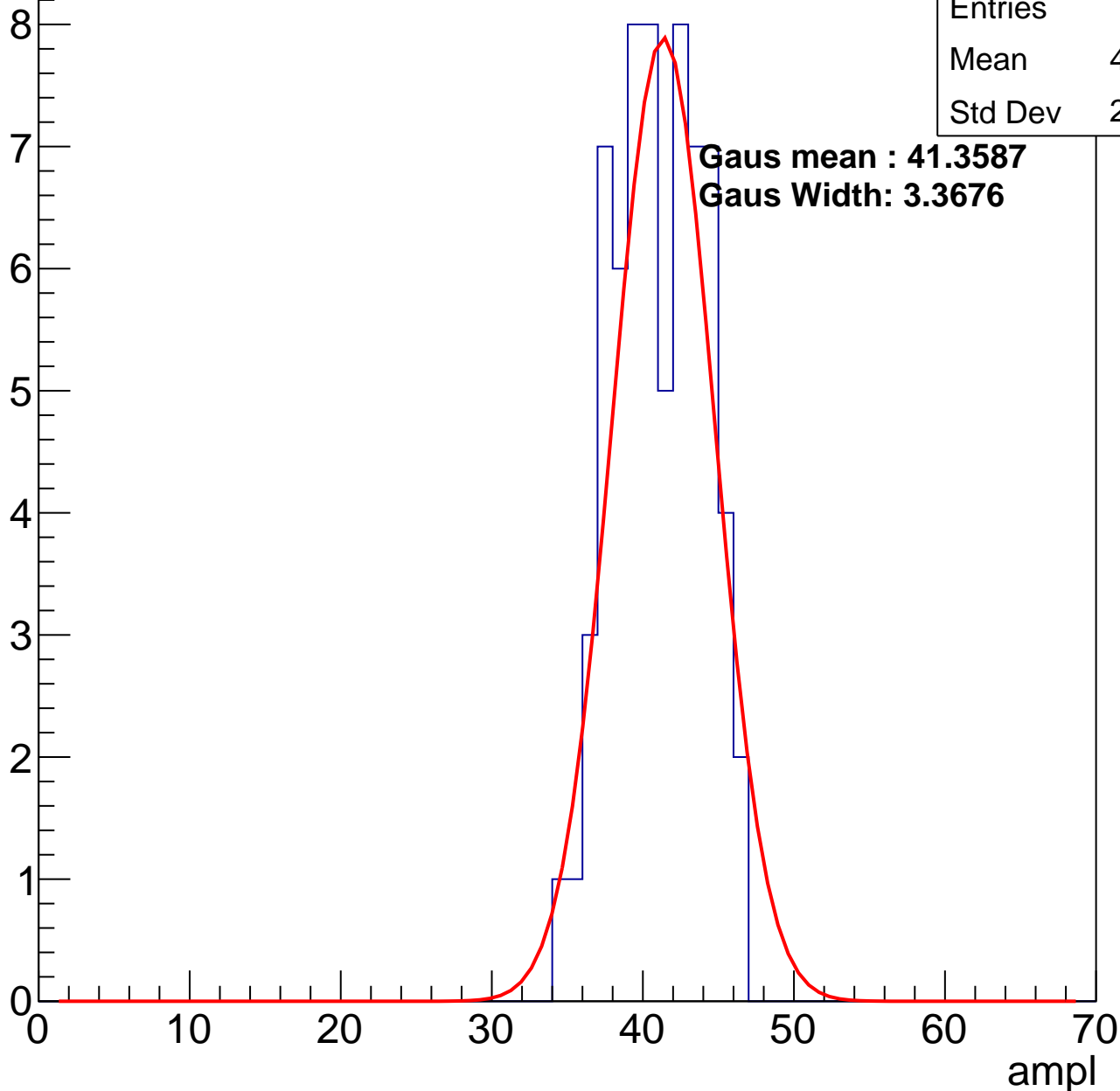
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	40.57
Std Dev	2.908

Gaus mean : 41.3587

Gaus Width: 3.3676



B1L003S, U21-ch75, adc3

calib_packv5_042523_0143.root, FC#13, port D2

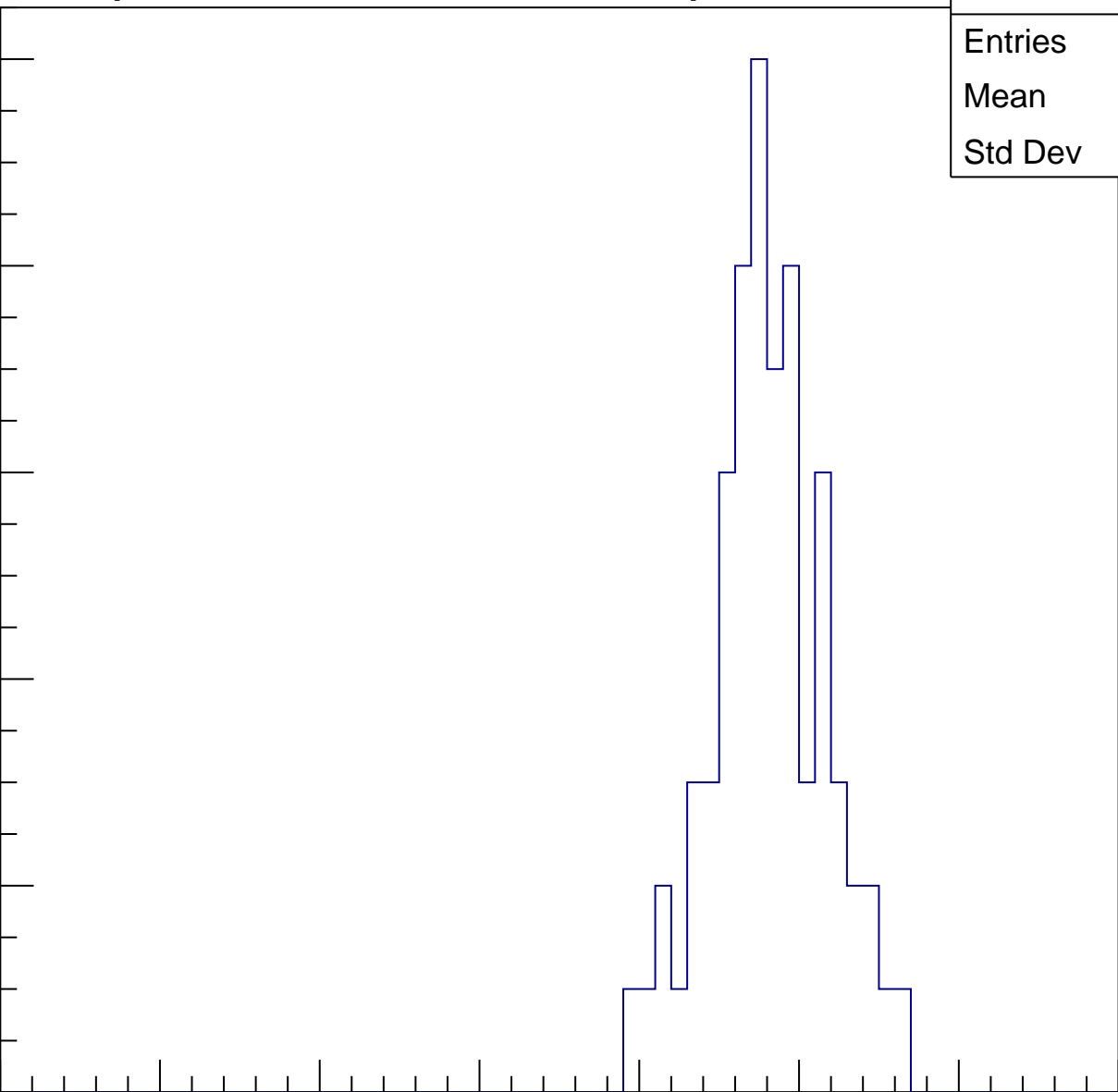
Entries	68
Mean	47.6
Std Dev	3.524

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

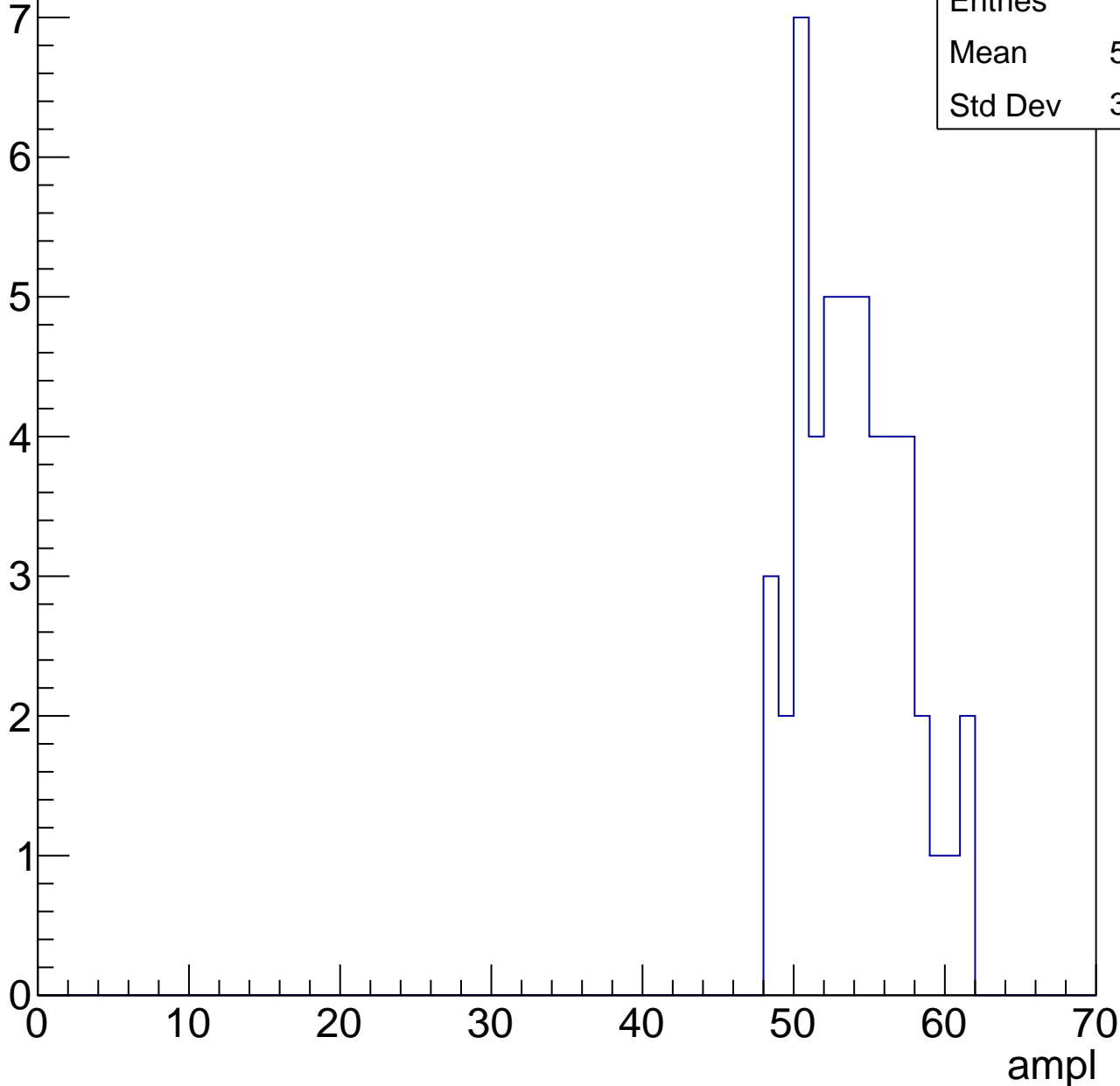


B1L003S, U21-ch75, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	53.47
Std Dev	3.393

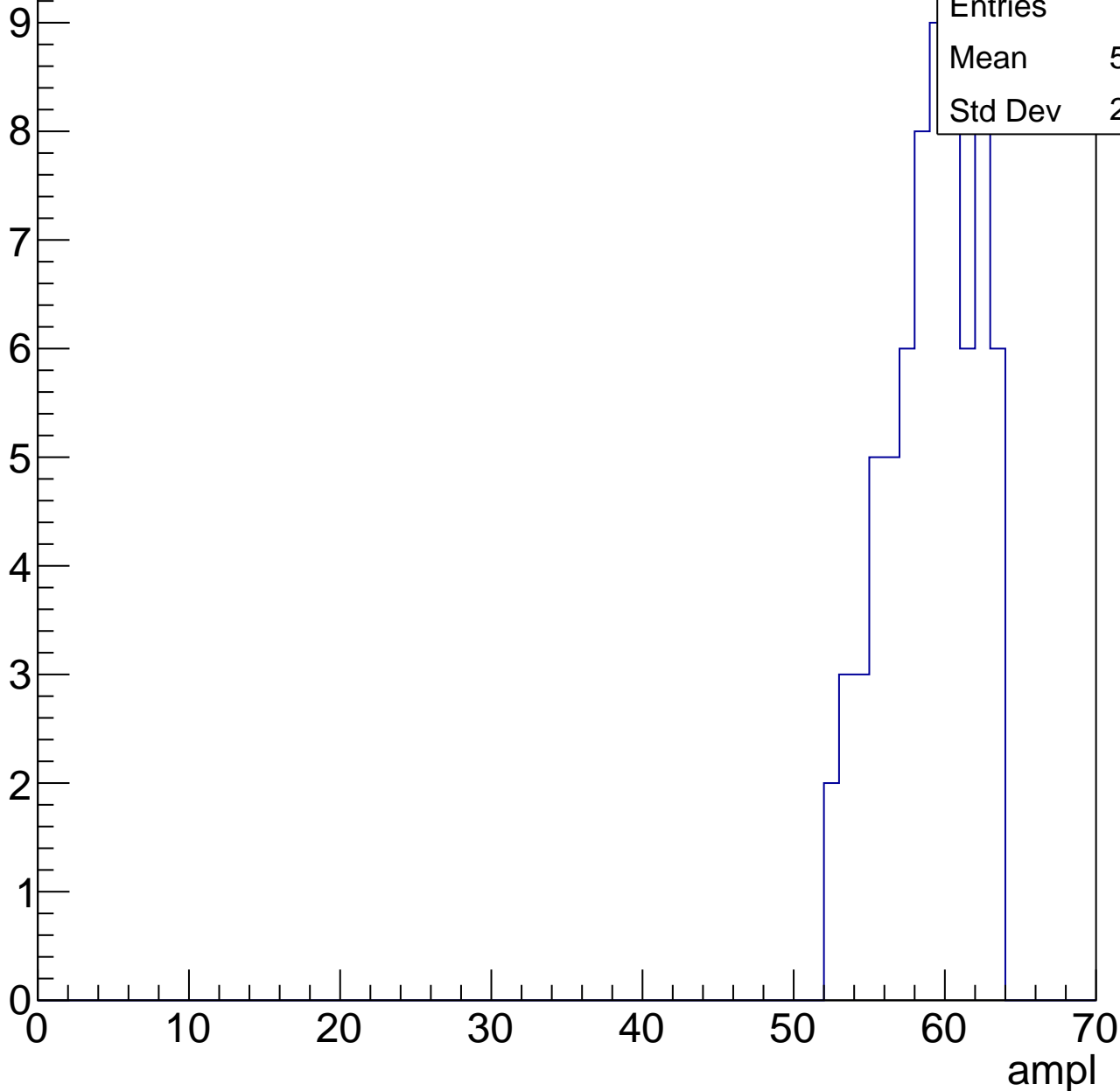


B1L003S, U21-ch75, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	58.53
Std Dev	2.984



B1L003S, U21-ch75, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

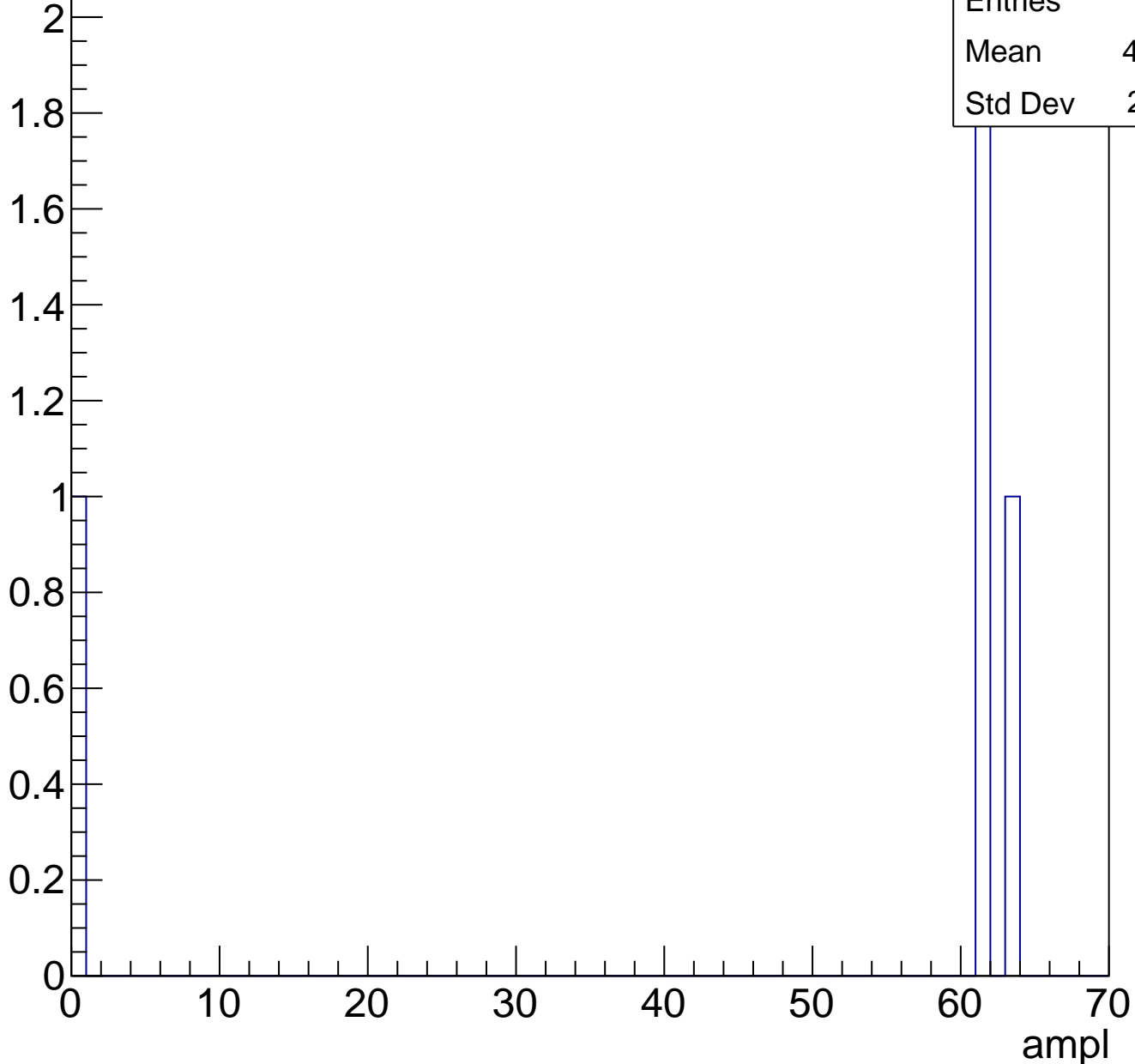


Entries	6
Mean	52.17
Std Dev	23.33

B1L003S, U21-ch75, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	46.25
Std Dev	26.71

B1L003S, U21-ch76, adc0

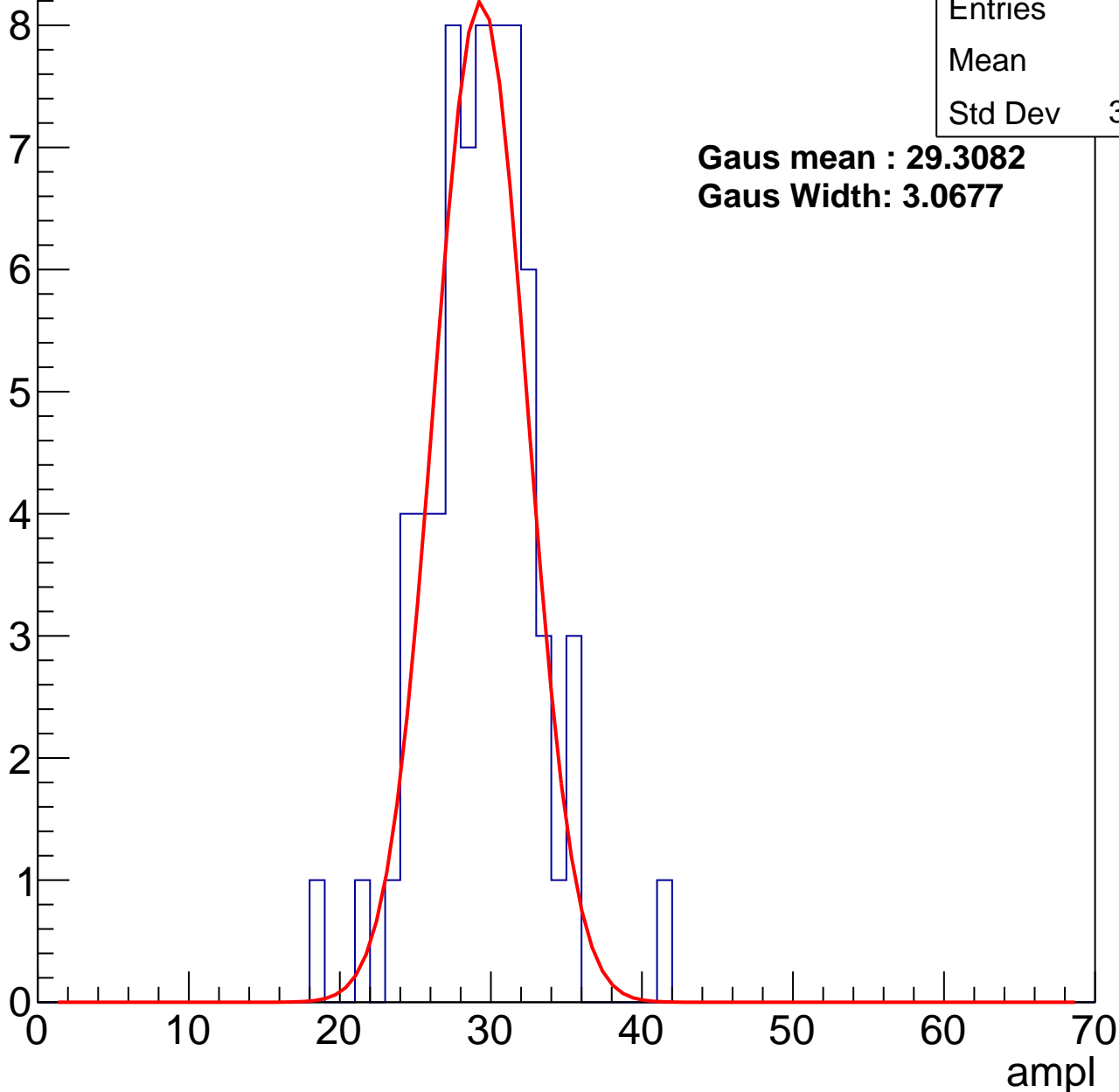
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.9
Std Dev	3.594

Gaus mean : 29.3082

Gaus Width: 3.0677



B1L003S, U21-ch76, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	36
Std Dev	3.658

Gaus mean : 36.4160

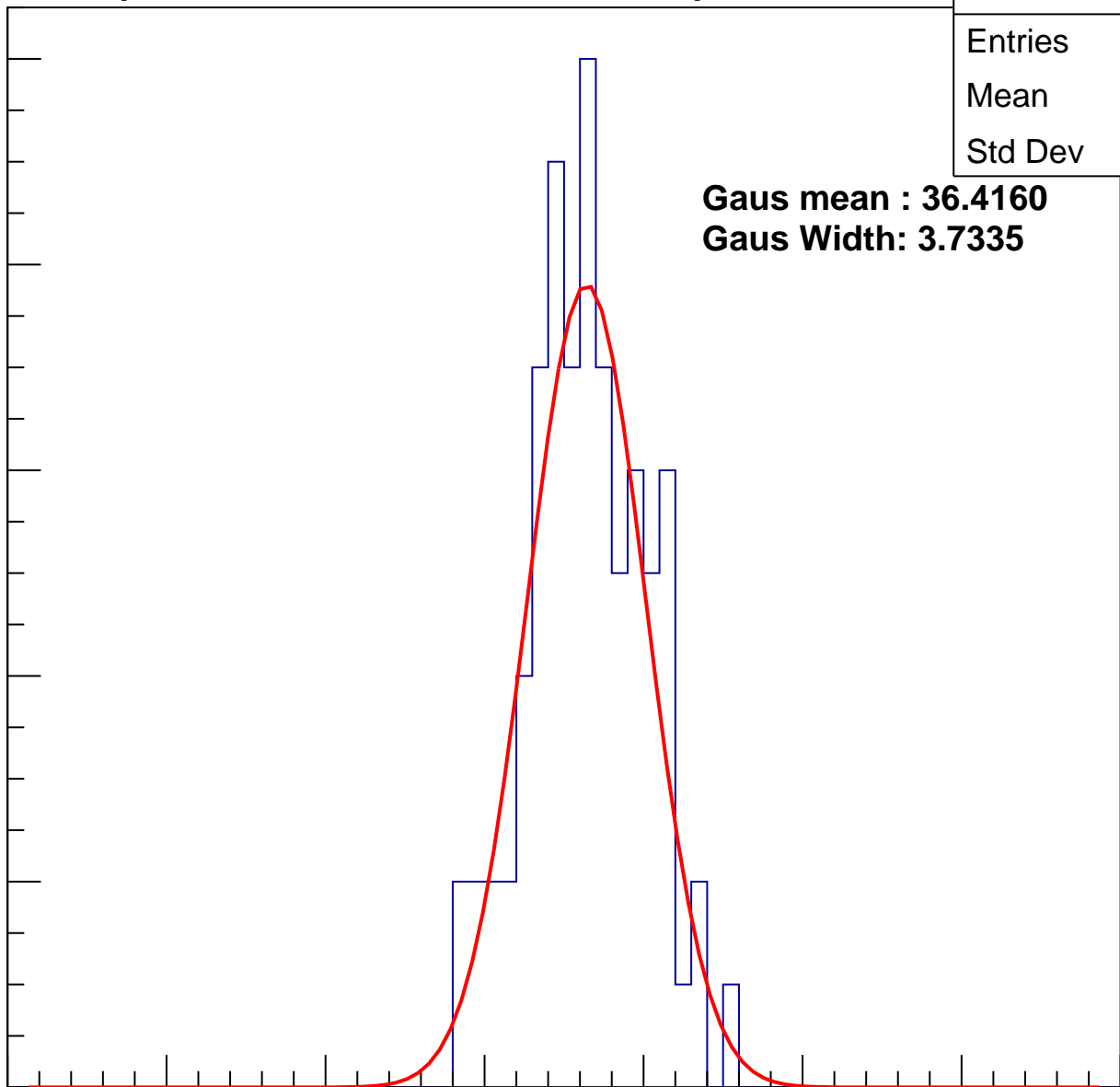
Gaus Width: 3.7335

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch76, adc2

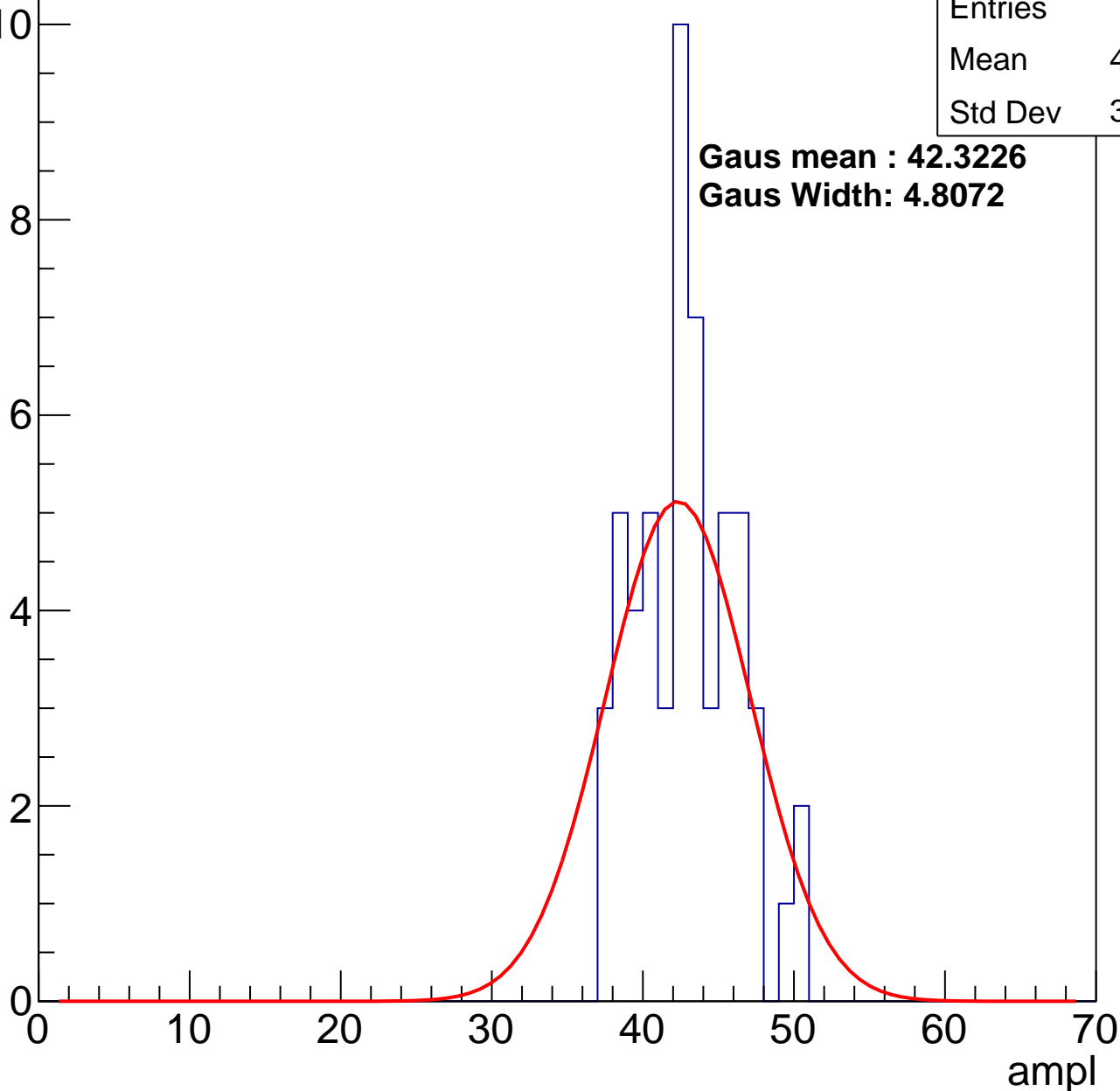
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.46
Std Dev	3.268

Gaus mean : 42.3226

Gaus Width: 4.8072



B1L003S, U21-ch76, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	49.06
Std Dev	3.46

Entry

10

8

6

4

2

0

0

10

20

30

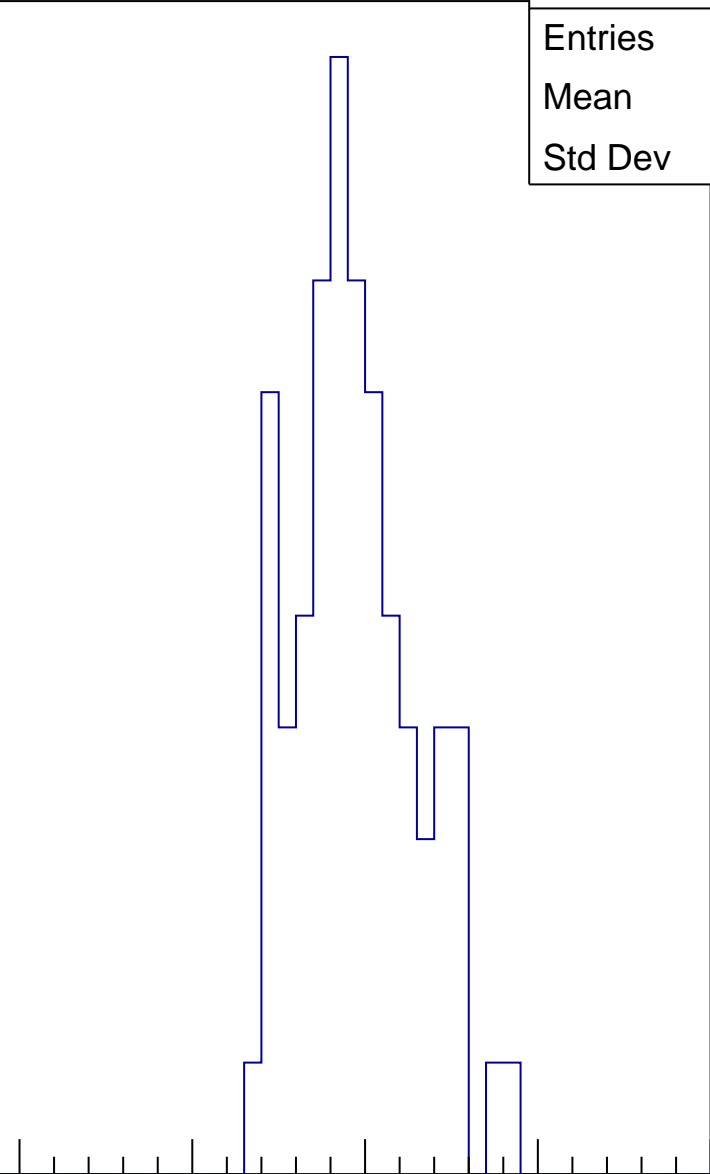
40

50

60

70

ampl

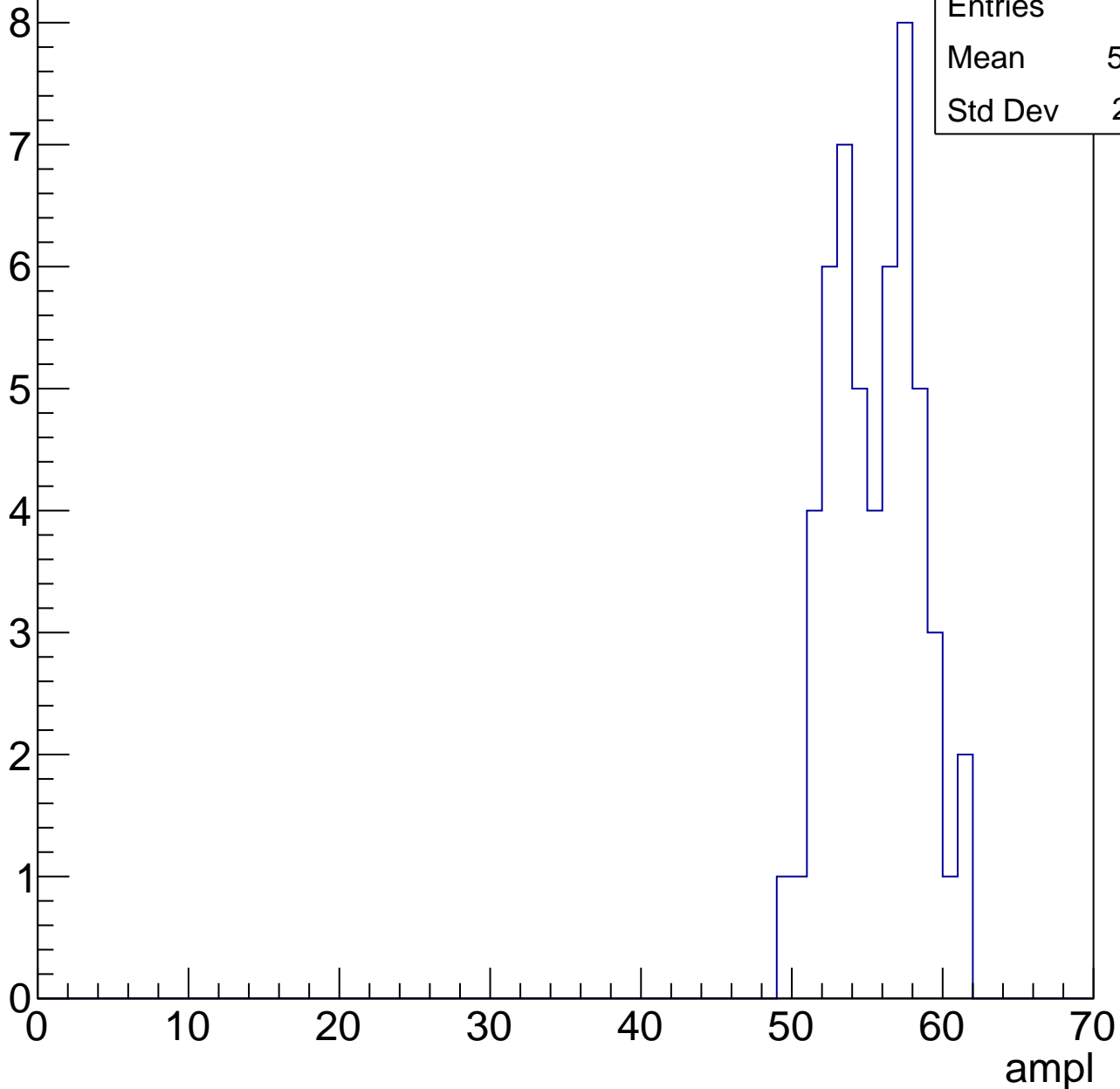


B1L003S, U21-ch76, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	55.04
Std Dev	2.881

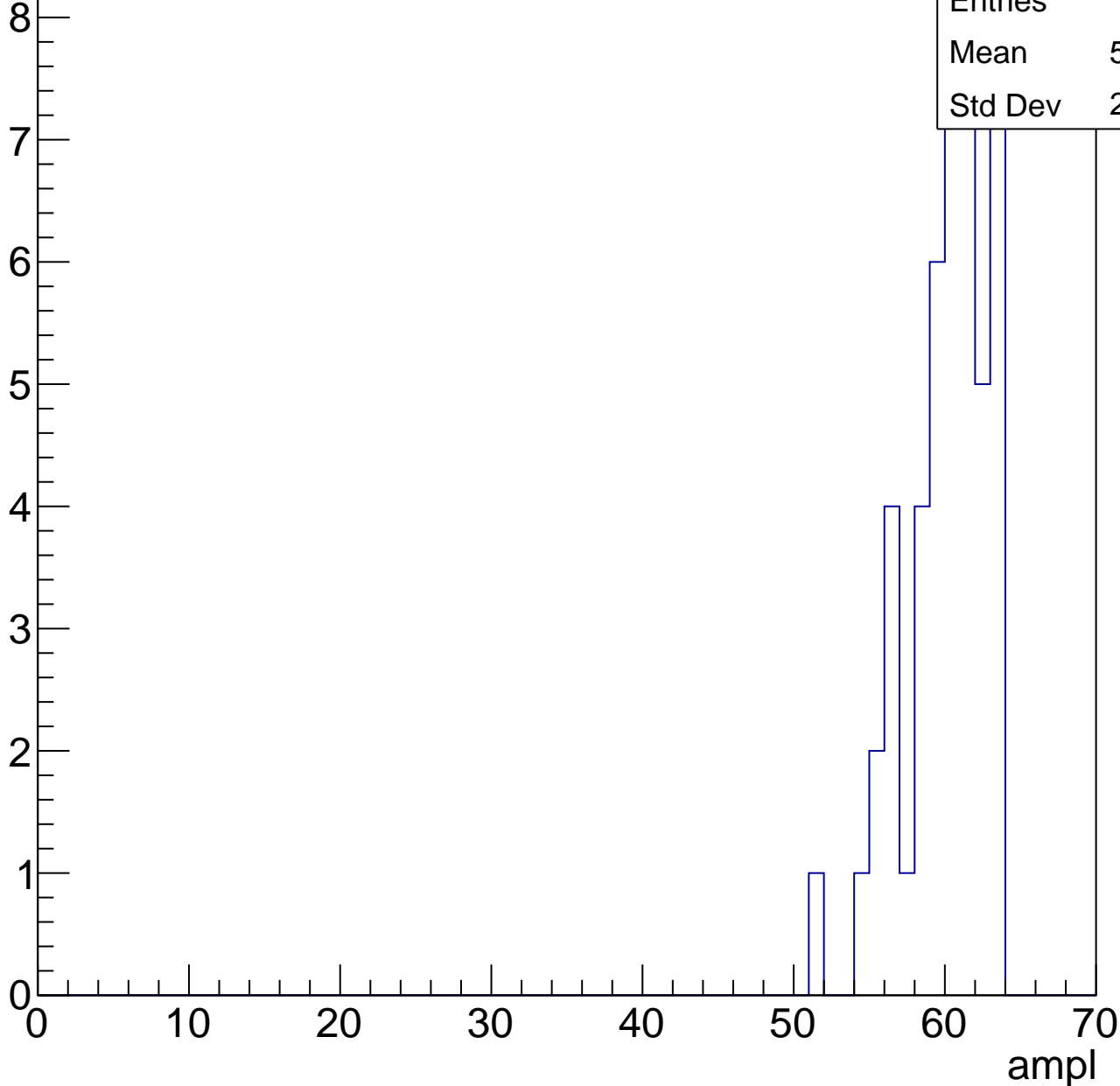


B1L003S, U21-ch76, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	59.67
Std Dev	2.726

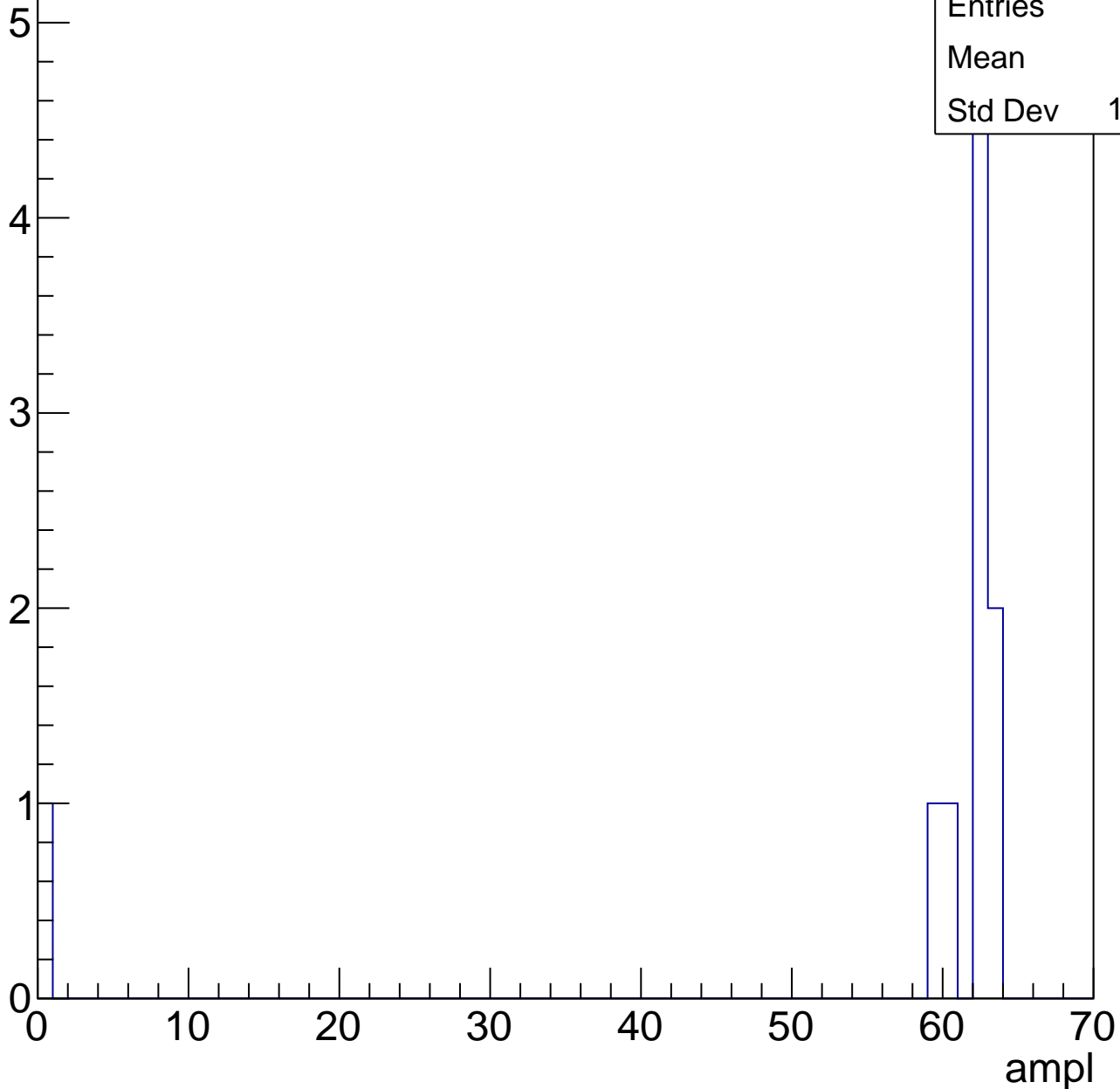


B1L003S, U21-ch76, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.5
Std Dev	18.54



B1L003S, U21-ch76, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch77, adc0

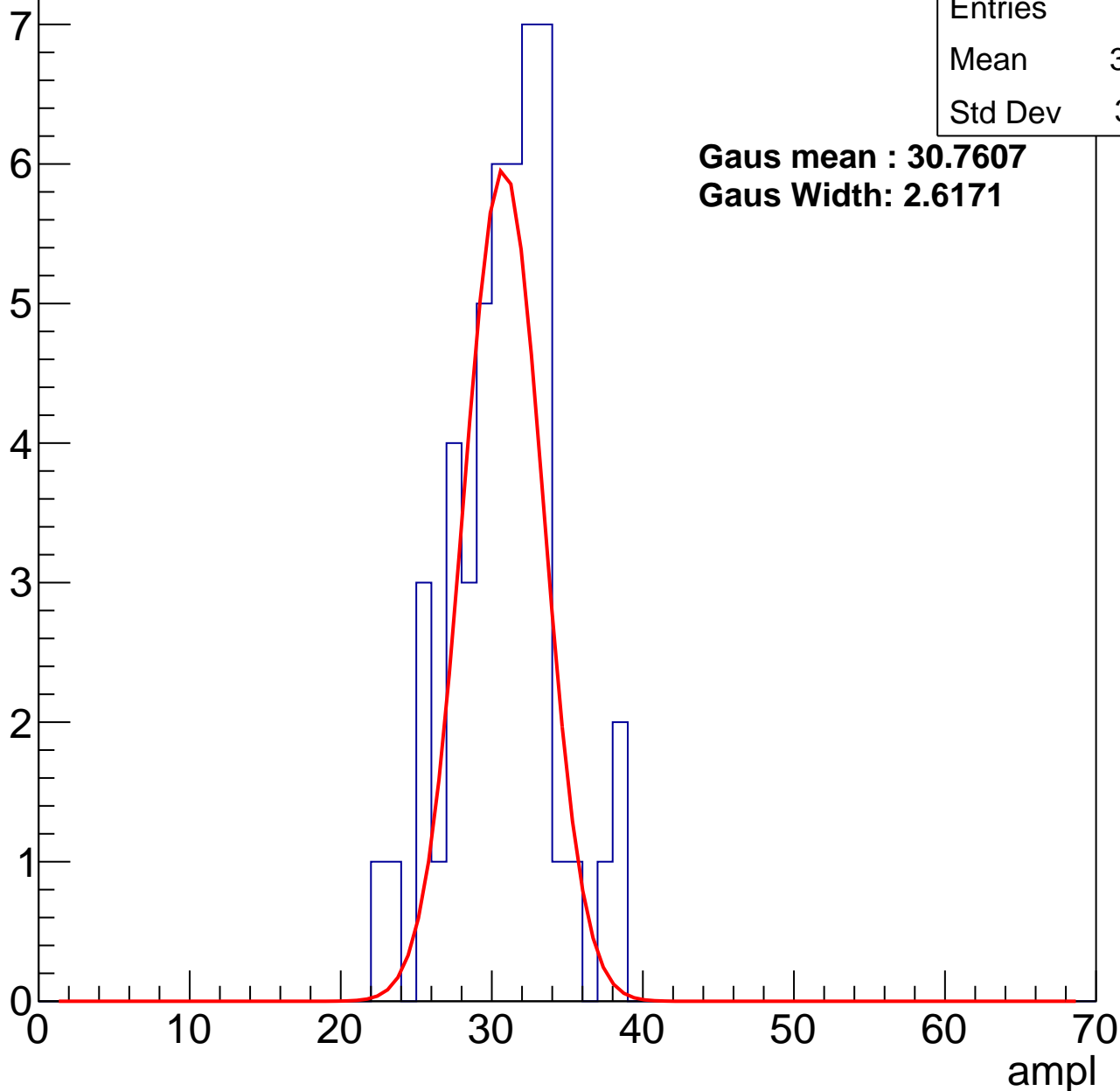
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	30.33
Std Dev	3.401

Gaus mean : 30.7607

Gaus Width: 2.6171



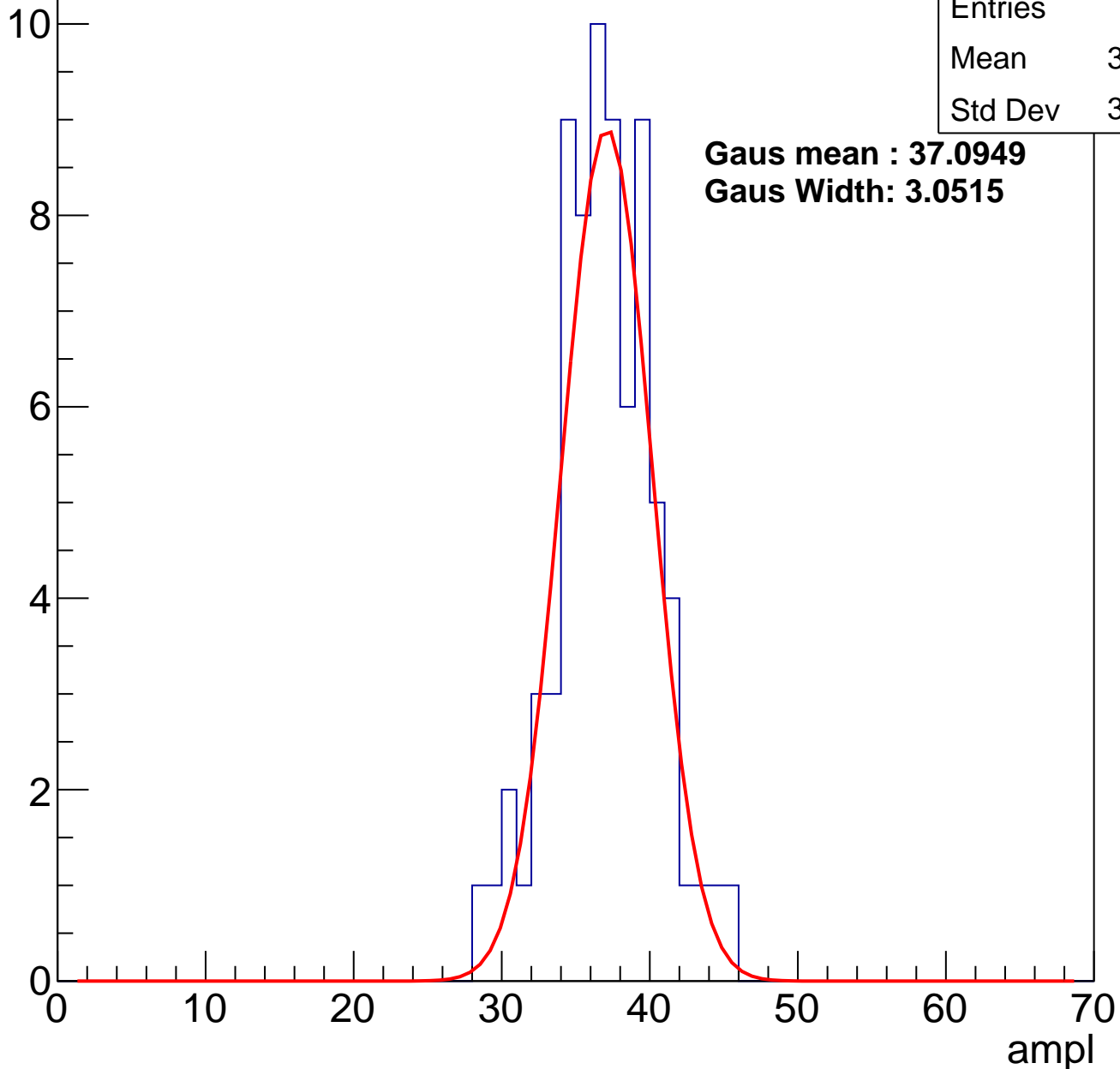
B1L003S, U21-ch77, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	36.52
Std Dev	3.332

Gaus mean : 37.0949
Gaus Width: 3.0515

Entry



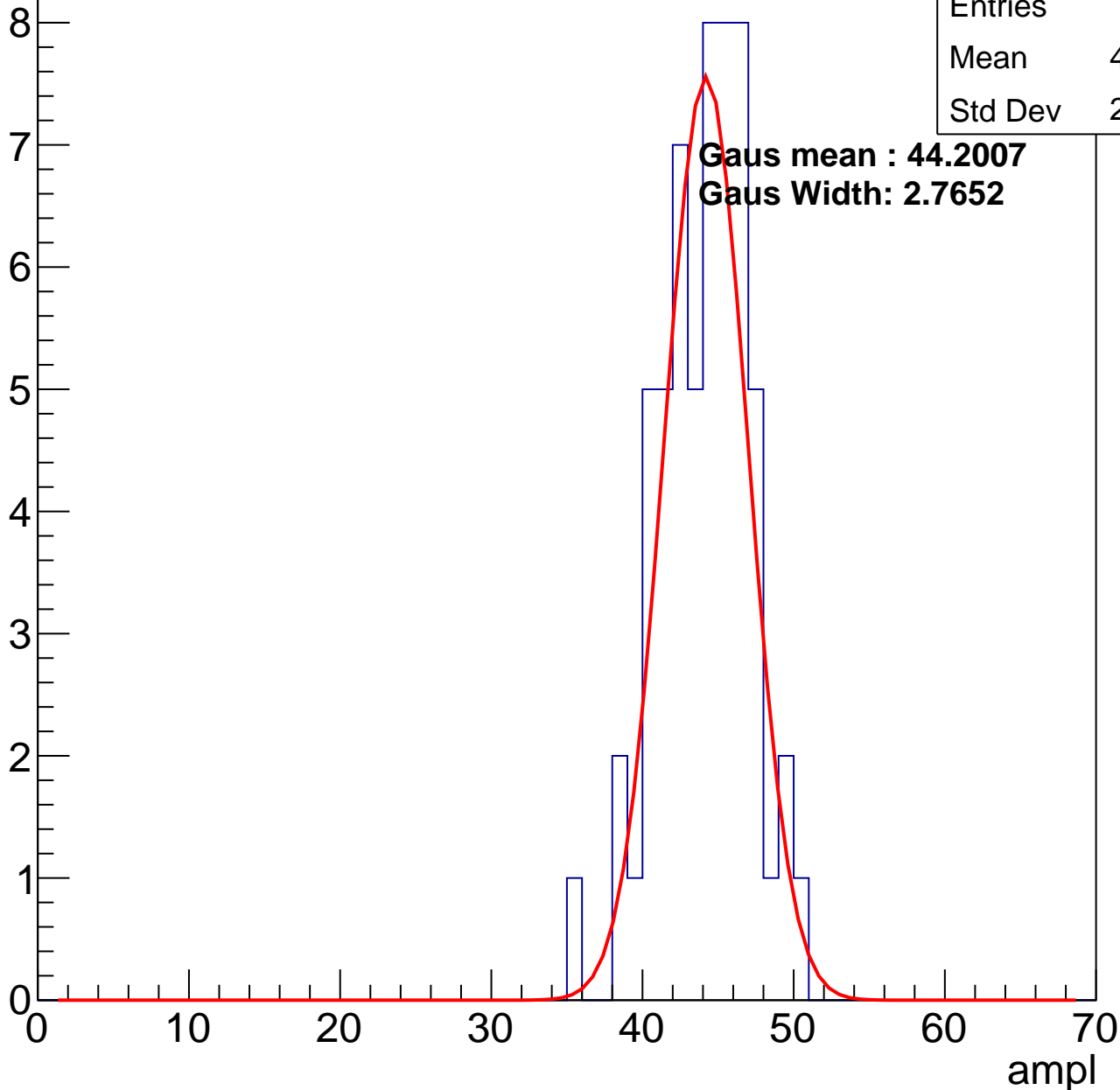
B1L003S, U21-ch77, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.64
Std Dev	2.956

Gaus mean : 44.2007
Gaus Width: 2.7652

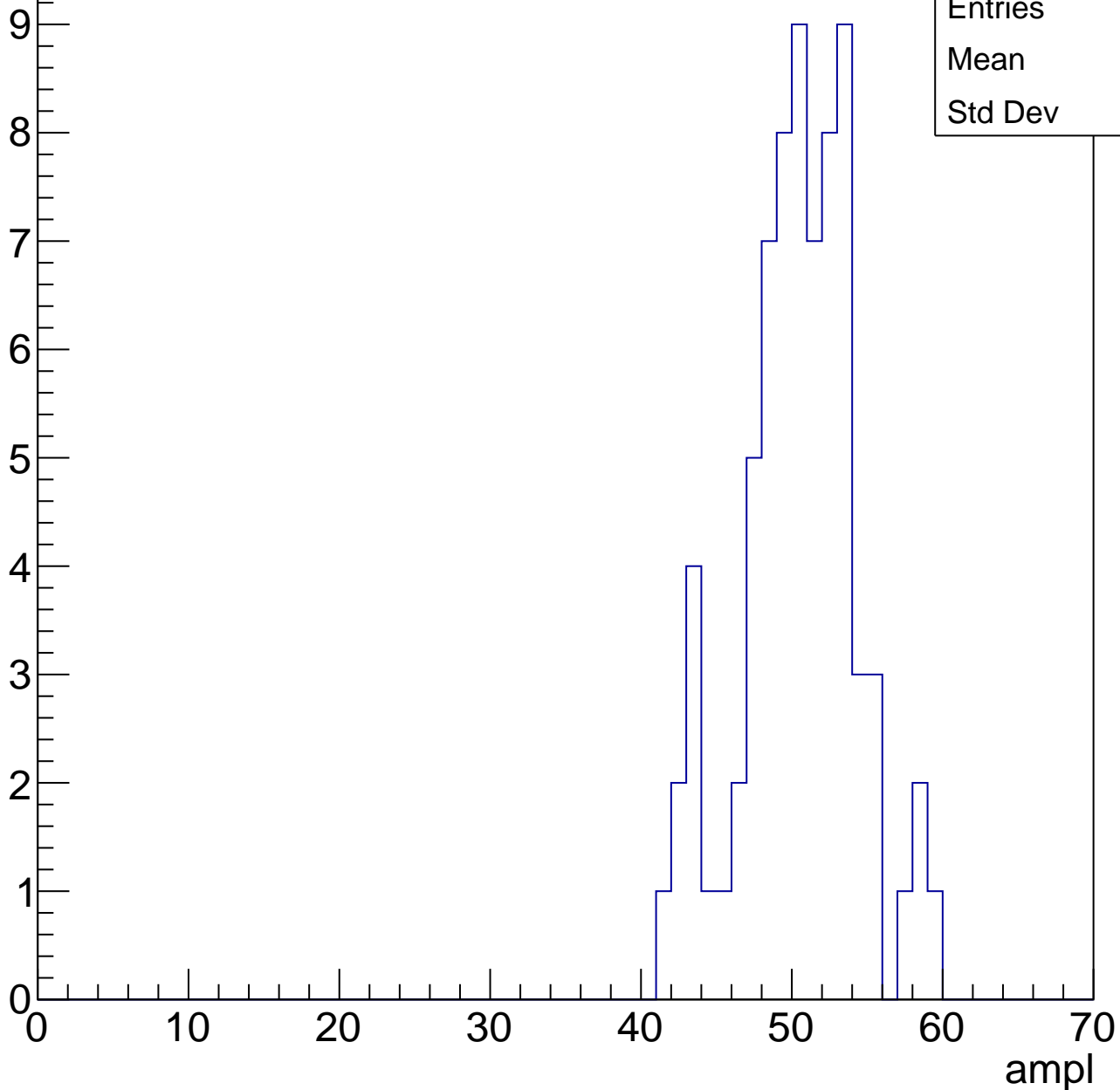


B1L003S, U21-ch77, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	50
Std Dev	3.82

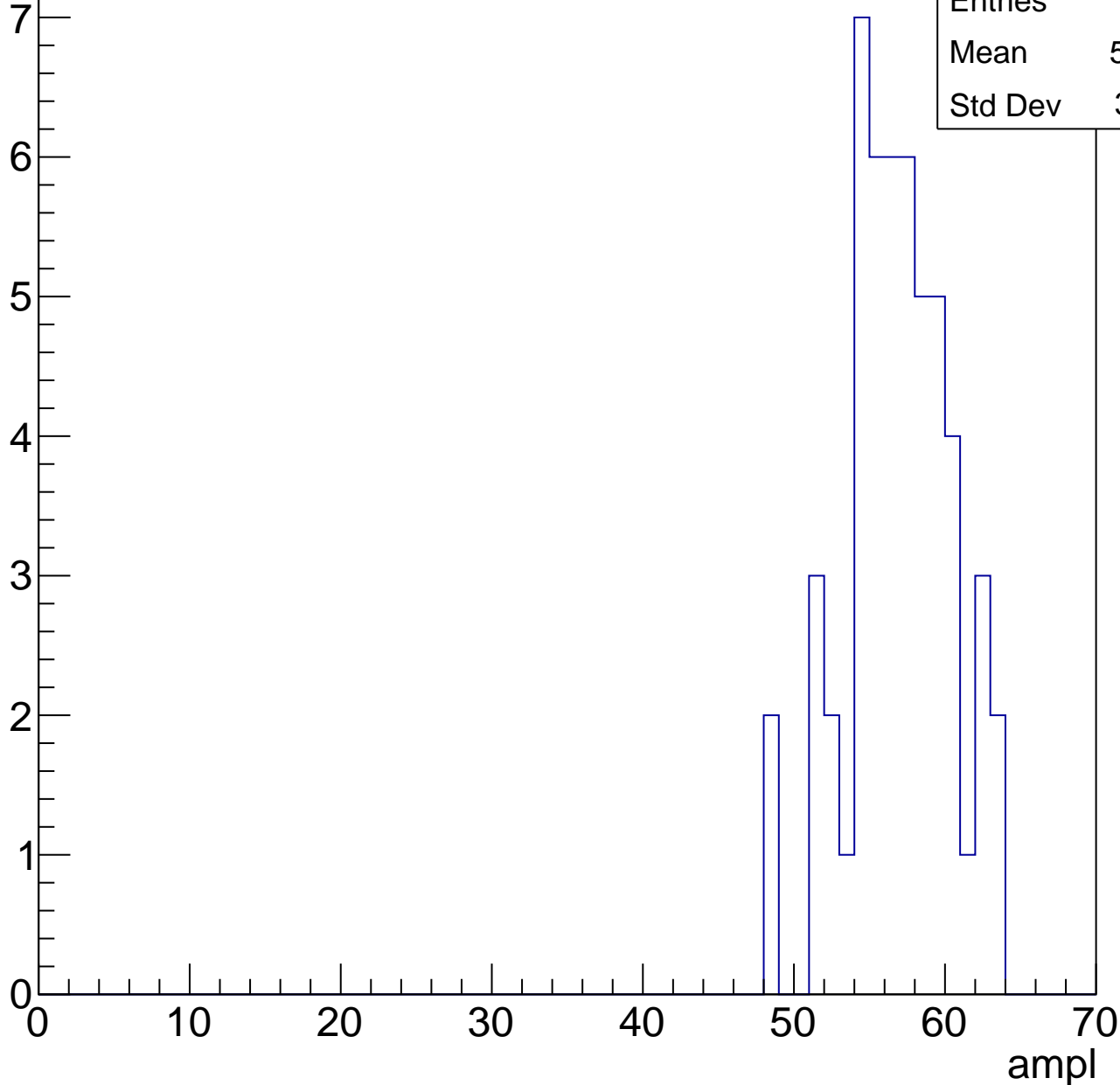


B1L003S, U21-ch77, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	56.42
Std Dev	3.461

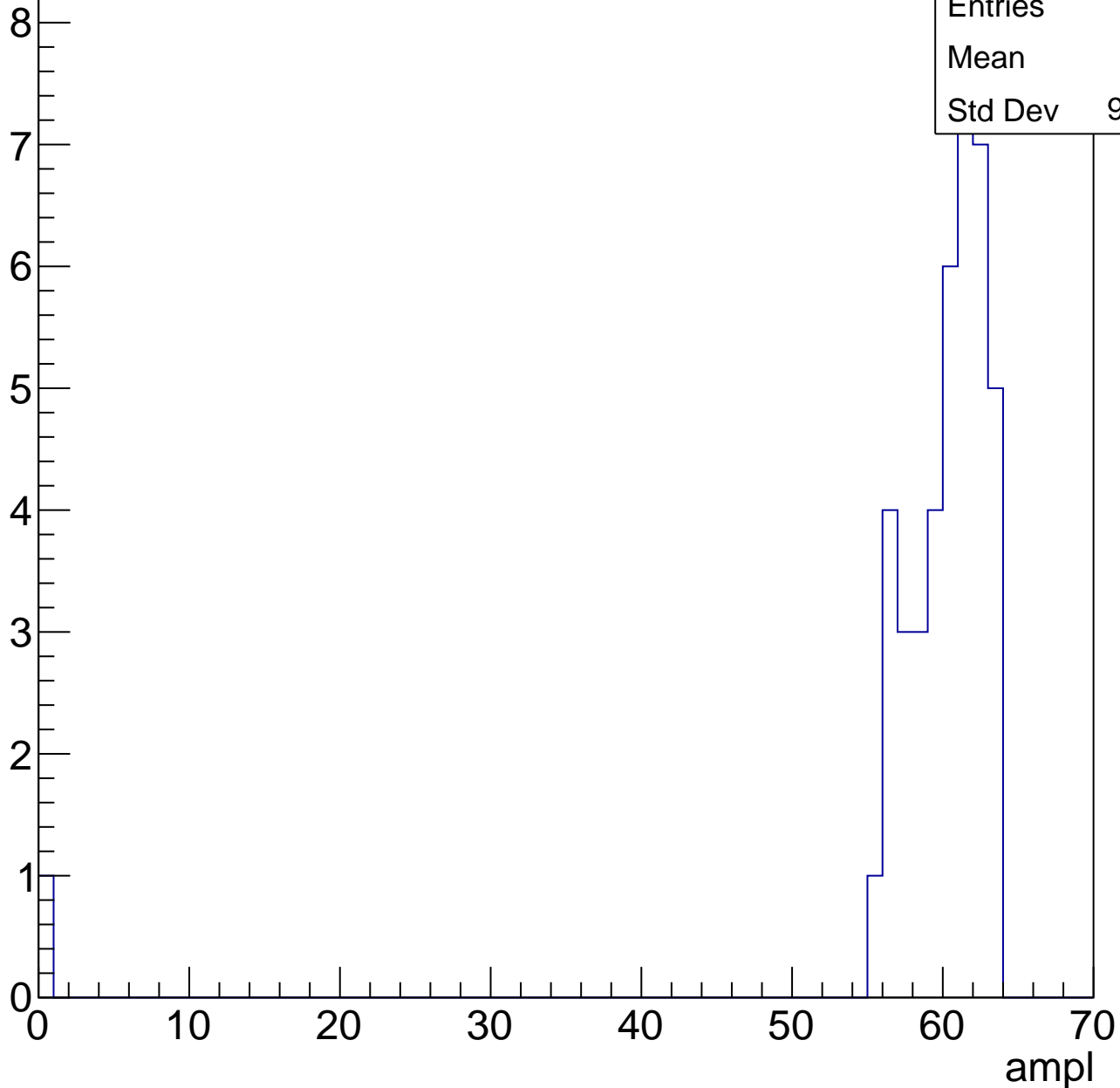


B1L003S, U21-ch77, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	58.5
Std Dev	9.409



B1L003S, U21-ch77, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch77, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch78, adc0

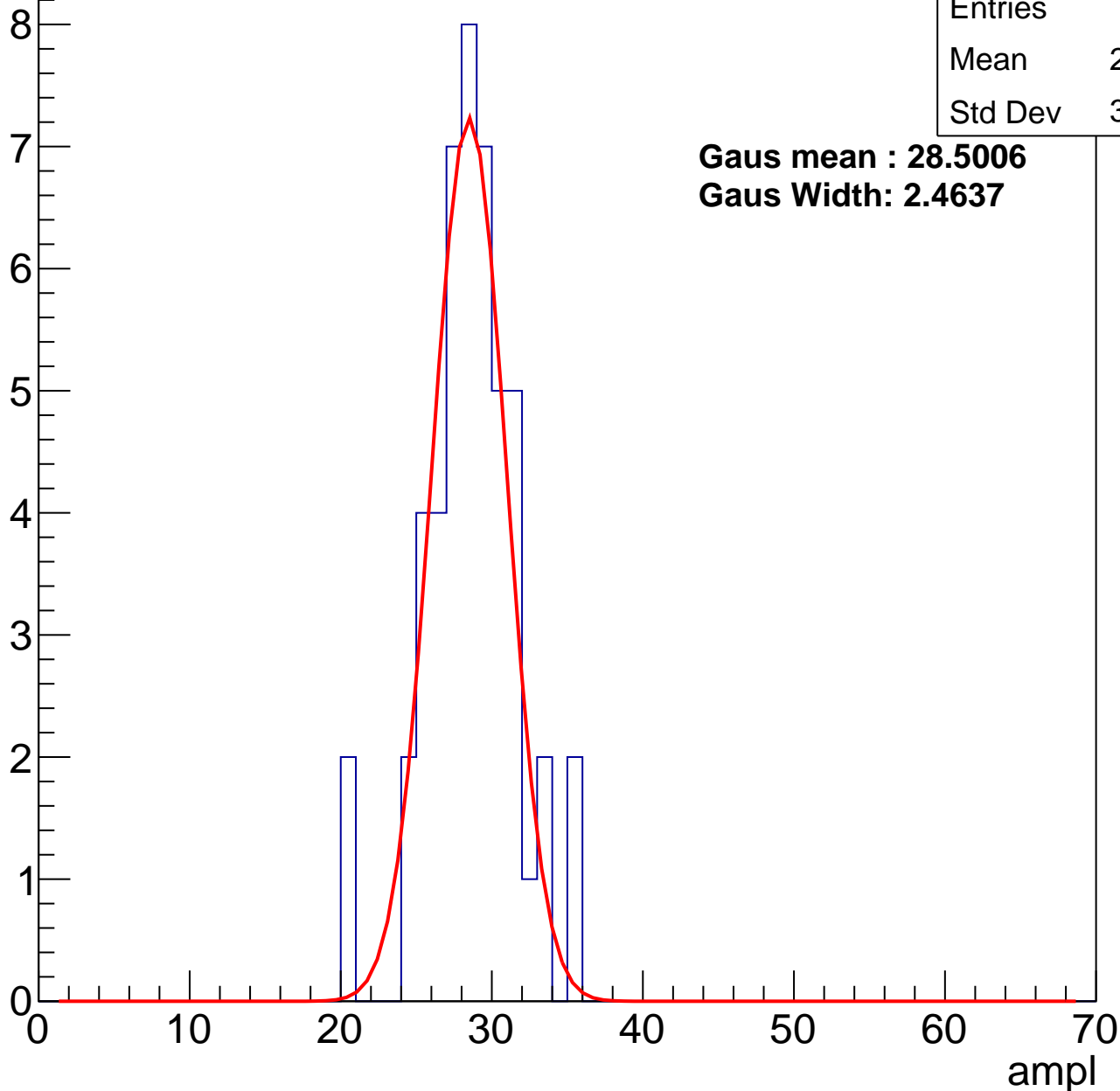
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	28.18
Std Dev	3.042

Gaus mean : 28.5006

Gaus Width: 2.4637



B1L003S, U21-ch78, adc1

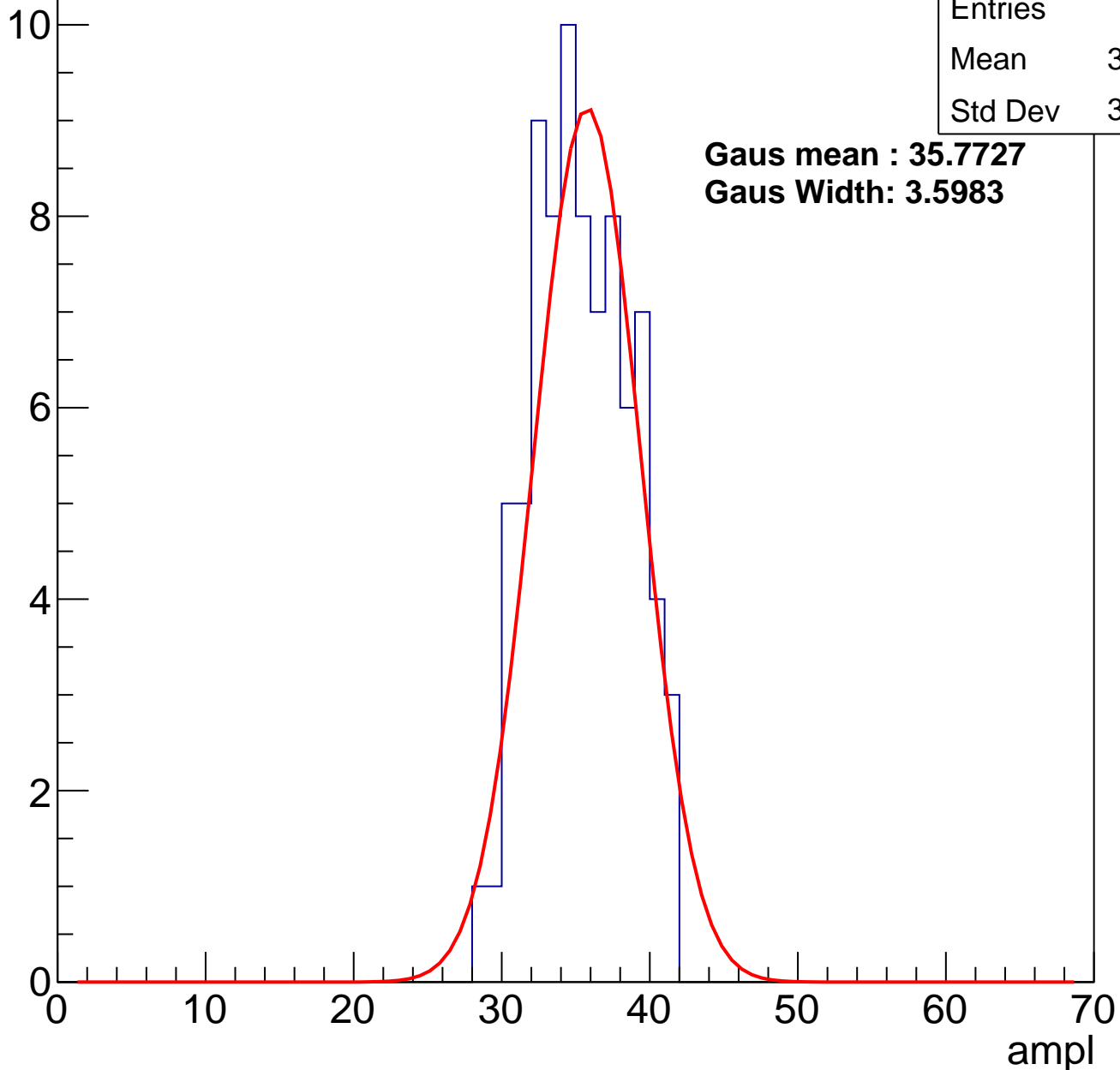
calib_packv5_042523_0143.root, FC#13, port D2

Entries	82
Mean	34.95
Std Dev	3.173

Gaus mean : 35.7727

Gaus Width: 3.5983

Entry



B1L003S, U21-ch78, adc2

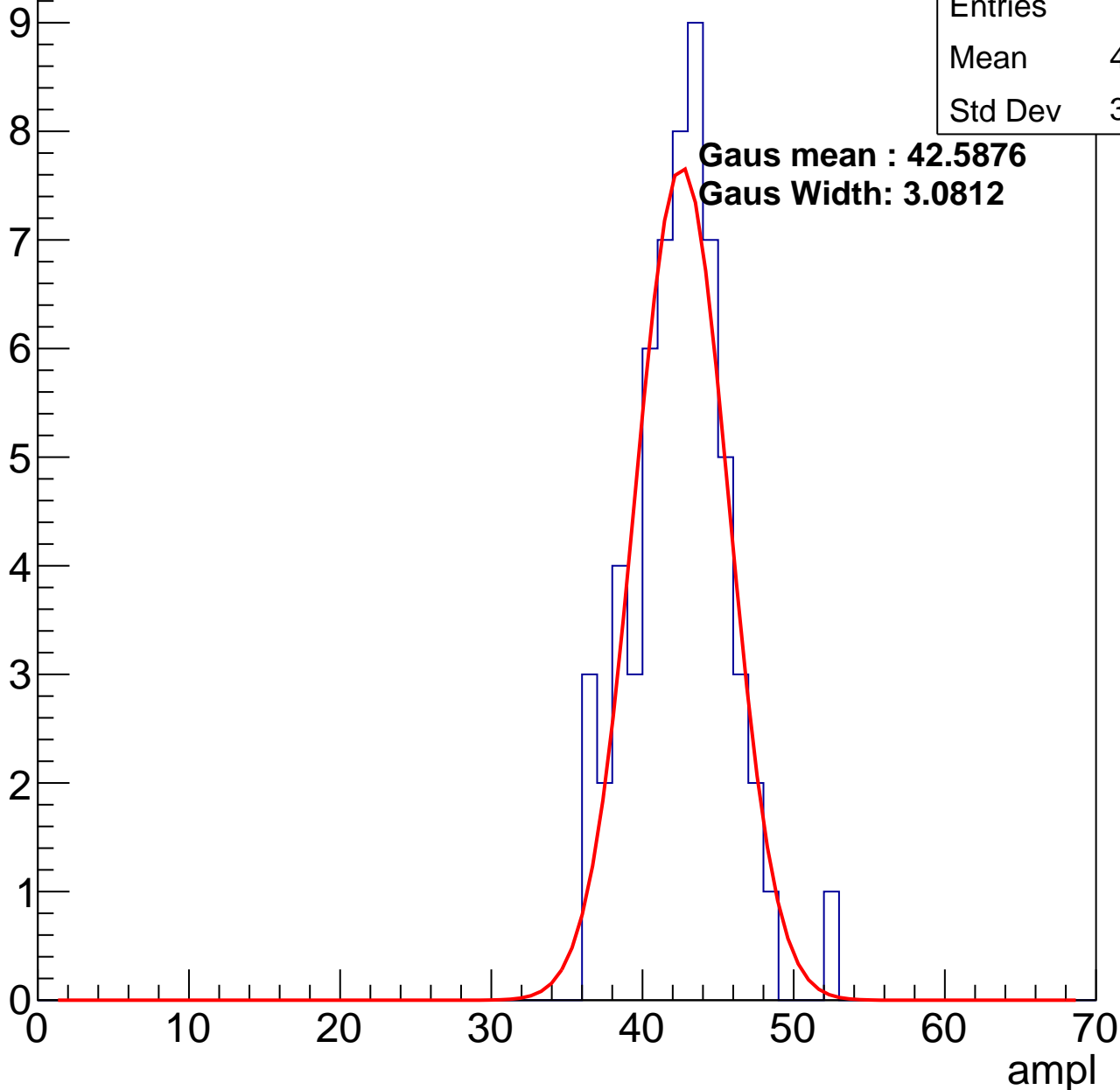
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.07
Std Dev	3.125

Gaus mean : 42.5876

Gaus Width: 3.0812

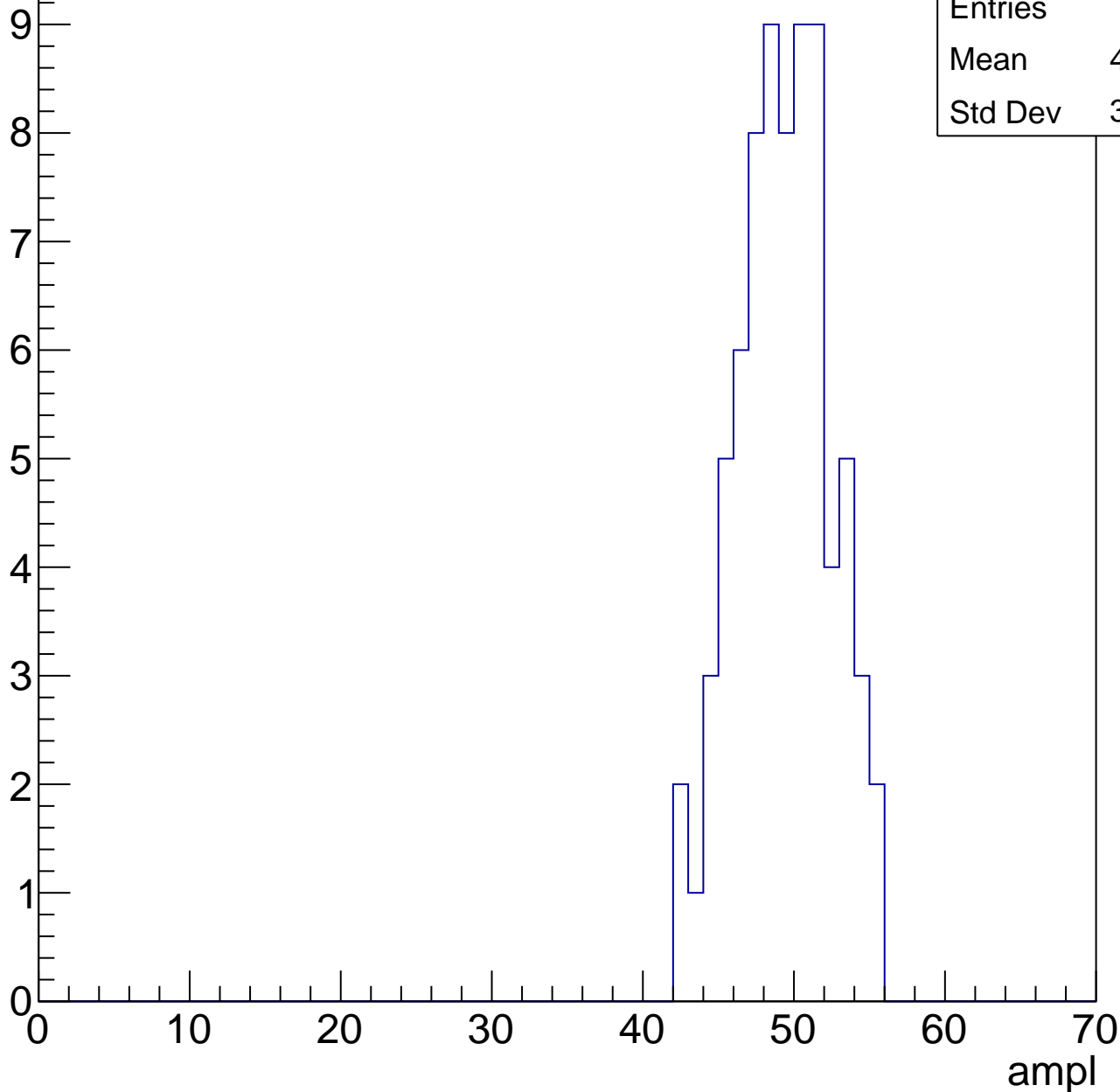


B1L003S, U21-ch78, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	48.84
Std Dev	3.054

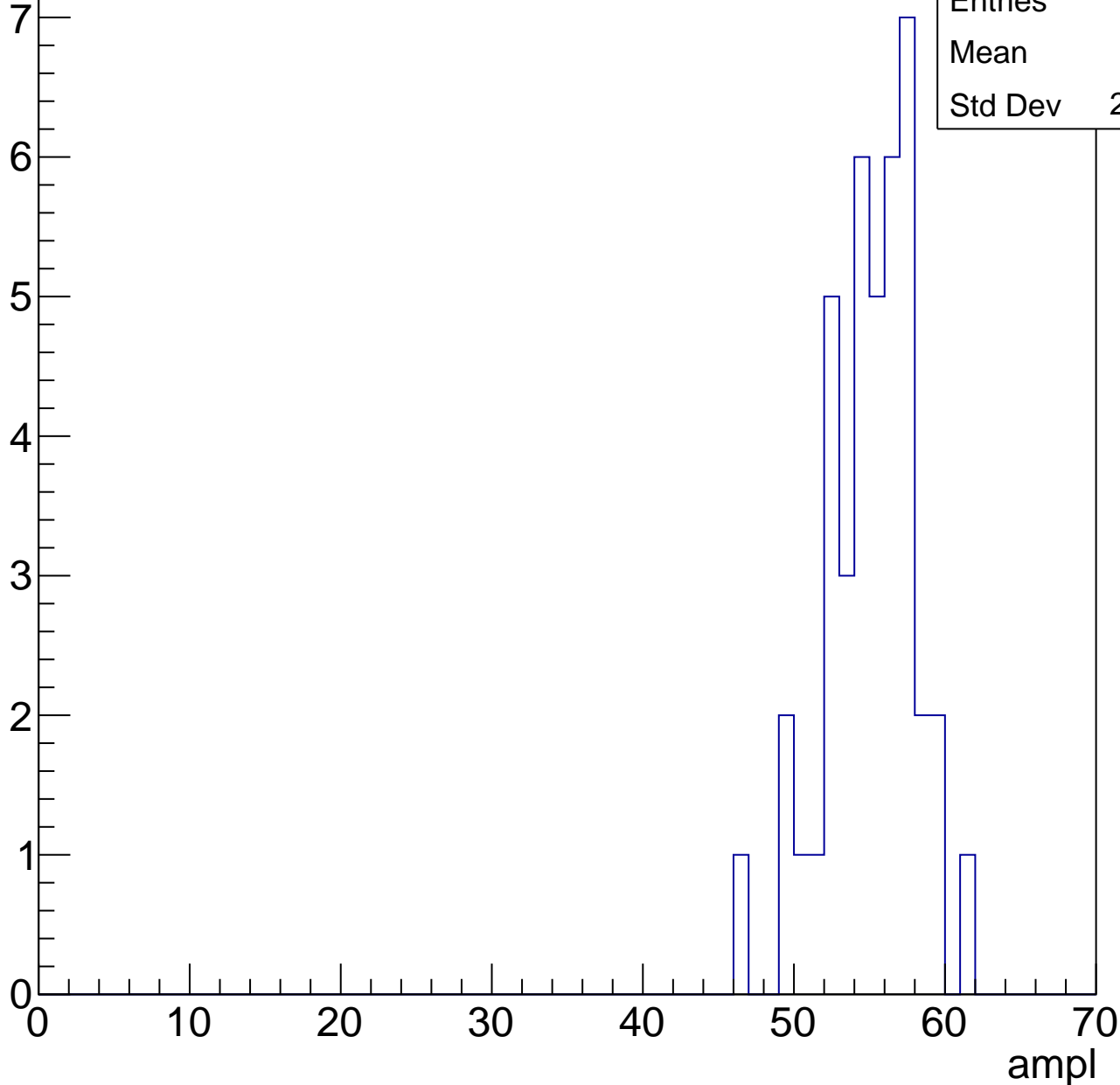


B1L003S, U21-ch78, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	42
Mean	54.6
Std Dev	2.969



B1L003S, U21-ch78, adc5

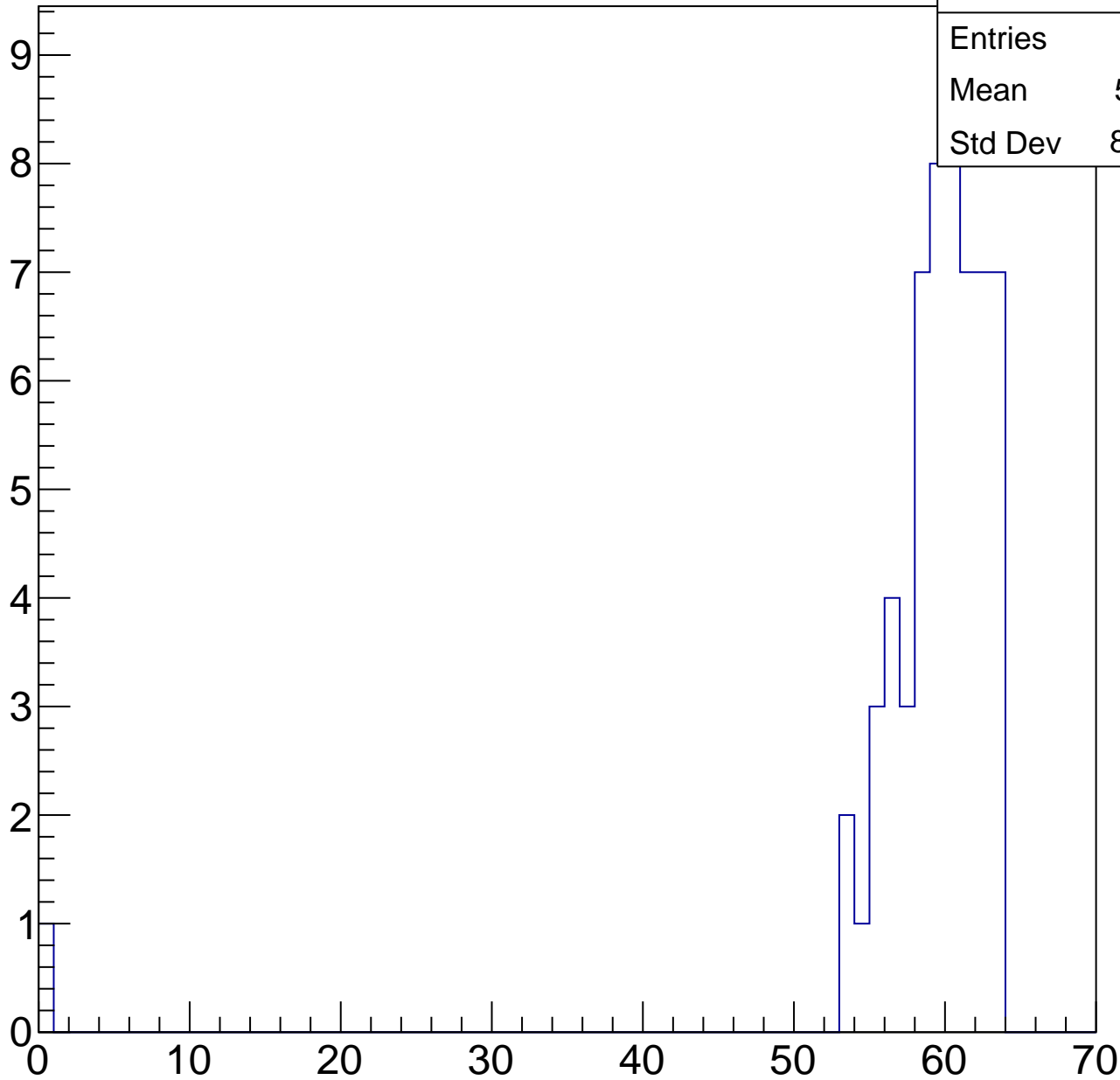
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	59
Mean	58.31
Std Dev	8.093

ampl



B1L003S, U21-ch78, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

10

Mean

61.5

Std Dev

1.5

Entries	10
Mean	61.5
Std Dev	1.5

B1L003S, U21-ch78, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch79, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	73
Mean	29.1
Std Dev	4.933

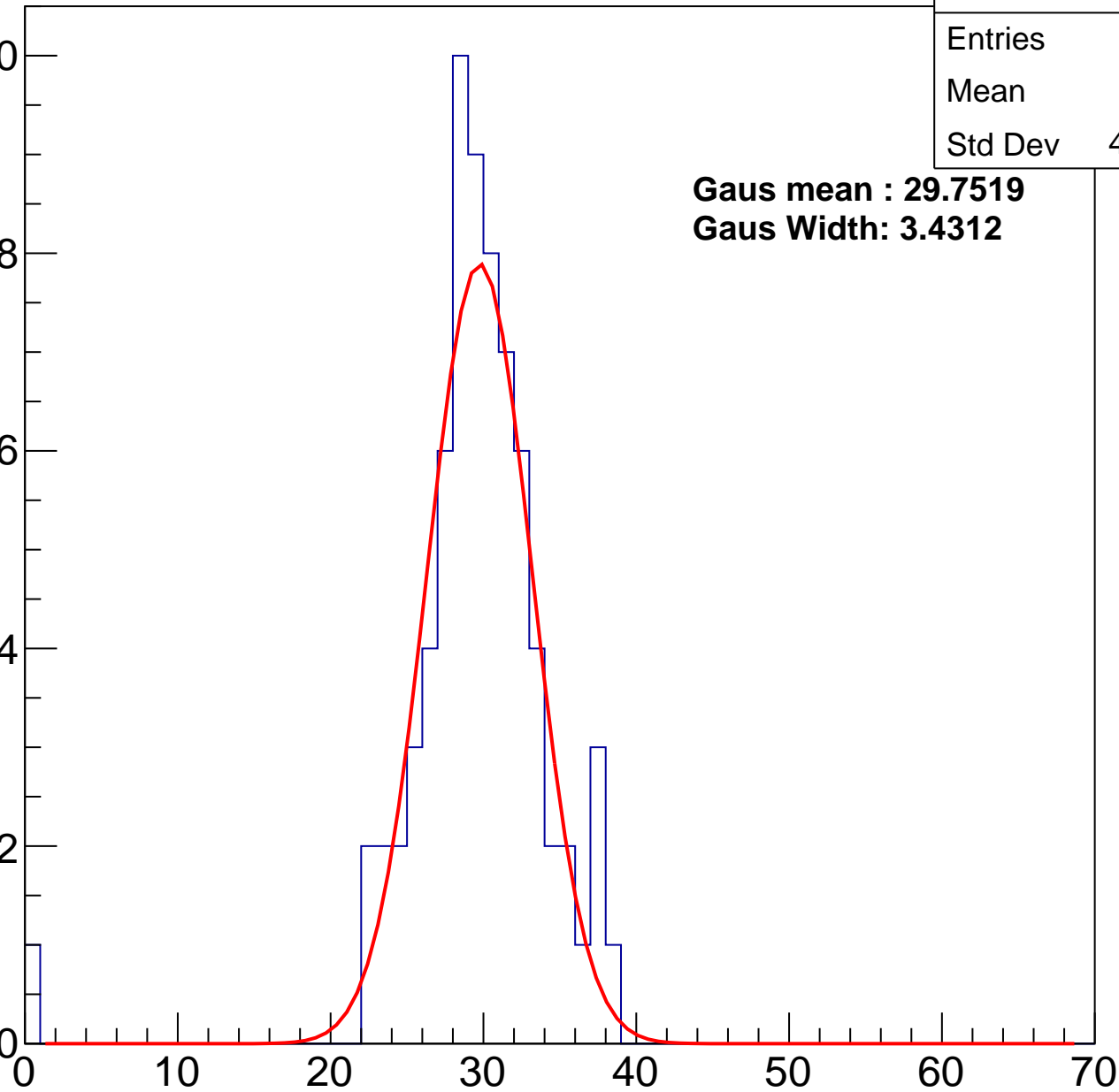
Gaus mean : 29.7519

Gaus Width: 3.4312

Entry

10
8
6
4
2
0

ampl



B1L003S, U21-ch79, adc1

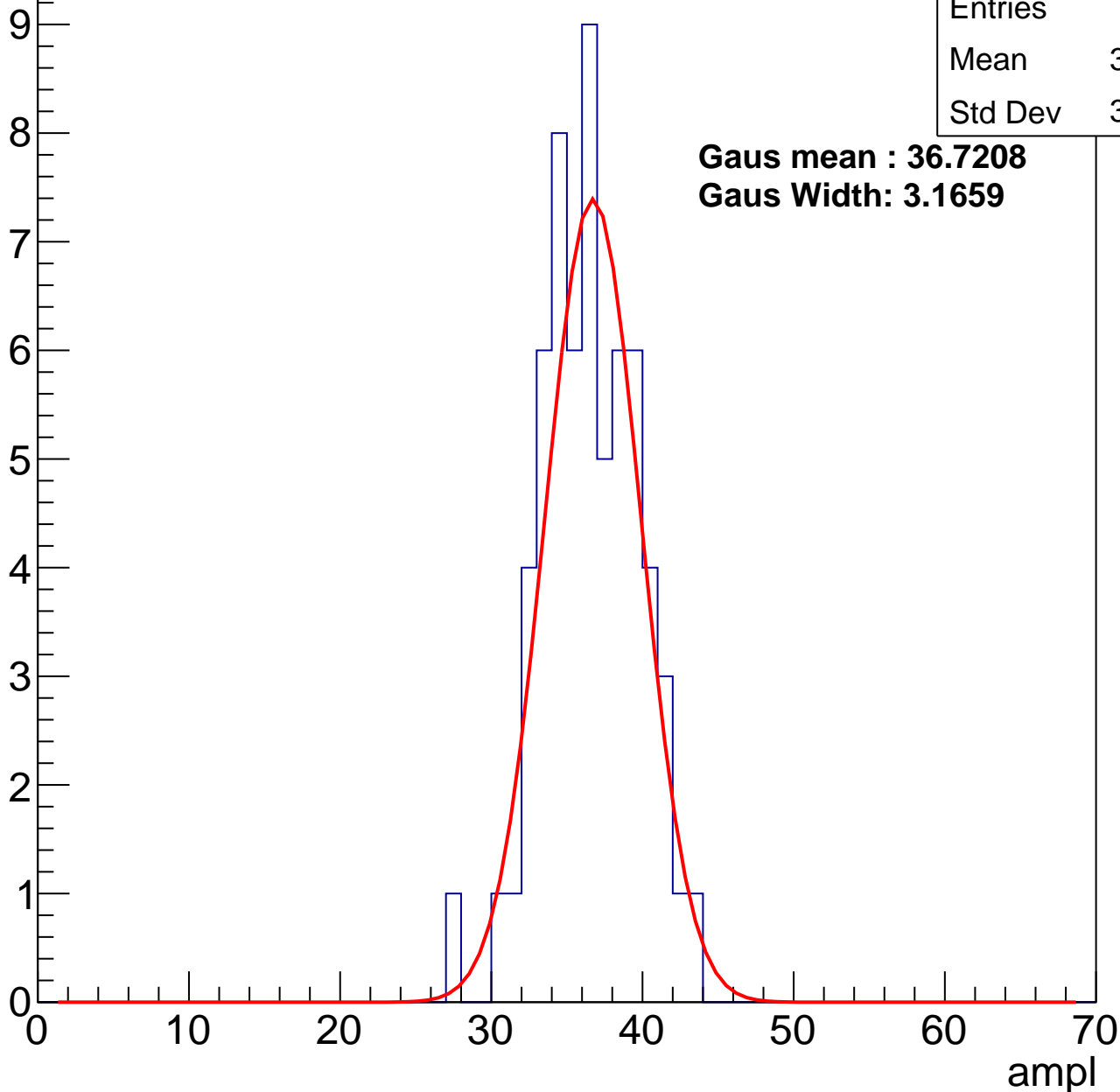
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.05
Std Dev	3.123

Gaus mean : 36.7208

Gaus Width: 3.1659



B1L003S, U21-ch79, adc2

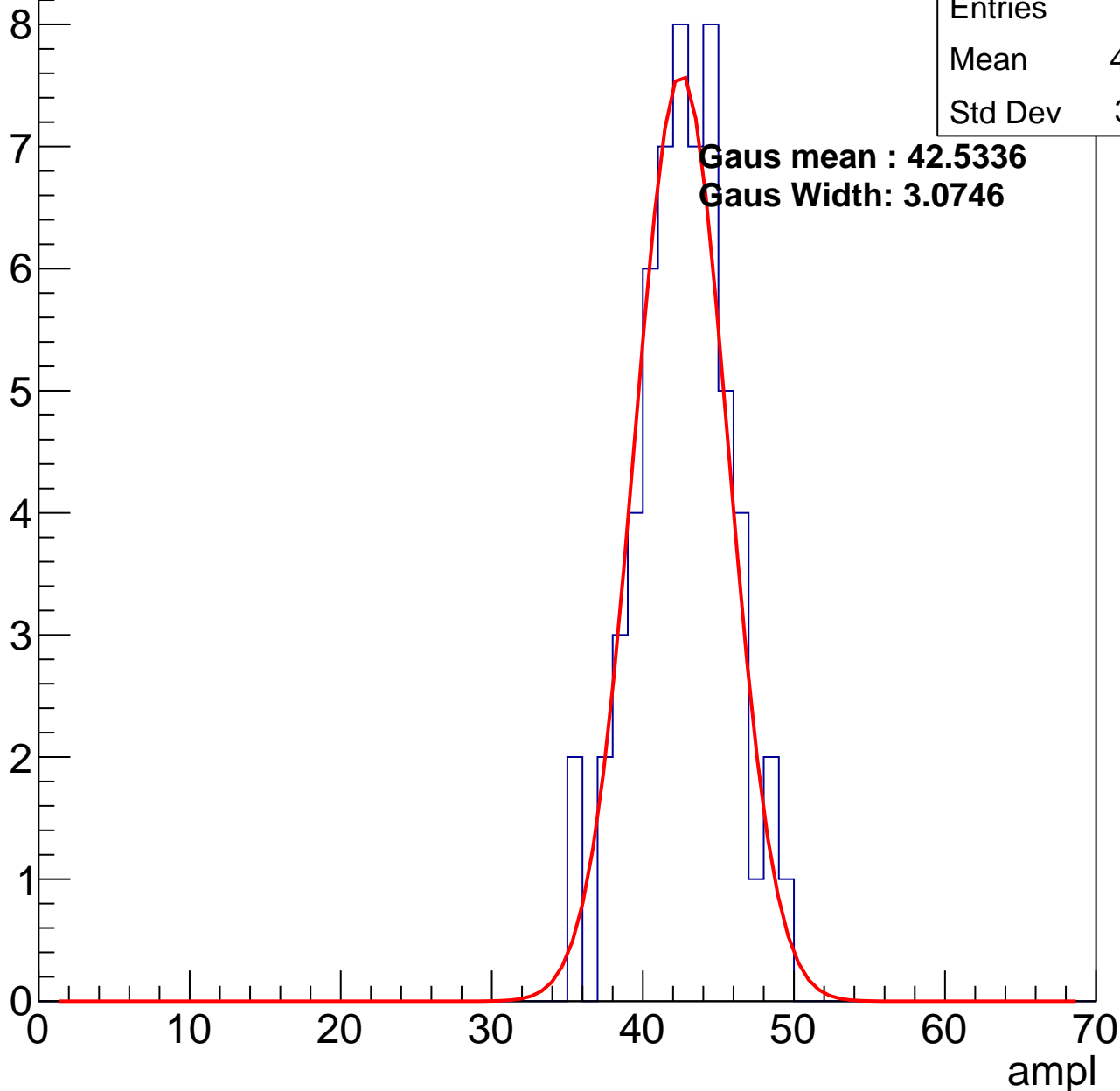
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	42.18
Std Dev	3.041

Gaus mean : 42.5336

Gaus Width: 3.0746

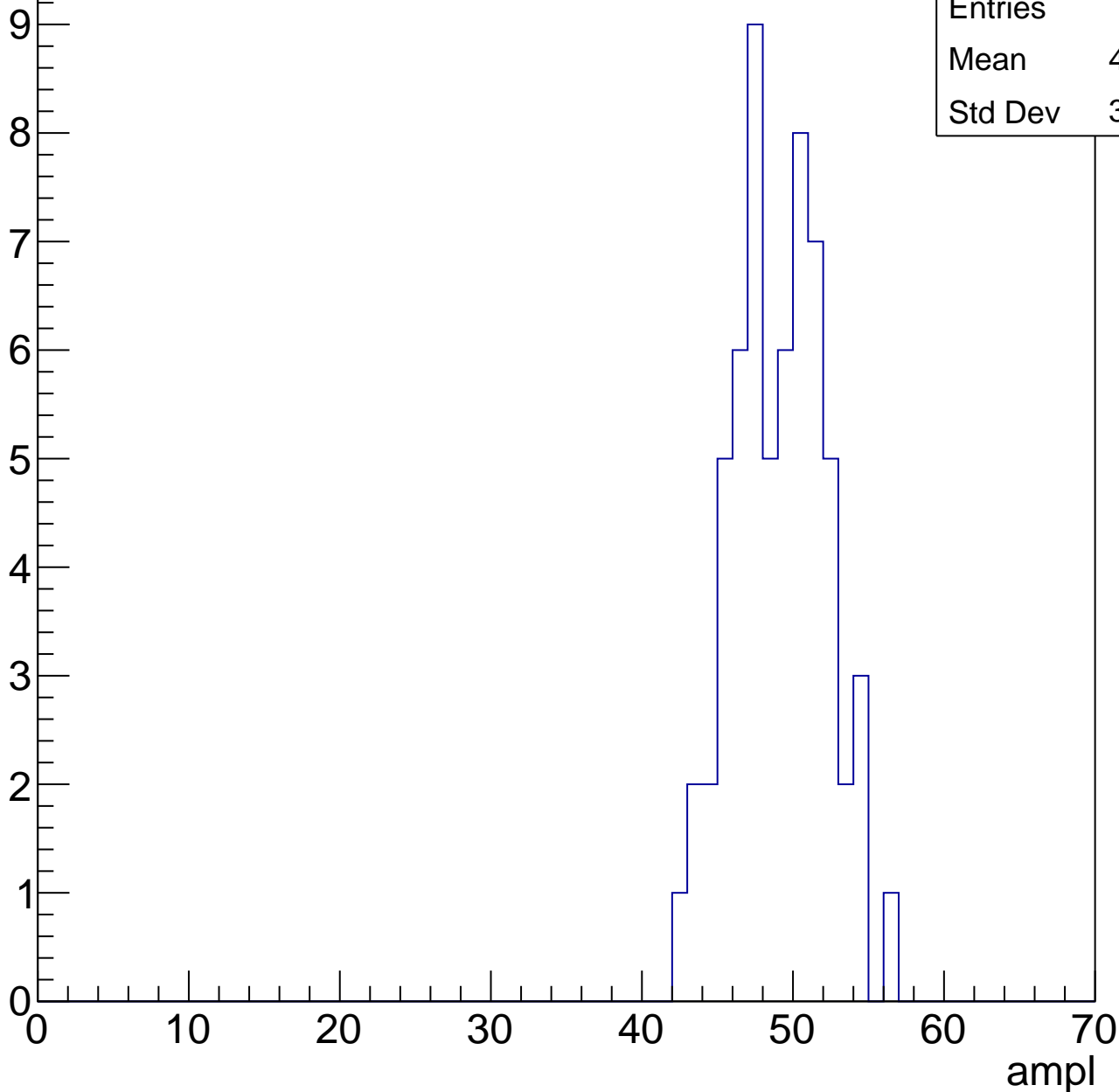


B1L003S, U21-ch79, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.63
Std Dev	3.044

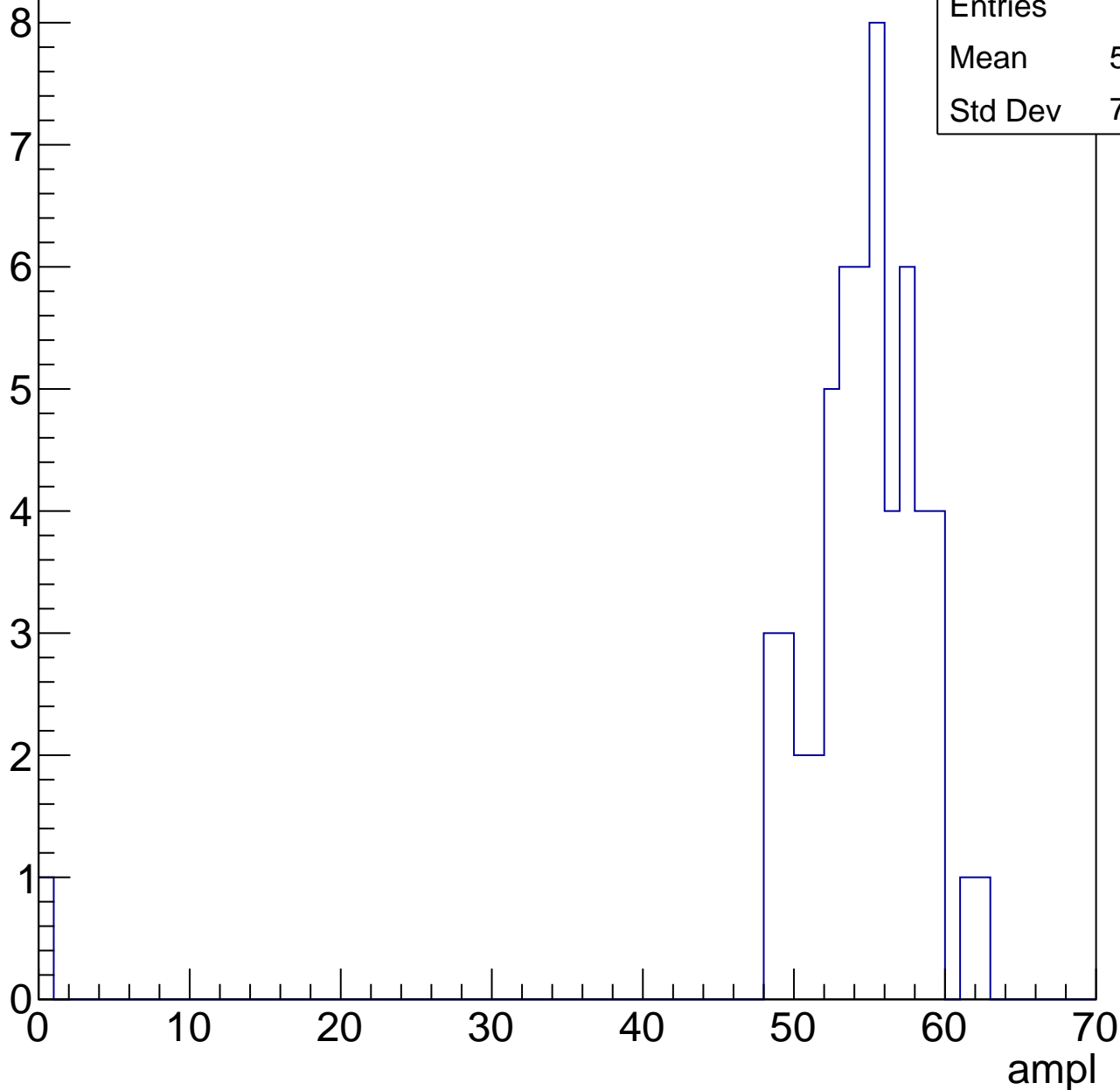


B1L003S, U21-ch79, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	53.43
Std Dev	7.919

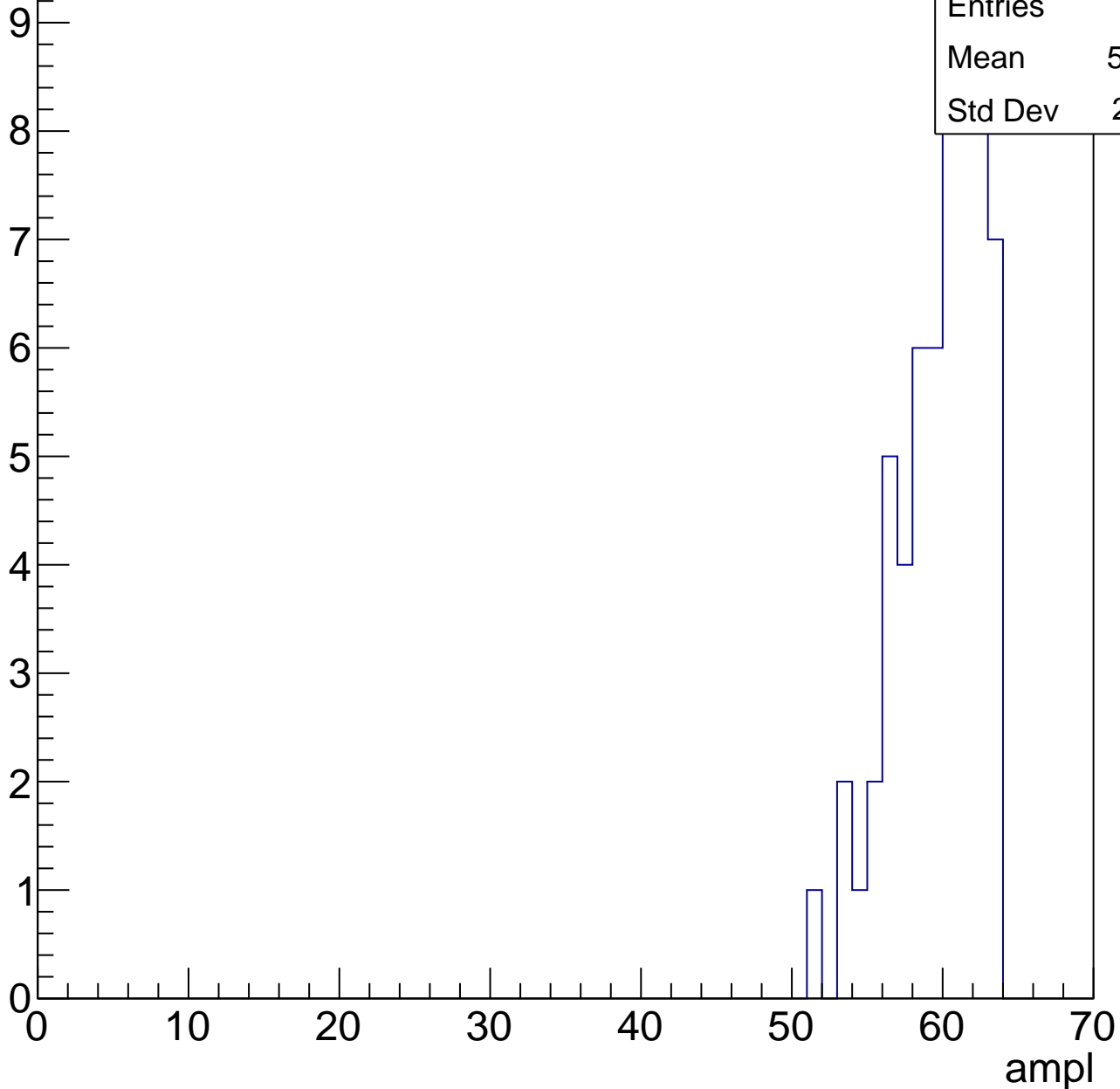


B1L003S, U21-ch79, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

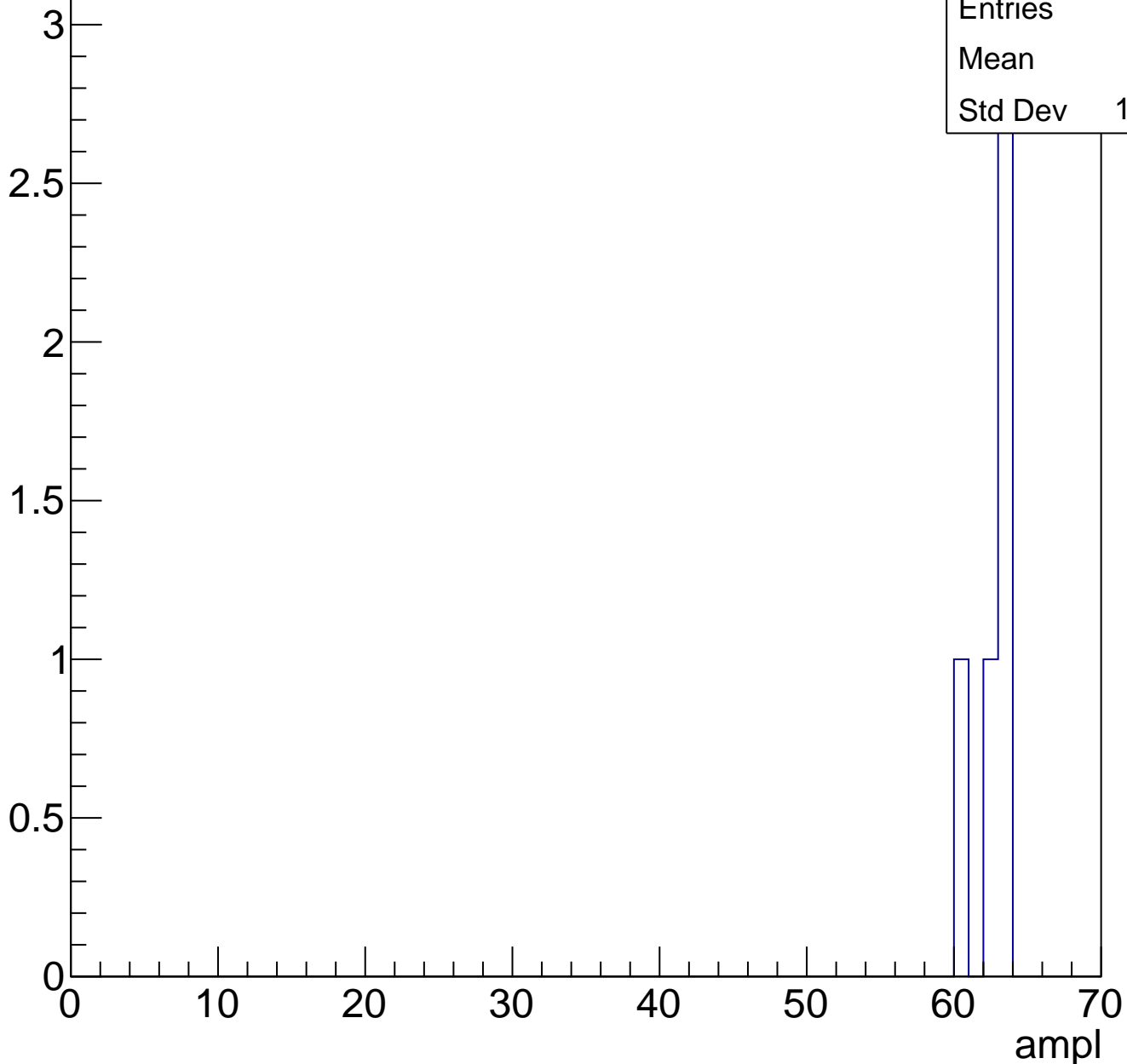
Entries	60
Mean	59.28
Std Dev	2.841



B1L003S, U21-ch79, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch79, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch80, adc0

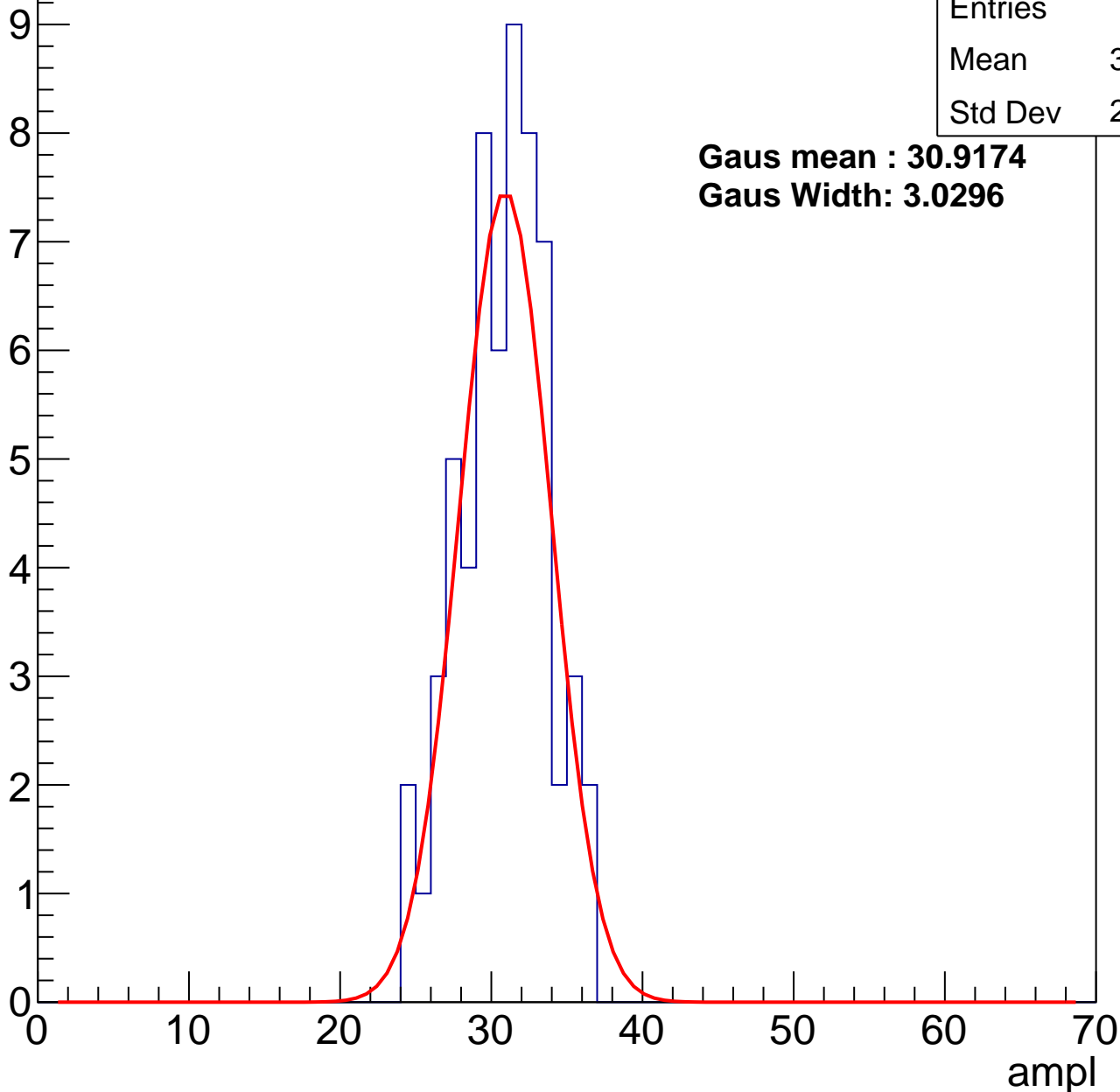
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	30.35
Std Dev	2.857

Gaus mean : 30.9174

Gaus Width: 3.0296



B1L003S, U21-ch80, adc1

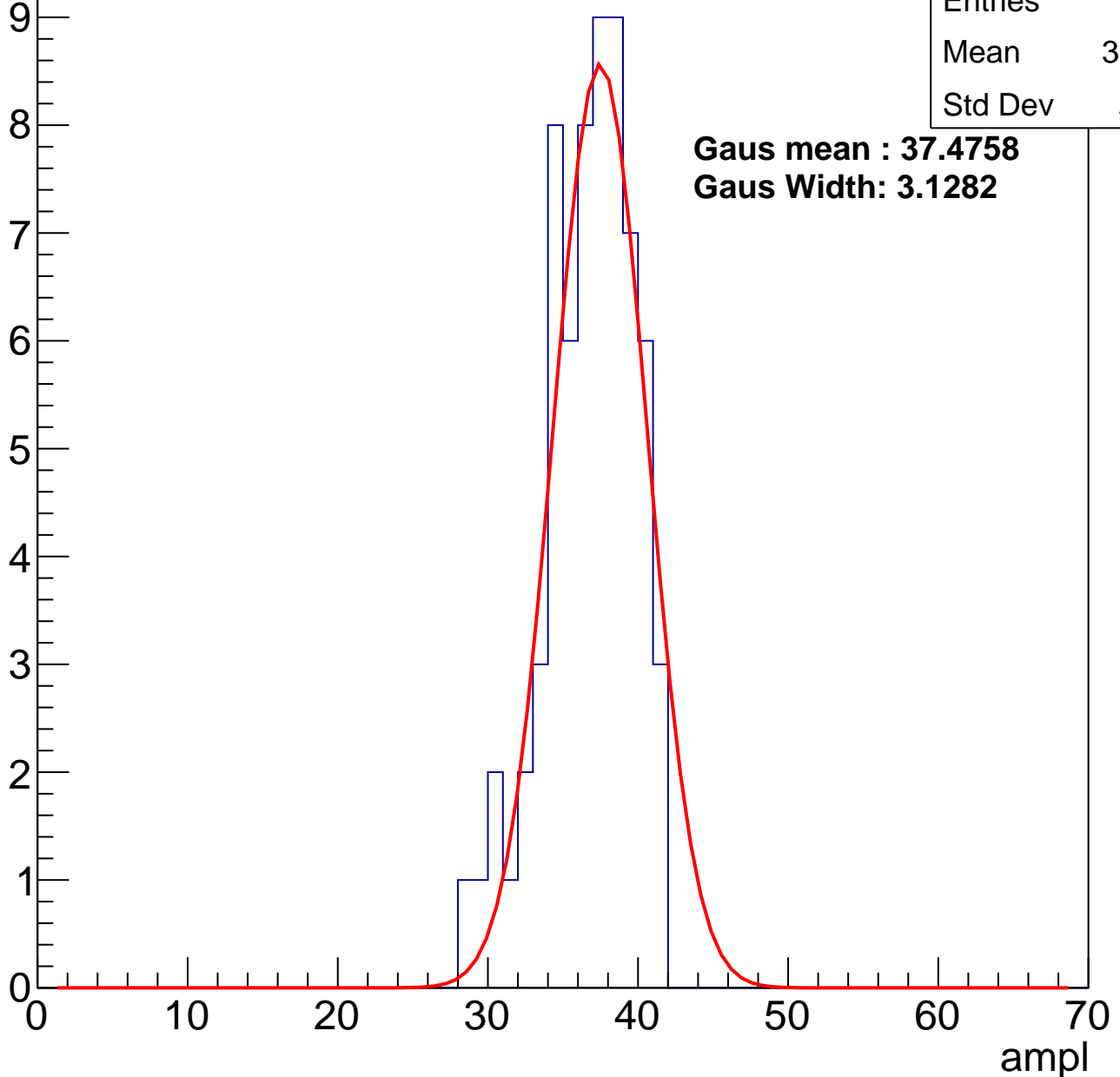
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.24
Std Dev	2.97

Gaus mean : 37.4758

Gaus Width: 3.1282



B1L003S, U21-ch80, adc2

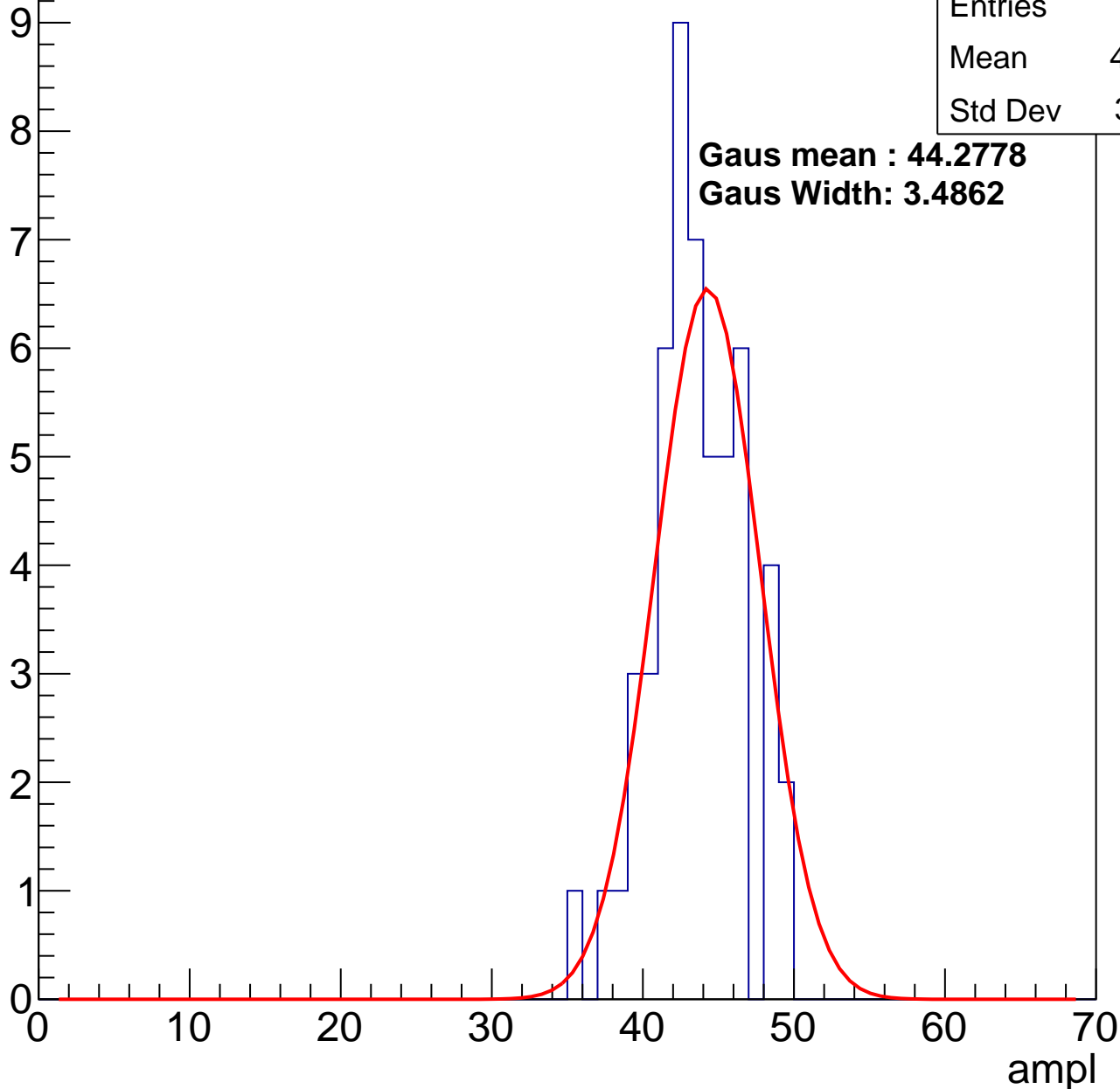
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.08
Std Dev	3.021

Gaus mean : 44.2778

Gaus Width: 3.4862



B1L003S, U21-ch80, adc3

calib_packv5_042523_0143.root, FC#13, port D2

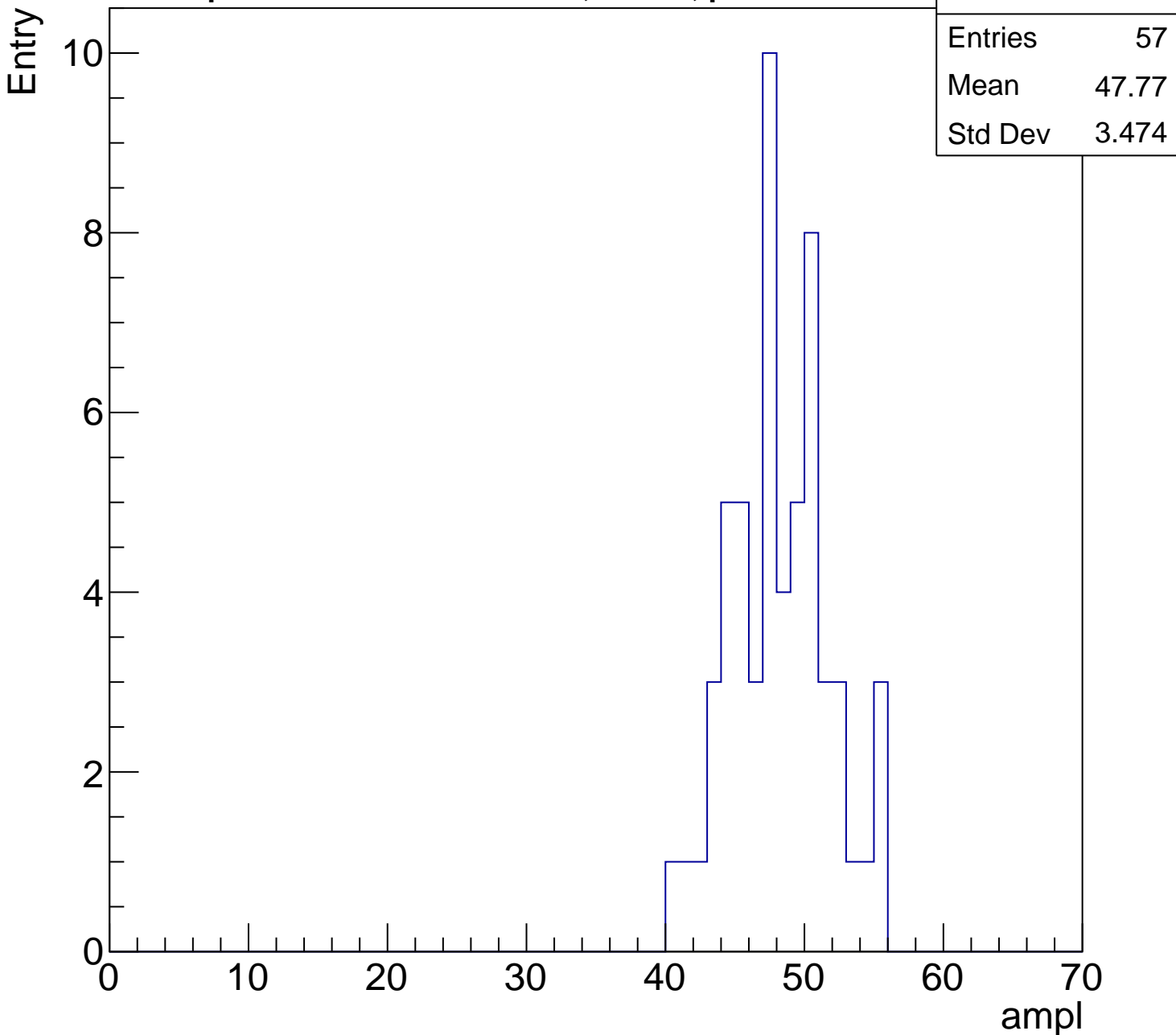
Entries	57
Mean	47.77
Std Dev	3.474

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

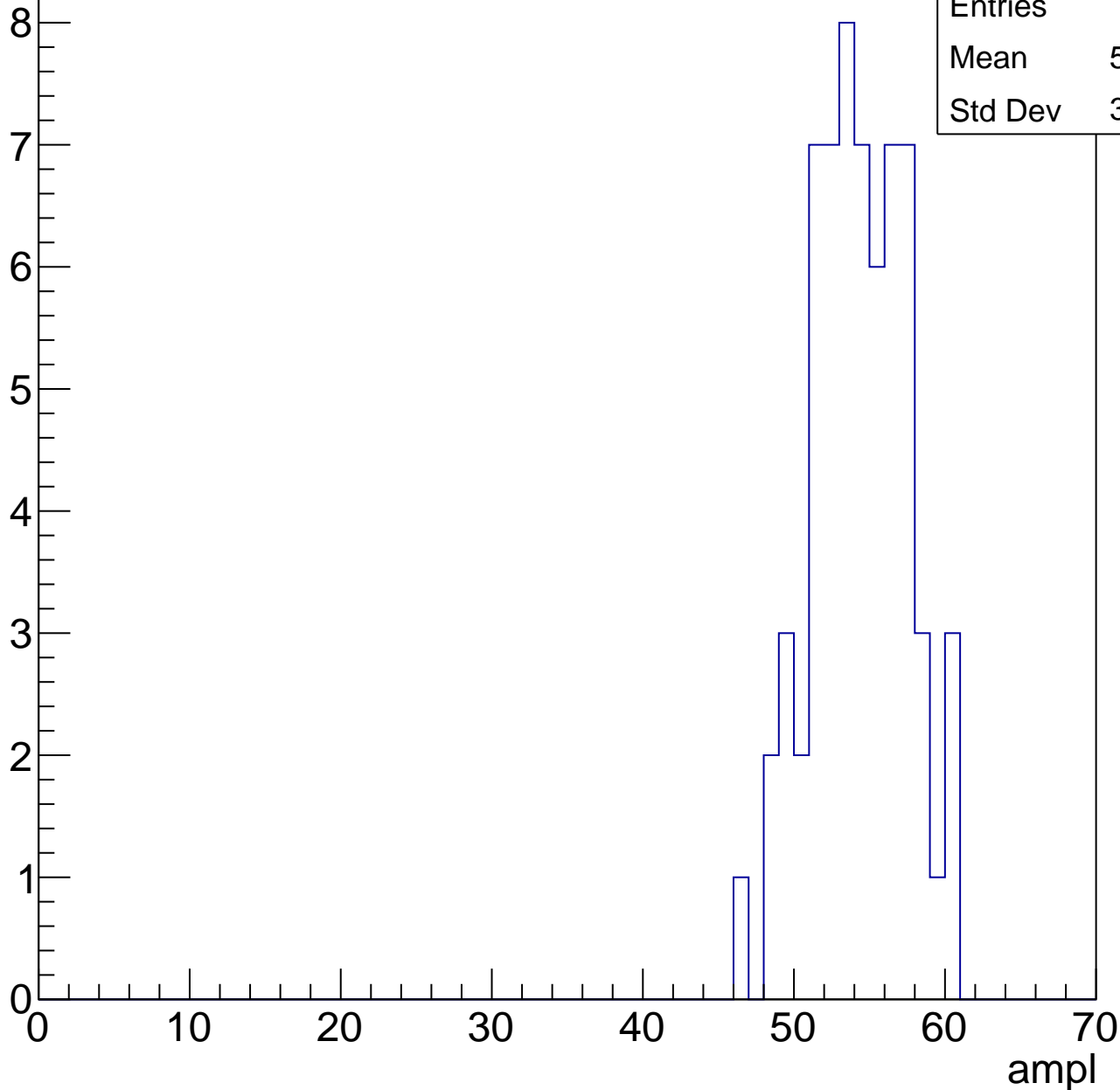


B1L003S, U21-ch80, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

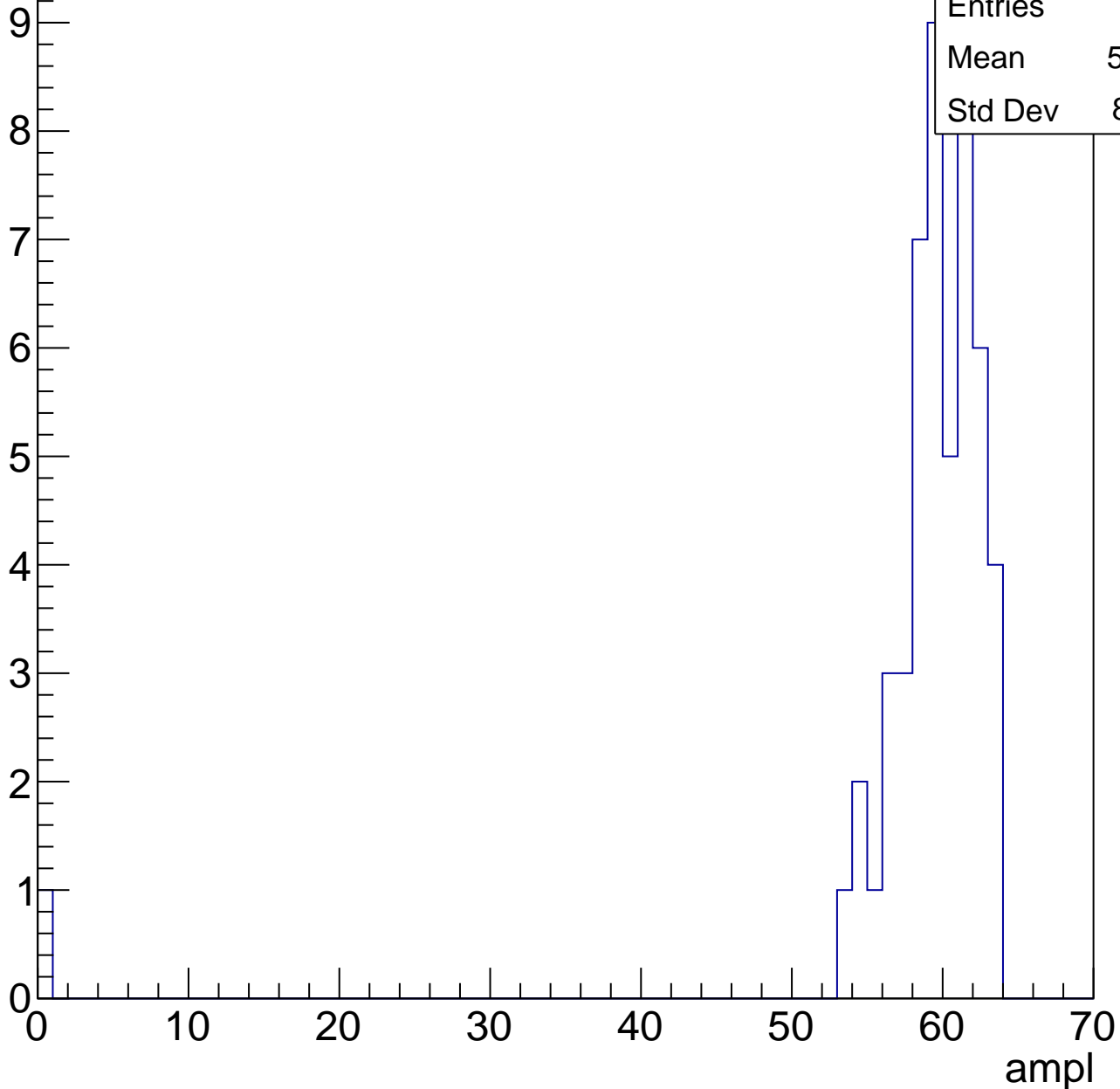
Entries	64
Mean	53.84
Std Dev	3.109



B1L003S, U21-ch80, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

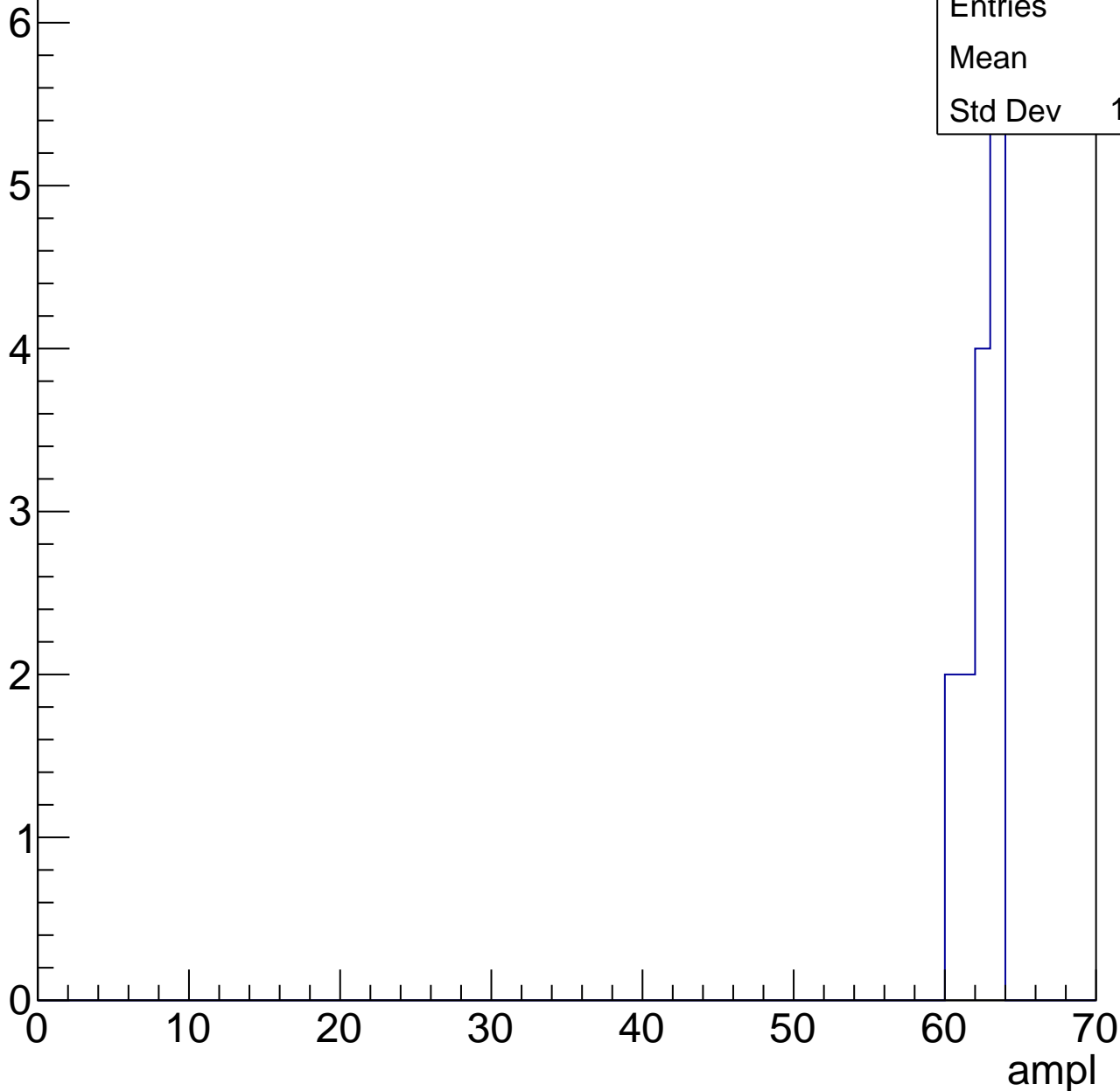


B1L003S, U21-ch80, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	14
Mean	62
Std Dev	1.069



B1L003S, U21-ch80, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch81, adc0

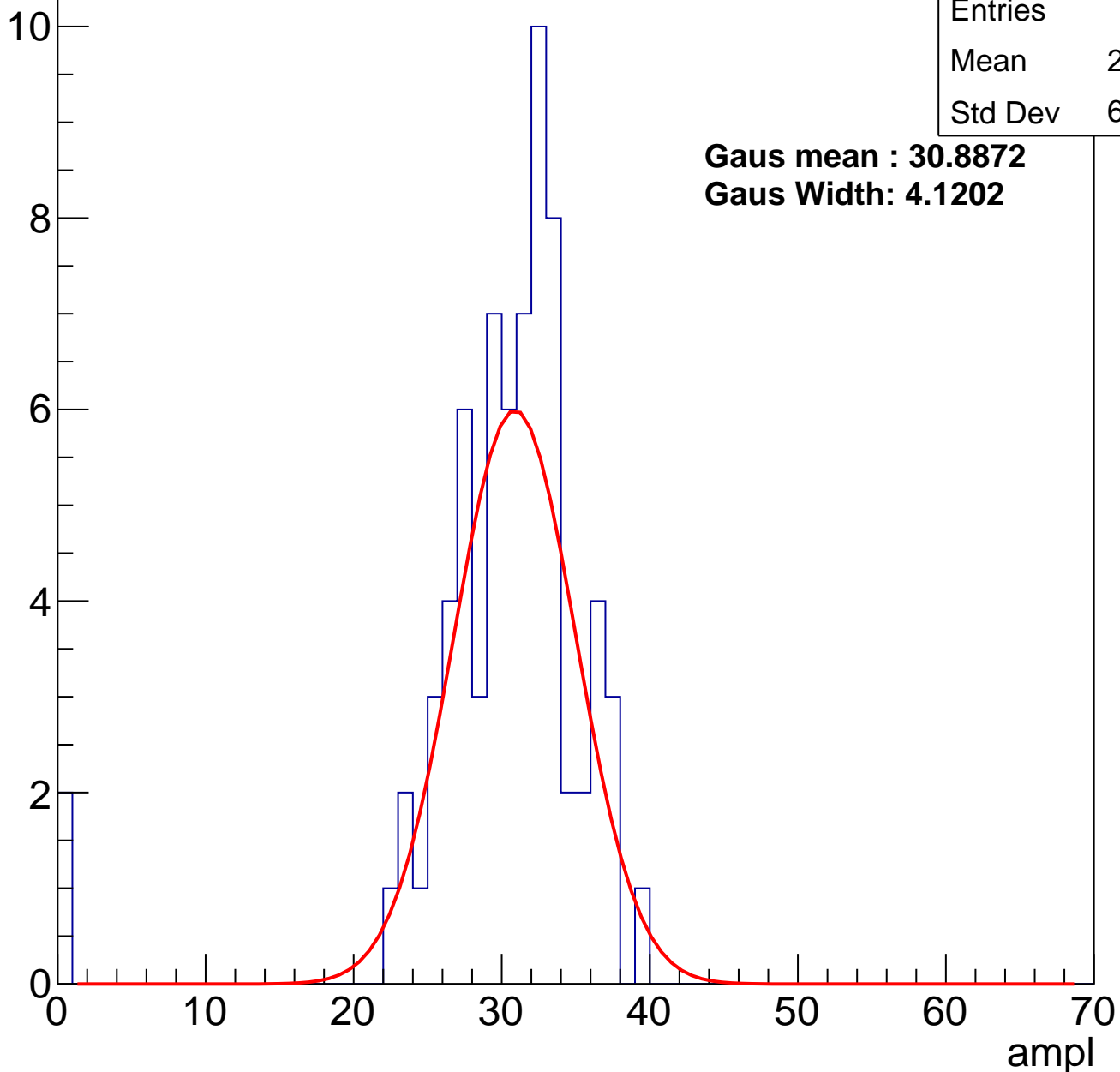
calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	29.62
Std Dev	6.206

Gaus mean : 30.8872

Gaus Width: 4.1202

Entry

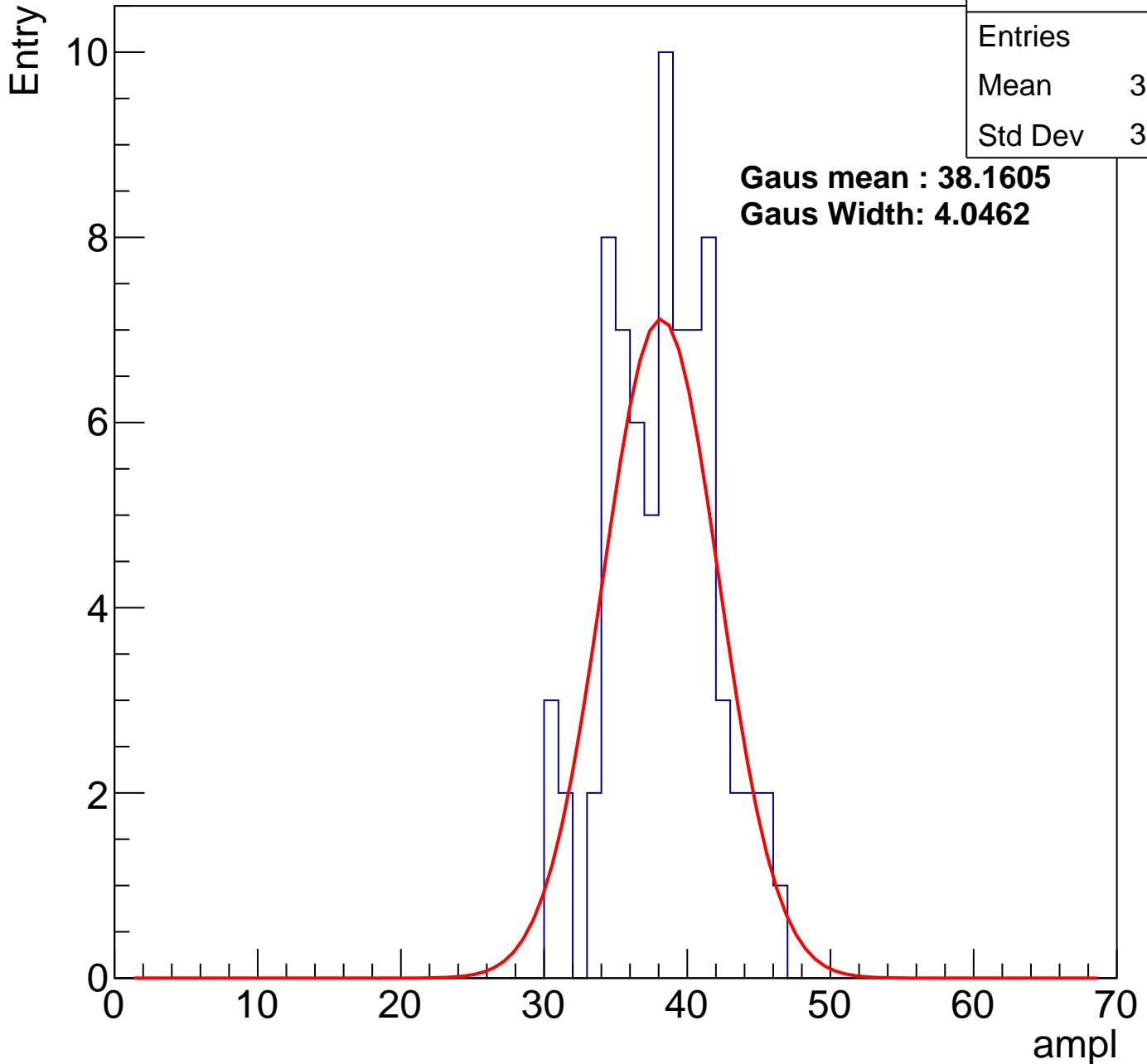


B1L003S, U21-ch81, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	75
Mean	37.77
Std Dev	3.643

Gaus mean : 38.1605
Gaus Width: 4.0462



B1L003S, U21-ch81, adc2

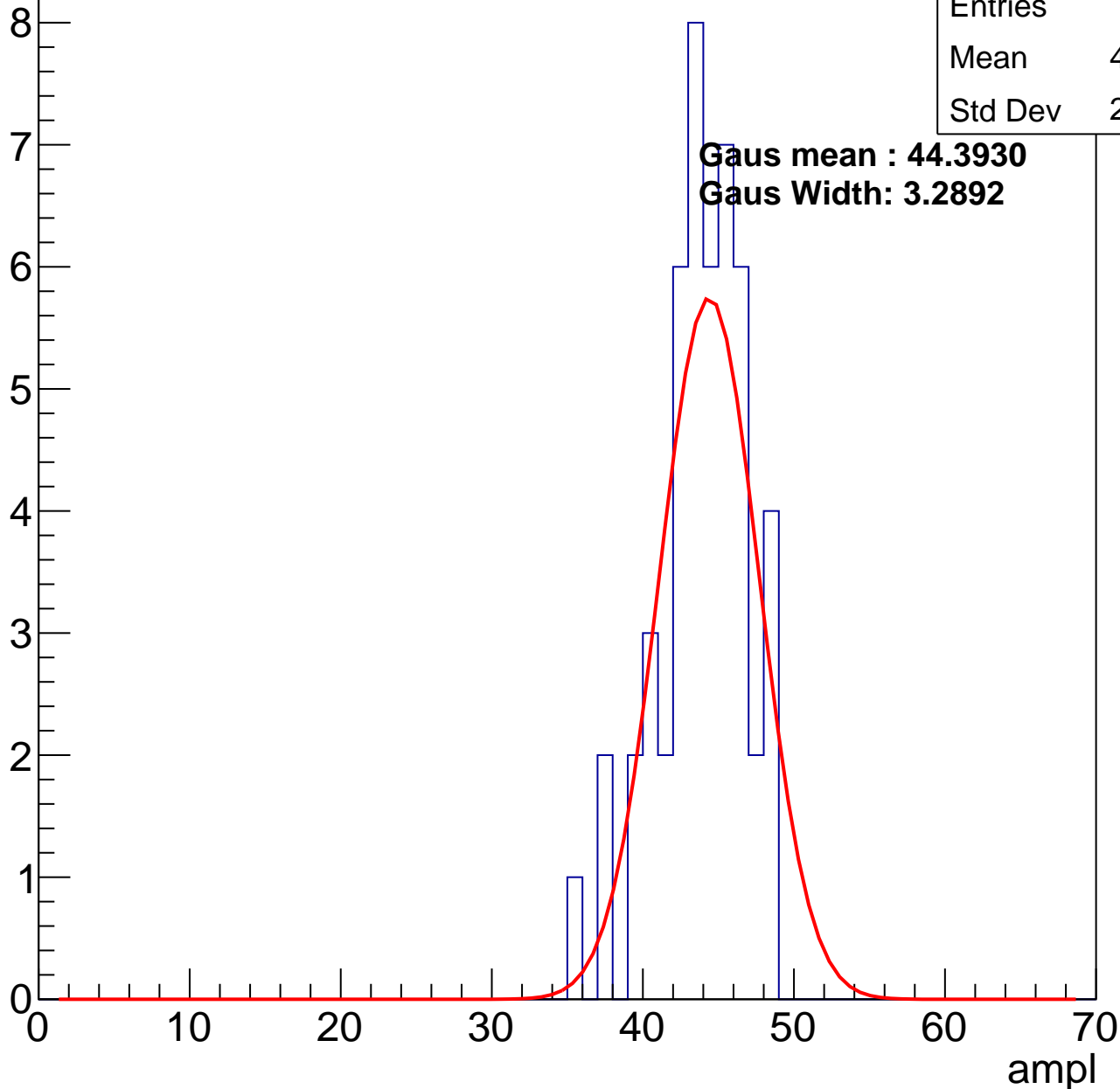
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	43.39
Std Dev	2.933

Gaus mean : 44.3930

Gaus Width: 3.2892



B1L003S, U21-ch81, adc3

calib_packv5_042523_0143.root, FC#13, port D2

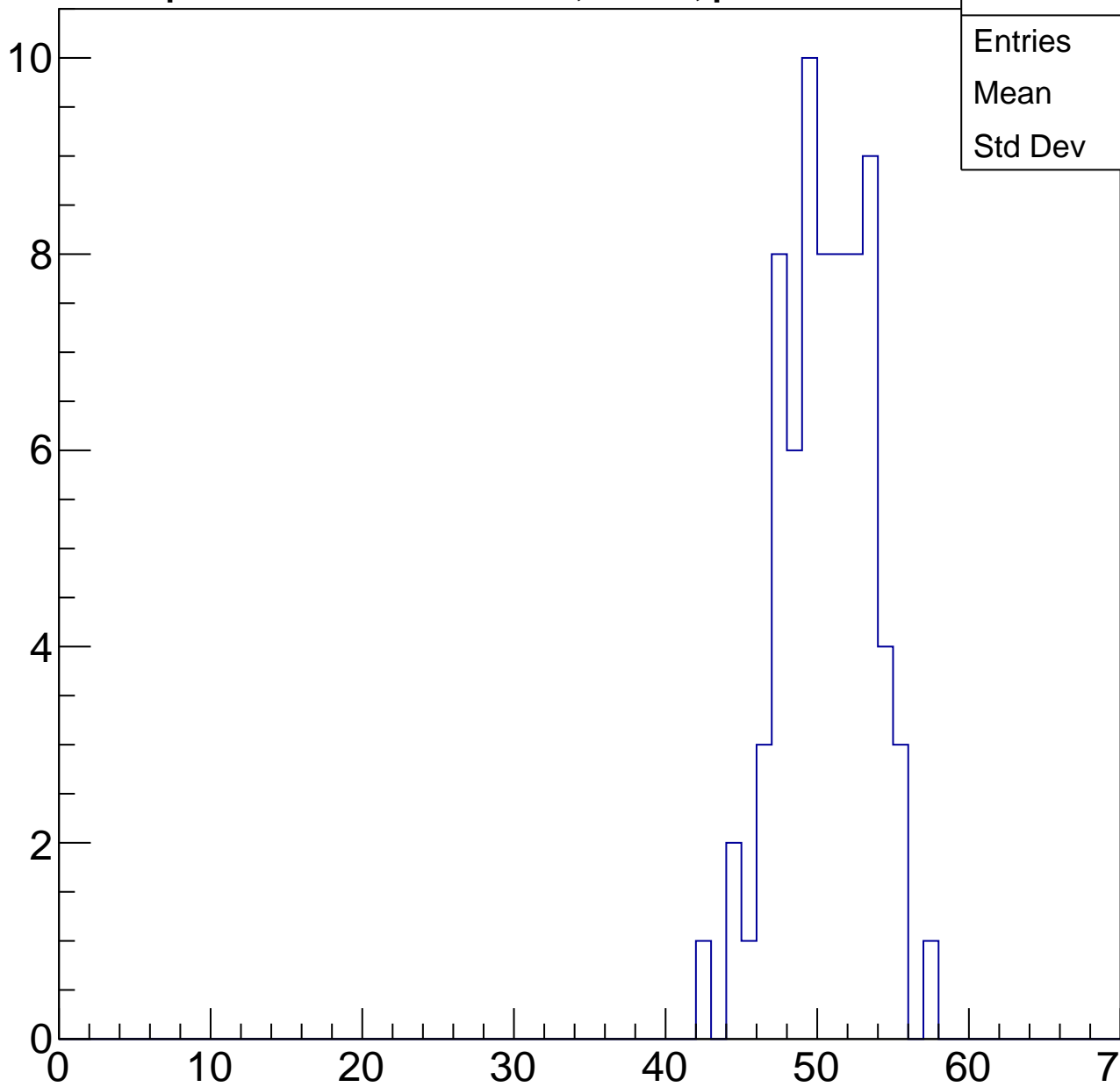
Entries	72
Mean	50.08
Std Dev	2.943

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

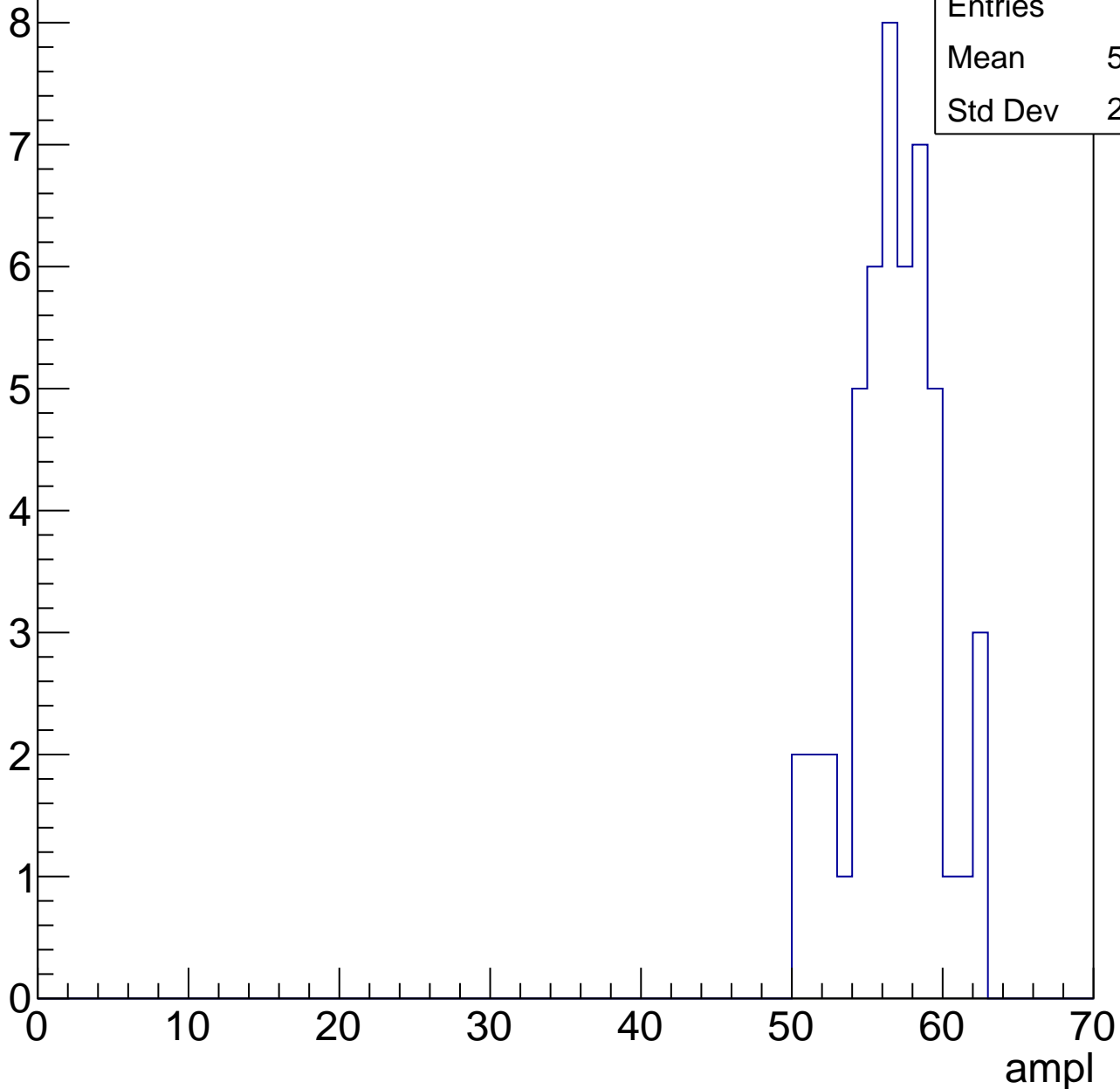


B1L003S, U21-ch81, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	56.27
Std Dev	2.905

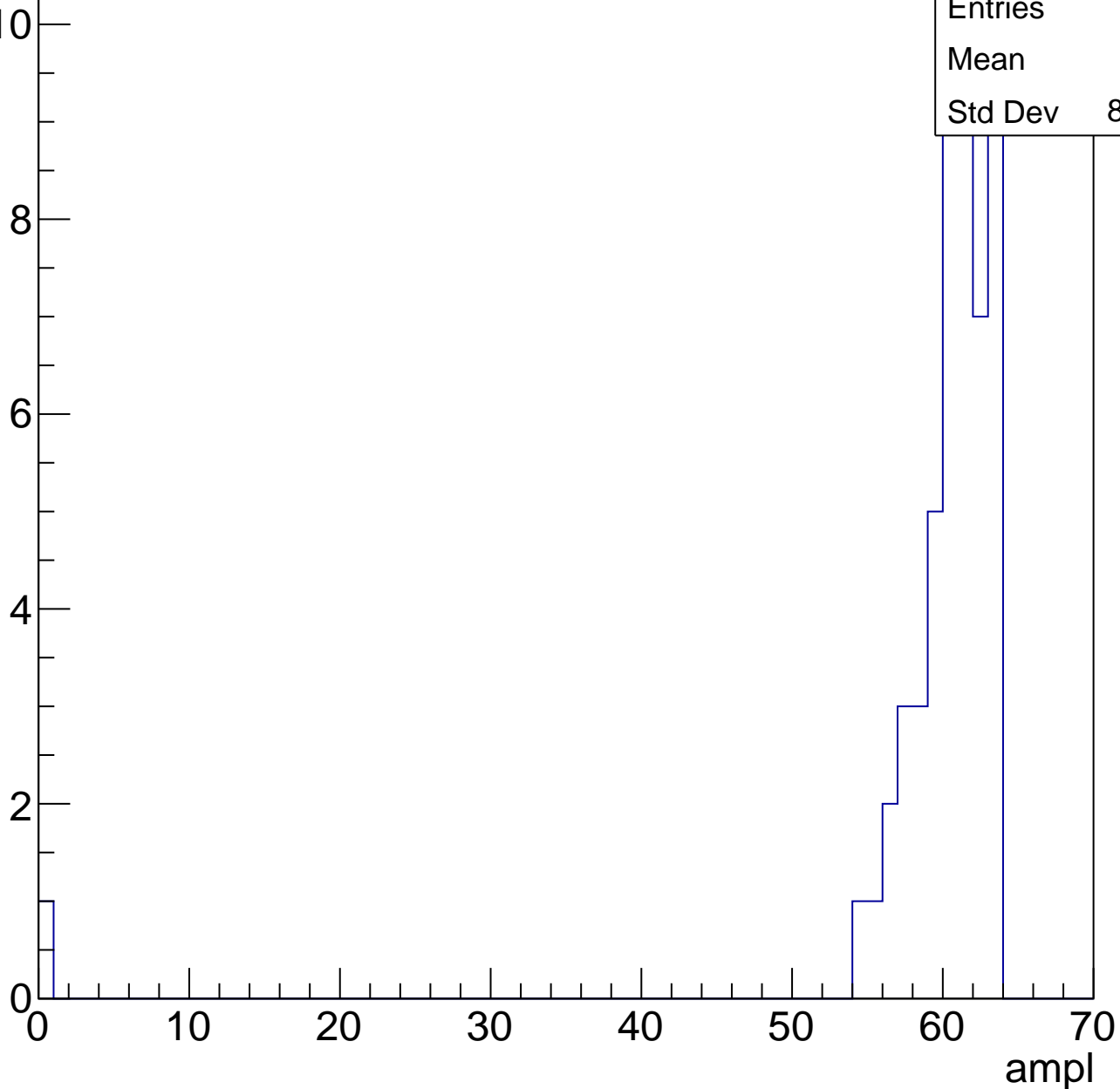


B1L003S, U21-ch81, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	59.1
Std Dev	8.657



B1L003S, U21-ch81, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch81, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch82, adc0

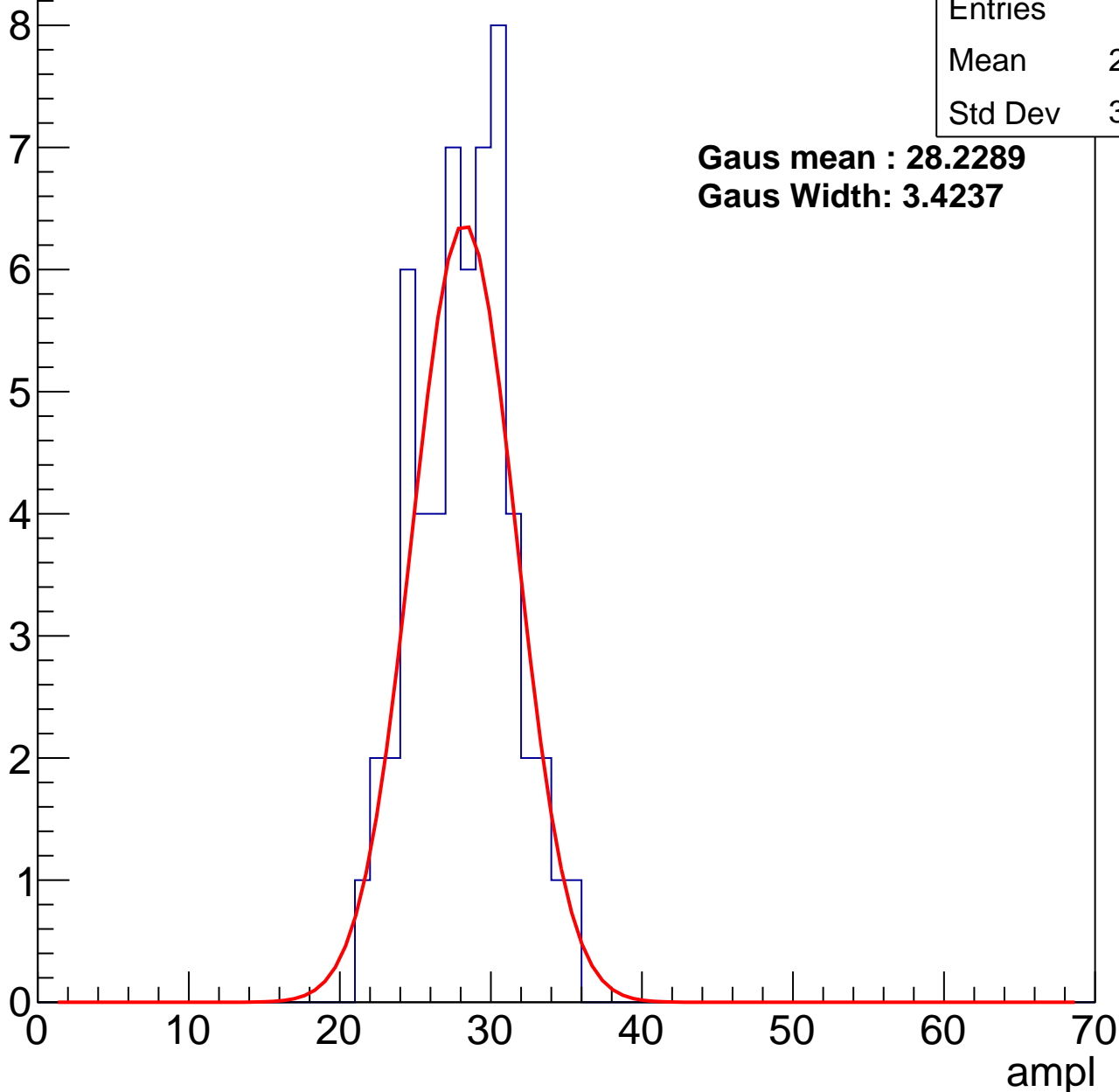
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	27.75
Std Dev	3.147

Gaus mean : 28.2289

Gaus Width: 3.4237



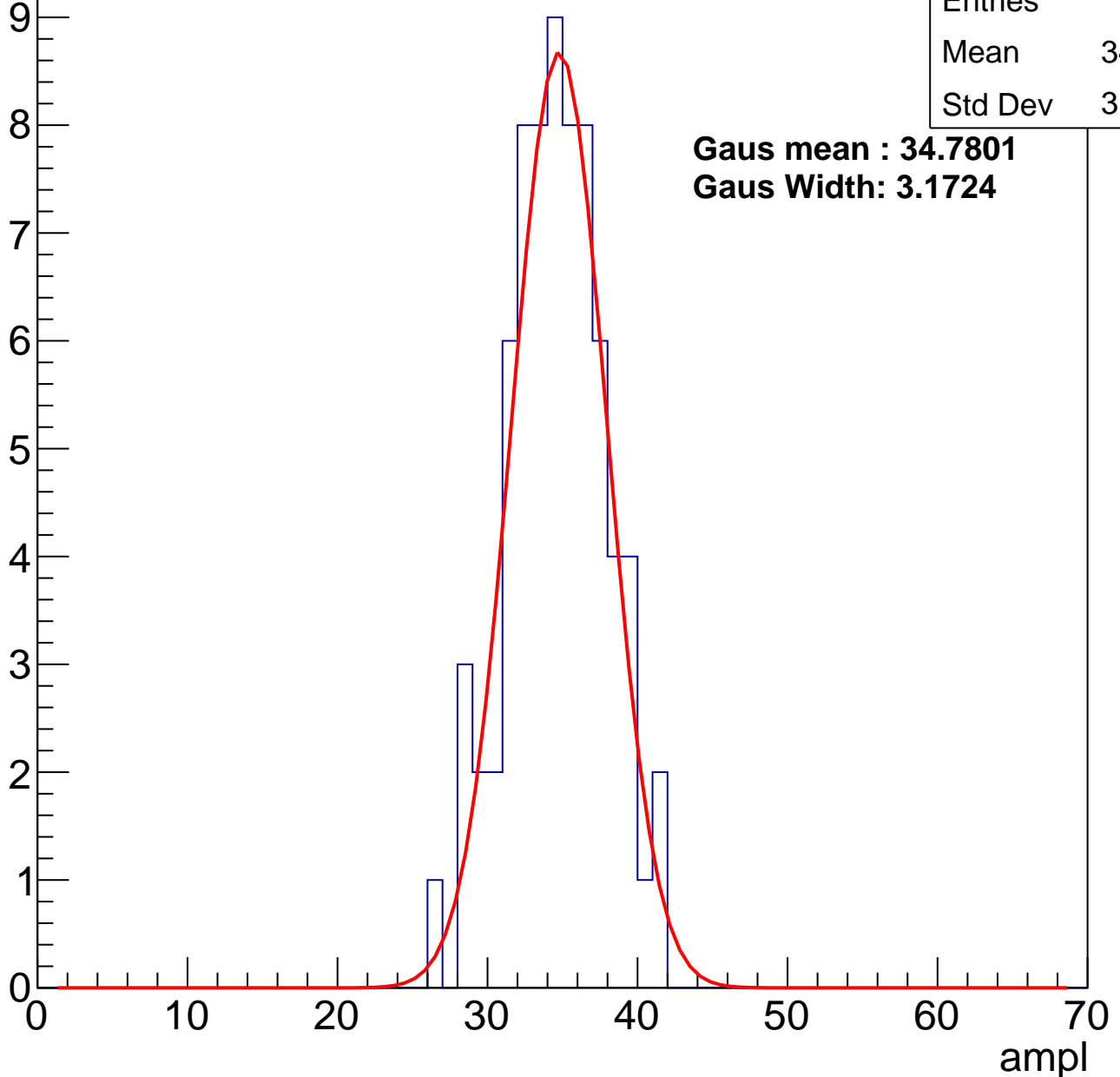
B1L003S, U21-ch82, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	34.17
Std Dev	3.202

Gaus mean : 34.7801
Gaus Width: 3.1724



B1L003S, U21-ch82, adc2

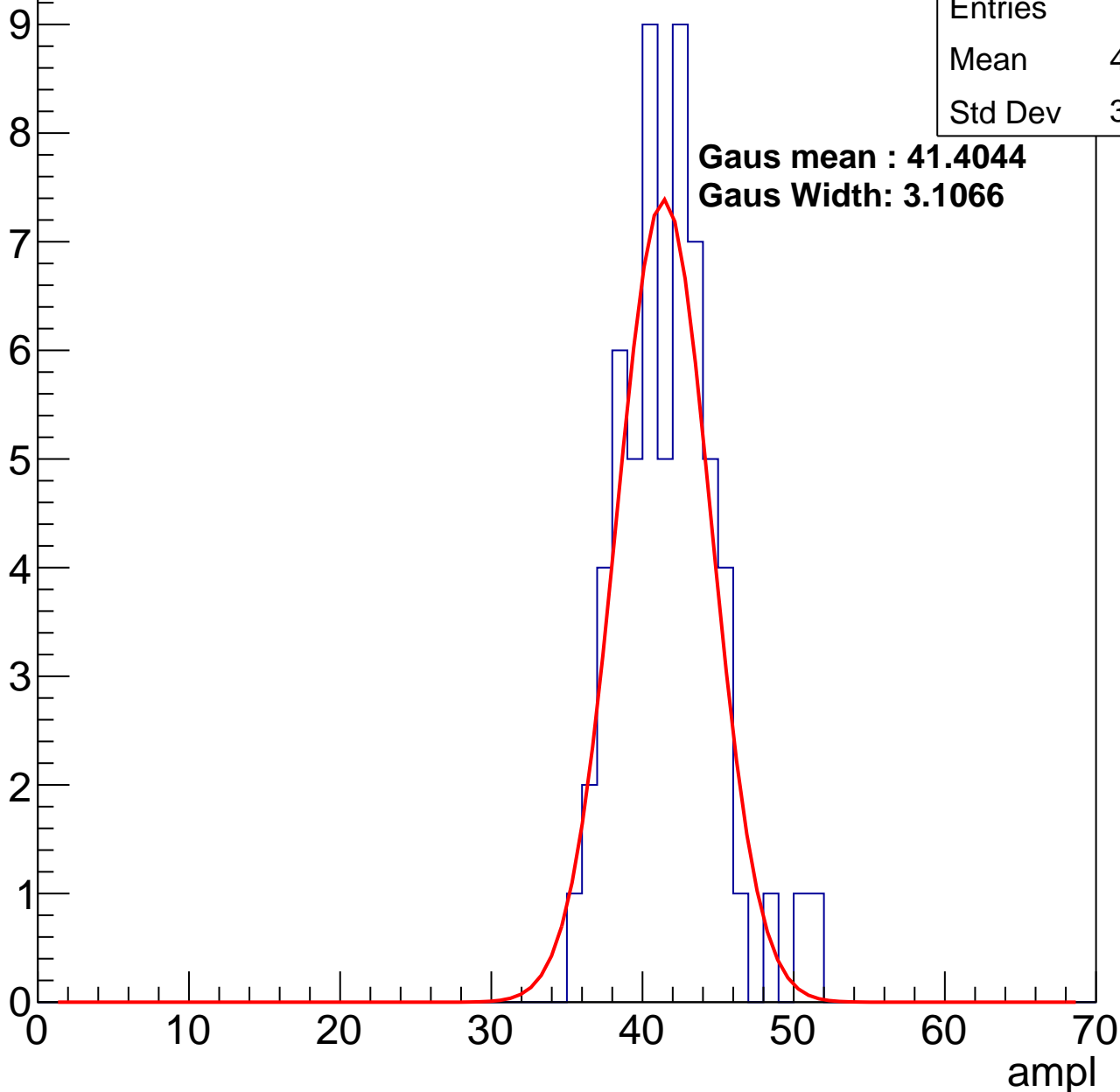
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	41.26
Std Dev	3.213

Gaus mean : 41.4044

Gaus Width: 3.1066



B1L003S, U21-ch82, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	65
Mean	47.14
Std Dev	3.22

Entry

10

8

6

4

2

0

0

10

20

30

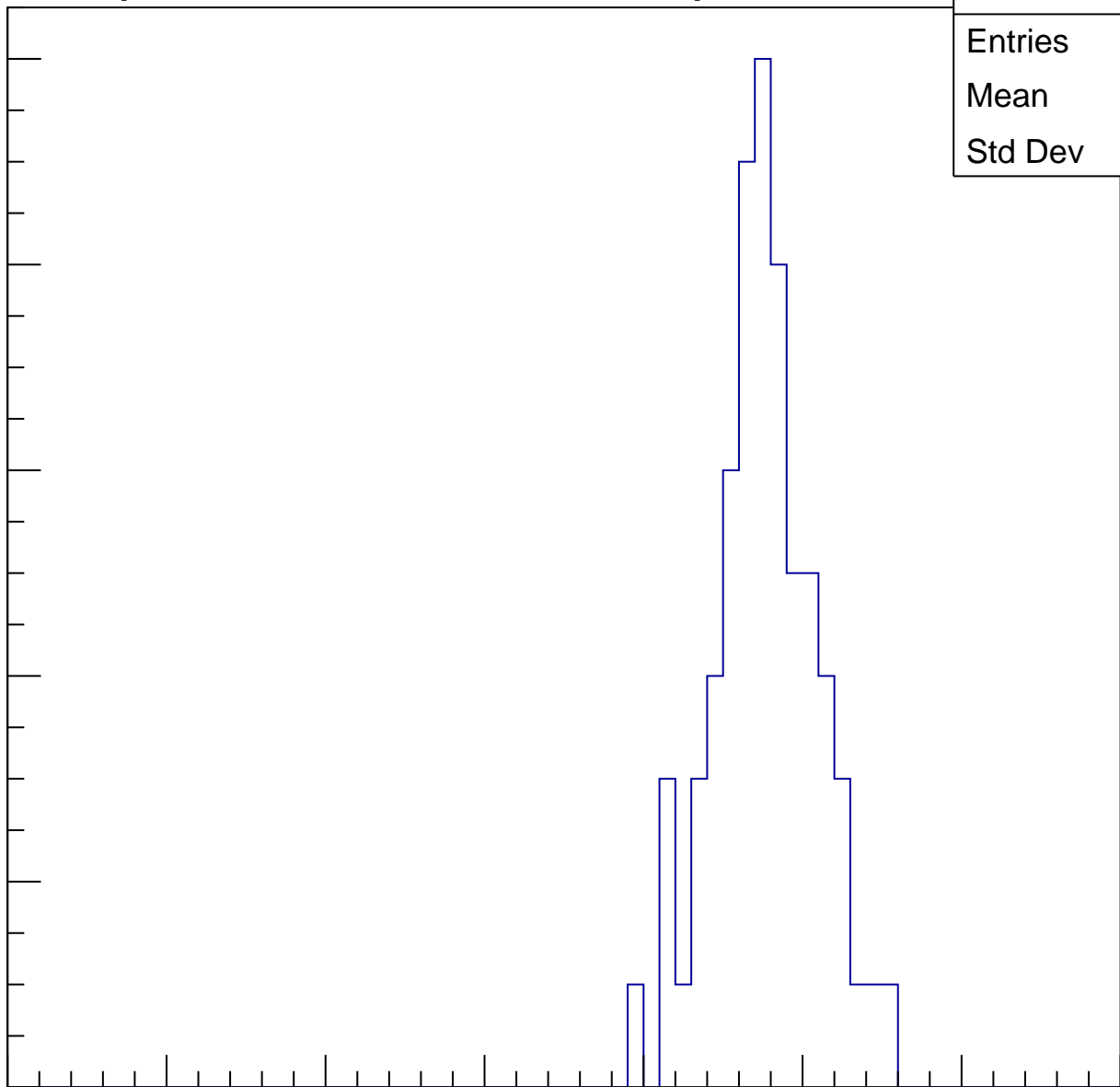
40

50

60

70

ampl

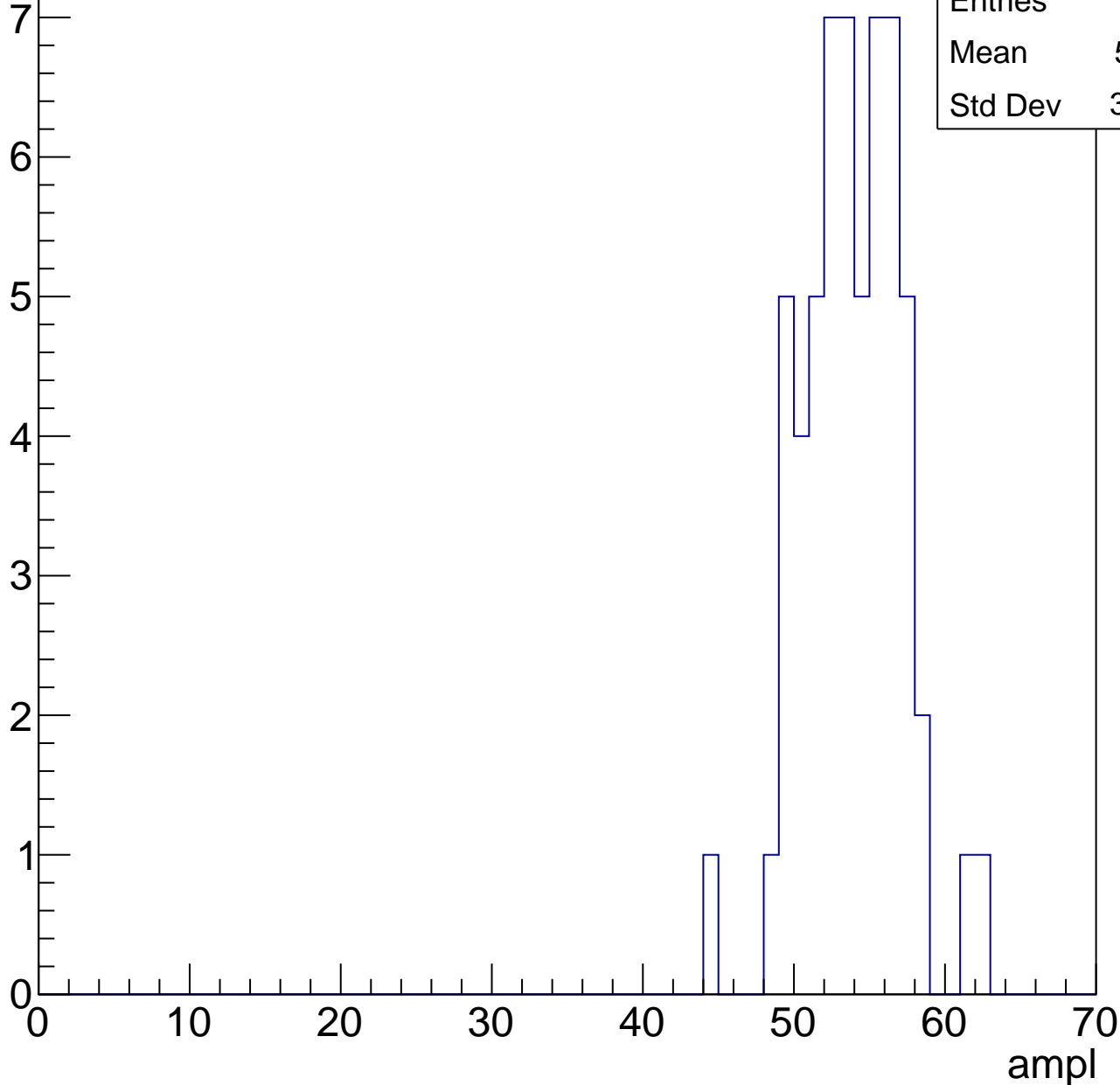


B1L003S, U21-ch82, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	53.41
Std Dev	3.243

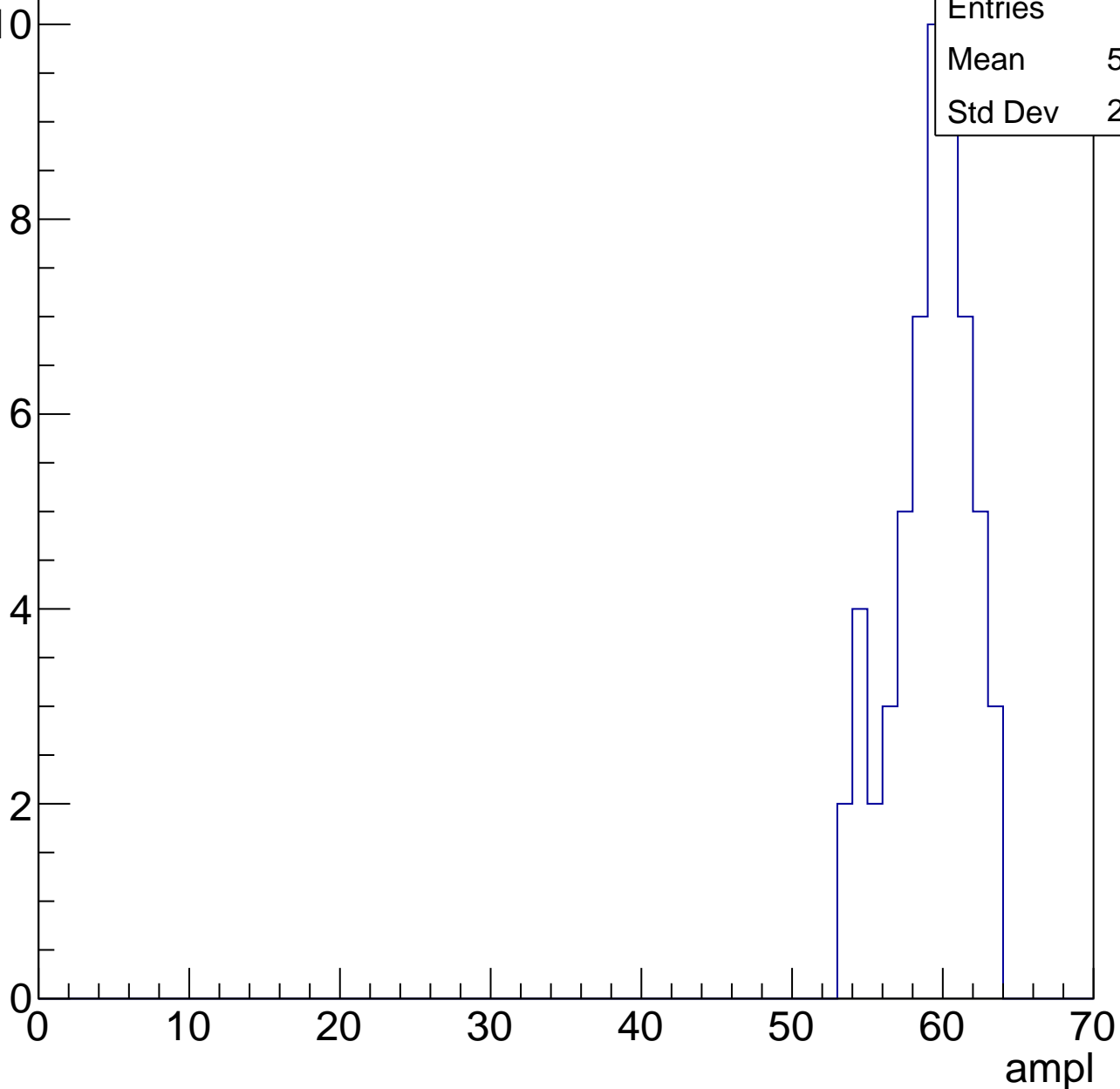


B1L003S, U21-ch82, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	58.72
Std Dev	2.594

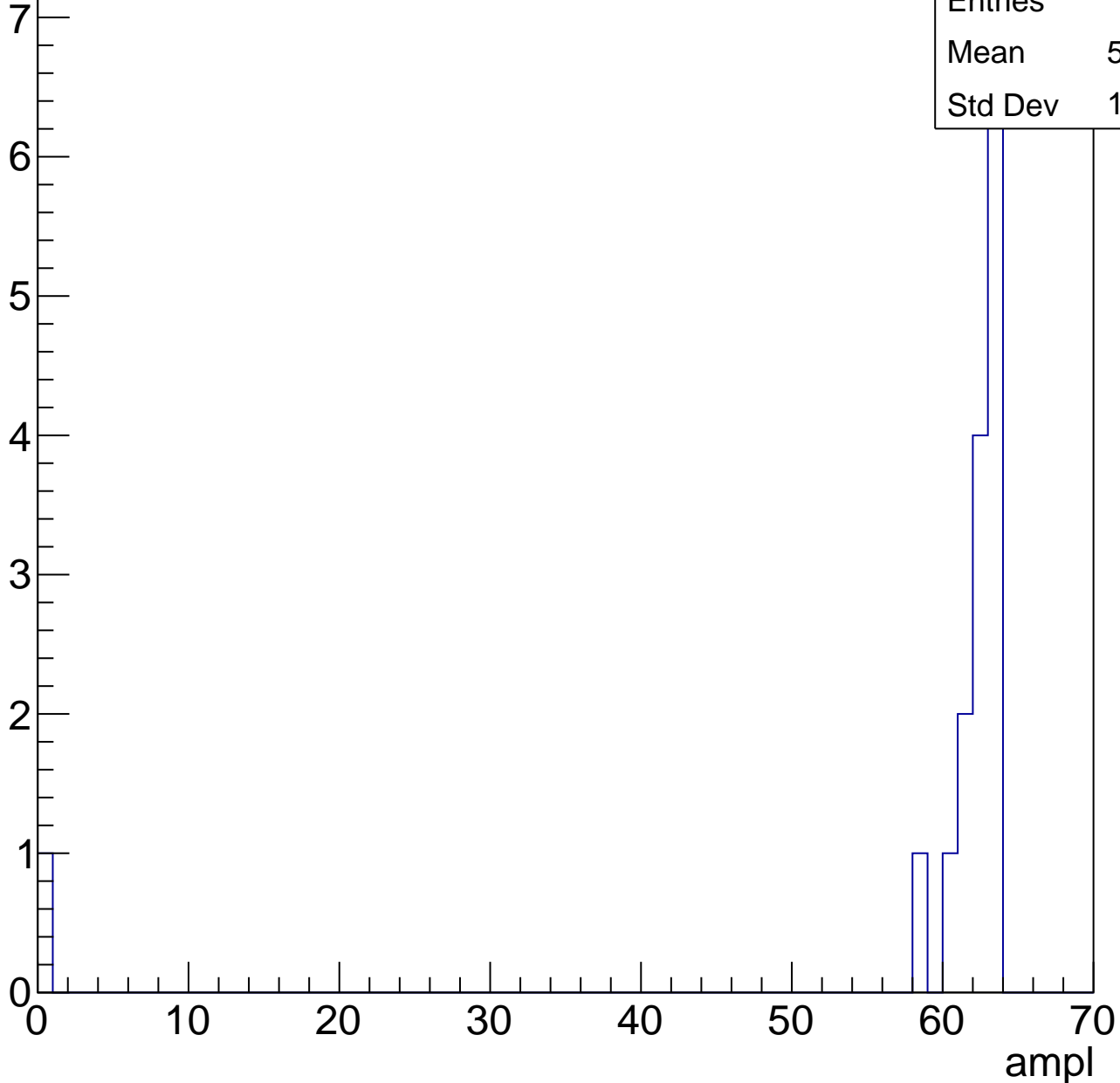


B1L003S, U21-ch82, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	58.06
Std Dev	15.05



B1L003S, U21-ch82, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch83, adc0

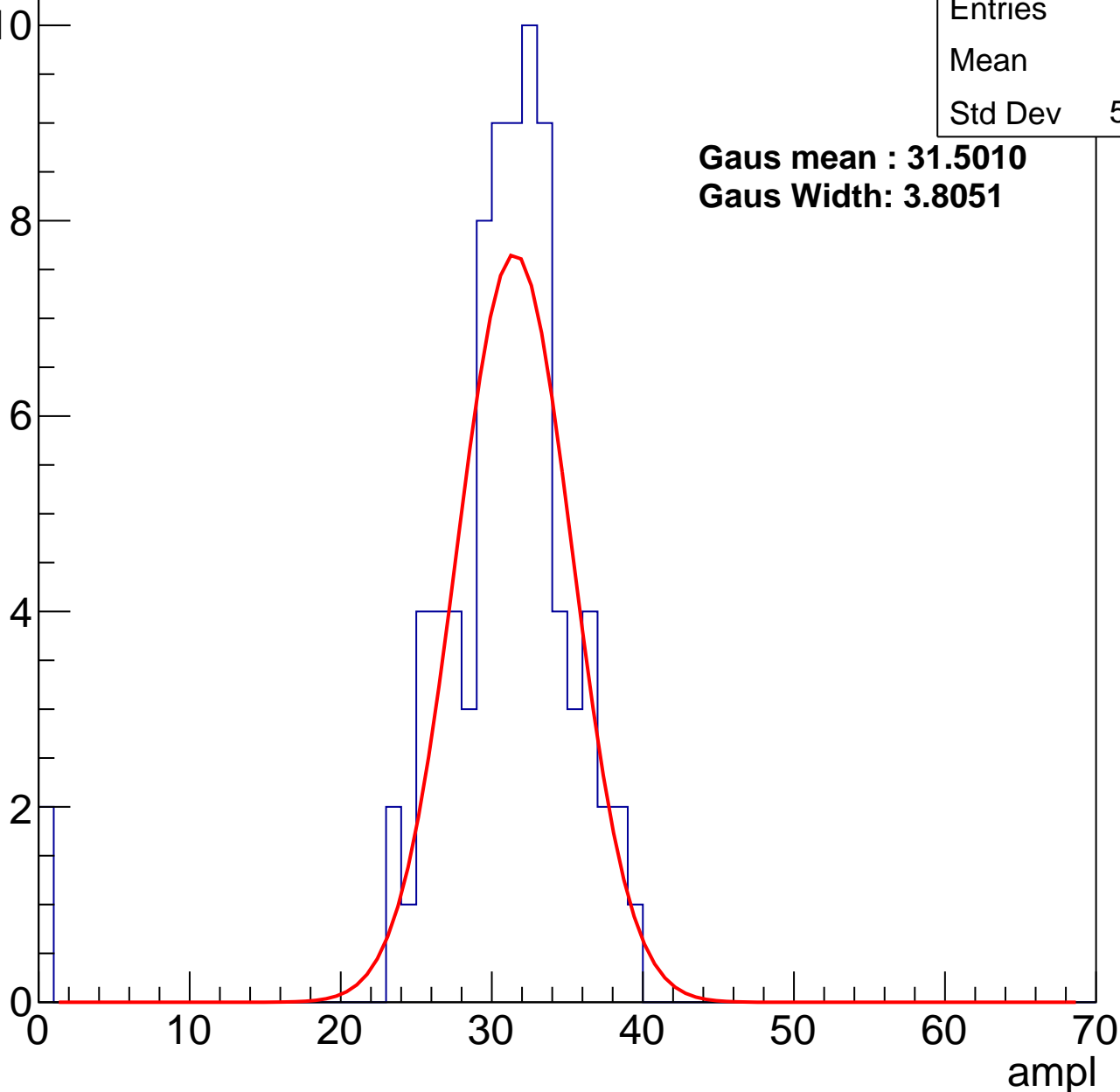
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	30.1
Std Dev	5.956

Gaus mean : 31.5010

Gaus Width: 3.8051



B1L003S, U21-ch83, adc1

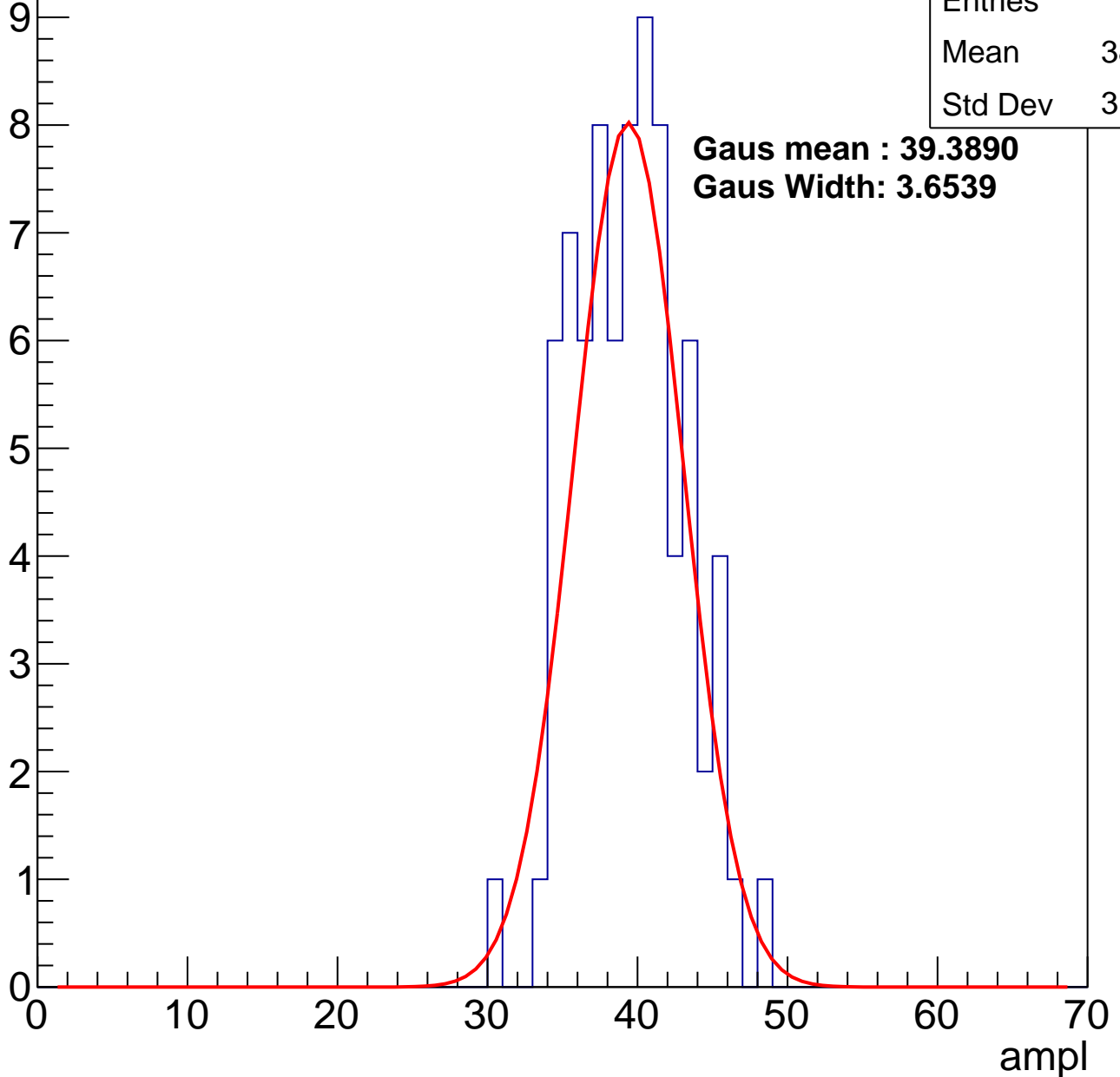
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	38.97
Std Dev	3.523

Gaus mean : 39.3890

Gaus Width: 3.6539



B1L003S, U21-ch83, adc2

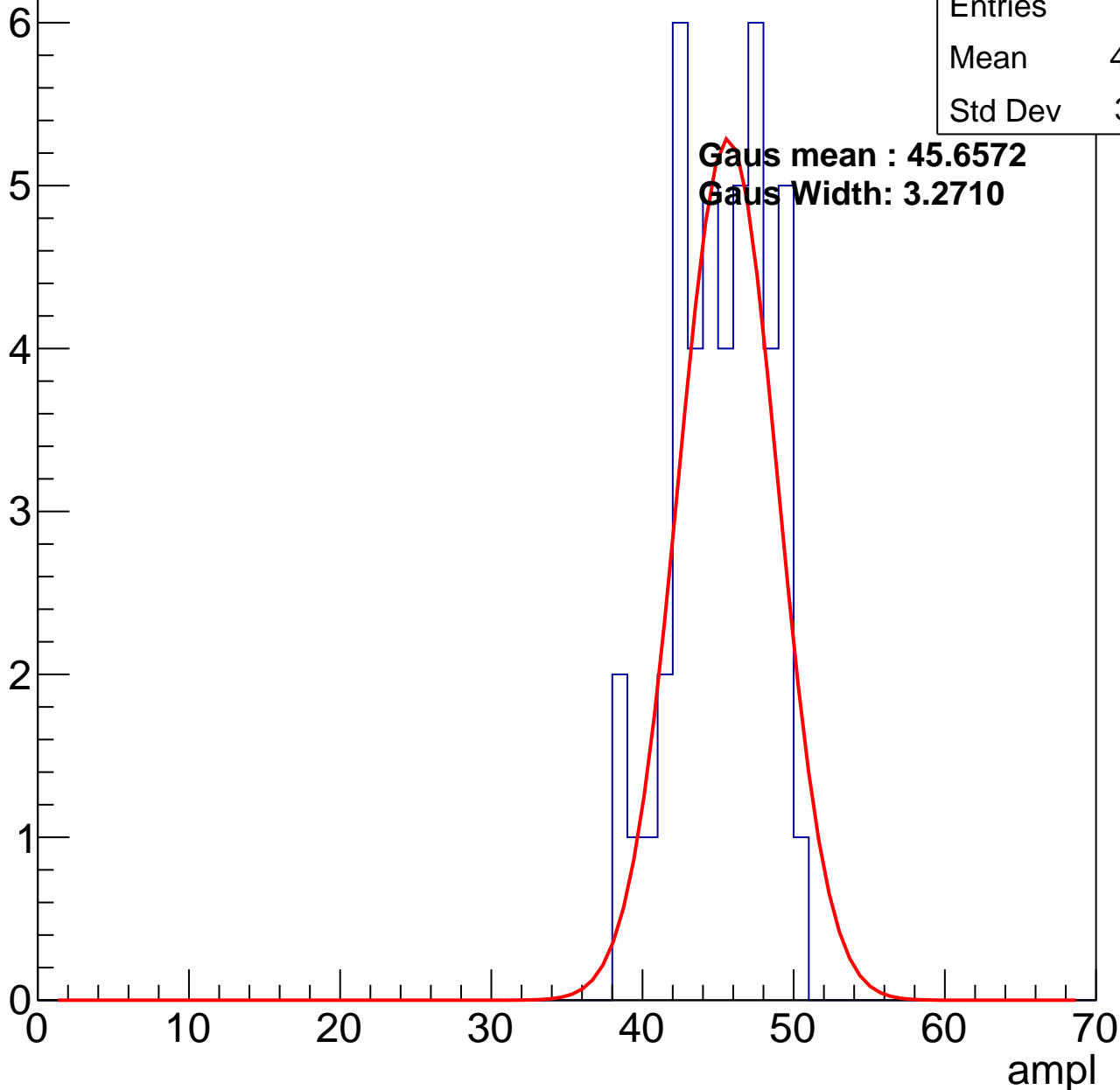
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	44.78
Std Dev	3.071

Gaus mean : 45.6572

Gaus Width: 3.2710

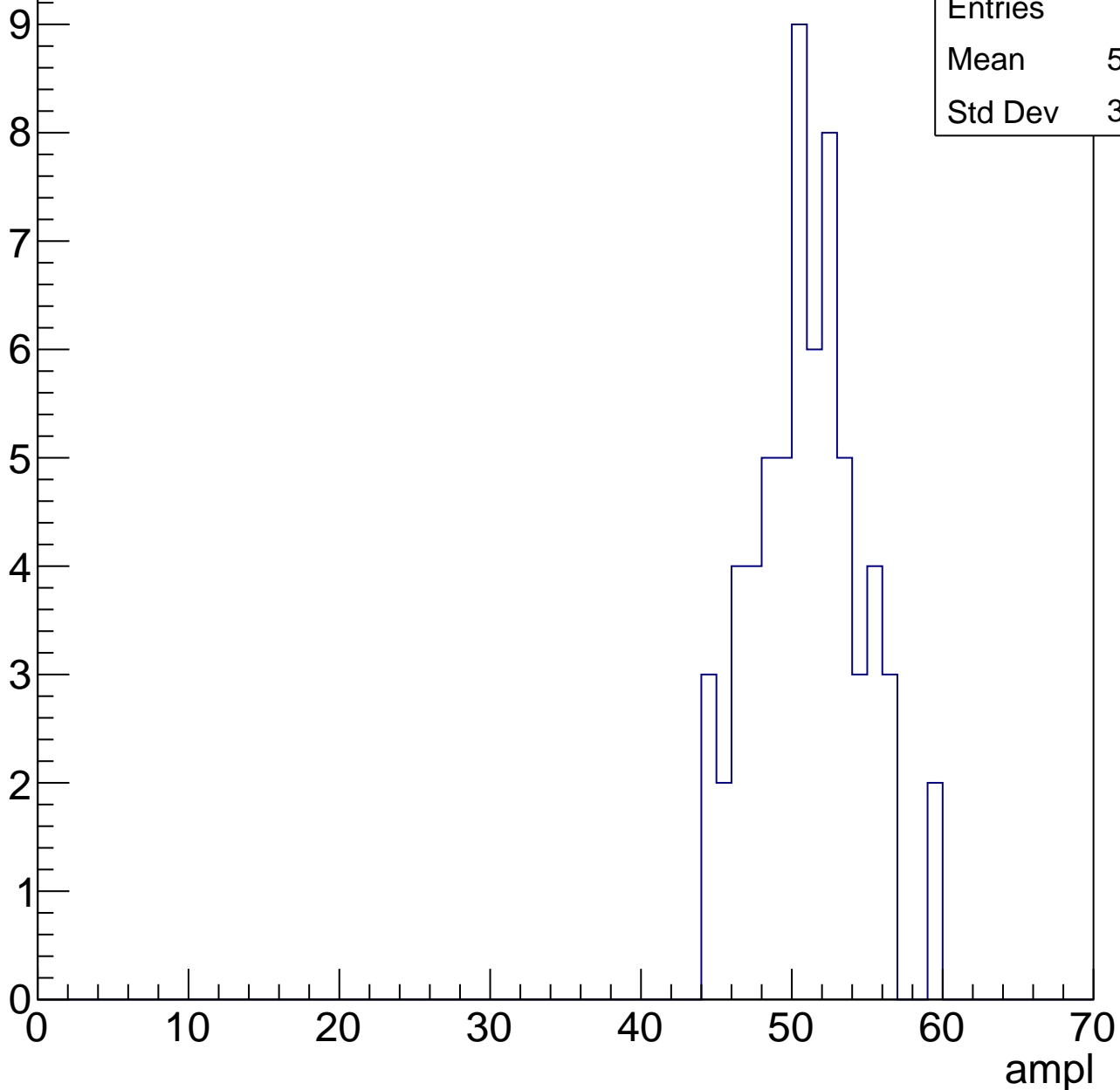


B1L003S, U21-ch83, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	50.54
Std Dev	3.486



B1L003S, U21-ch83, adc4

calib_packv5_042523_0143.root, FC#13, port D2

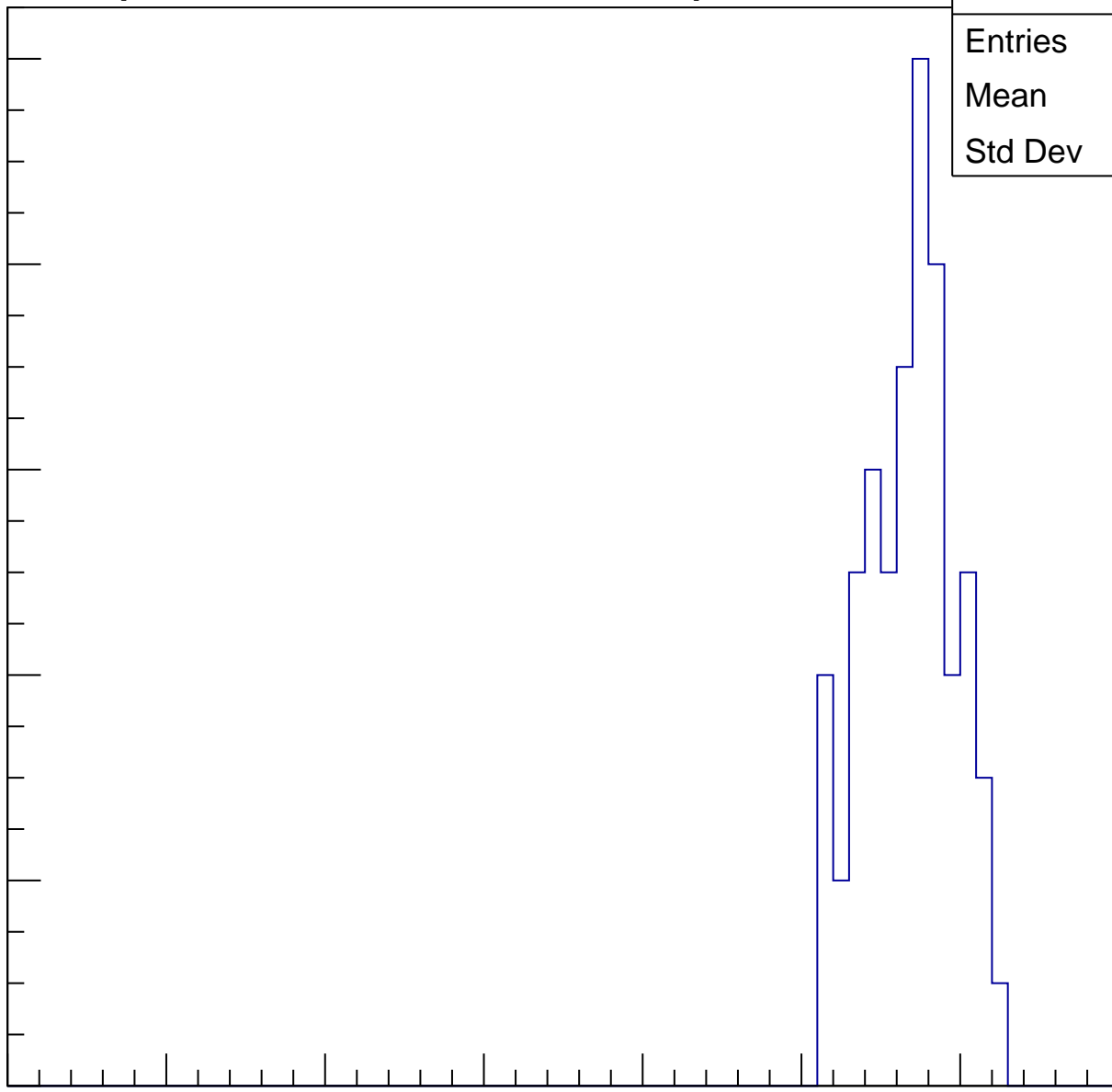
Entries	60
Mean	56.32
Std Dev	2.796

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

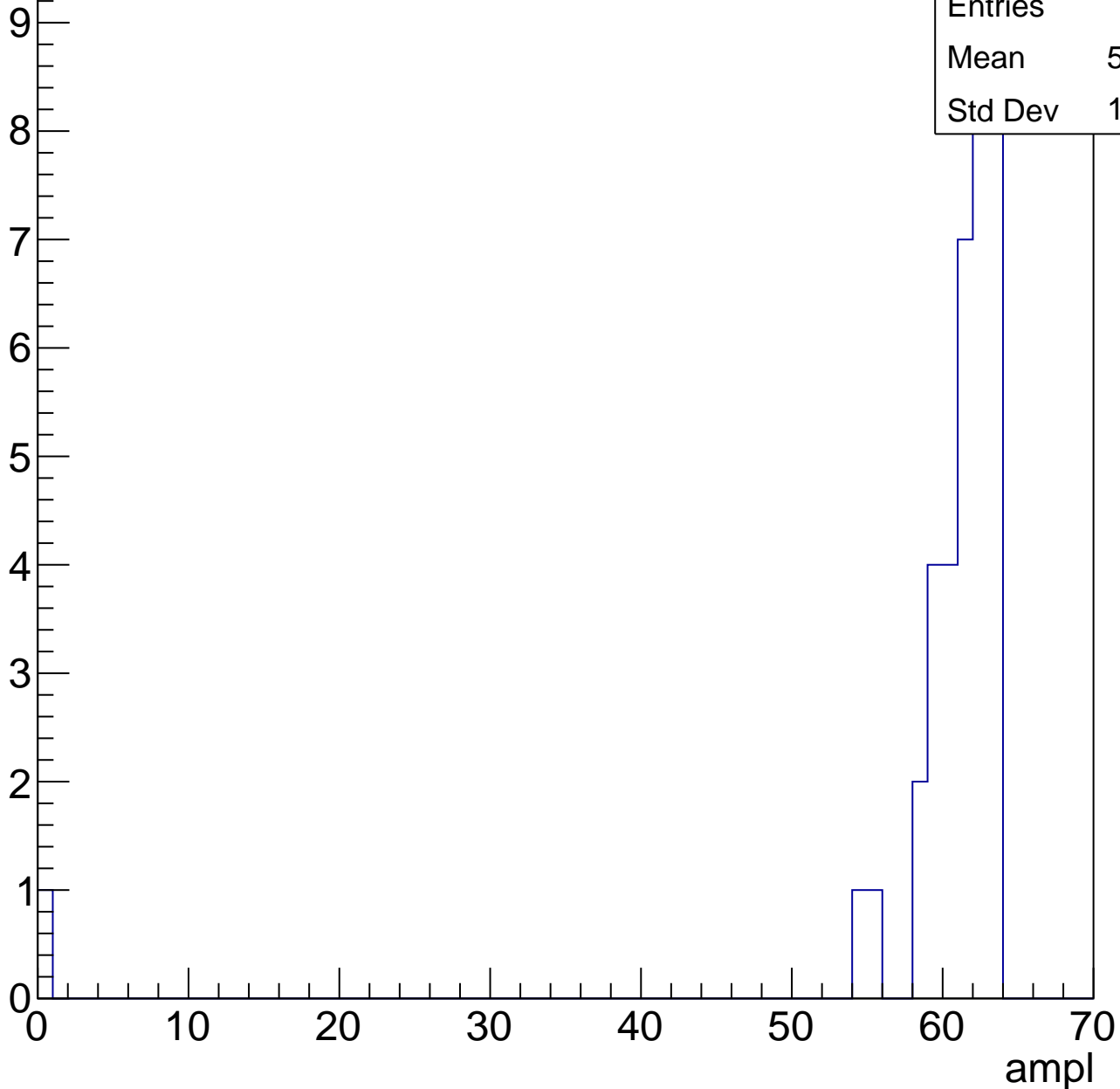


B1L003S, U21-ch83, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

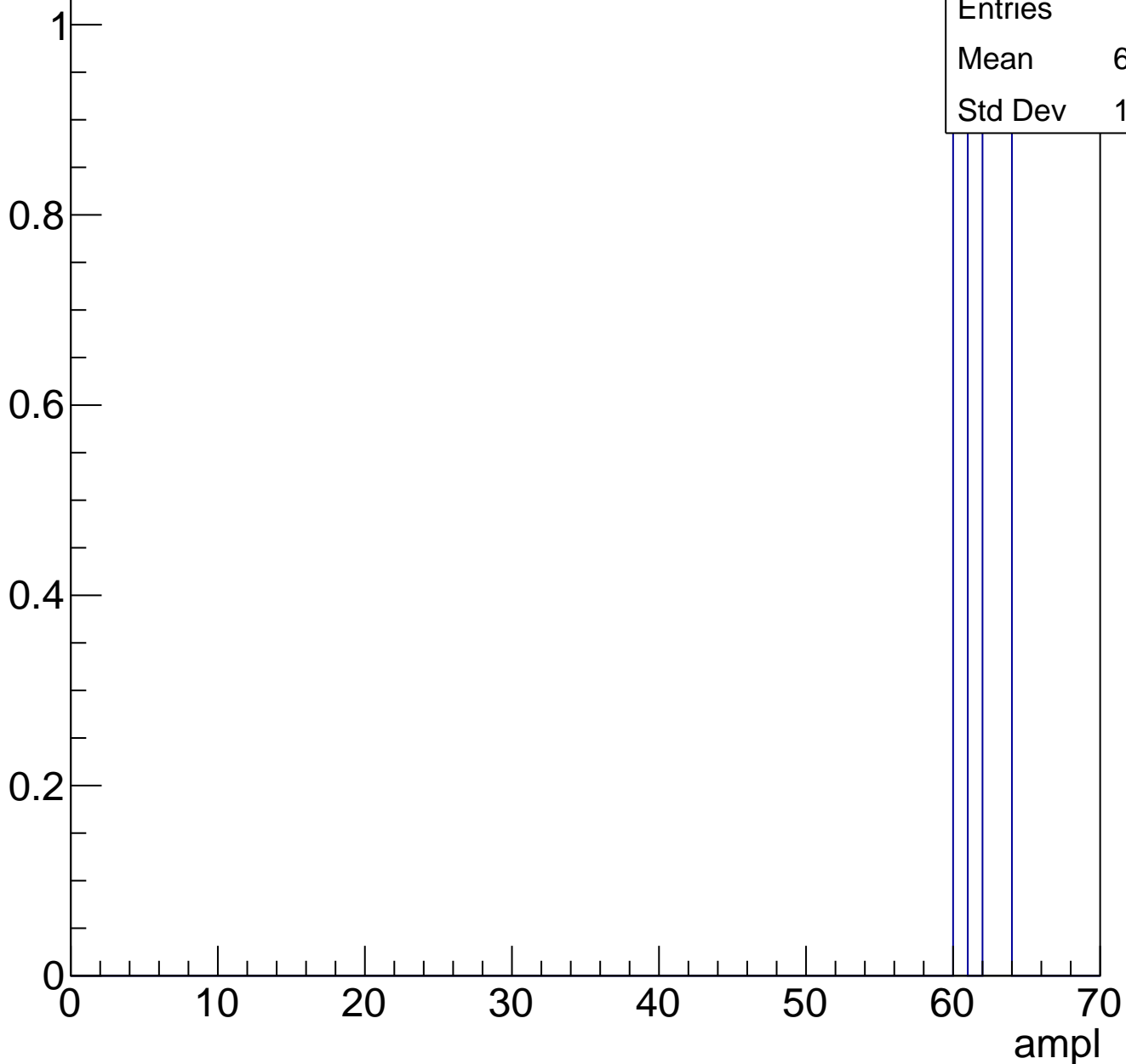
Entries	37
Mean	59.22
Std Dev	10.09



B1L003S, U21-ch83, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch83, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch84, adc0

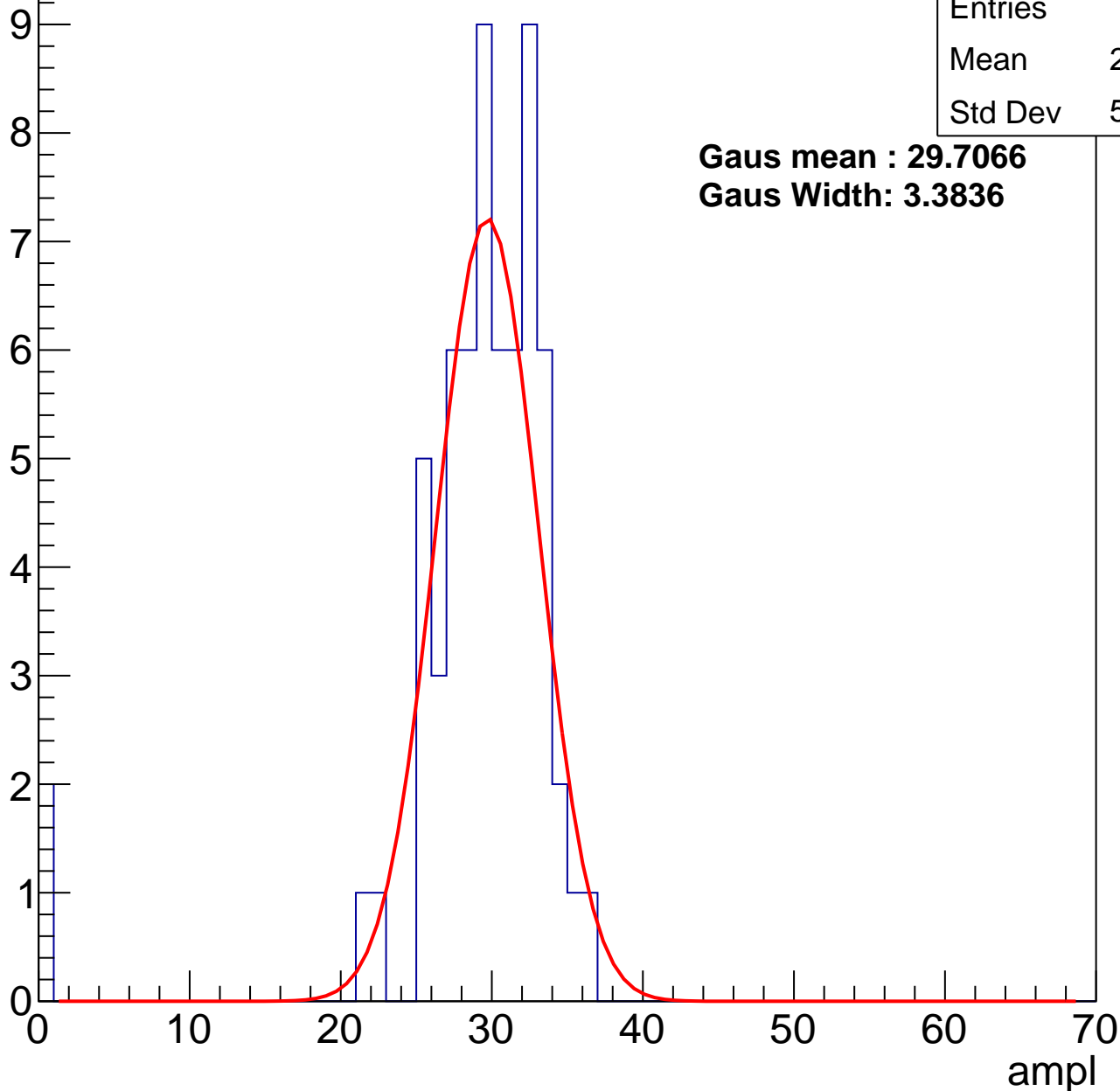
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.56
Std Dev	5.945

Gaus mean : 29.7066

Gaus Width: 3.3836



B1L003S, U21-ch84, adc1

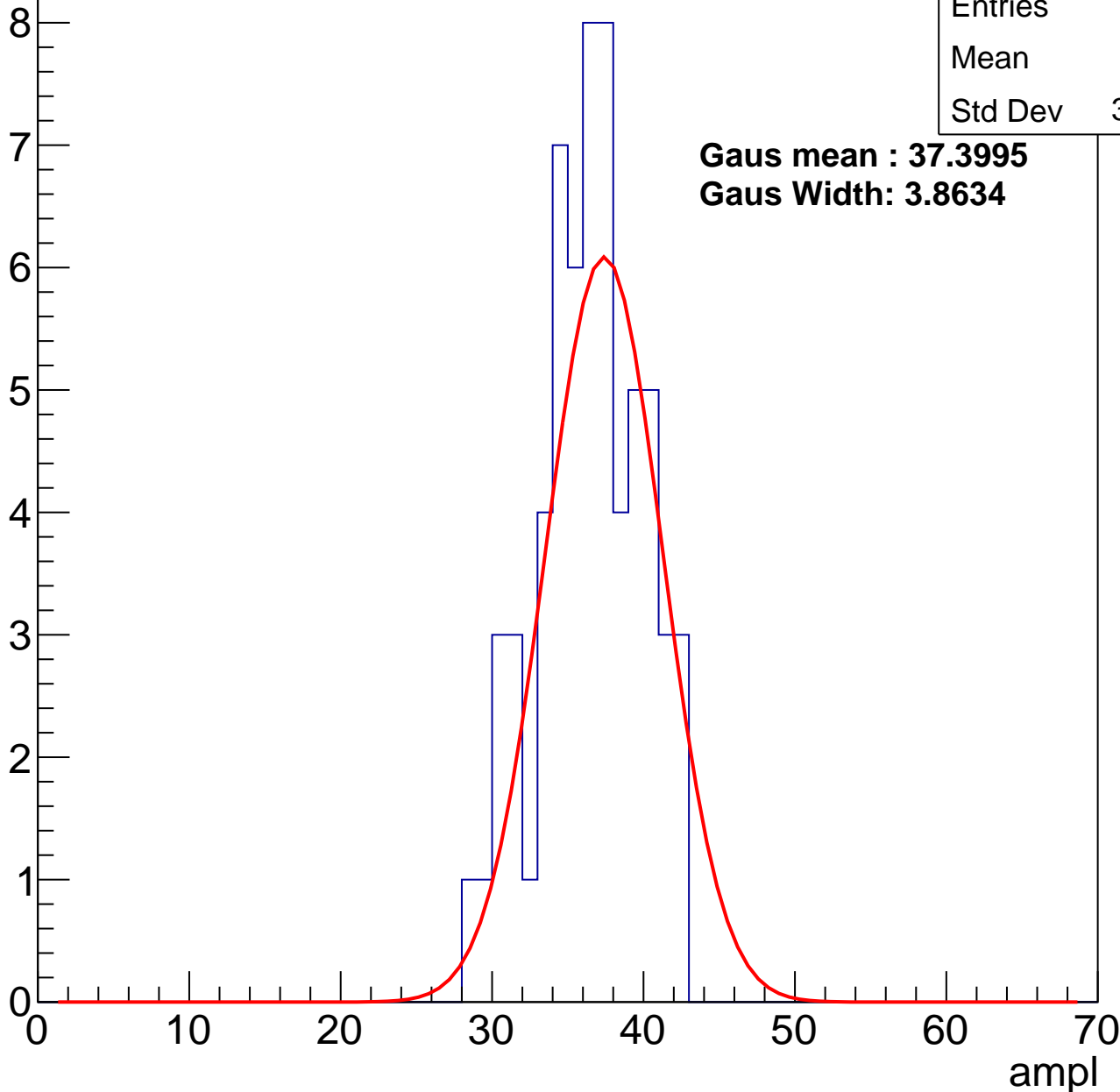
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36
Std Dev	3.394

Gaus mean : 37.3995

Gaus Width: 3.8634



B1L003S, U21-ch84, adc2

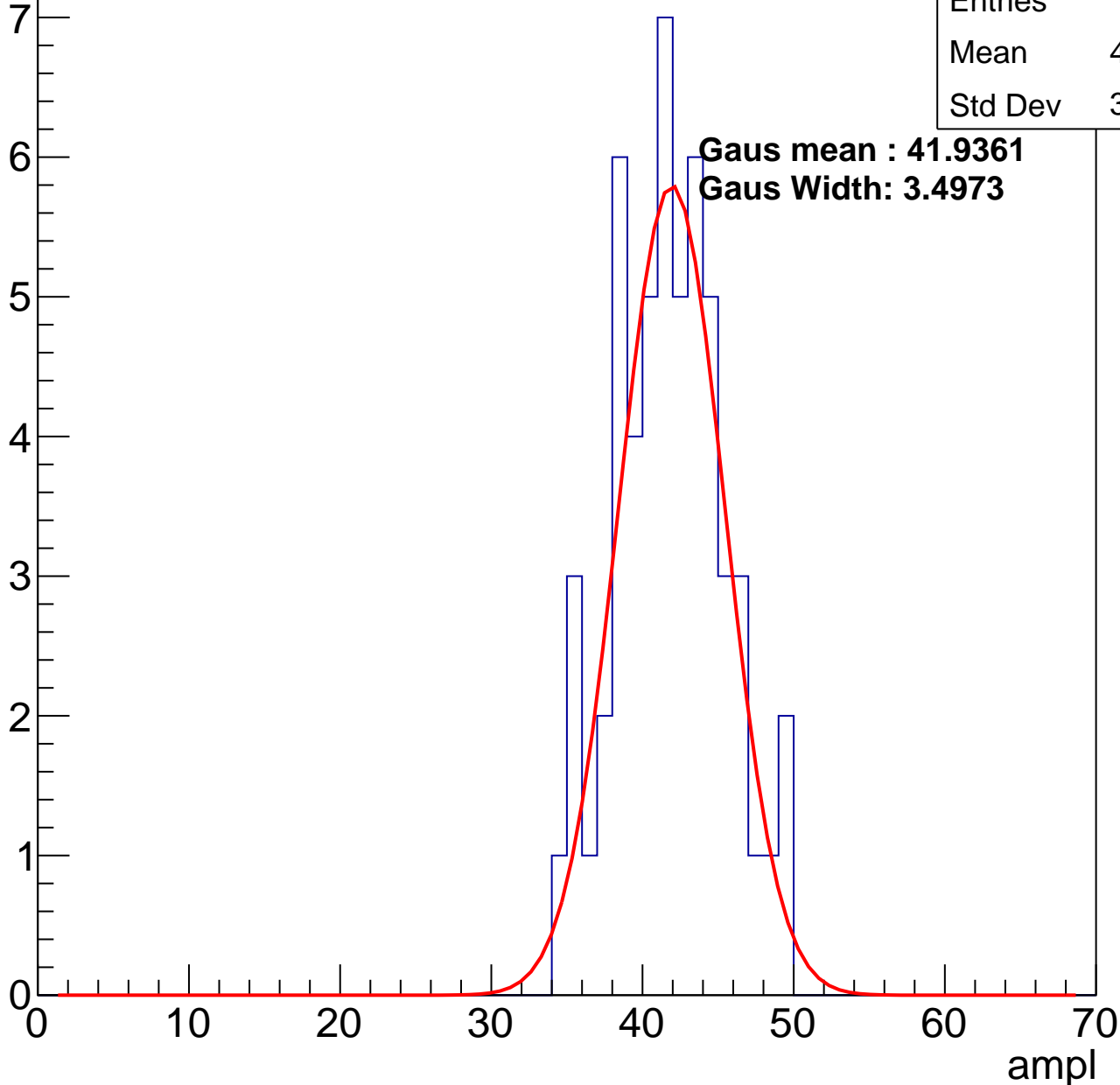
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	41.35
Std Dev	3.548

Gaus mean : 41.9361

Gaus Width: 3.4973

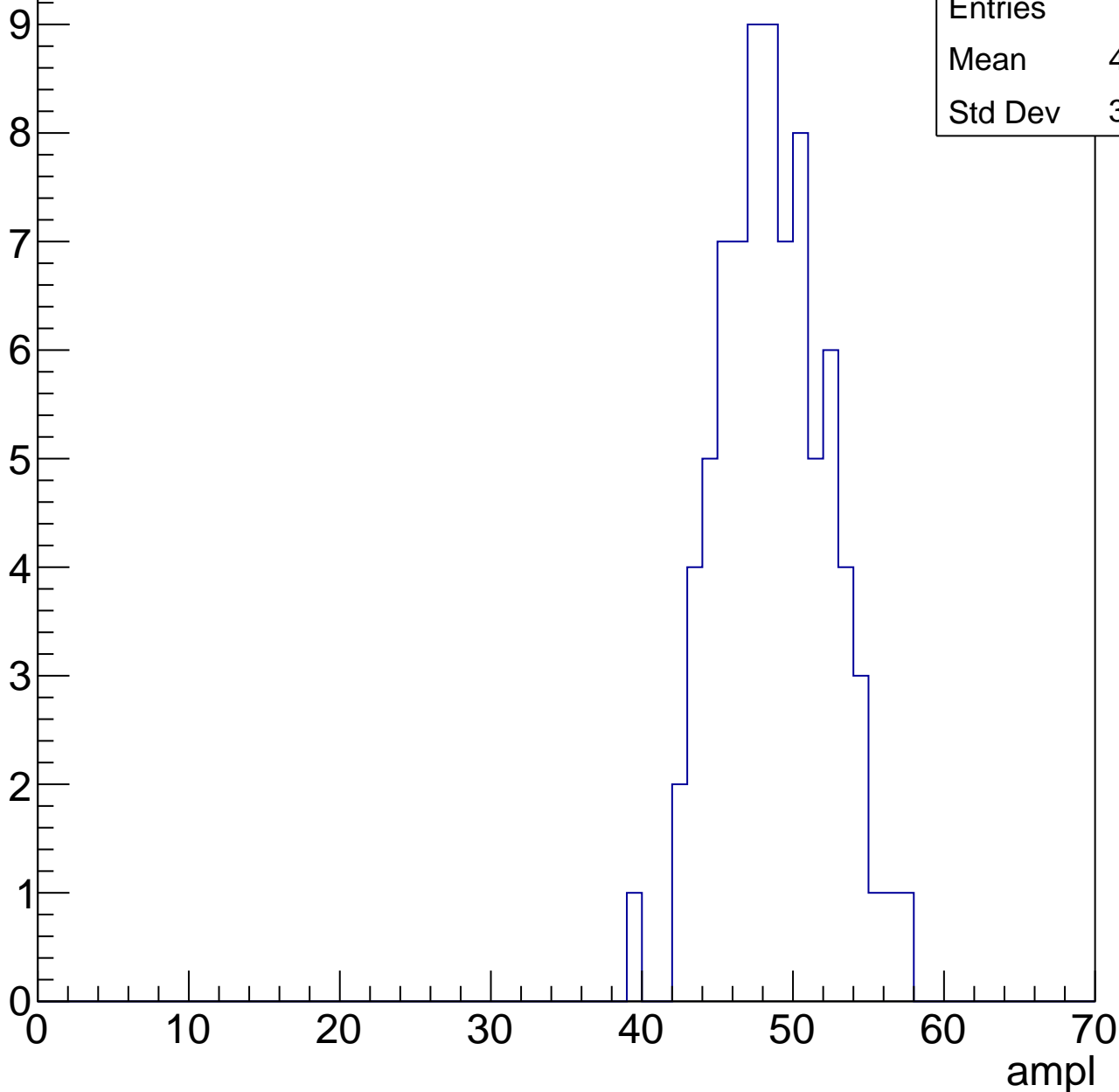


B1L003S, U21-ch84, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	48.24
Std Dev	3.554

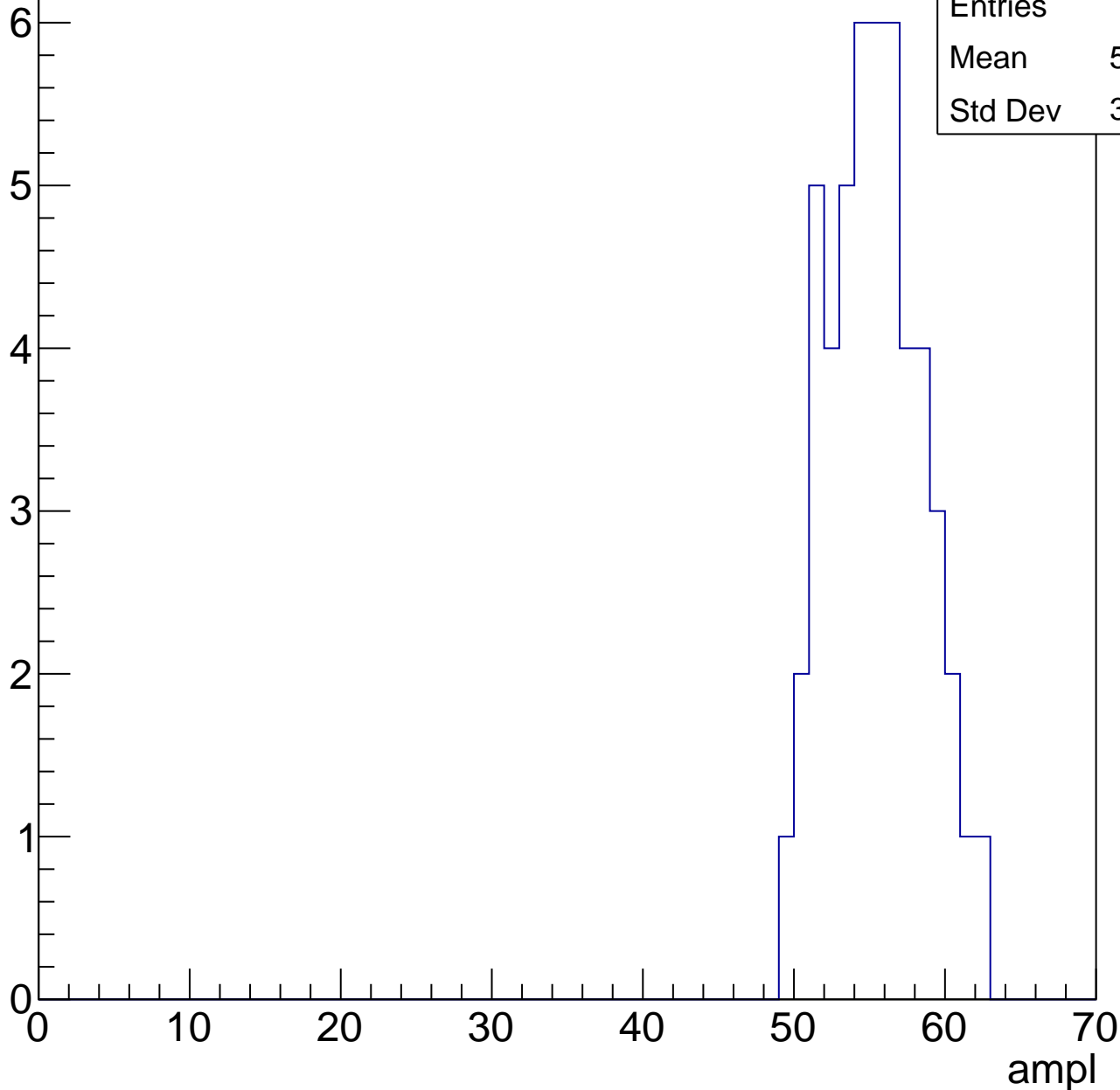


B1L003S, U21-ch84, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	54.94
Std Dev	3.062

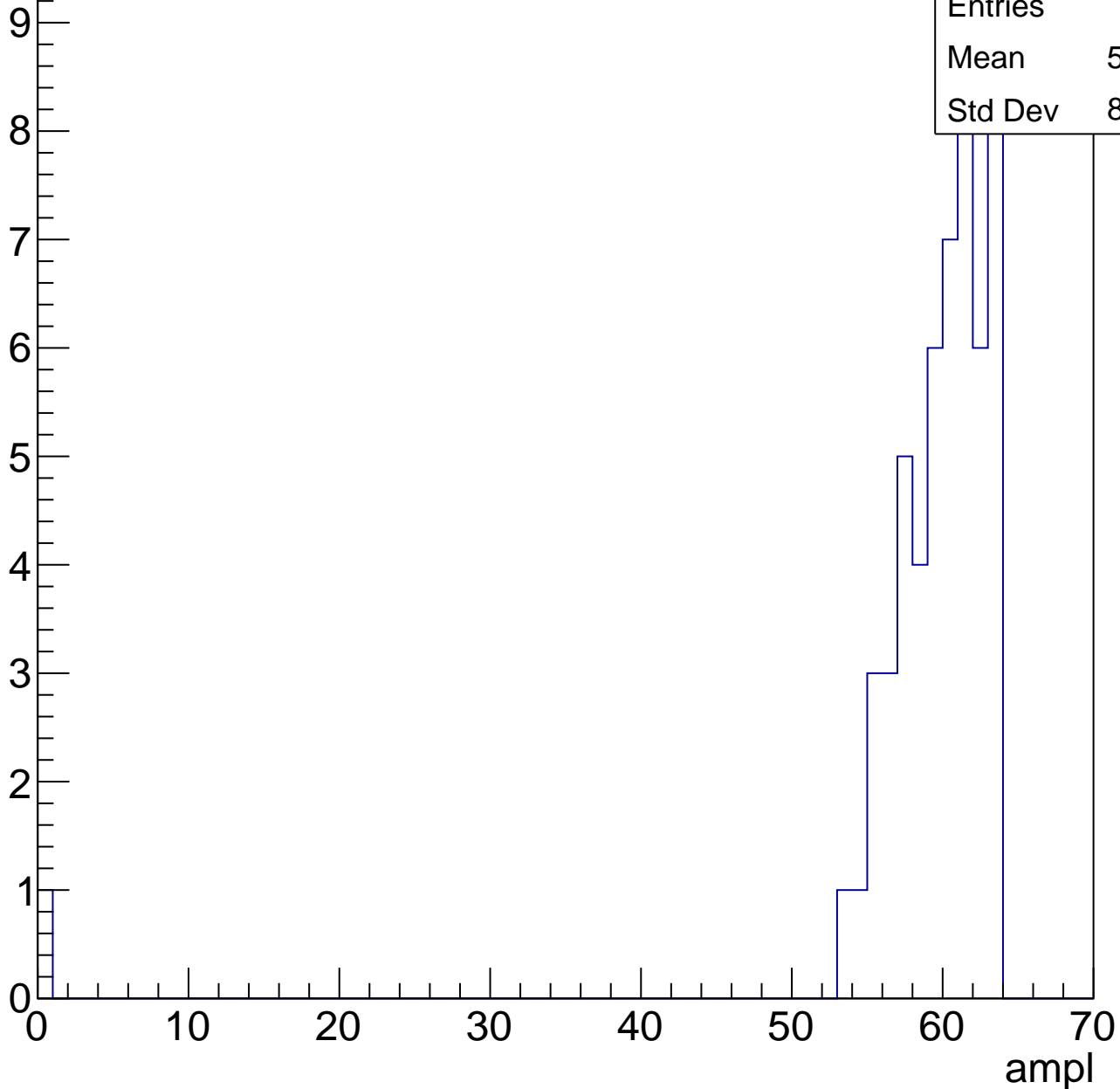


B1L003S, U21-ch84, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.44
Std Dev	8.443



B1L003S, U21-ch84, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

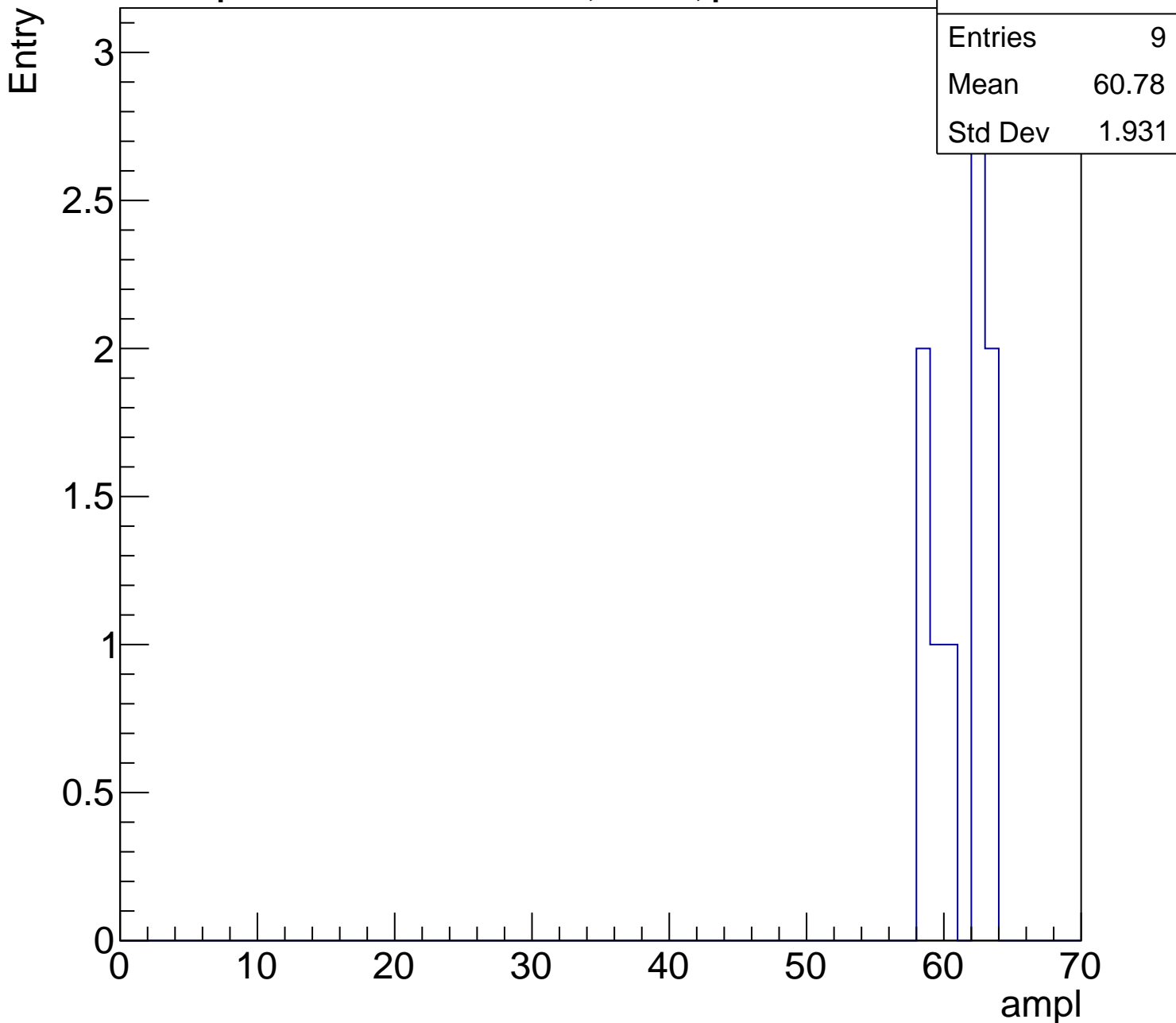
9

Mean

60.78

Std Dev

1.931



B1L003S, U21-ch84, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch85, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	78
Mean	29.91
Std Dev	4.751

Gaus mean : 30.7443

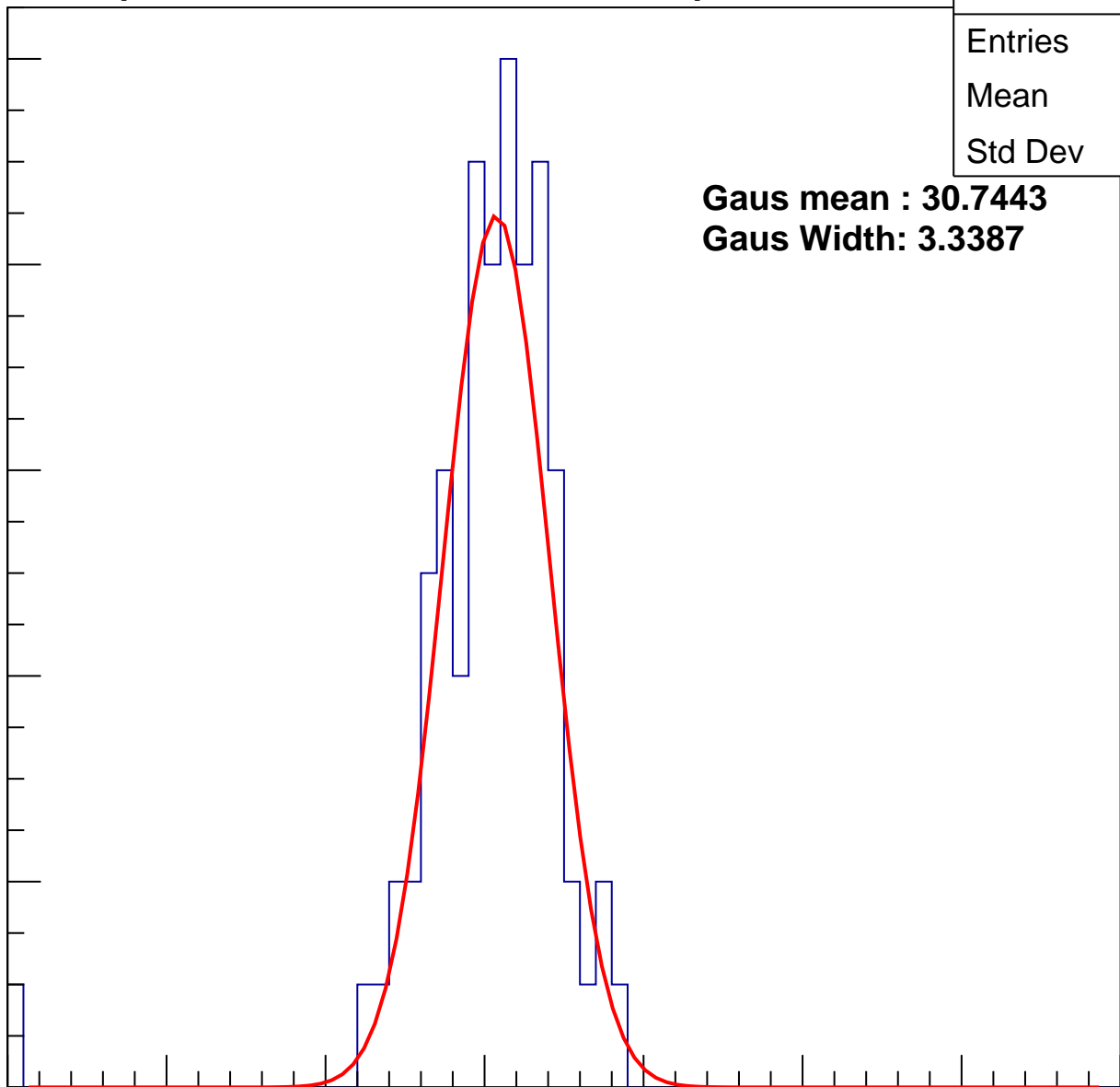
Gaus Width: 3.3387

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch85, adc1

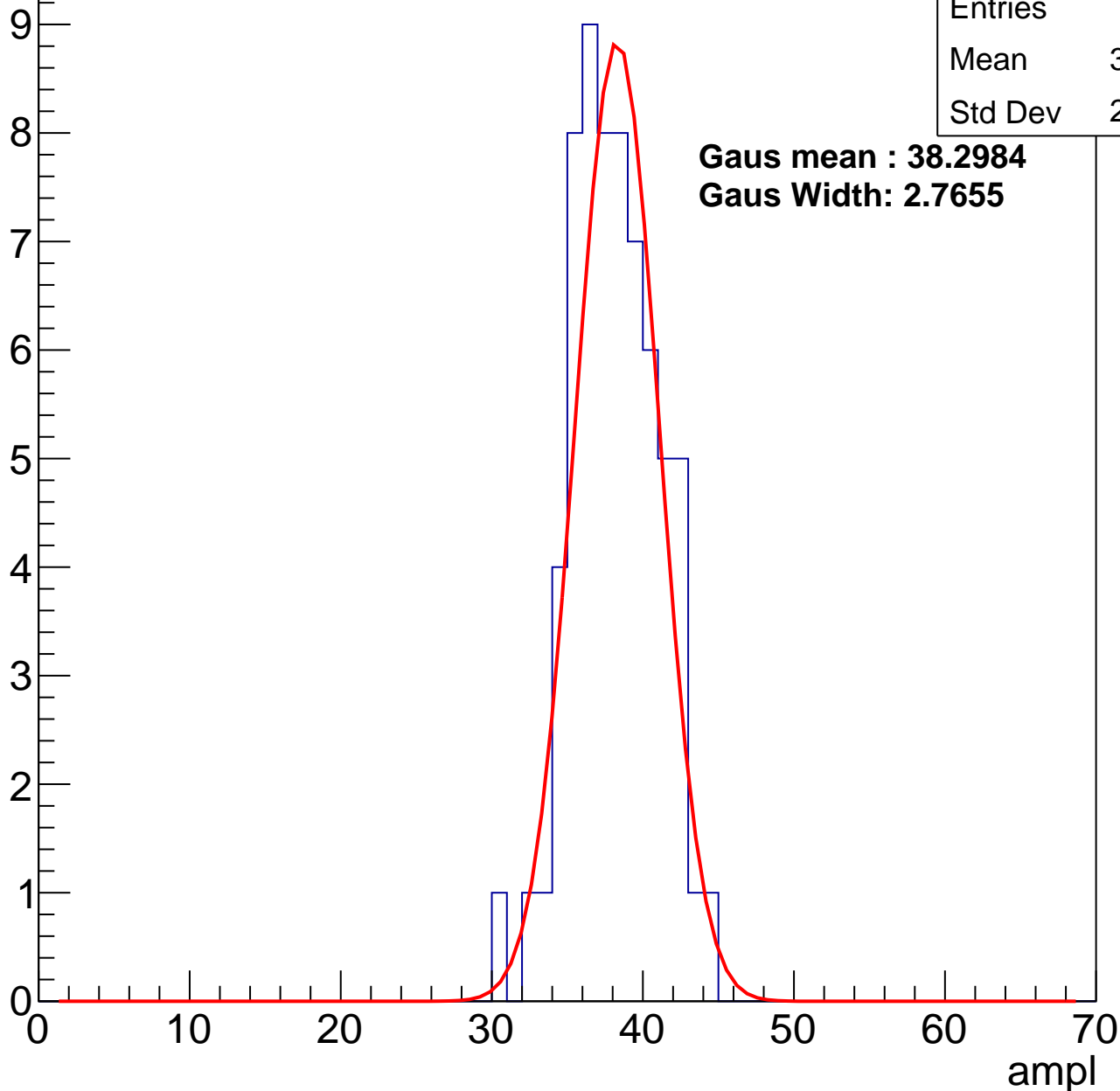
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	37.69
Std Dev	2.817

Gaus mean : 38.2984

Gaus Width: 2.7655



B1L003S, U21-ch85, adc2

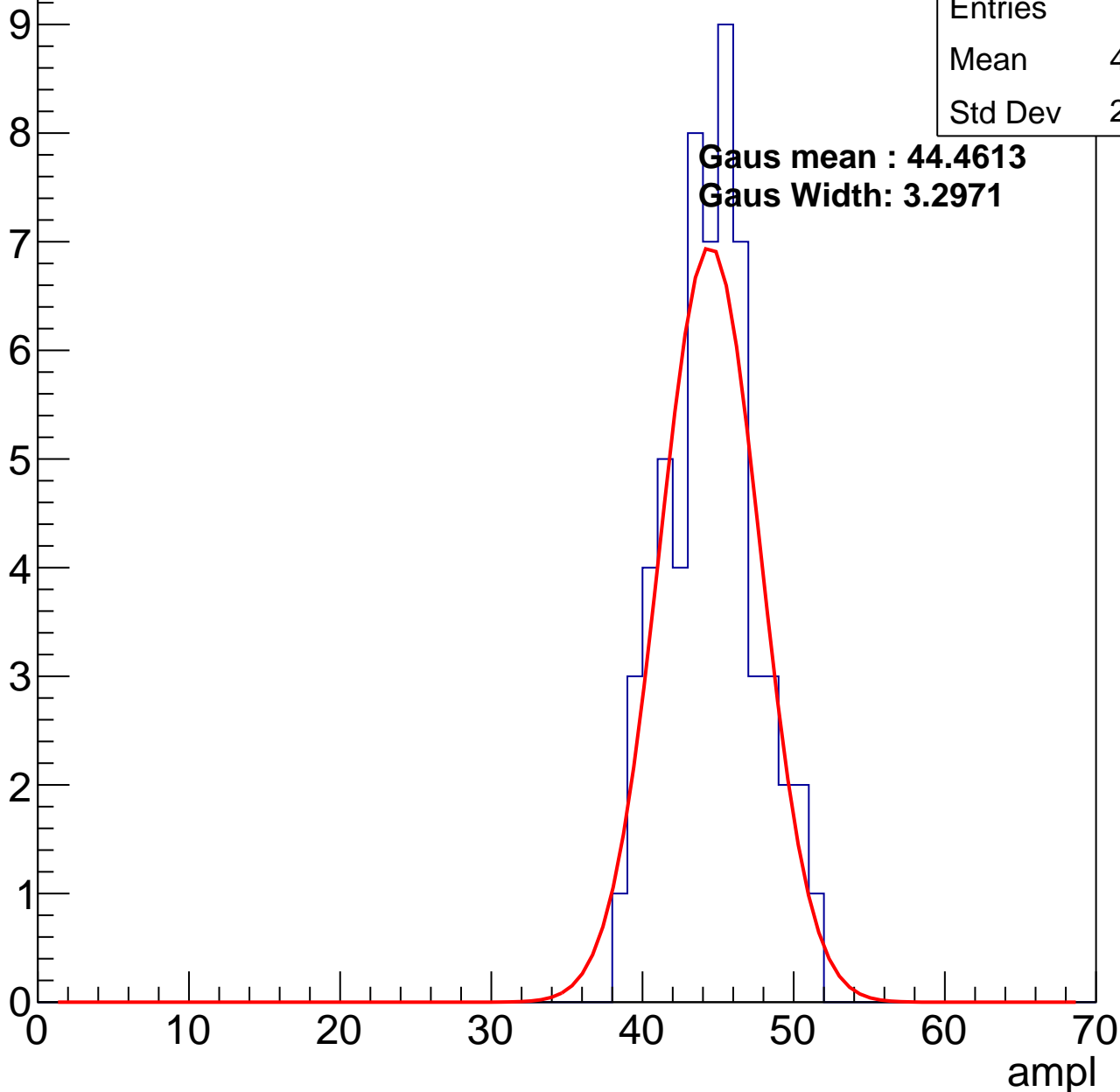
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	44.08
Std Dev	2.987

Gaus mean : 44.4613

Gaus Width: 3.2971

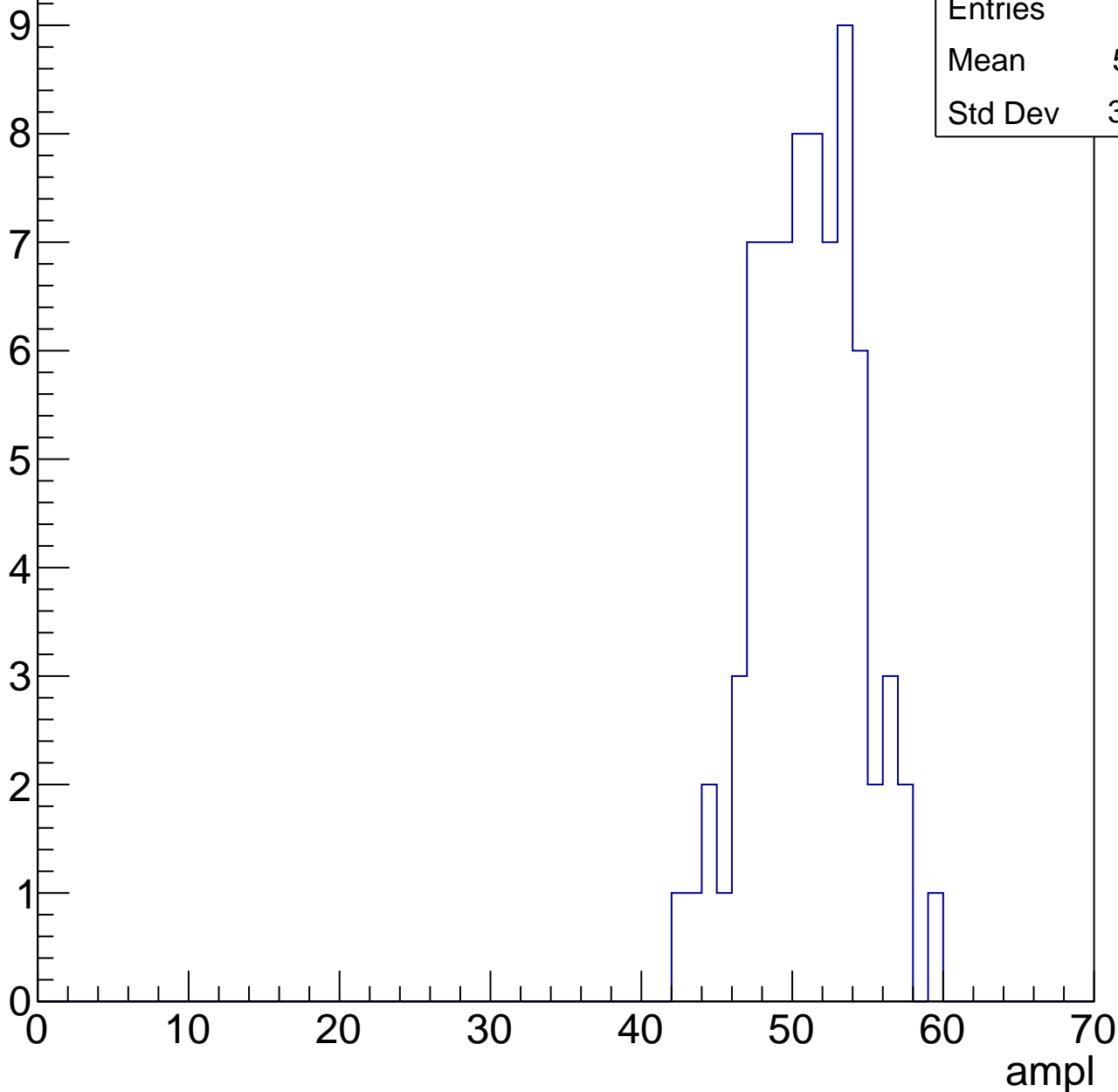


B1L003S, U21-ch85, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	50.51
Std Dev	3.439

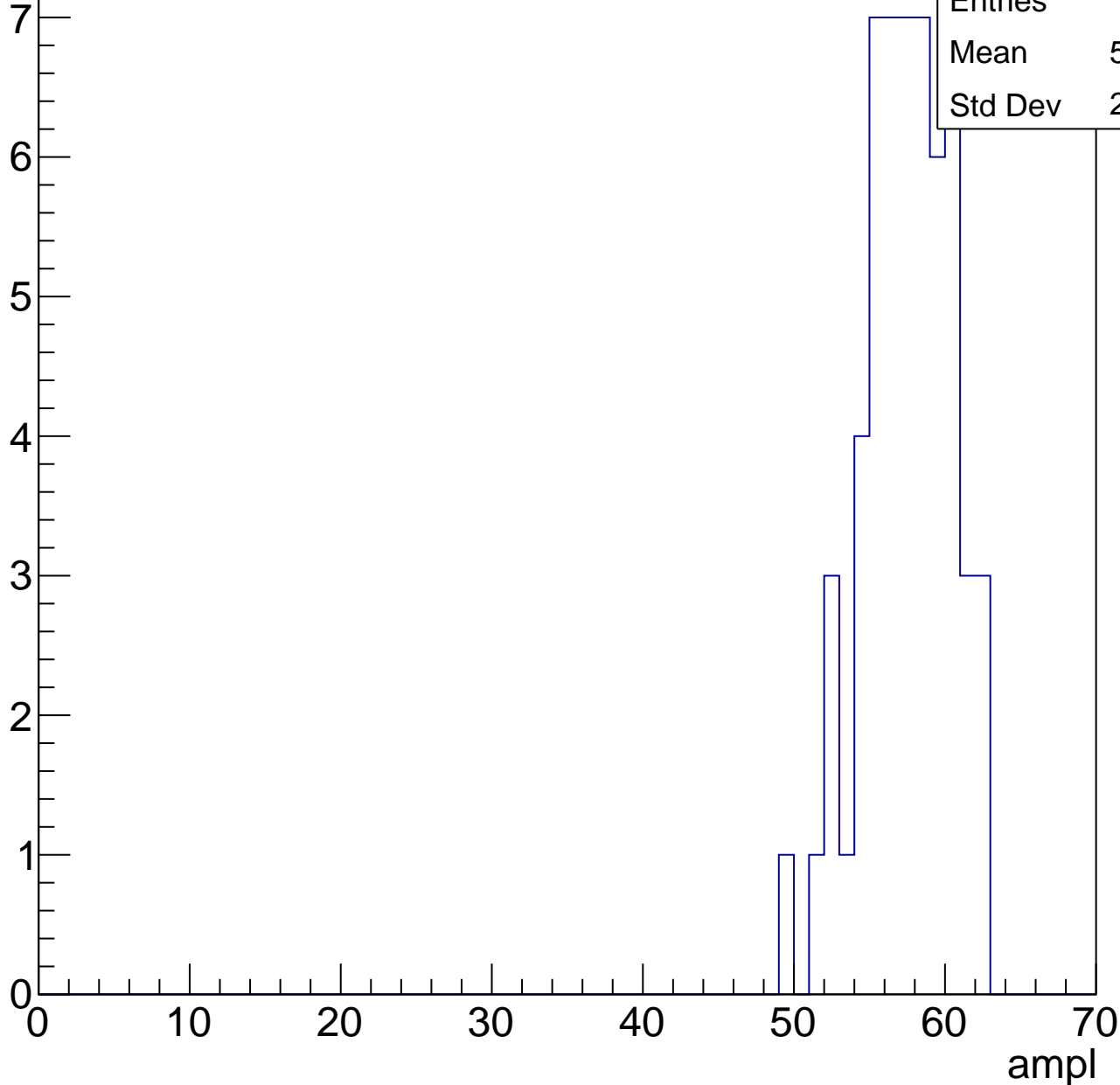


B1L003S, U21-ch85, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	57.02
Std Dev	2.899



B1L003S, U21-ch85, adc5

calib_packv5_042523_0143.root, FC#13, port D2

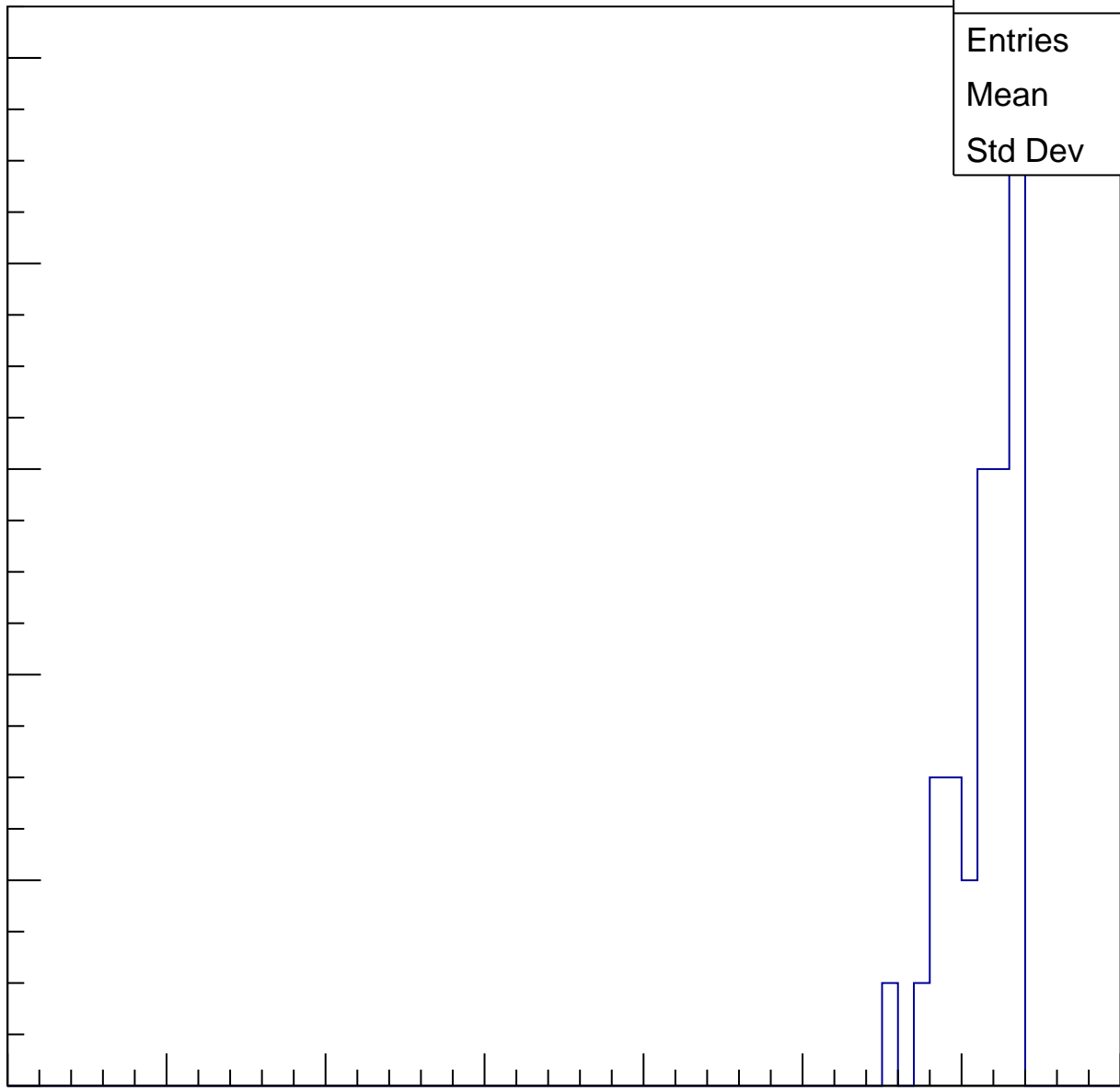
Entries	32
Mean	60.97
Std Dev	2.084

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

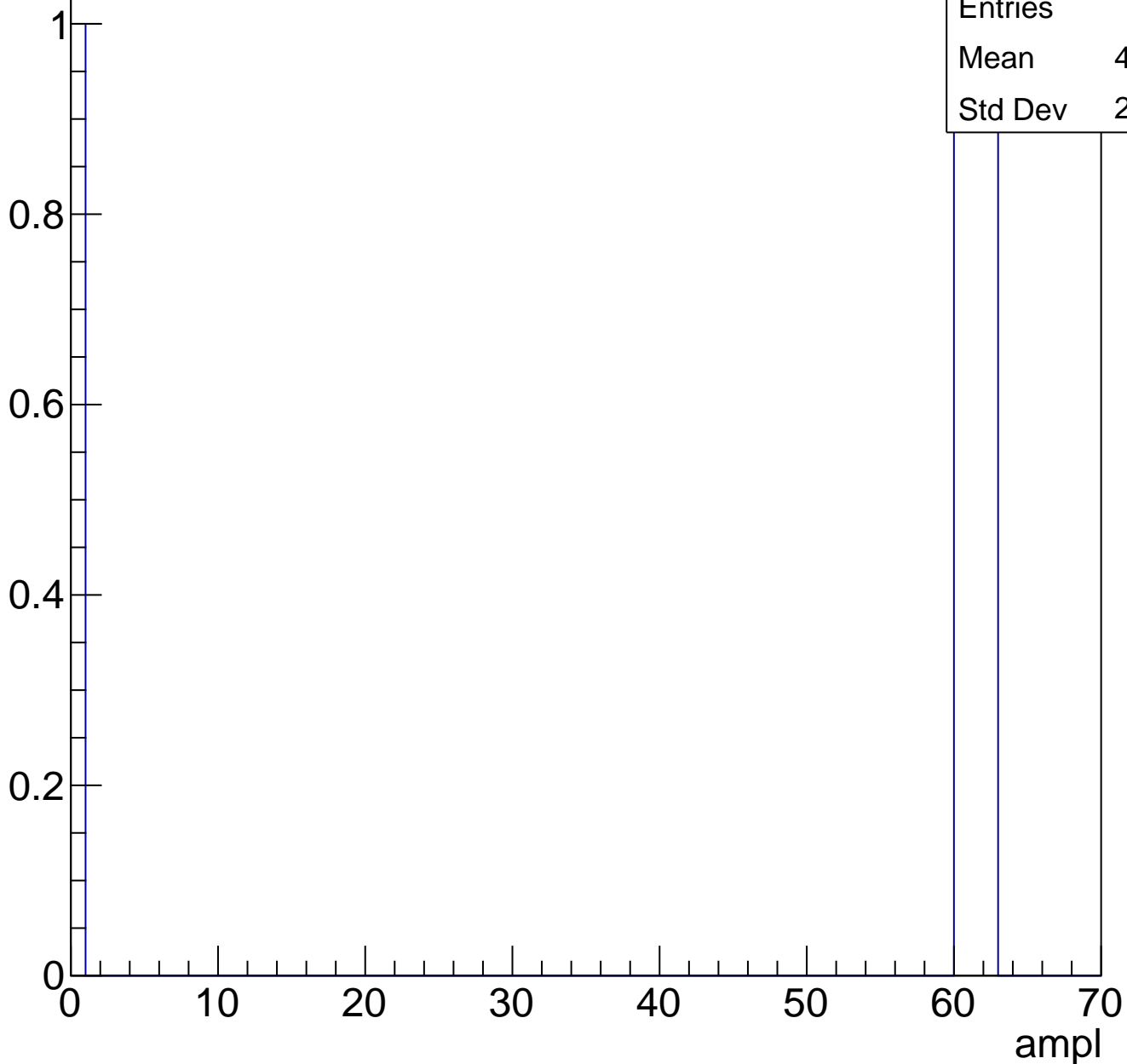
ampl



B1L003S, U21-ch85, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch85, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch86, adc0

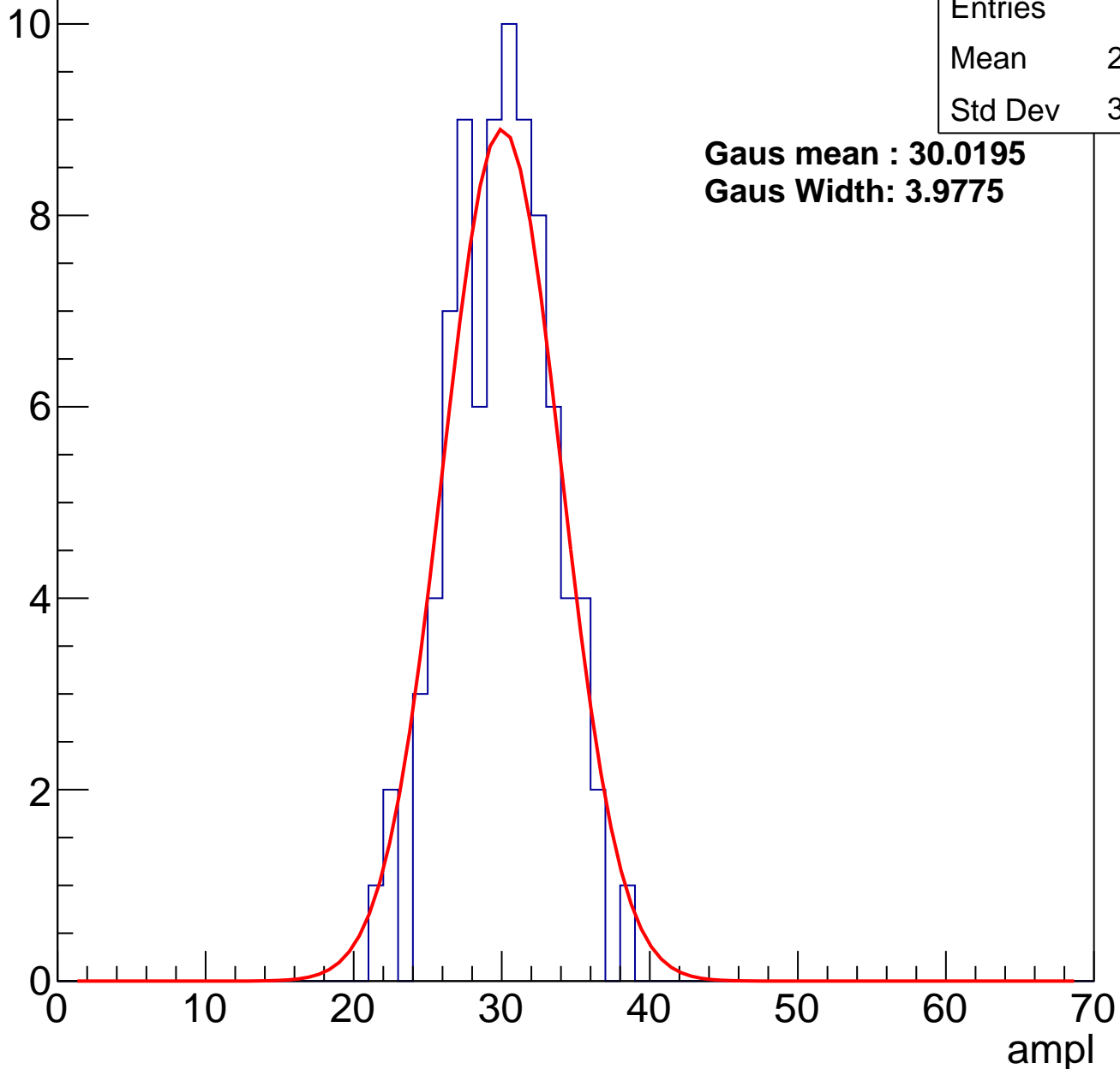
calib_packv5_042523_0143.root, FC#13, port D2

Entries	85
Mean	29.53
Std Dev	3.463

Gaus mean : 30.0195

Gaus Width: 3.9775

Entry



B1L003S, U21-ch86, adc1

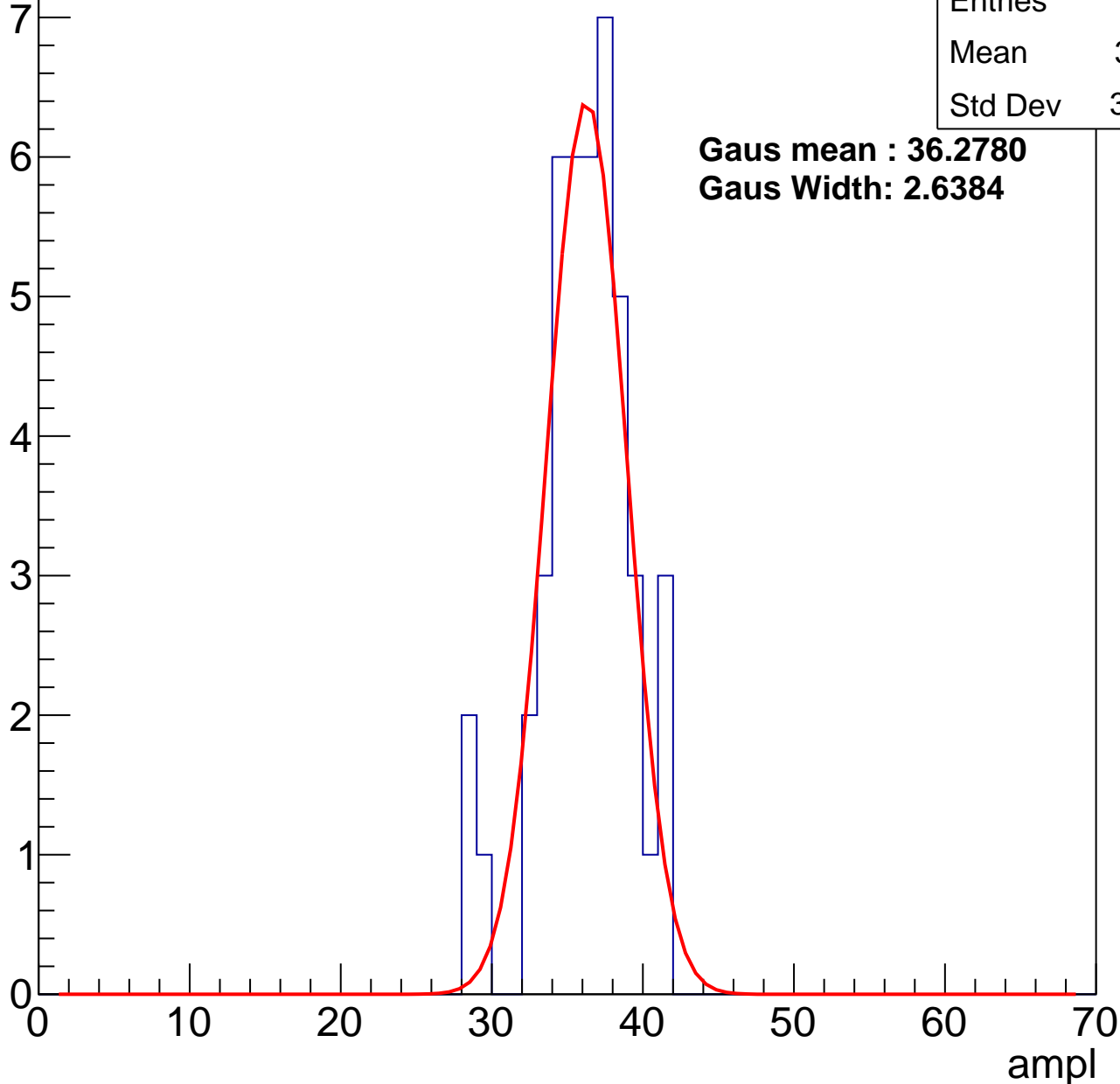
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	35.71
Std Dev	3.008

Gaus mean : 36.2780

Gaus Width: 2.6384



B1L003S, U21-ch86, adc2

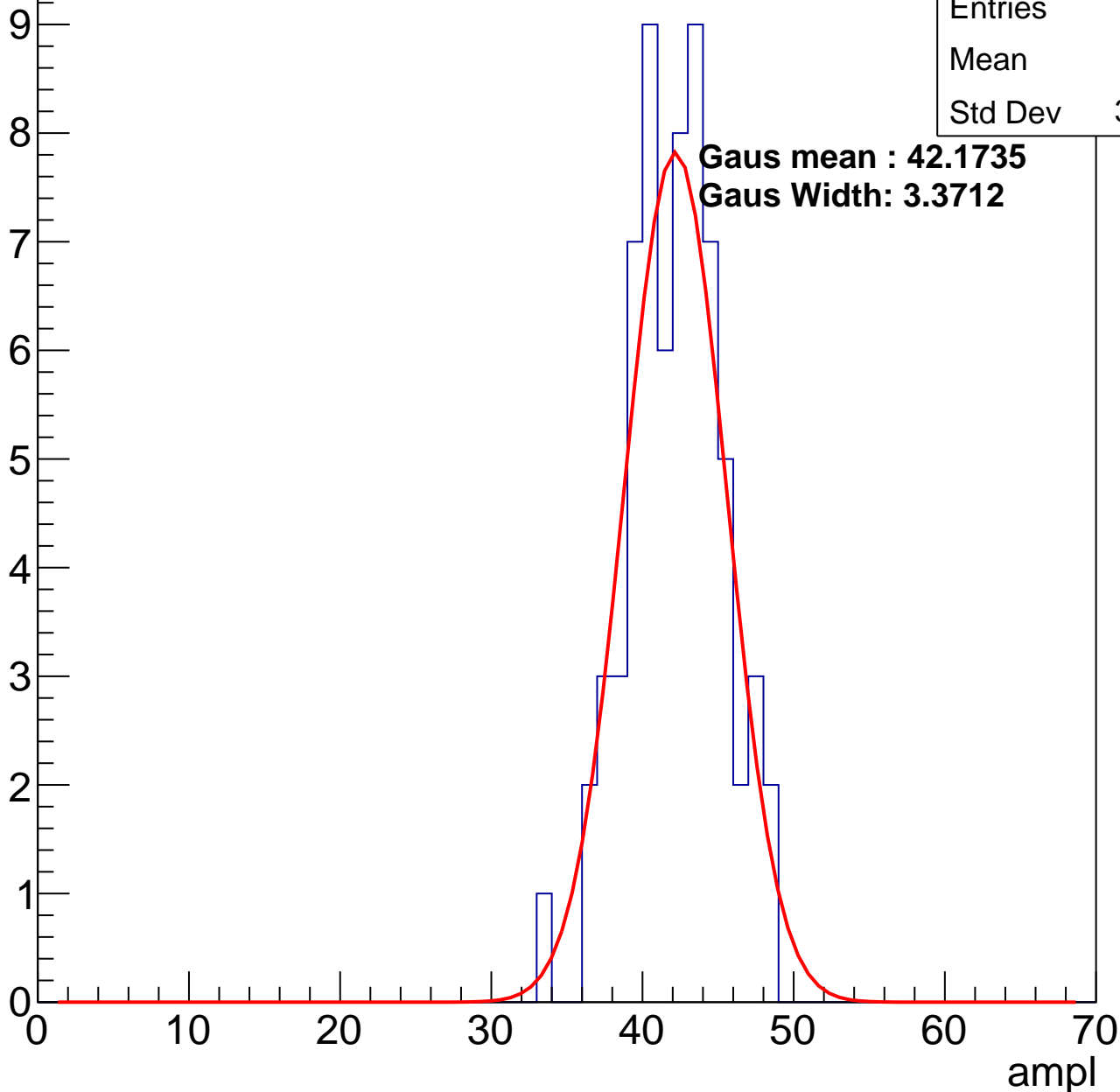
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.7
Std Dev	3.081

Gaus mean : 42.1735

Gaus Width: 3.3712



B1L003S, U21-ch86, adc3

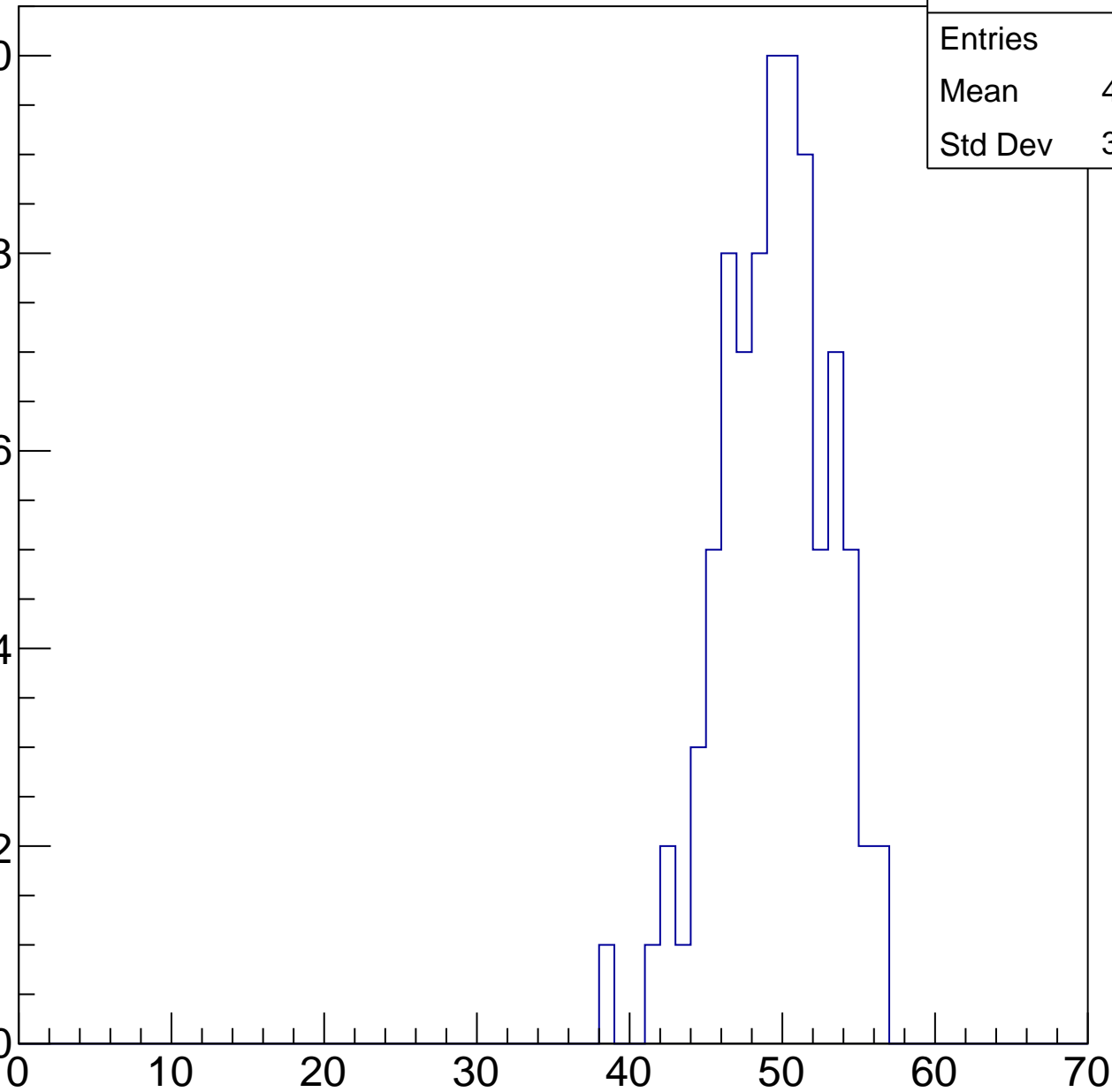
calib_packv5_042523_0143.root, FC#13, port D2

Entries	86
Mean	49.02
Std Dev	3.544

Entry

10
8
6
4
2
0

ampl

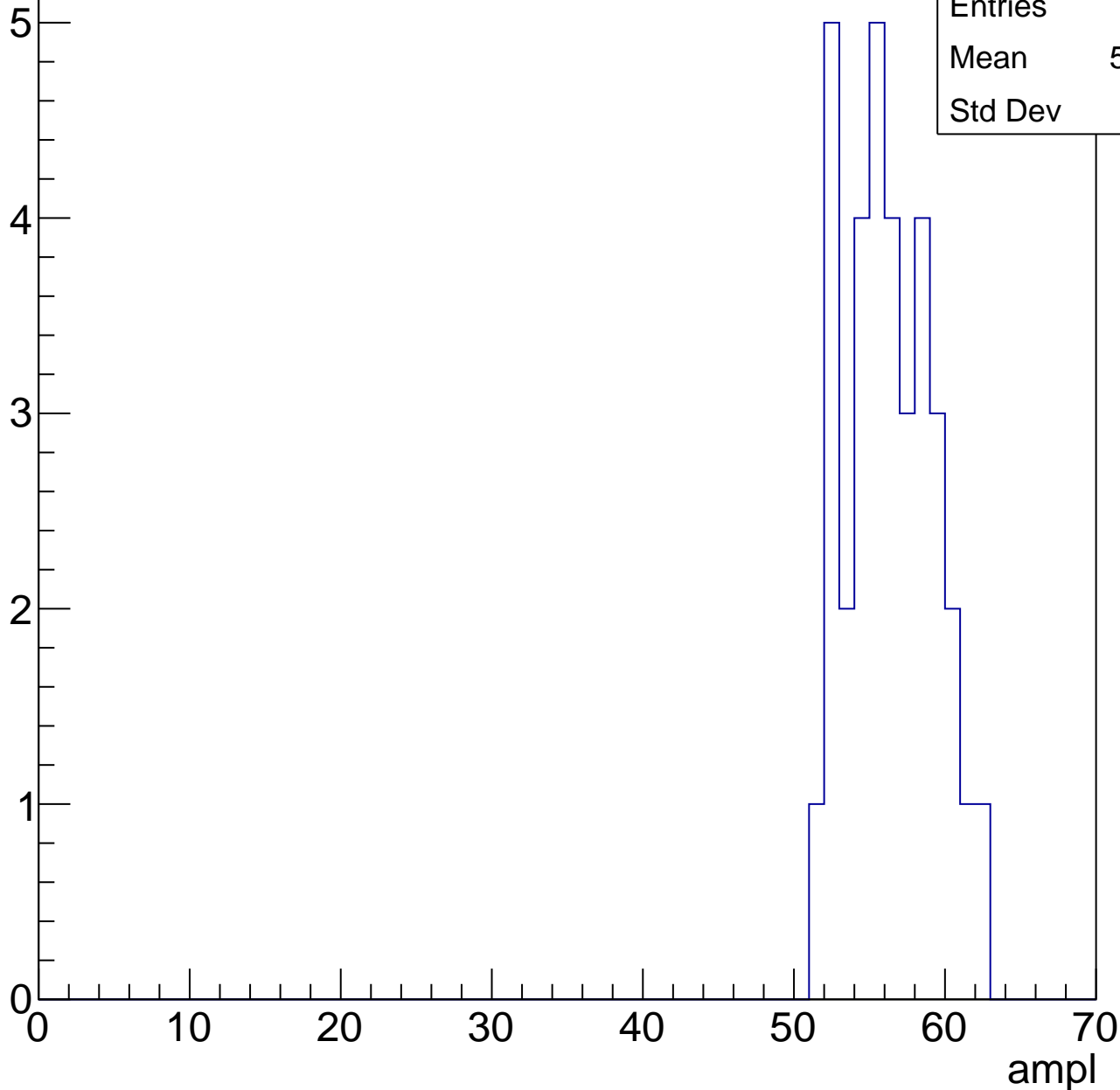


B1L003S, U21-ch86, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	55.86
Std Dev	2.84

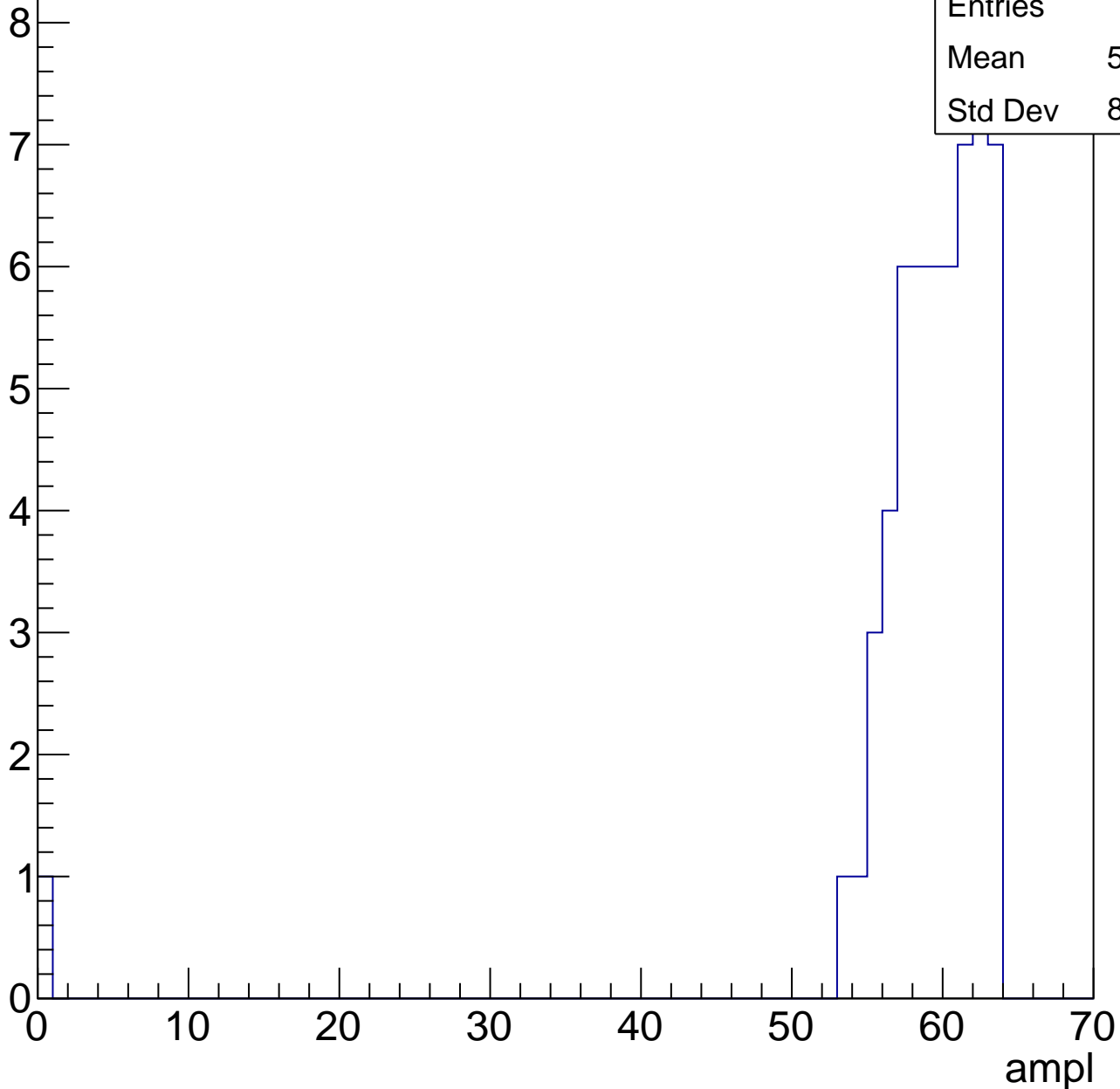


B1L003S, U21-ch86, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

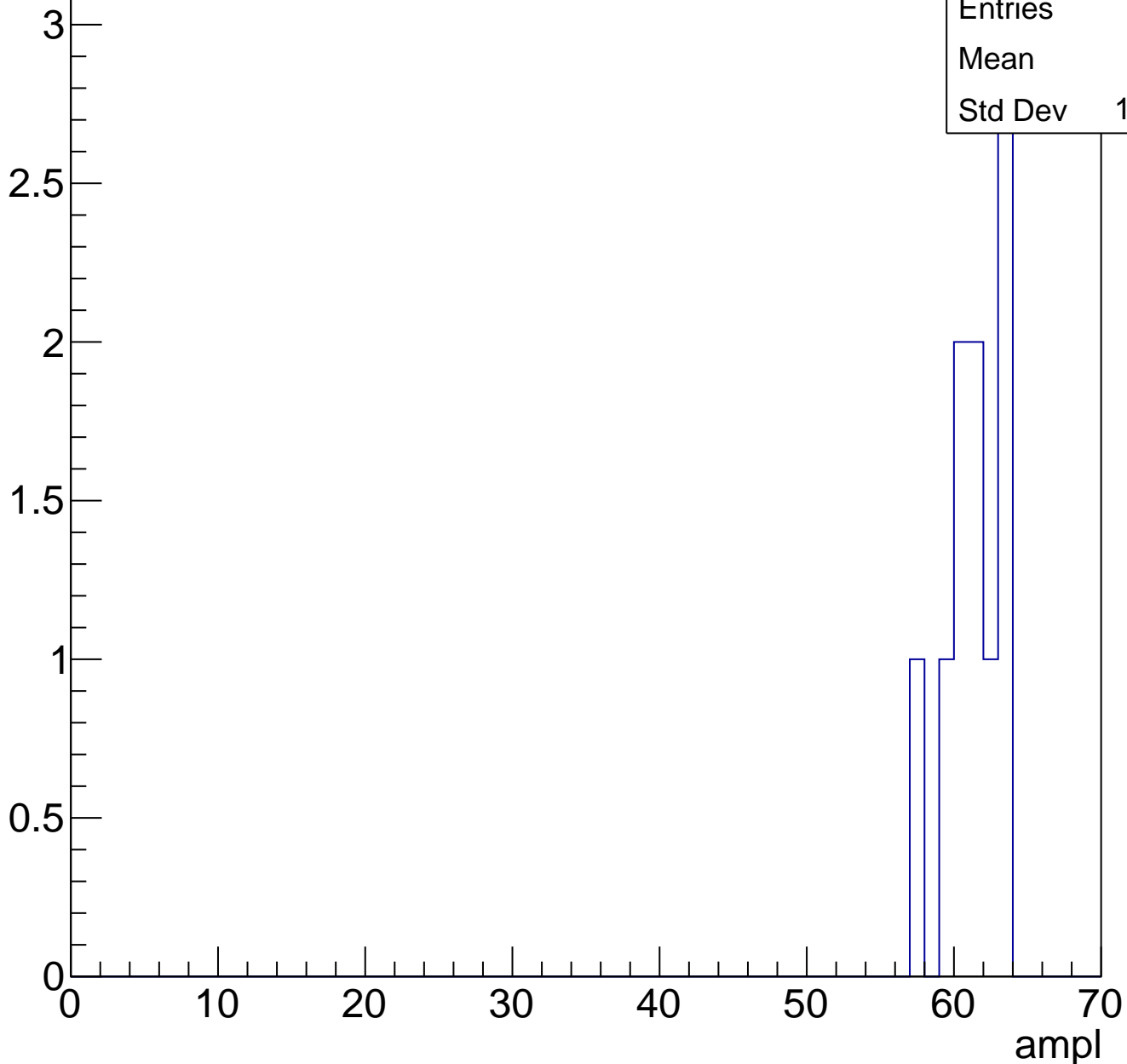
Entries	56
Mean	58.29
Std Dev	8.287



B1L003S, U21-ch86, adc6

calib_packv5_042523_0143.root, FC#13, port D2

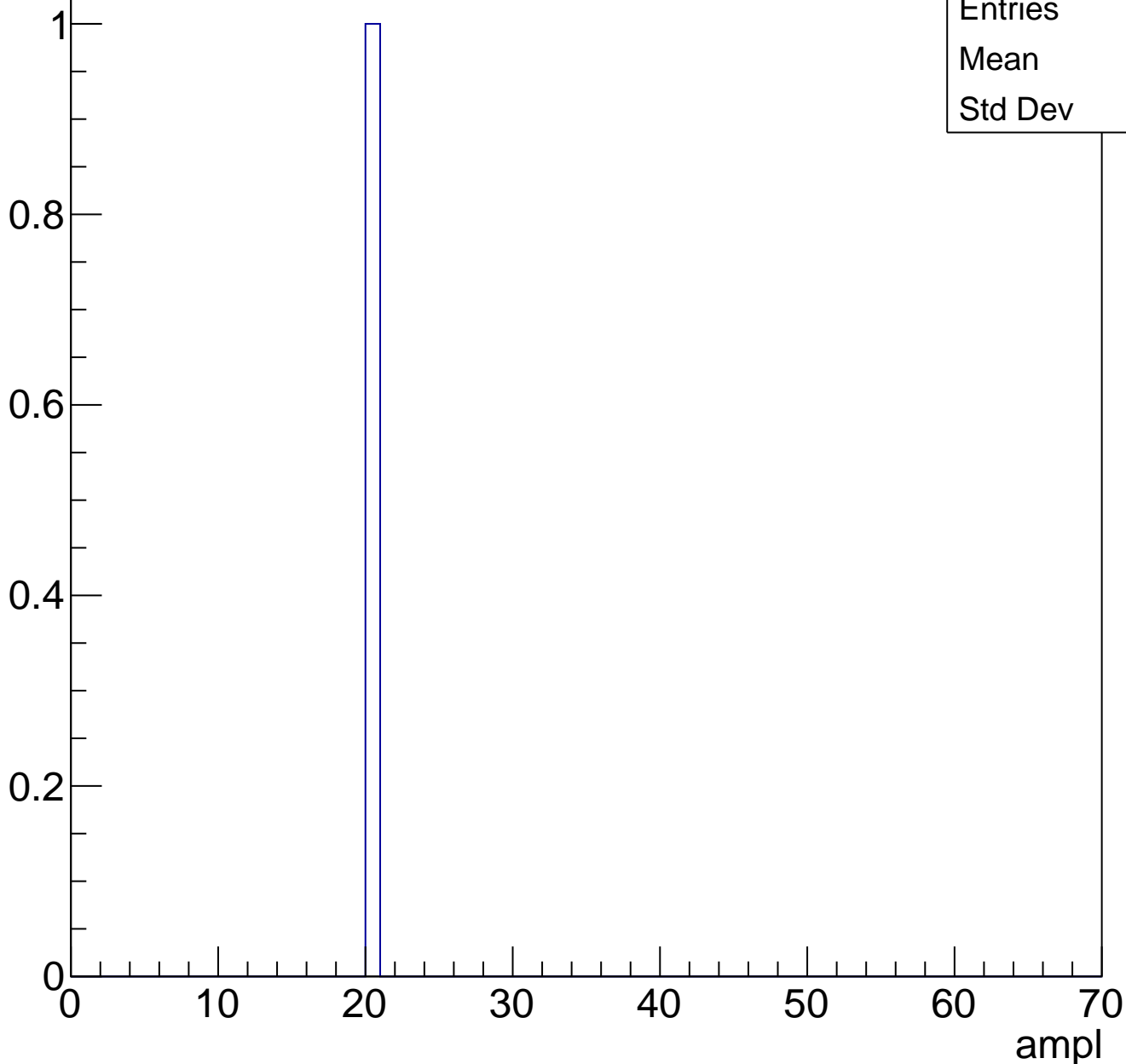
Entry



B1L003S, U21-ch86, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L003S, U21-ch87, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	71
Mean	31.76
Std Dev	3.891

Gaus mean : 32.4072

Gaus Width: 3.3975

Entry

10
8
6
4
2
0

ampl

0

10

20

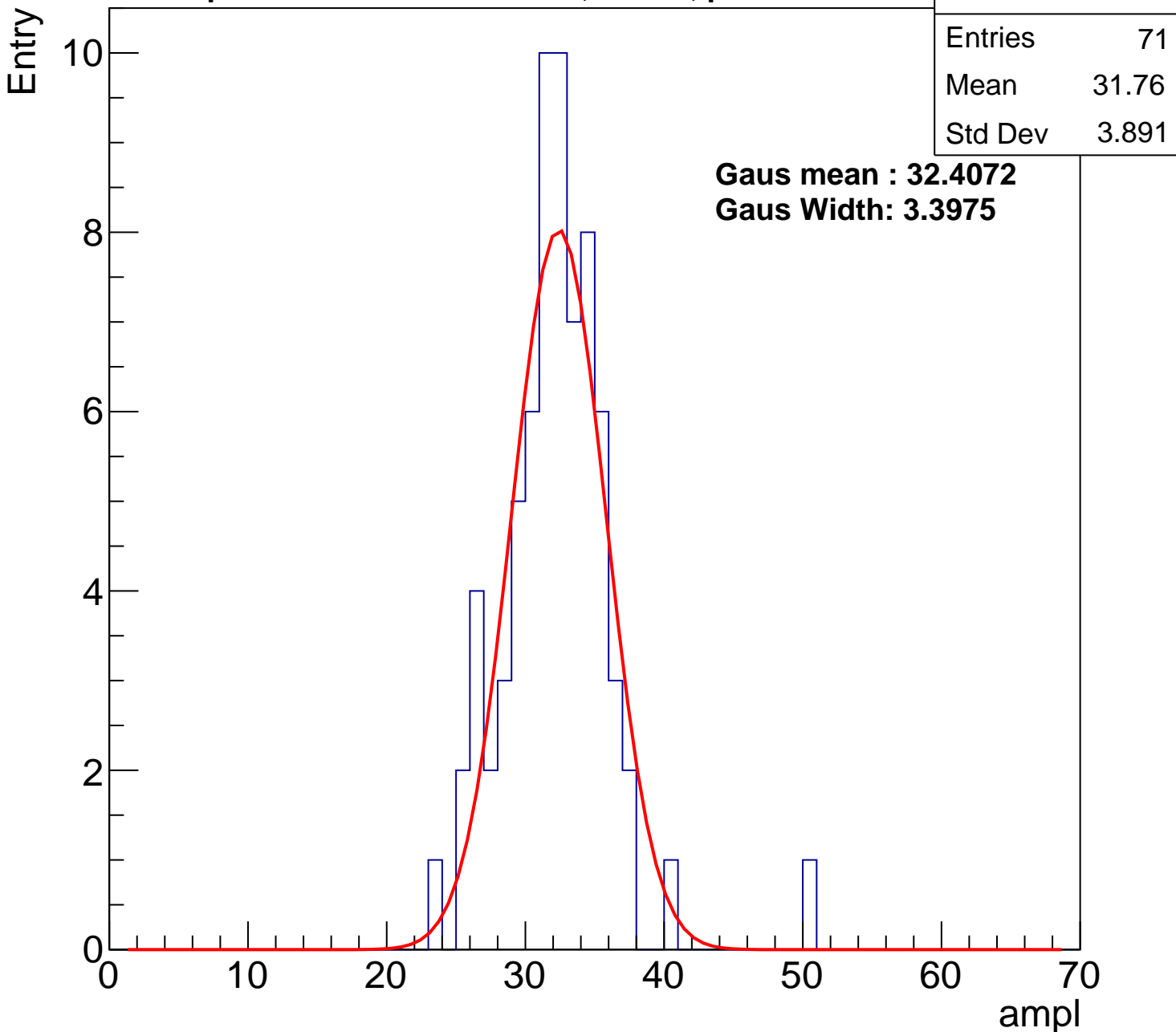
30

40

50

60

70



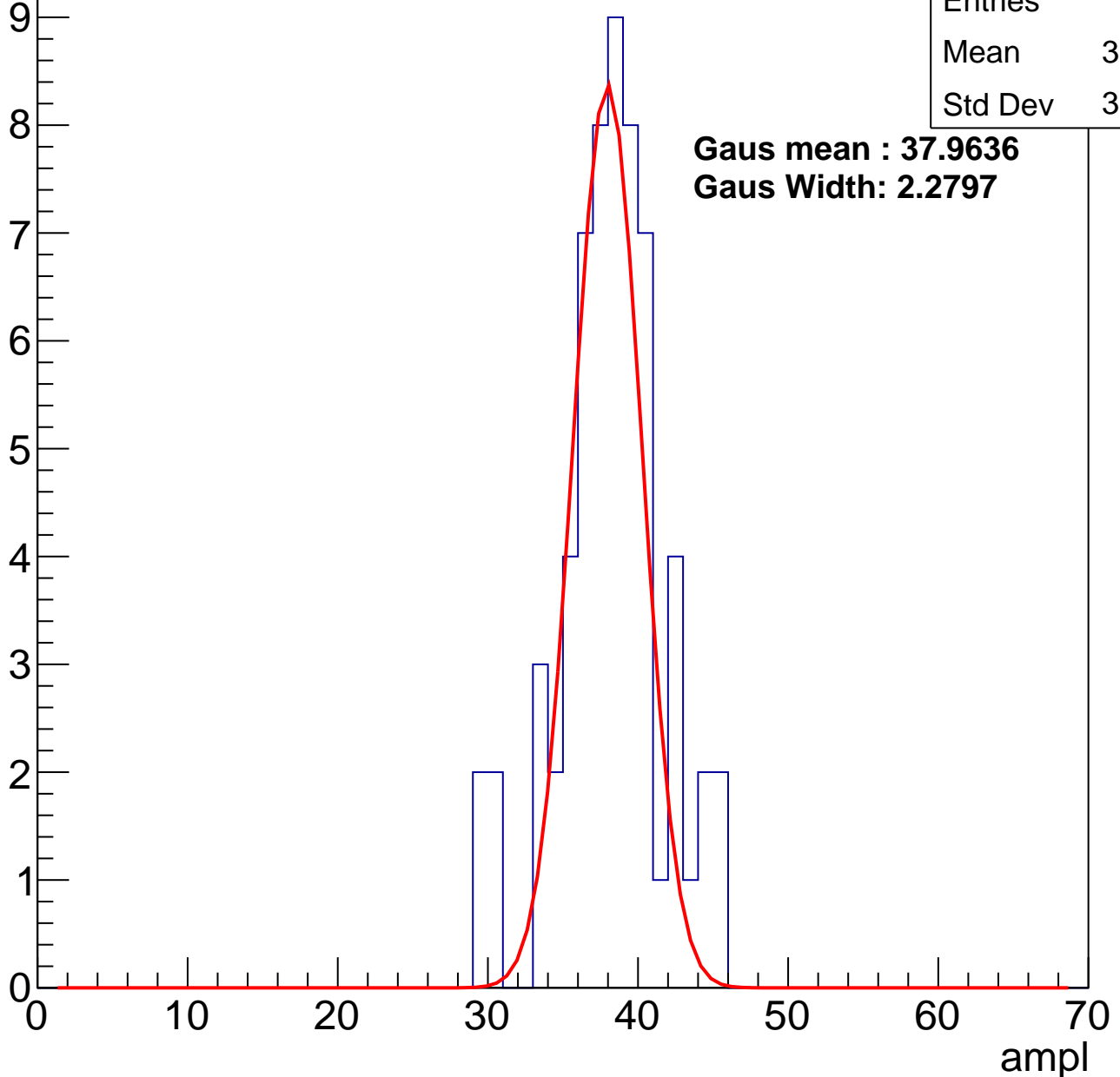
B1L003S, U21-ch87, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	37.69
Std Dev	3.518

Gaus mean : 37.9636
Gaus Width: 2.2797



B1L003S, U21-ch87, adc2

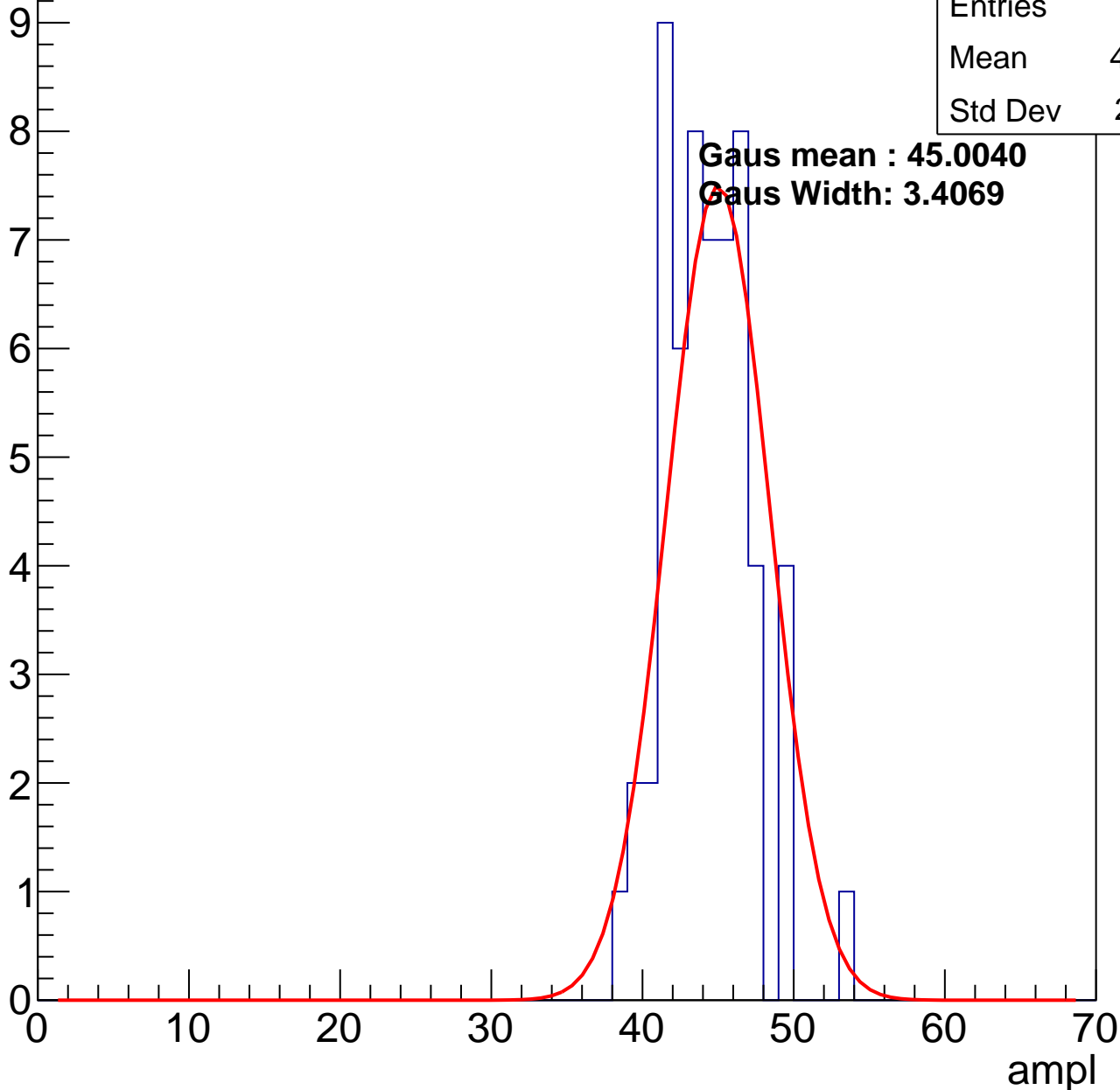
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	43.88
Std Dev	2.871

Gaus mean : 45.0040

Gaus Width: 3.4069

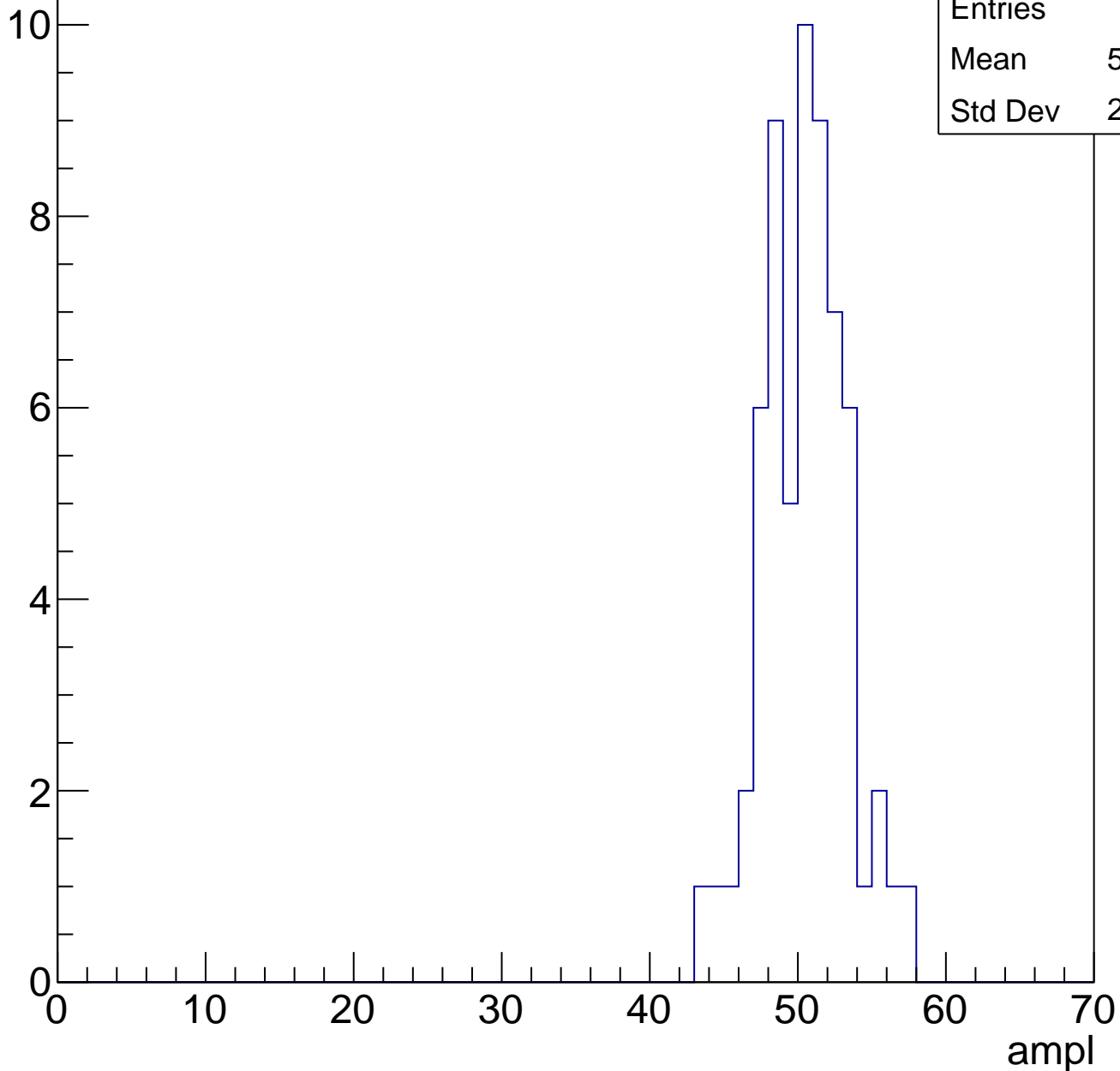


B1L003S, U21-ch87, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	50.02
Std Dev	2.779

Entry

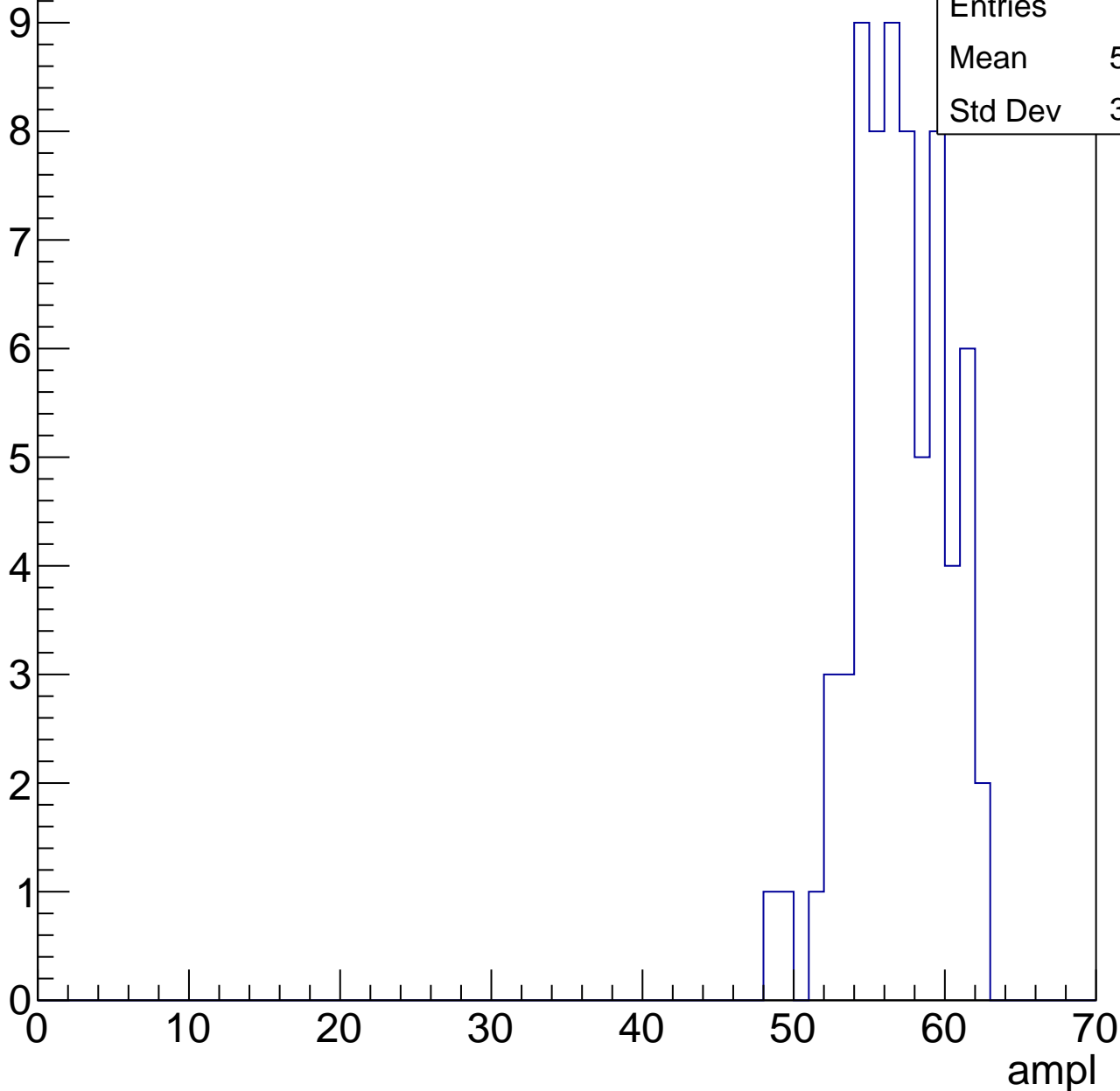


B1L003S, U21-ch87, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	56.49
Std Dev	3.037

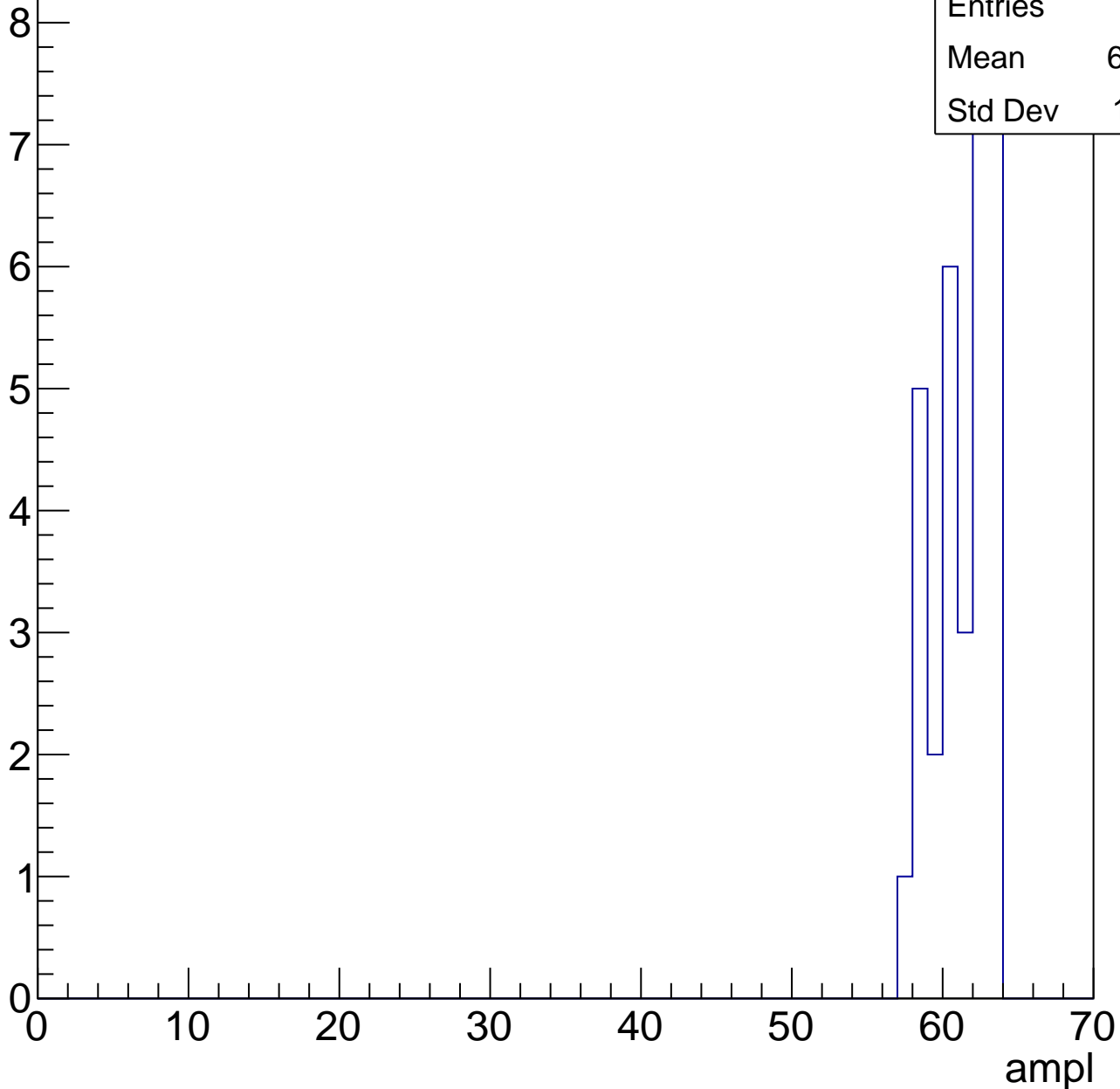


B1L003S, U21-ch87, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

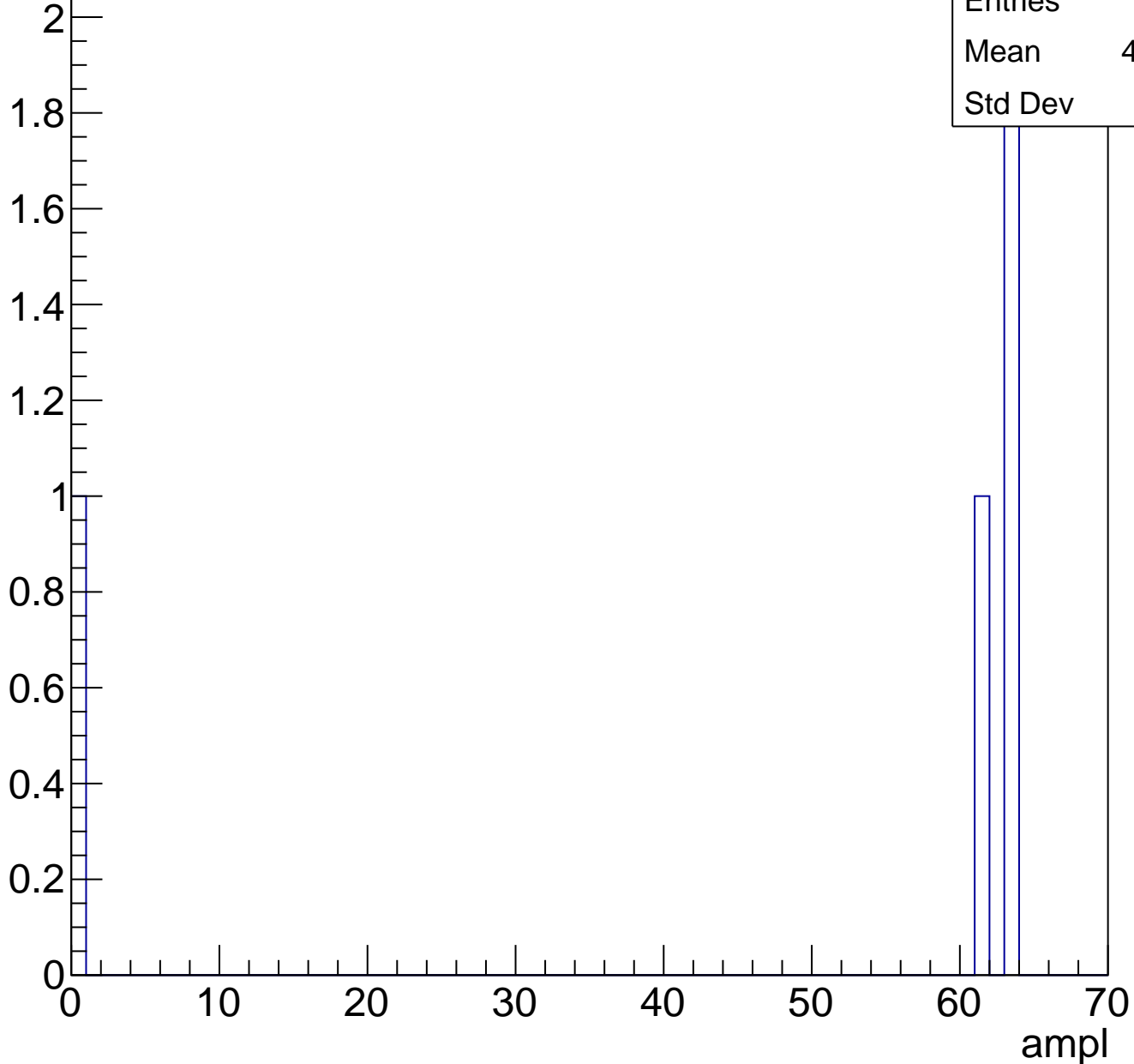
Entries	33
Mean	60.85
Std Dev	1.861



B1L003S, U21-ch87, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch87, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch88, adc0

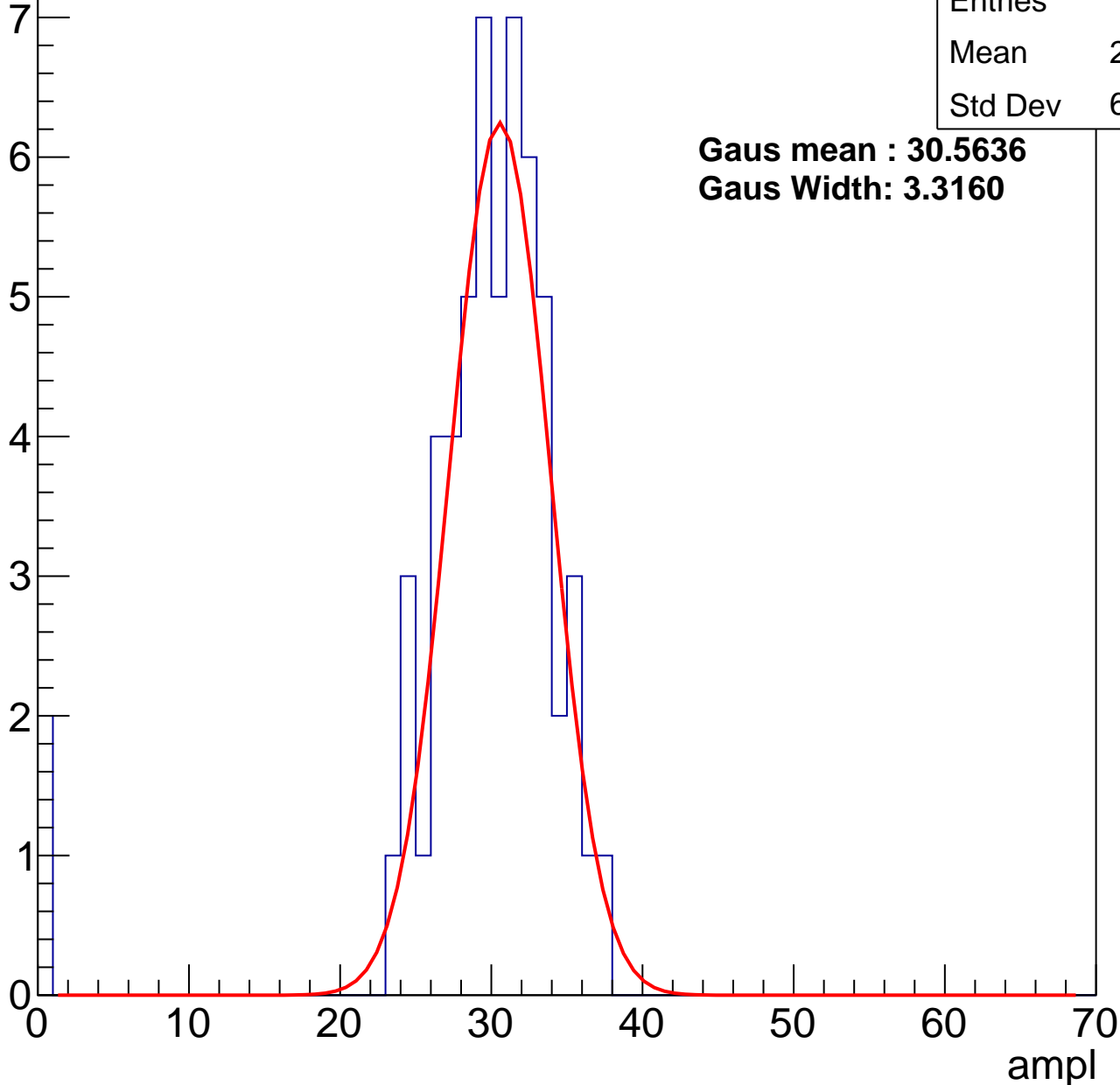
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	28.86
Std Dev	6.356

Gaus mean : 30.5636

Gaus Width: 3.3160



B1L003S, U21-ch88, adc1

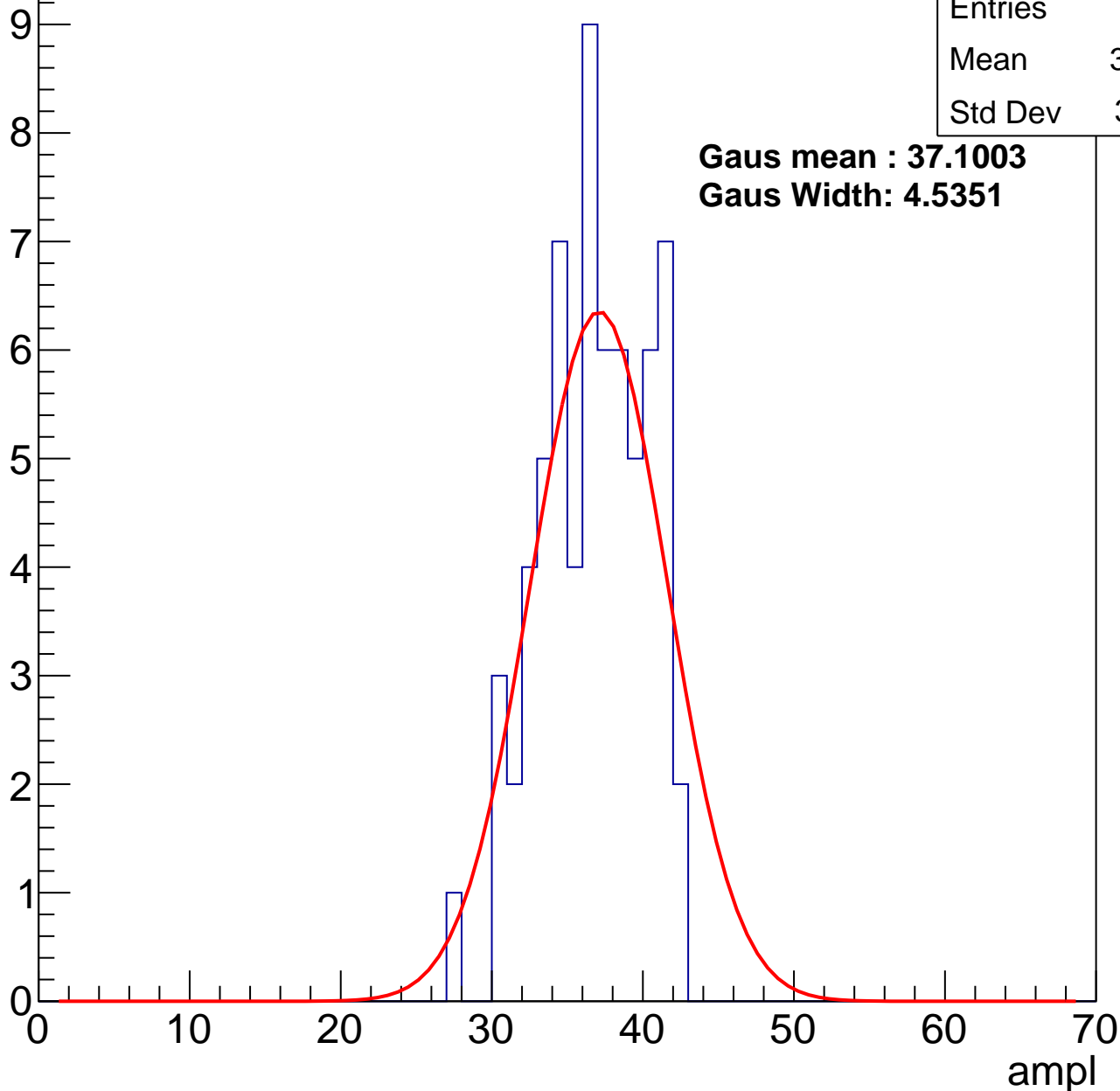
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	36.27
Std Dev	3.441

Gaus mean : 37.1003

Gaus Width: 4.5351



B1L003S, U21-ch88, adc2

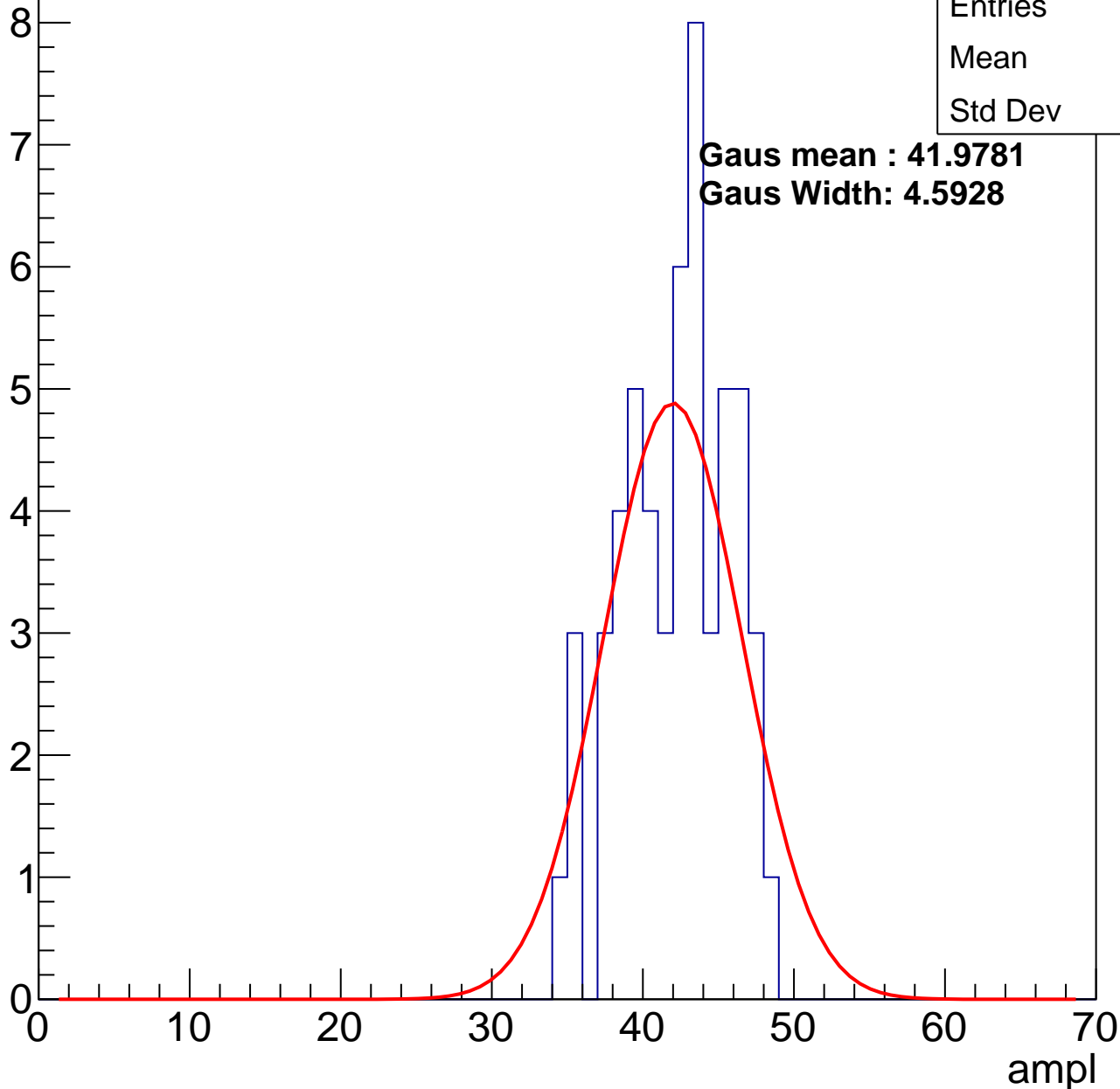
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	41.7
Std Dev	3.51

Gaus mean : 41.9781

Gaus Width: 4.5928

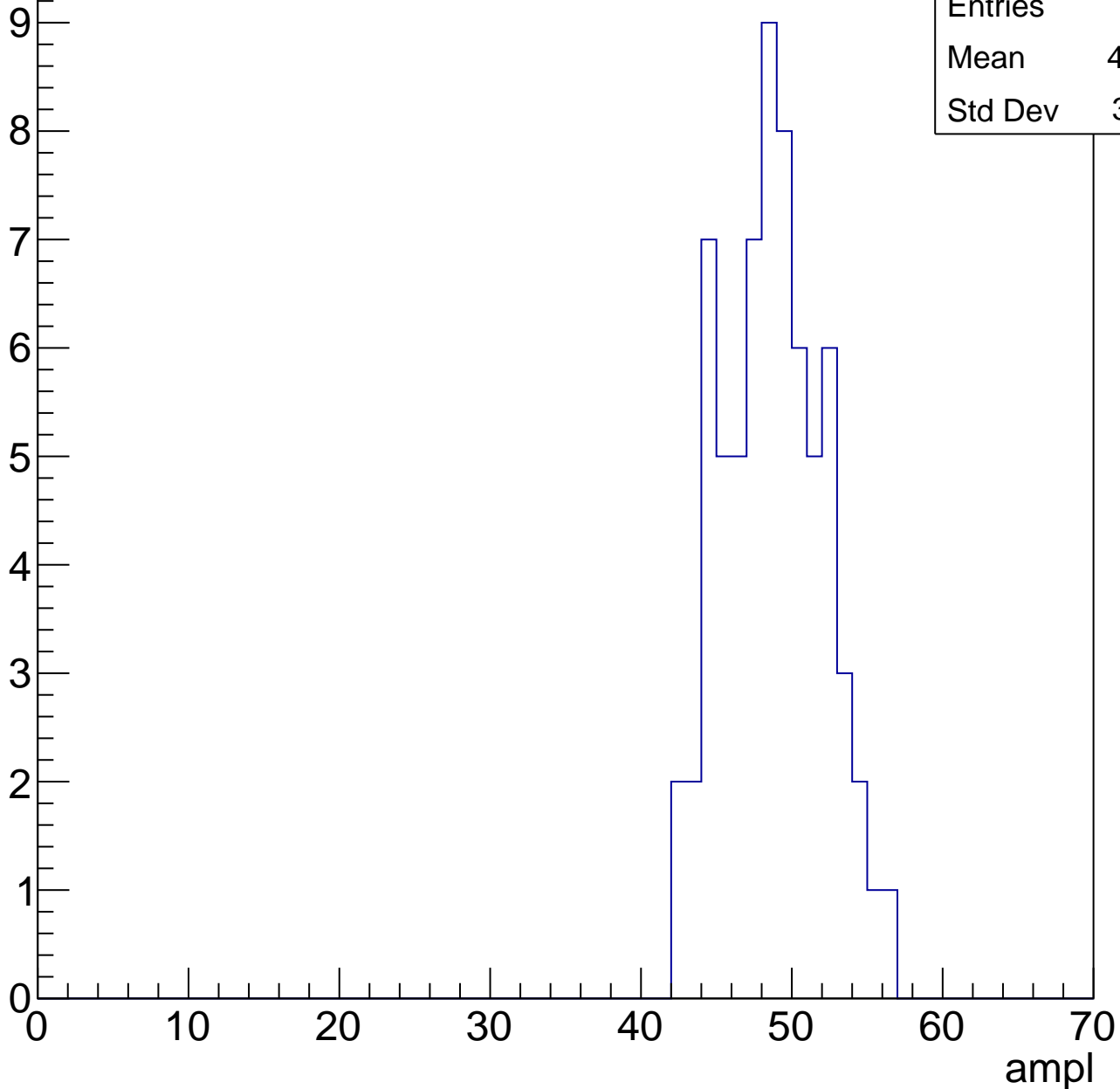


B1L003S, U21-ch88, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	48.28
Std Dev	3.261

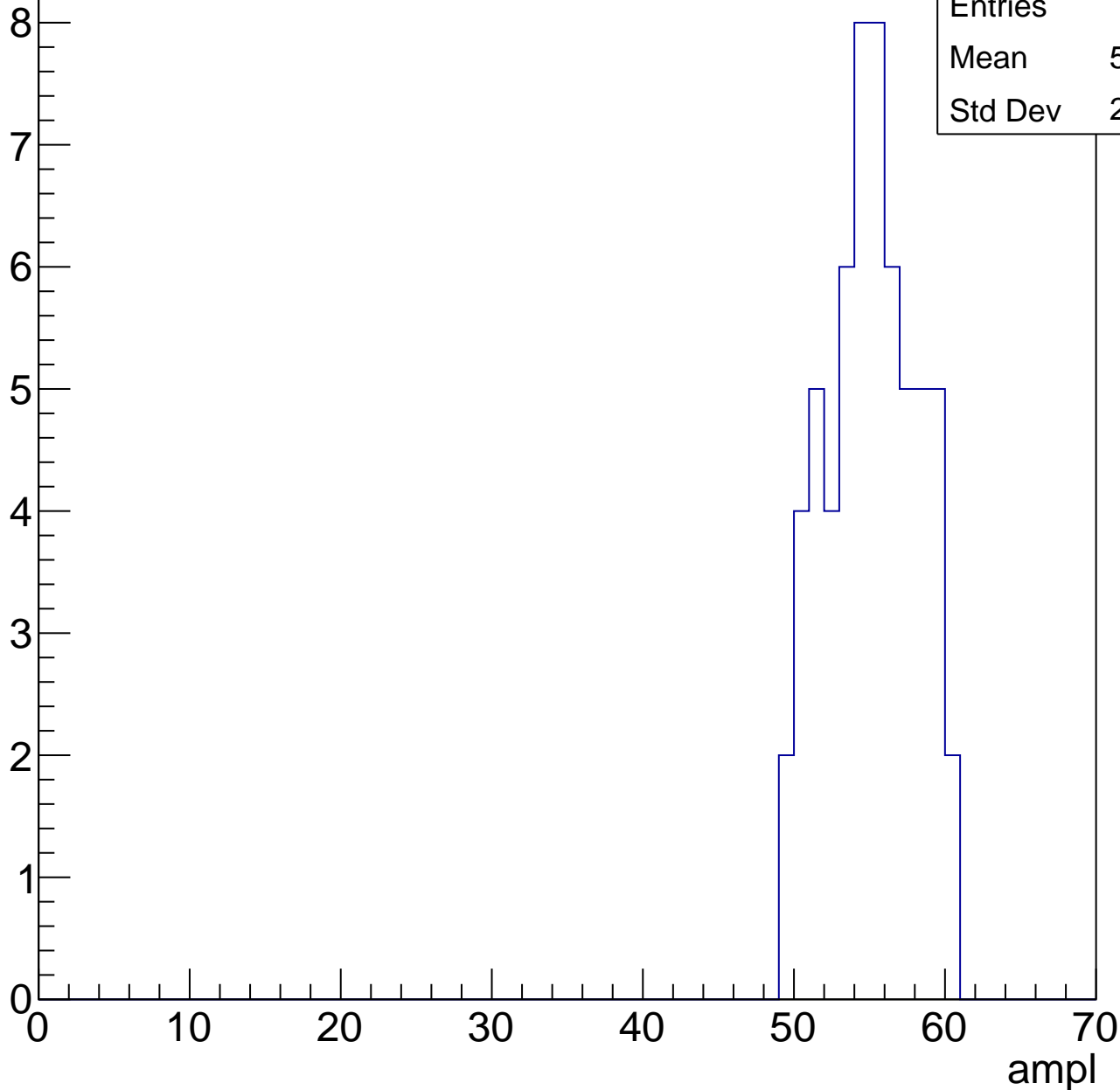


B1L003S, U21-ch88, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	54.62
Std Dev	2.922

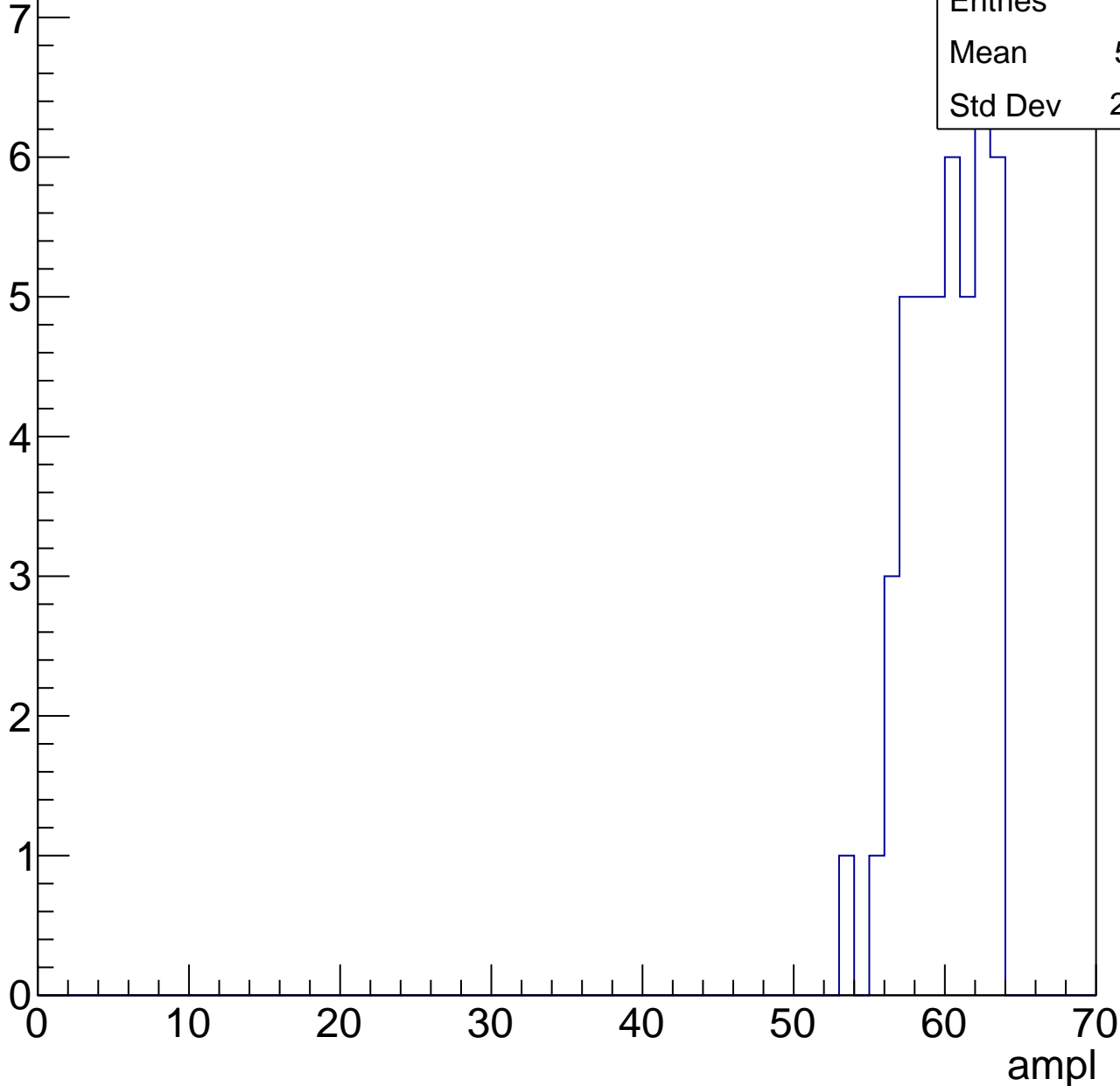


B1L003S, U21-ch88, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.61
Std Dev	2.488

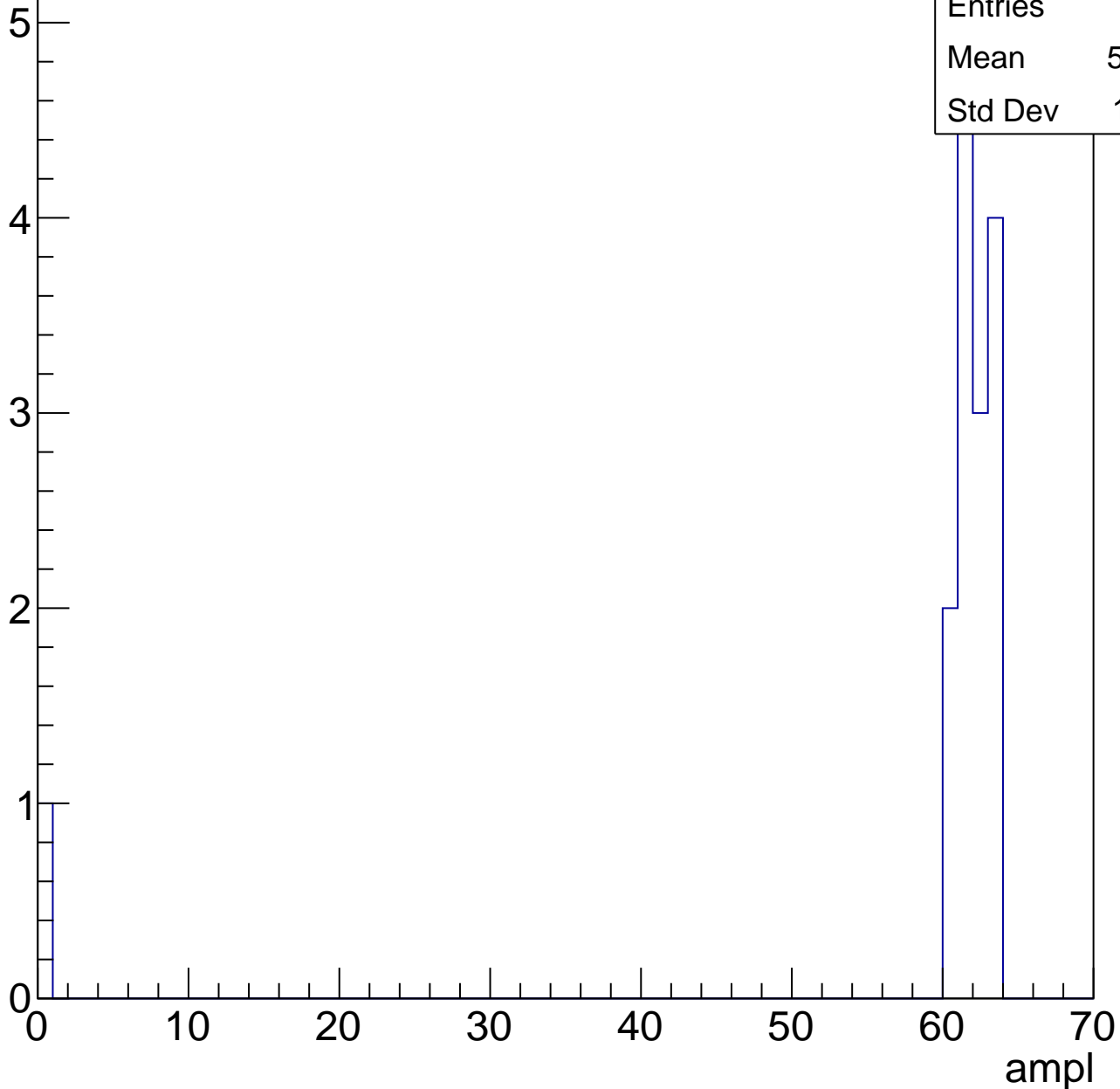


B1L003S, U21-ch88, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	15
Mean	57.53
Std Dev	15.41



B1L003S, U21-ch88, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch89, adc0

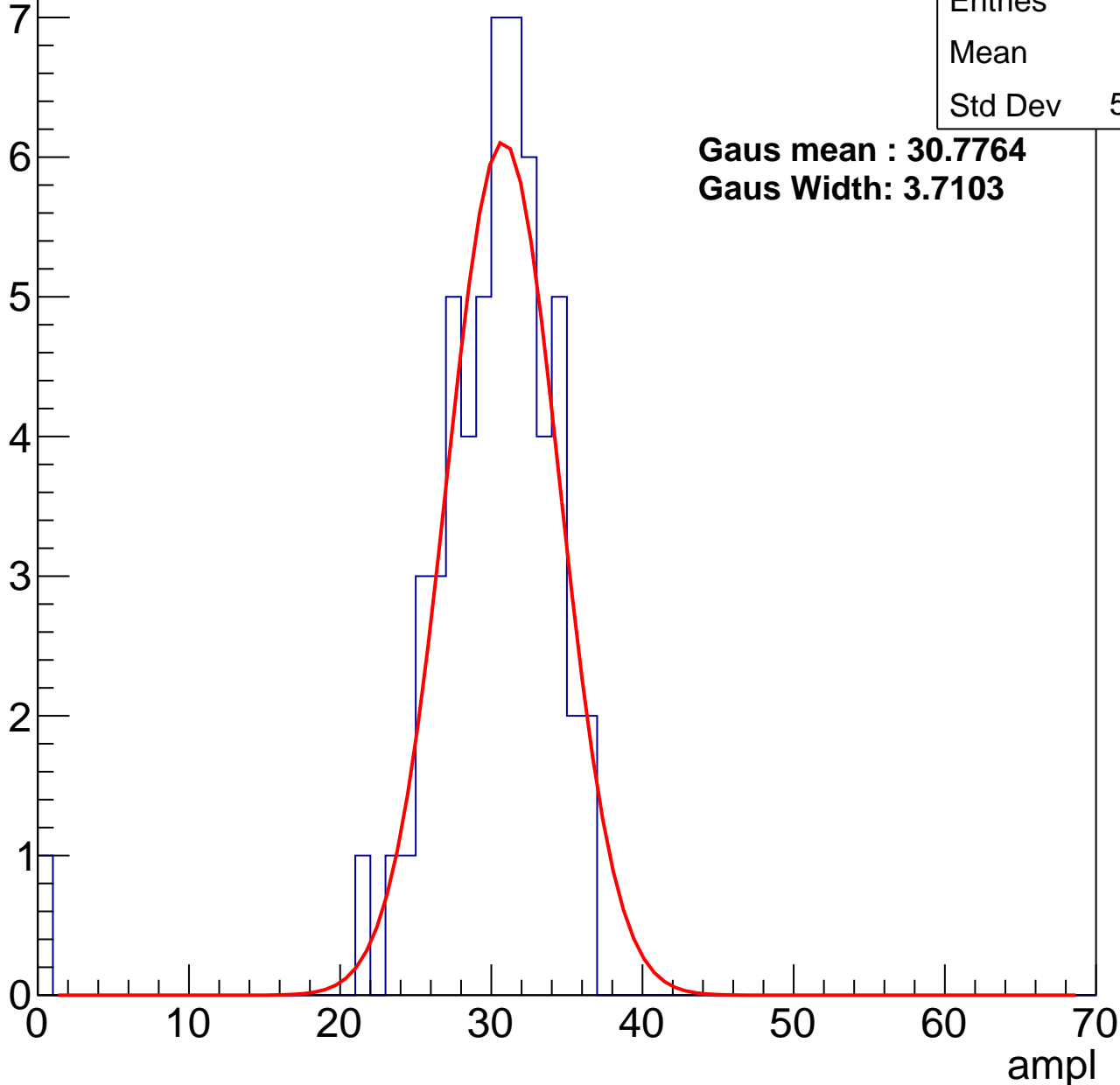
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	29.4
Std Dev	5.136

Gaus mean : 30.7764

Gaus Width: 3.7103



B1L003S, U21-ch89, adc1

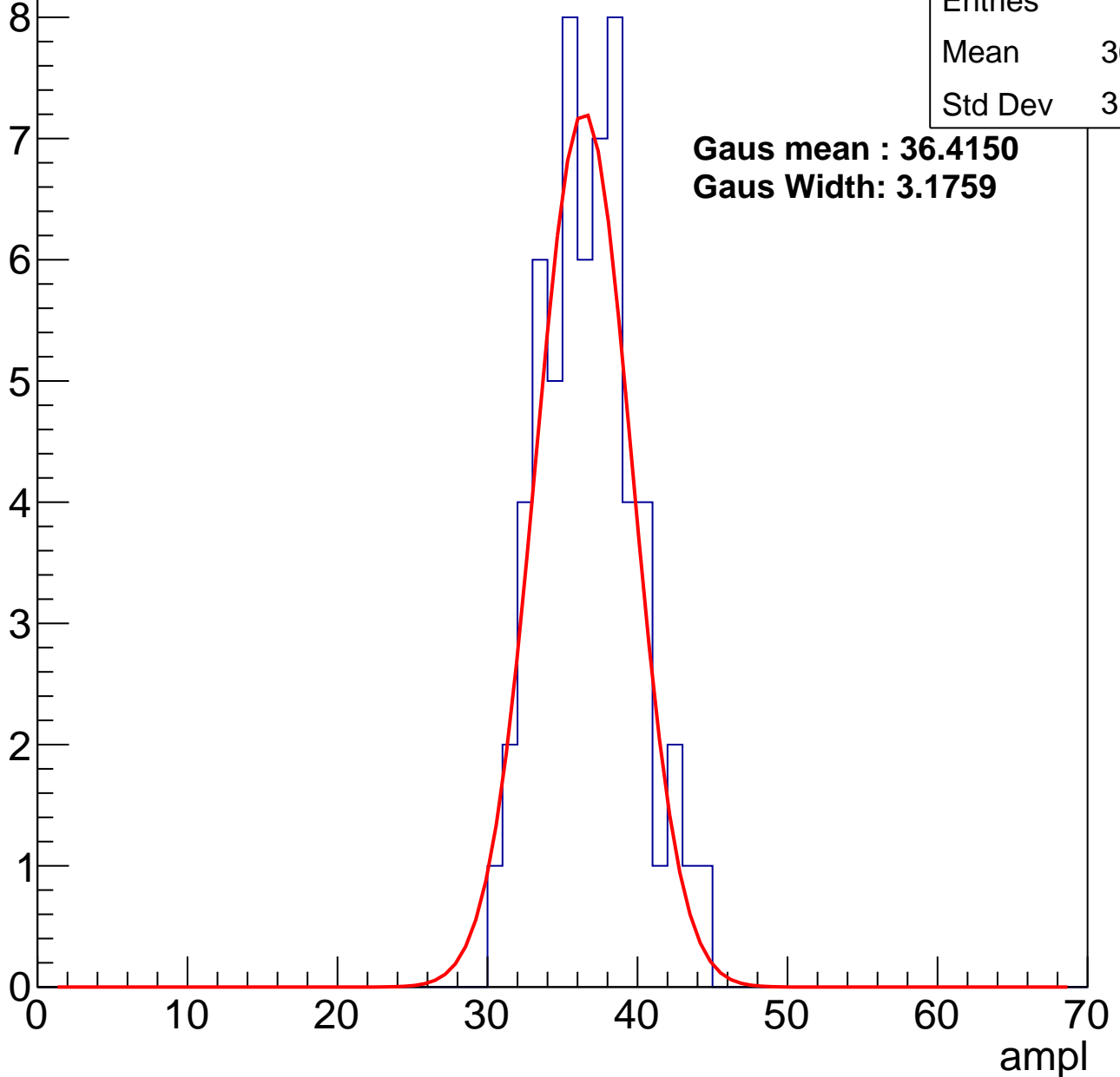
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	36.25
Std Dev	3.102

Gaus mean : 36.4150

Gaus Width: 3.1759



B1L003S, U21-ch89, adc2

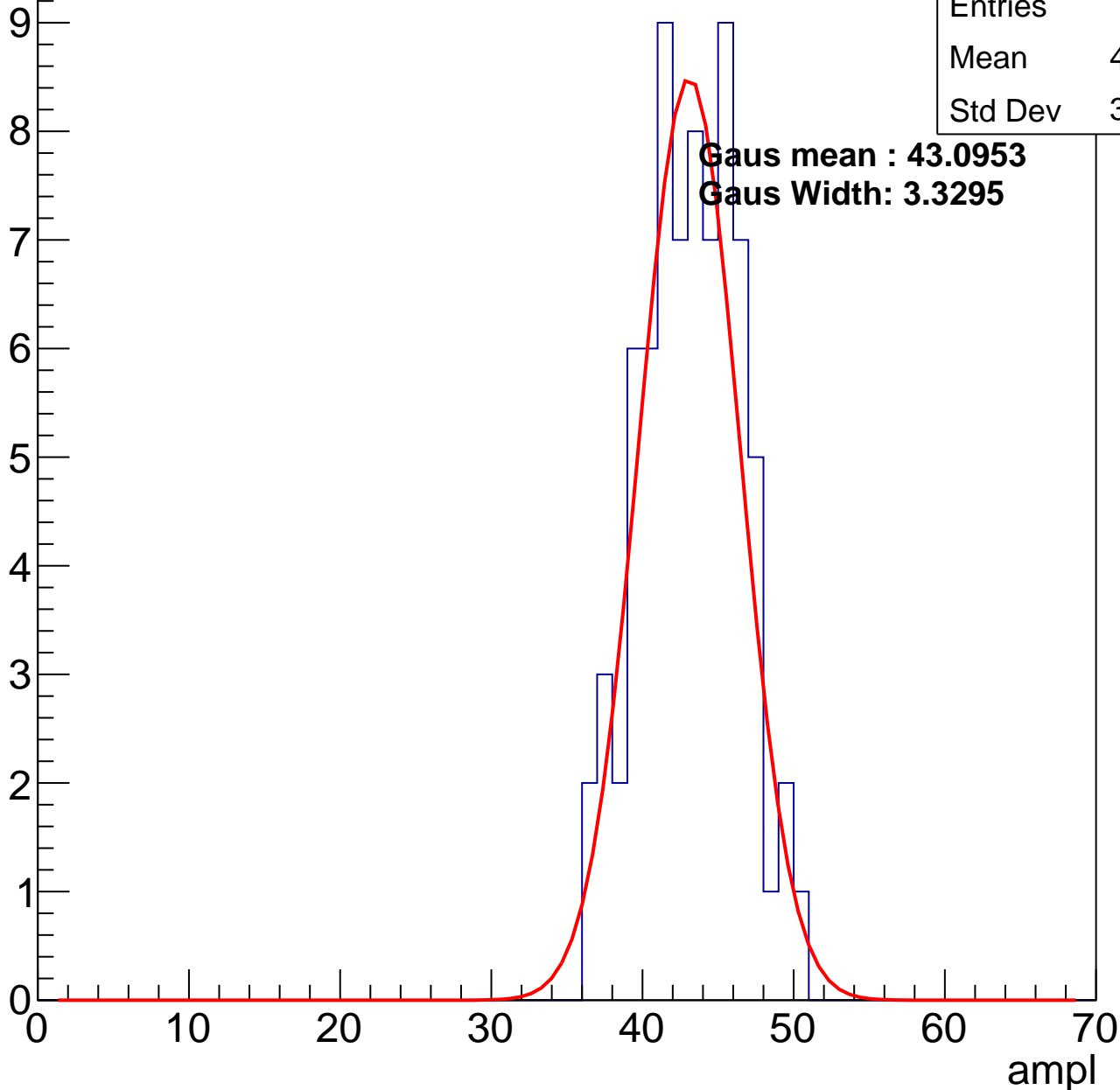
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	42.75
Std Dev	3.217

Gaus mean : 43.0953

Gaus Width: 3.3295

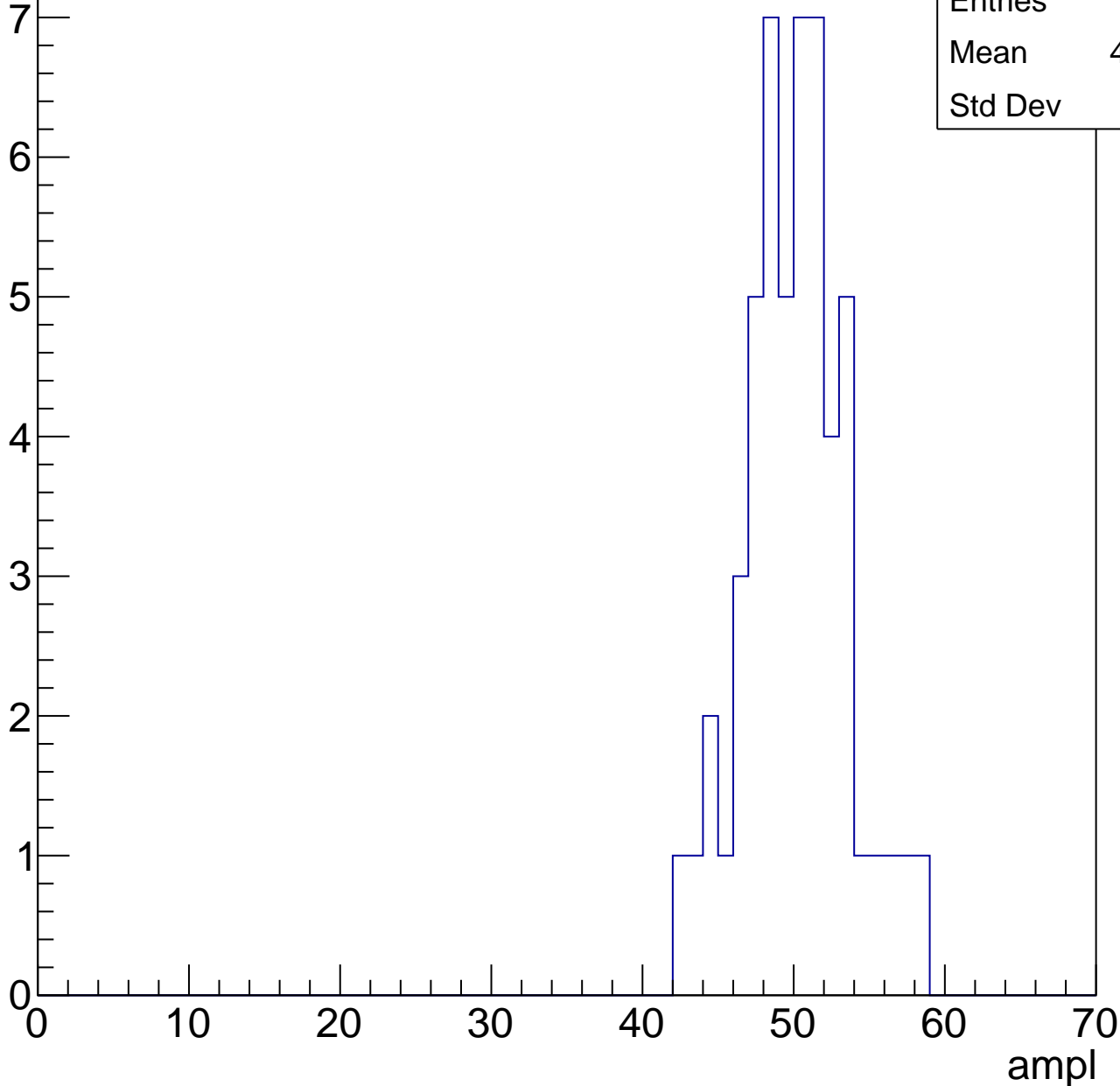


B1L003S, U21-ch89, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

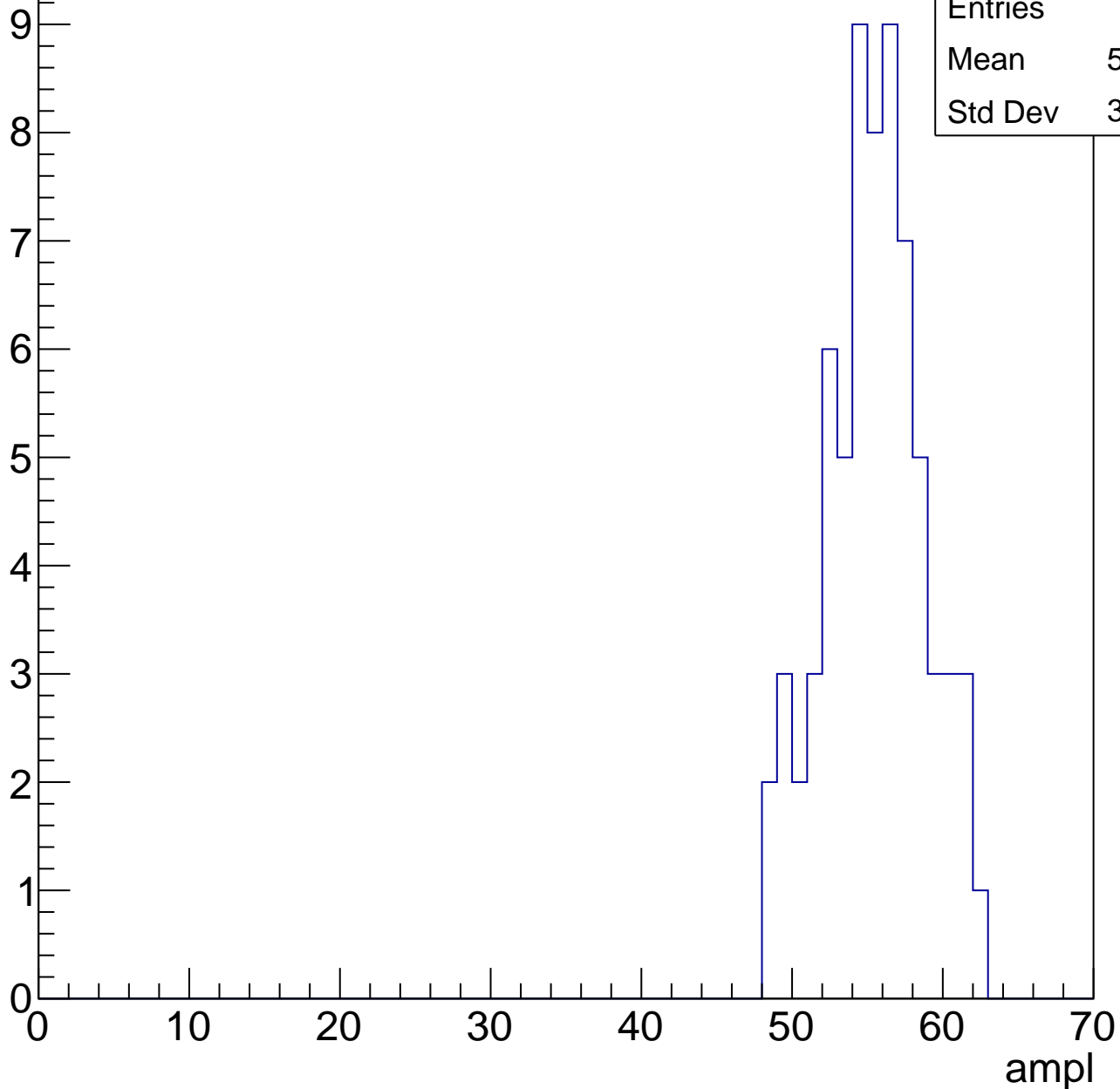
Entries	53
Mean	49.66
Std Dev	3.33



B1L003S, U21-ch89, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

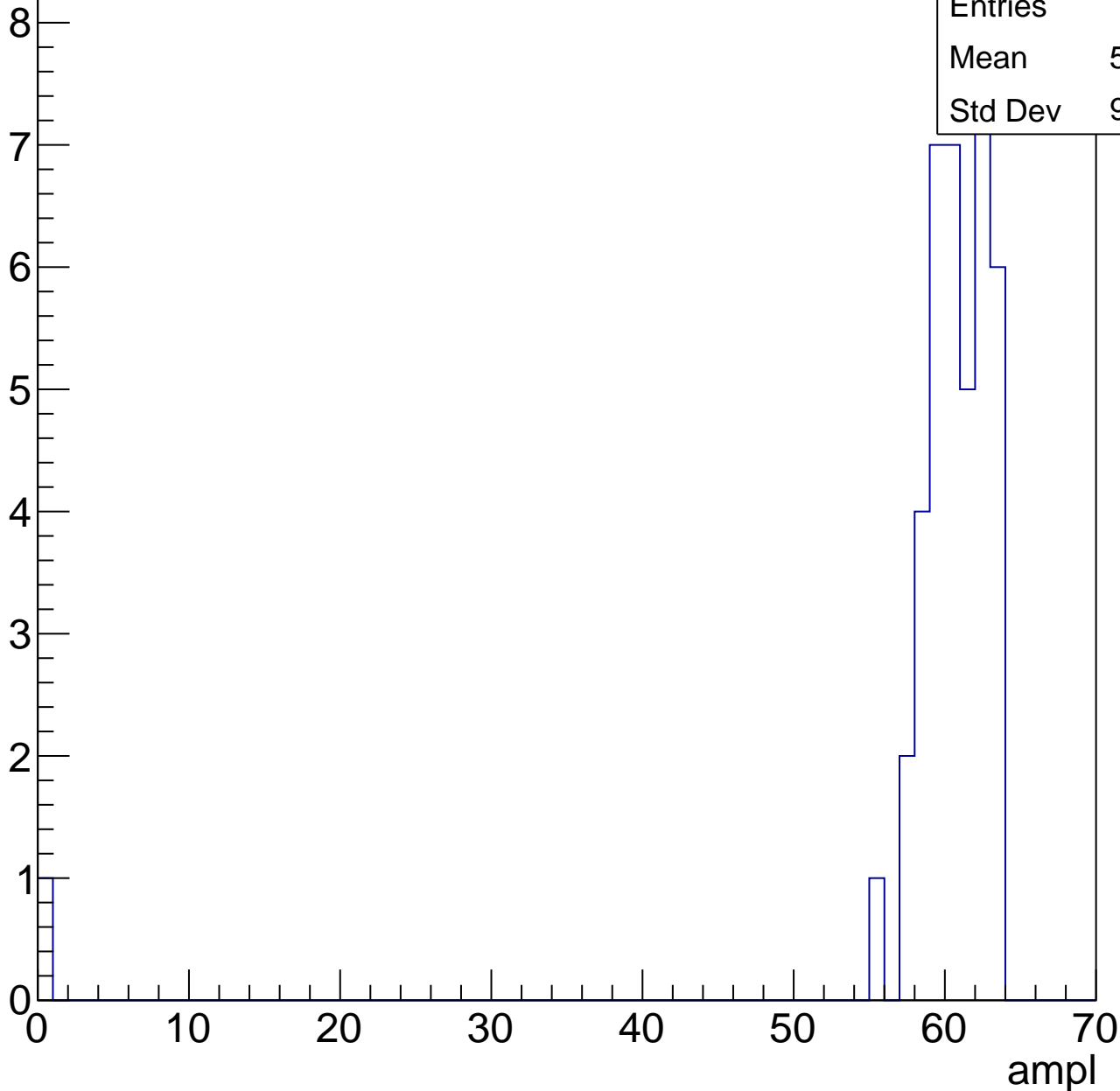


B1L003S, U21-ch89, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

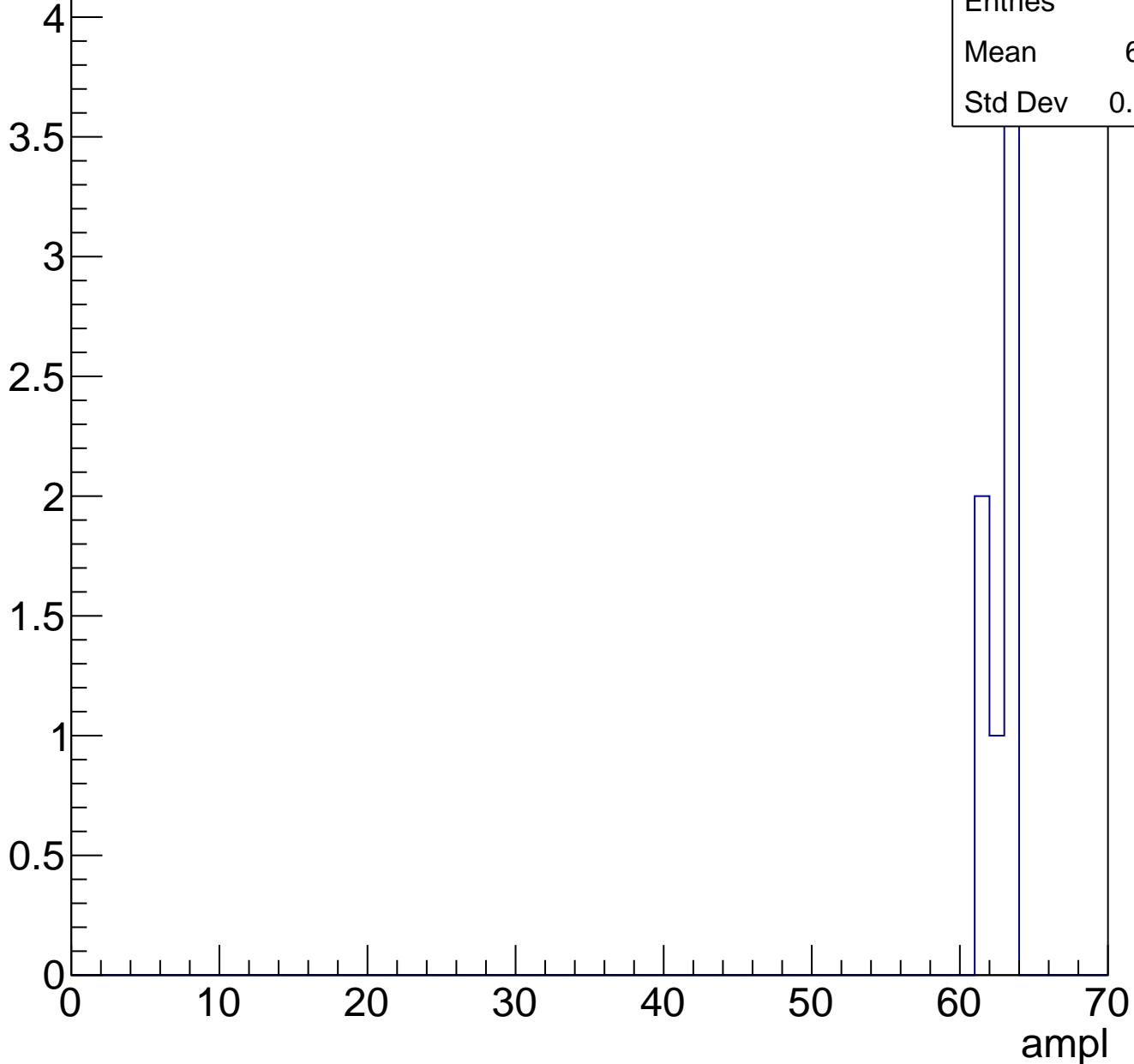
Entries	41
Mean	58.85
Std Dev	9.504



B1L003S, U21-ch89, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch89, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch90, adc0

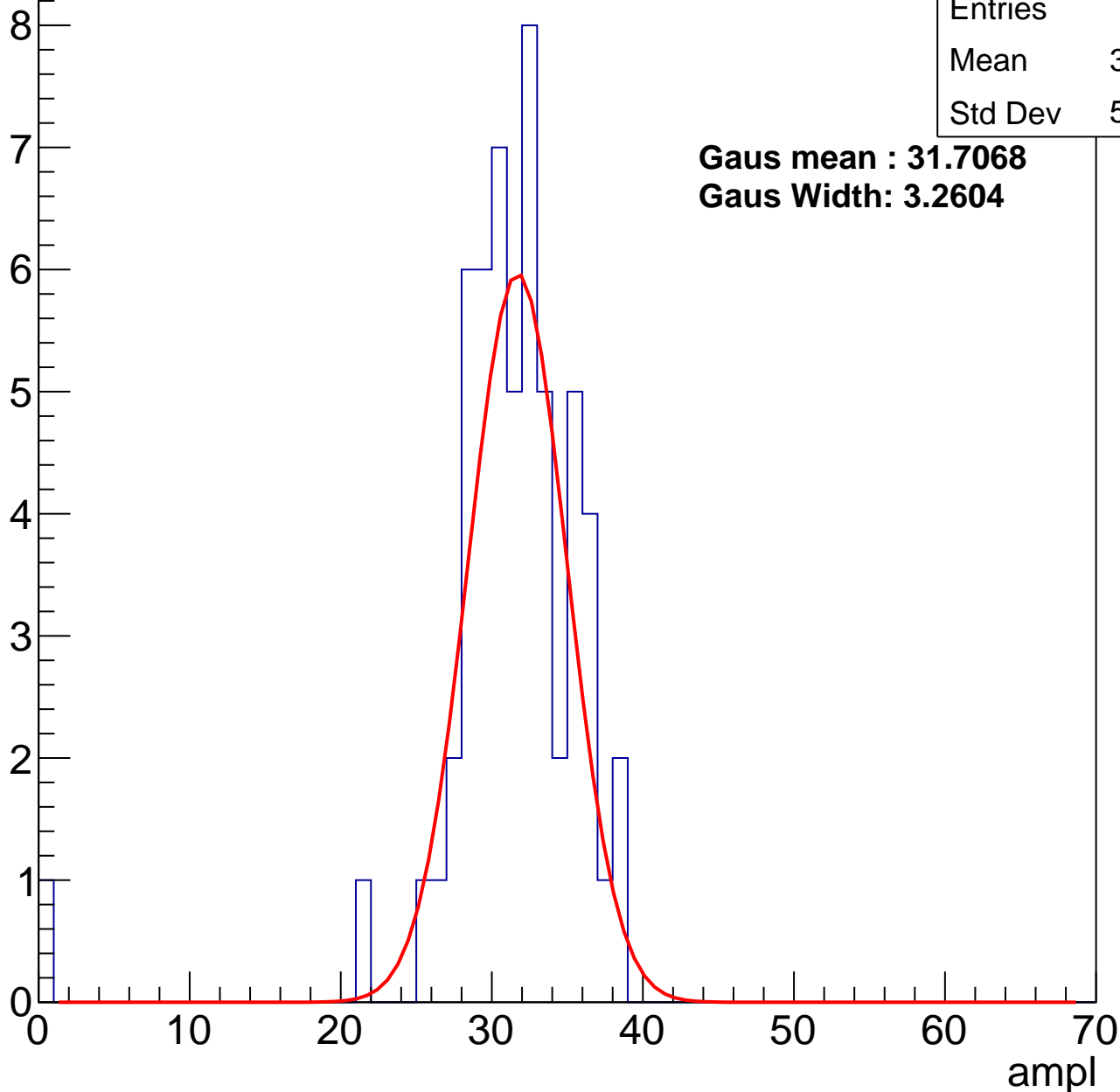
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	30.77
Std Dev	5.292

Gaus mean : 31.7068

Gaus Width: 3.2604



B1L003S, U21-ch90, adc1

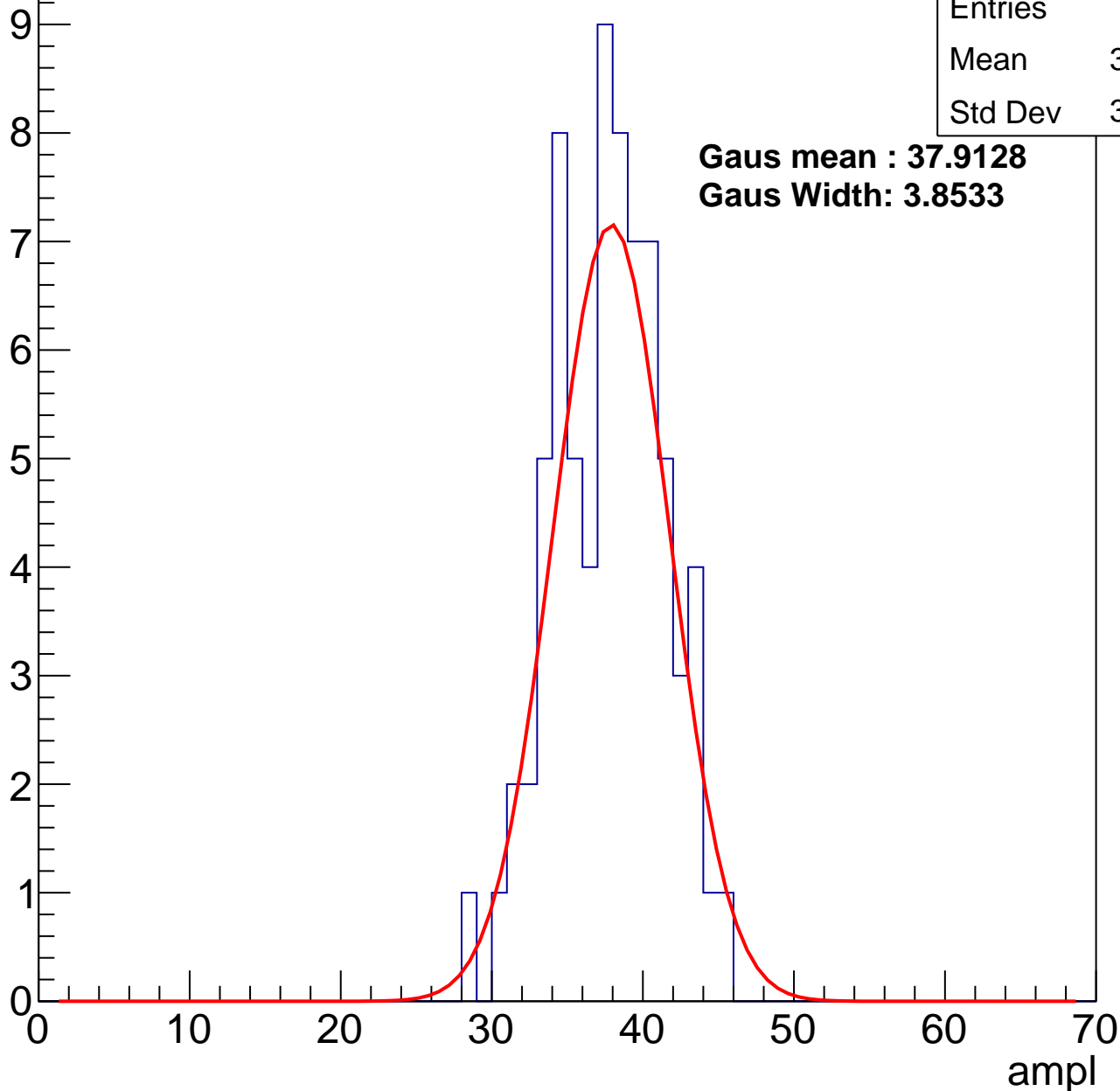
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37.29
Std Dev	3.575

Gaus mean : 37.9128

Gaus Width: 3.8533



B1L003S, U21-ch90, adc2

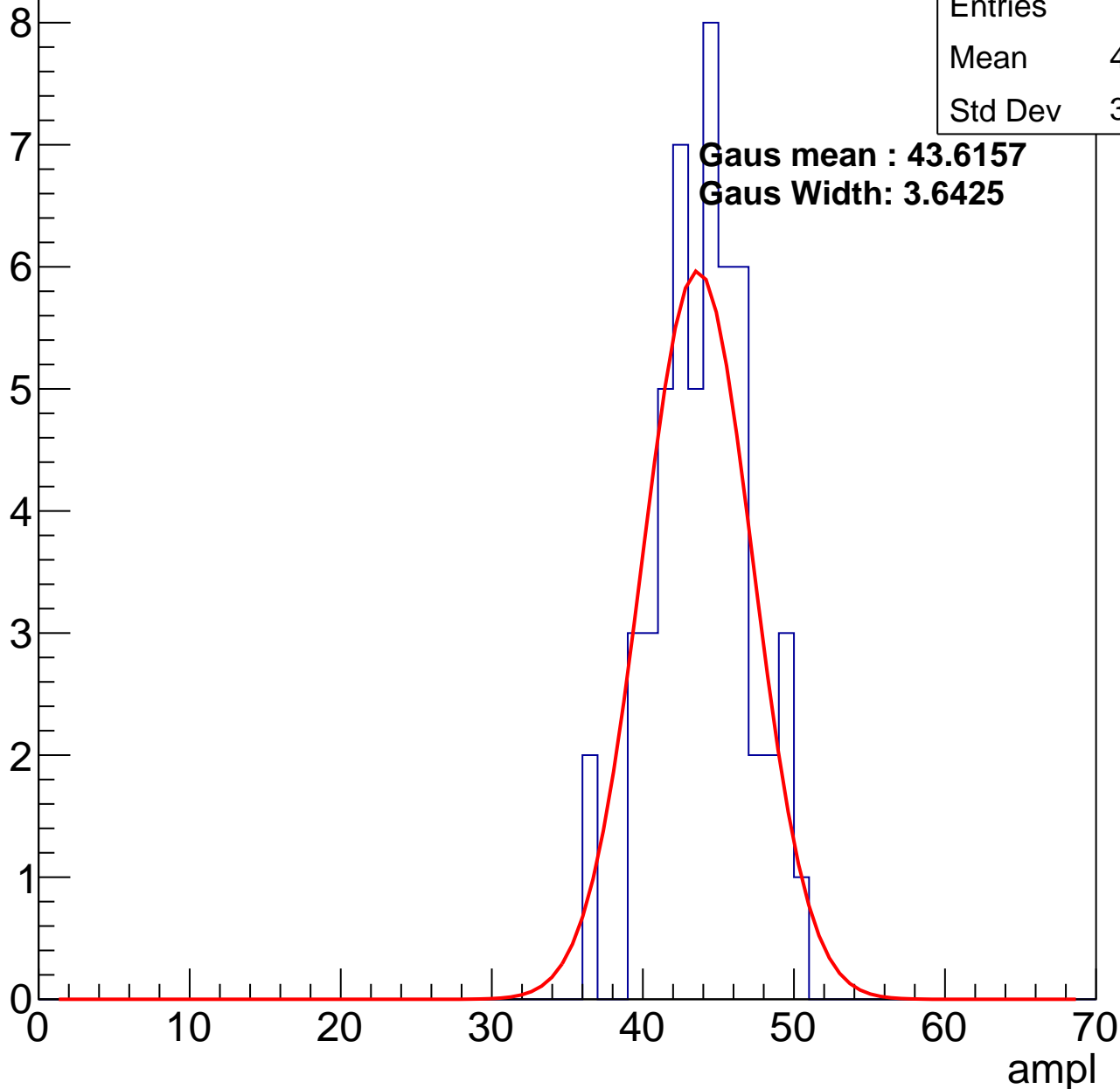
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	43.55
Std Dev	3.099

Gaus mean : 43.6157

Gaus Width: 3.6425

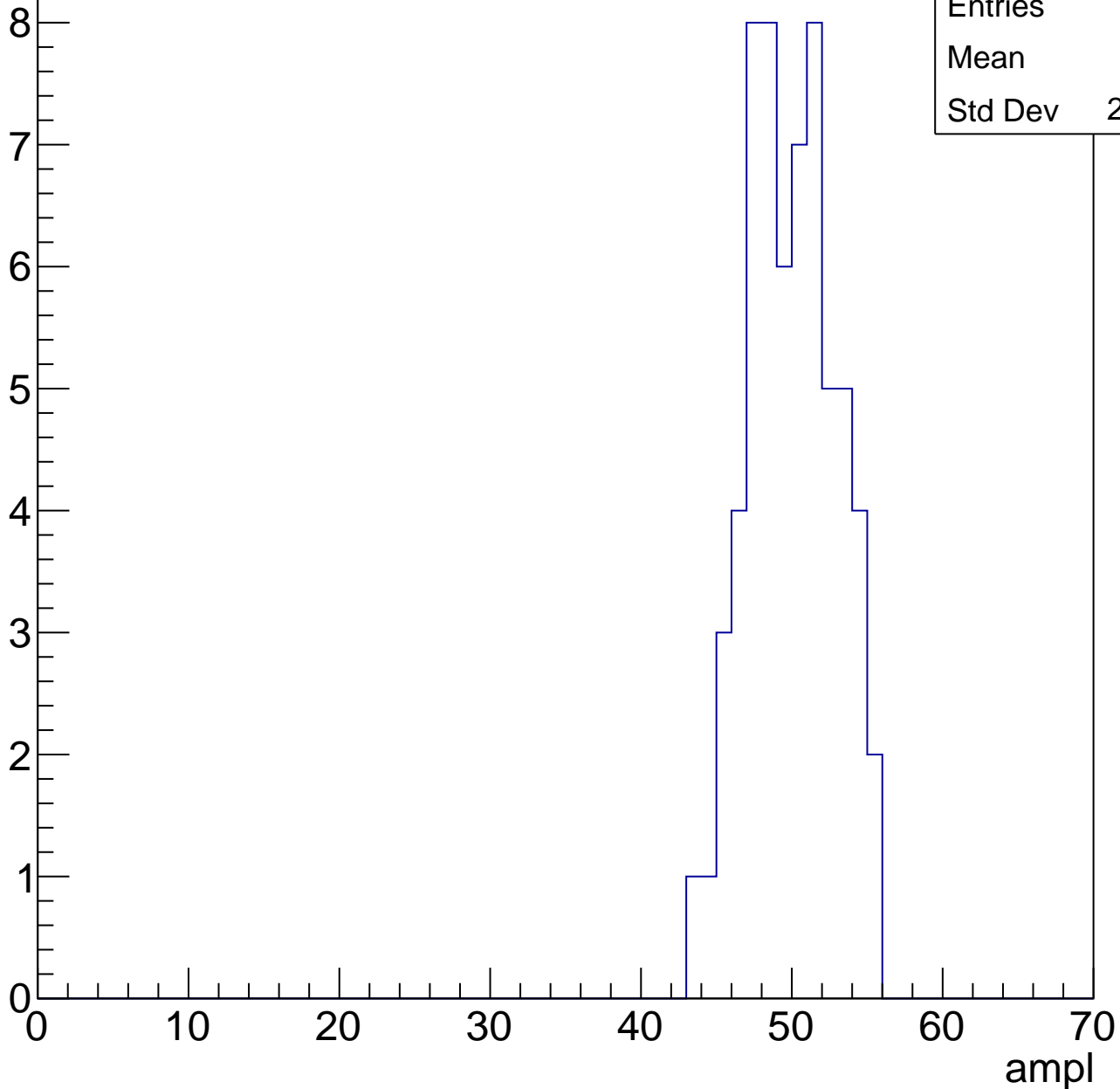


B1L003S, U21-ch90, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	49.5
Std Dev	2.855

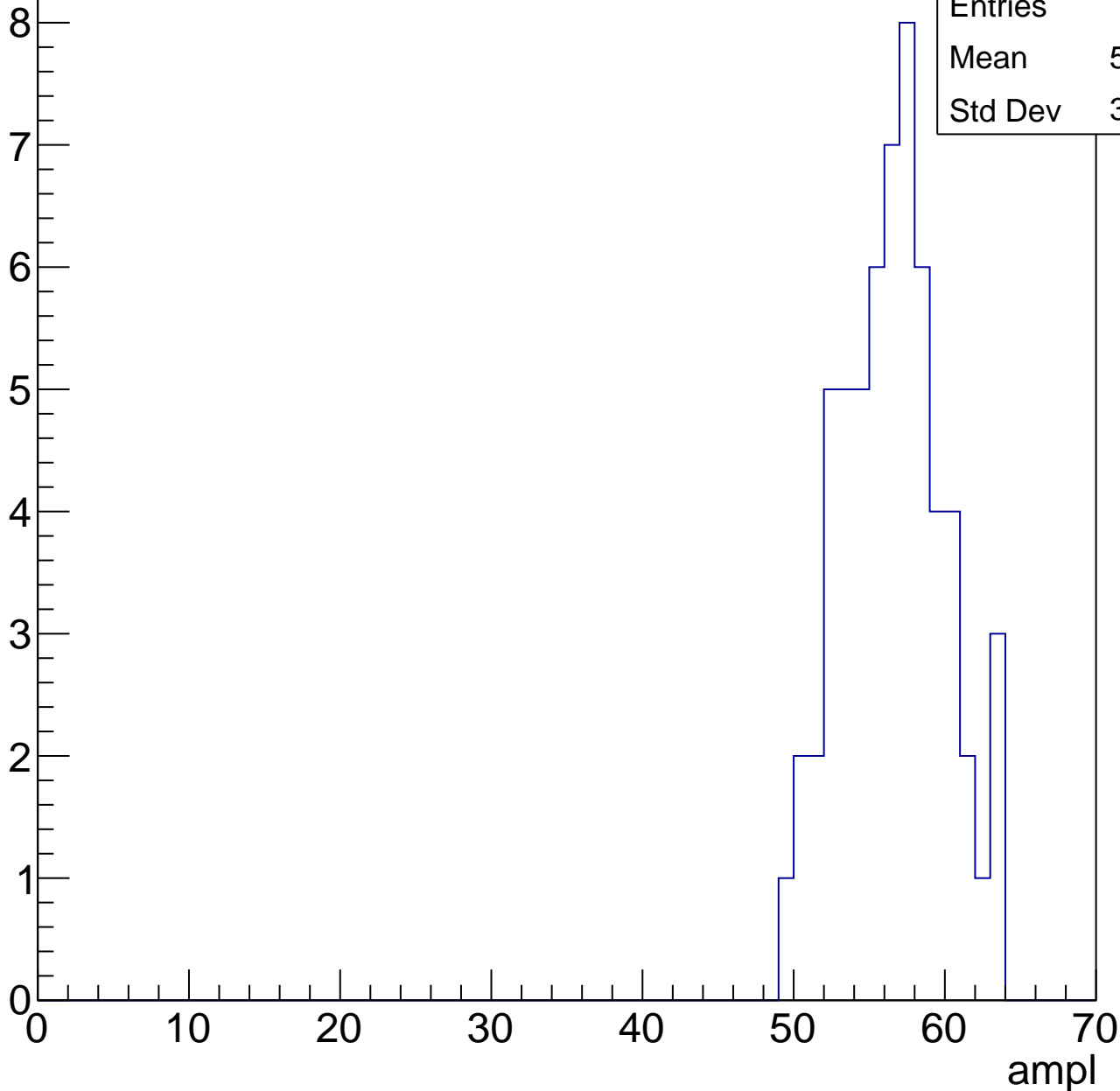


B1L003S, U21-ch90, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	56.08
Std Dev	3.355

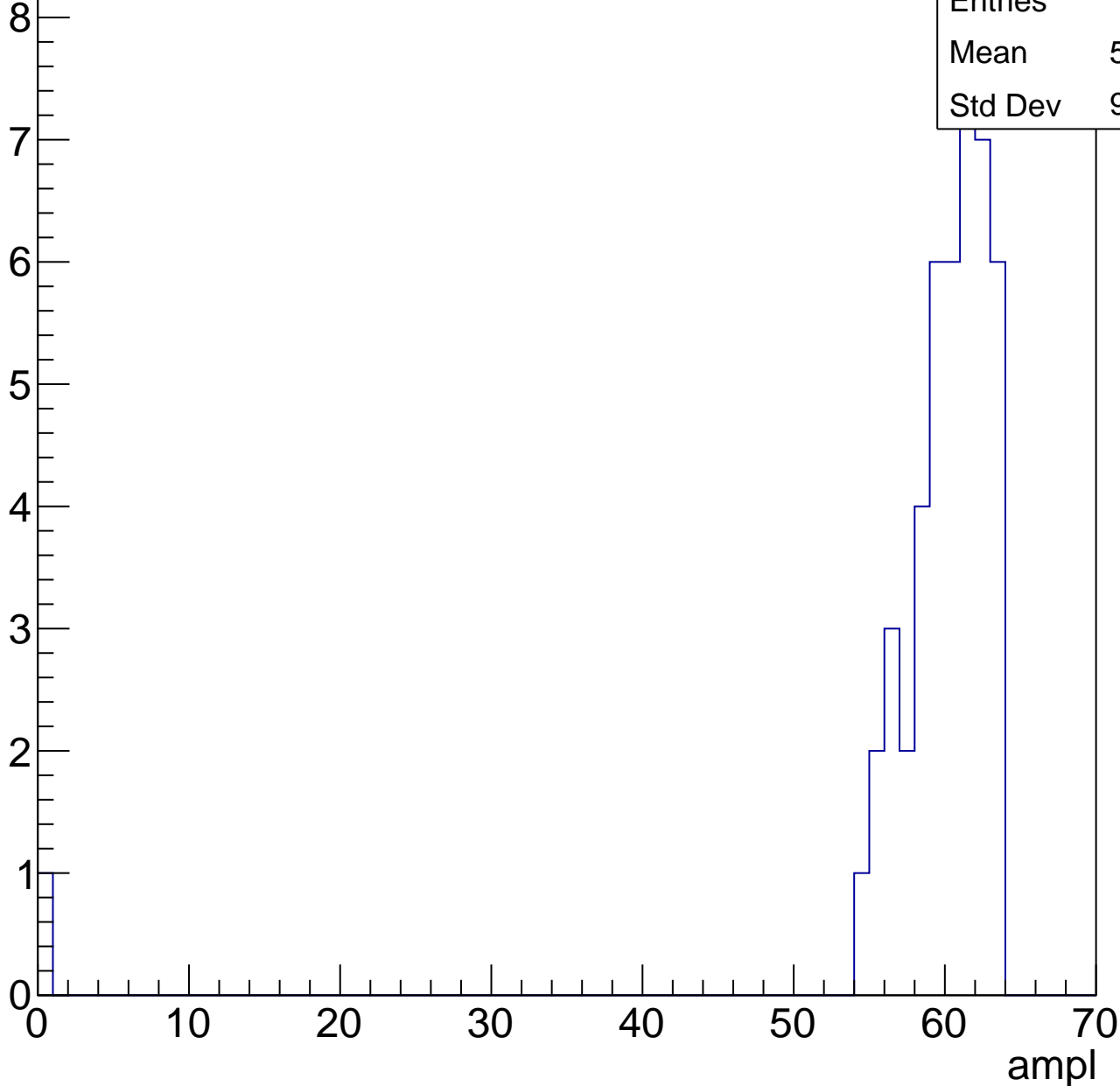


B1L003S, U21-ch90, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	58.52
Std Dev	9.045



B1L003S, U21-ch90, adc6

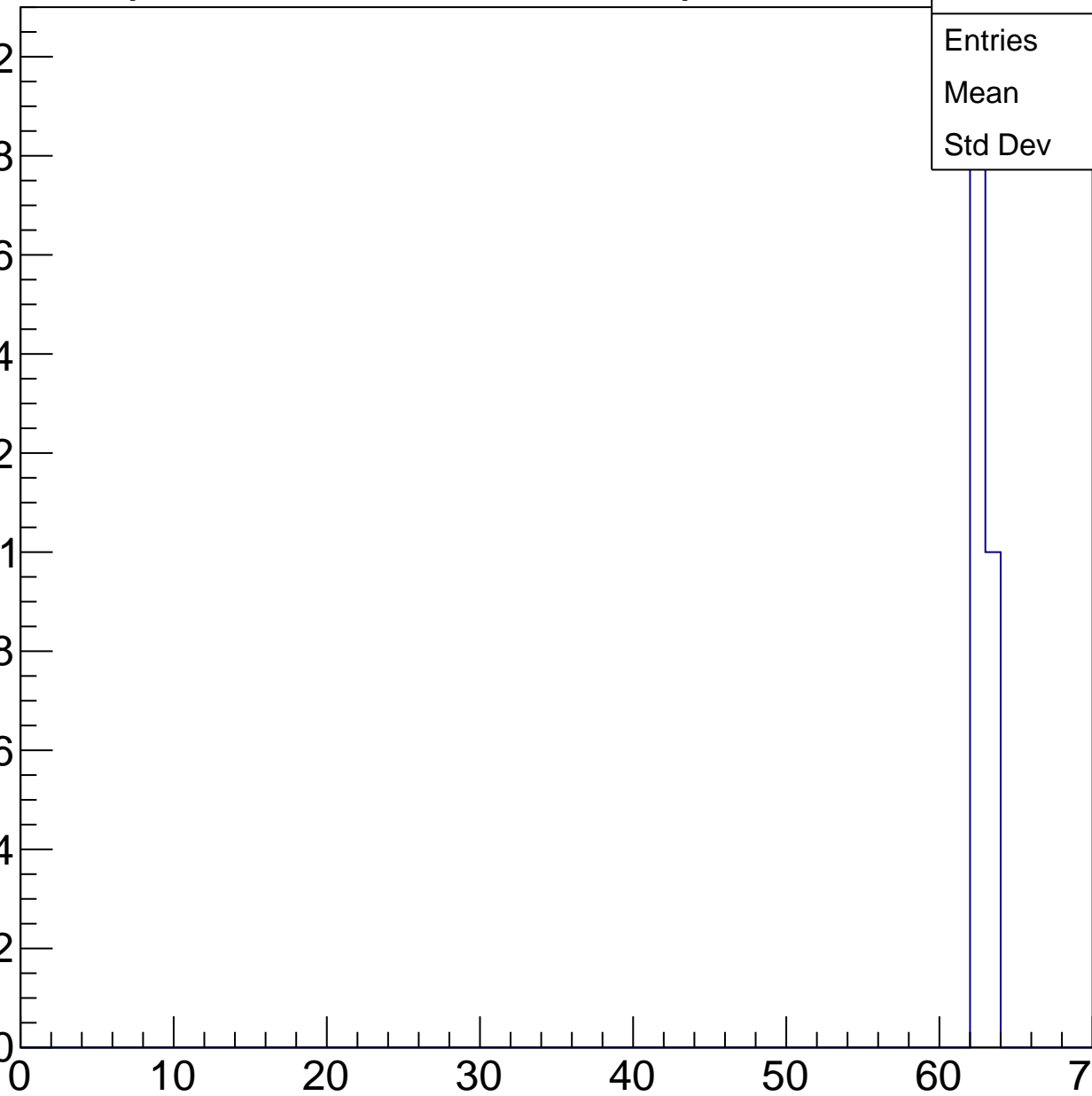
calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.4714

ampl



B1L003S, U21-ch90, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch91, adc0

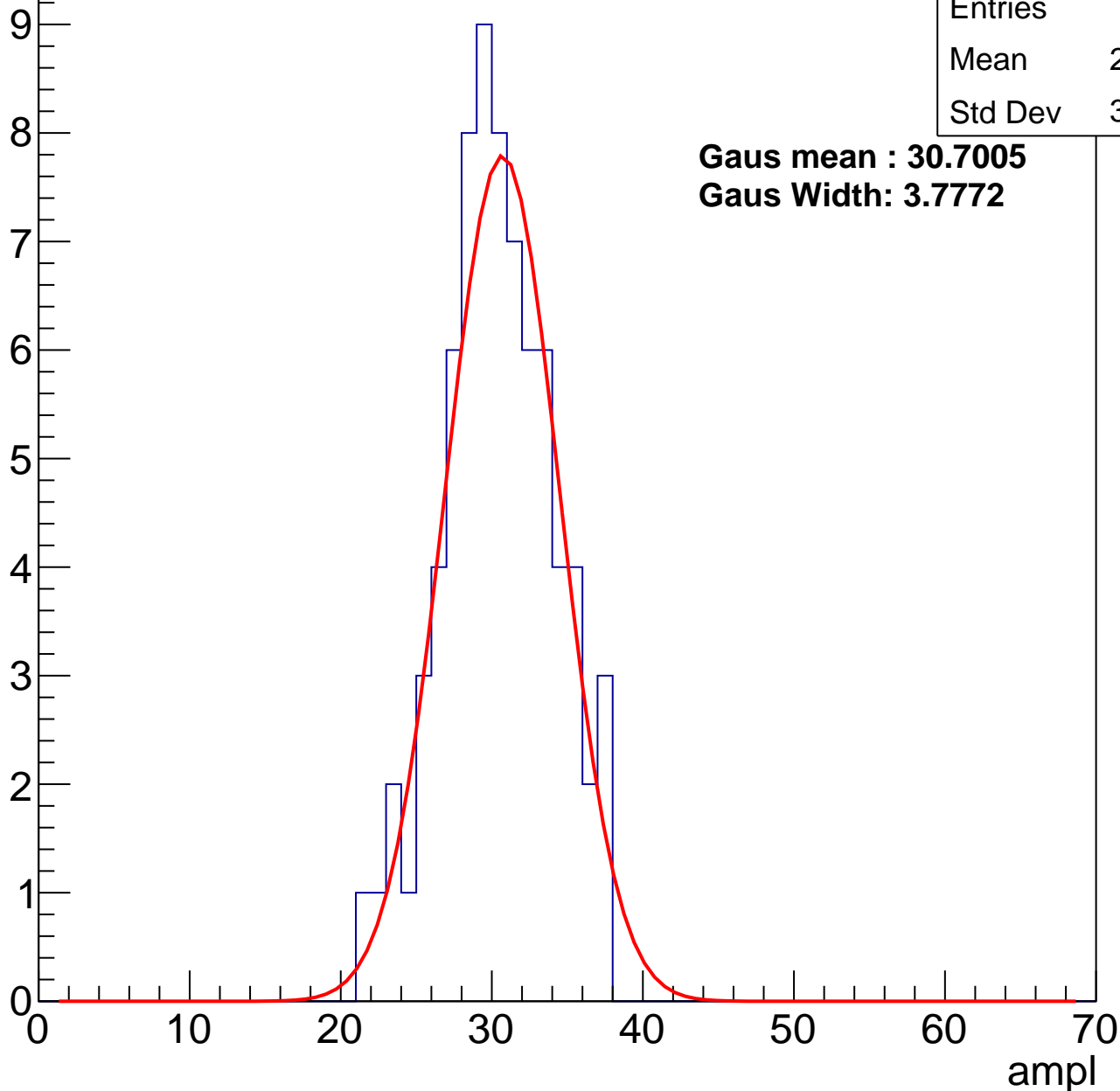
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	29.93
Std Dev	3.616

Gaus mean : 30.7005

Gaus Width: 3.7772



B1L003S, U21-ch91, adc1

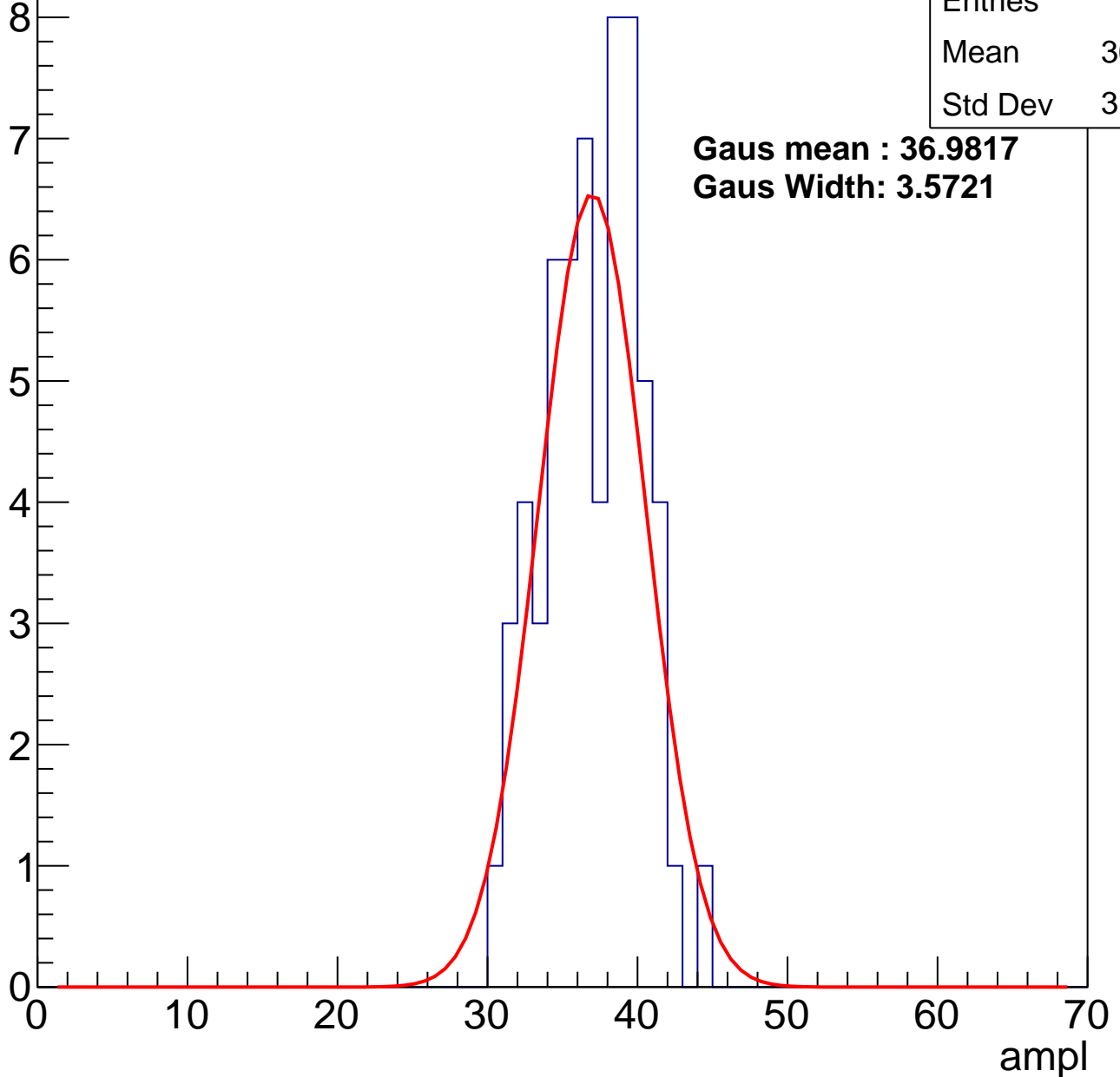
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.56
Std Dev	3.139

Gaus mean : 36.9817

Gaus Width: 3.5721

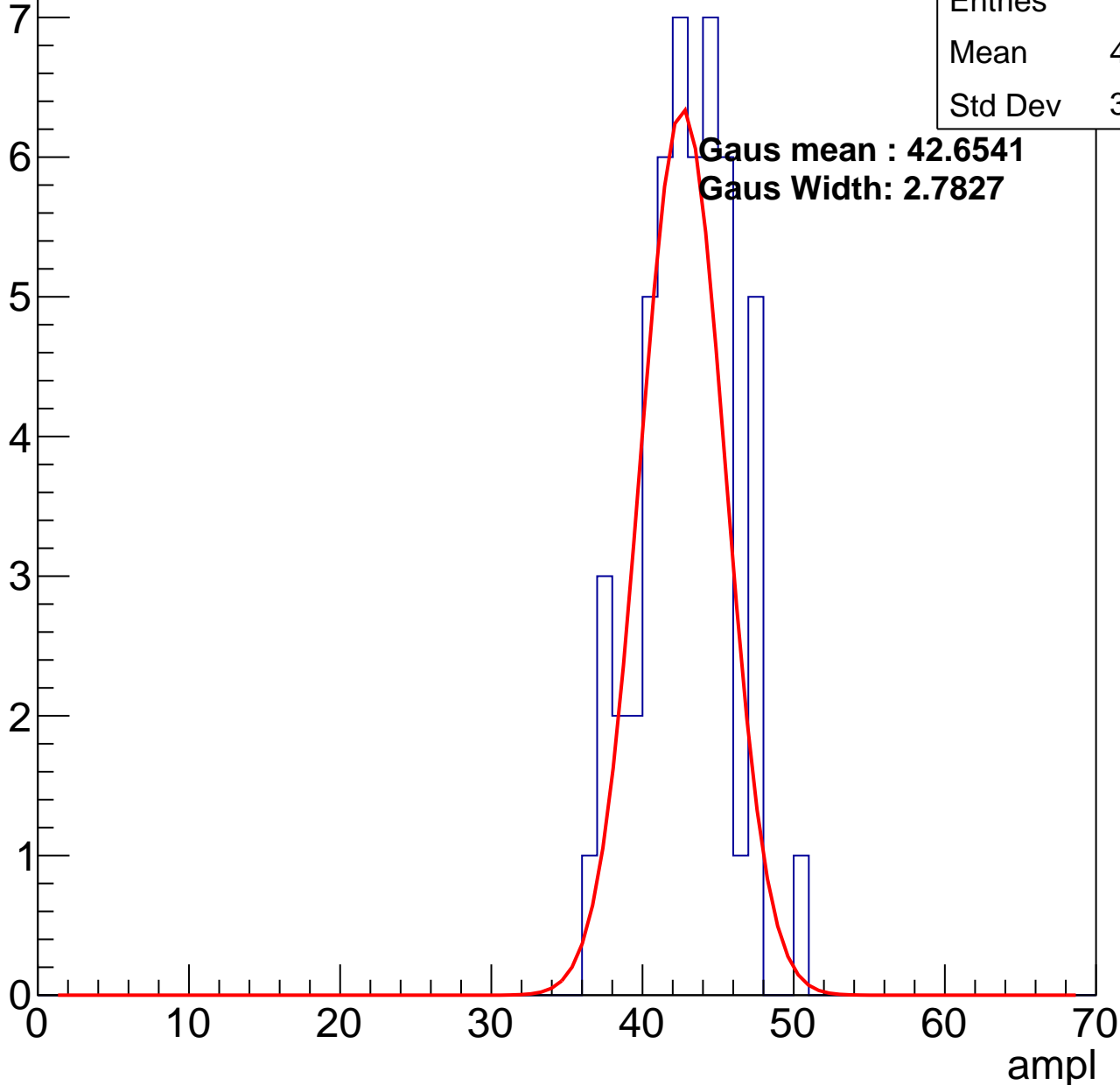


B1L003S, U21-ch91, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.46
Std Dev	3.003

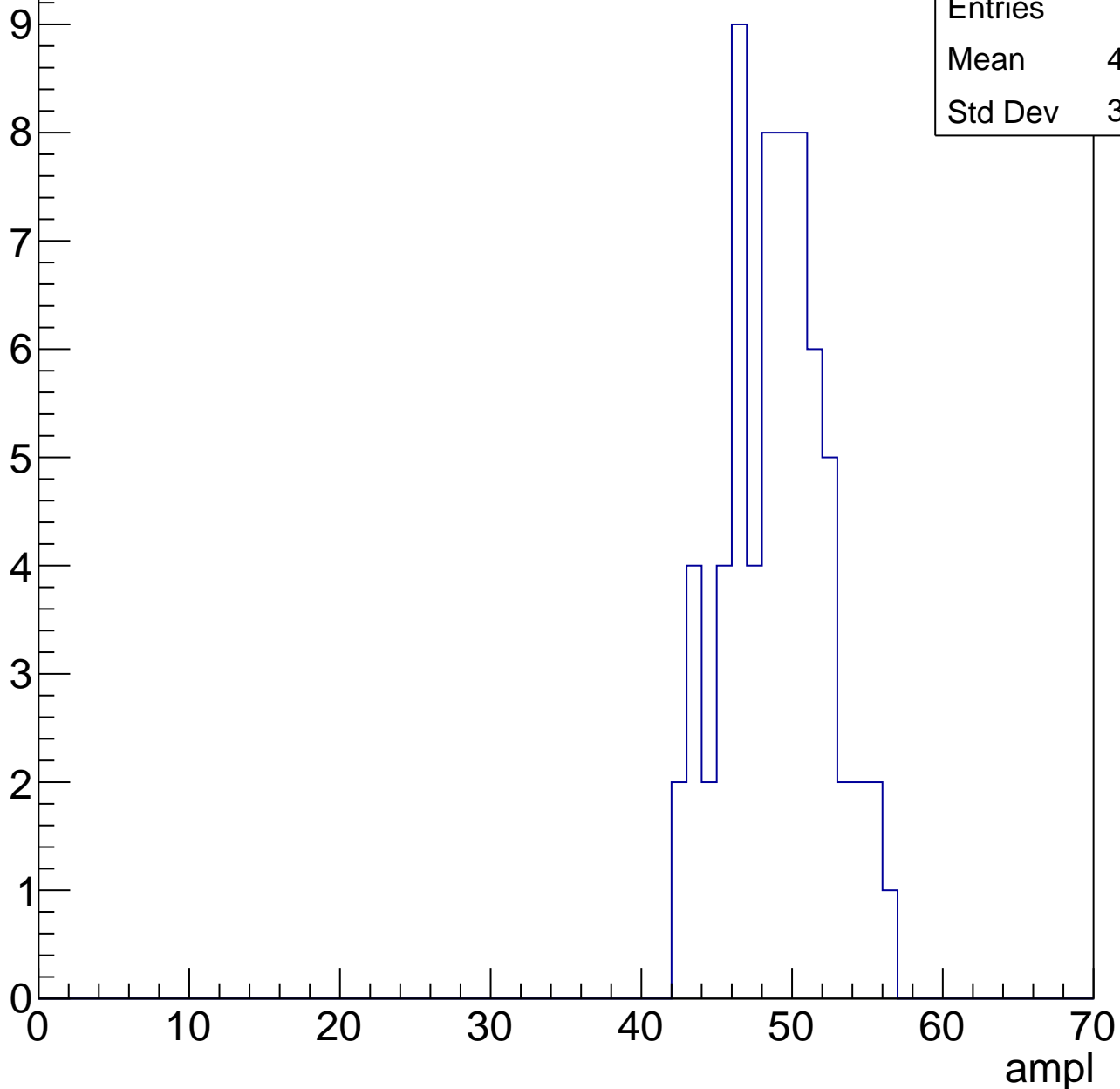


B1L003S, U21-ch91, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	48.48
Std Dev	3.284



B1L003S, U21-ch91, adc4

calib_packv5_042523_0143.root, FC#13, port D2

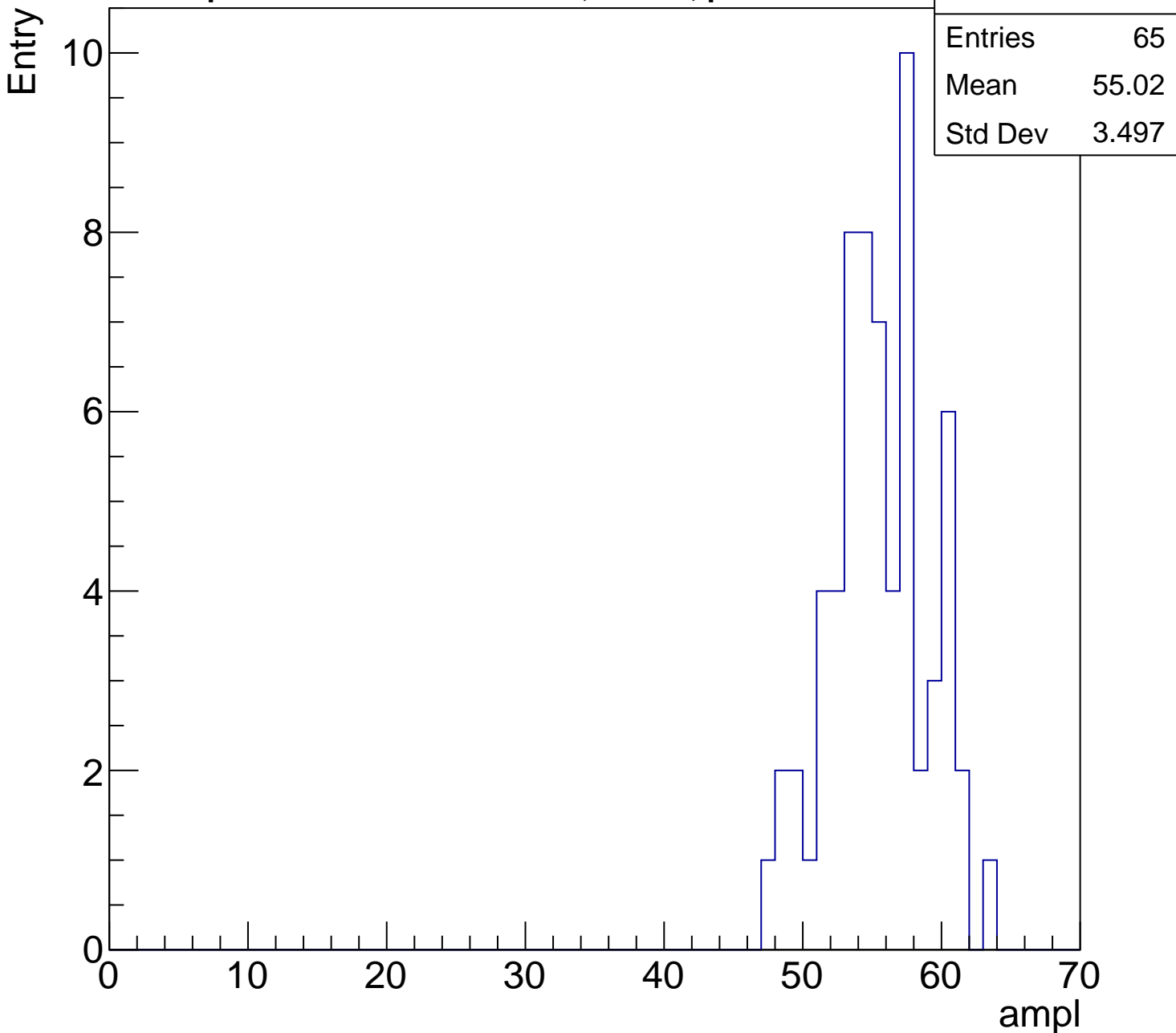
Entries	65
Mean	55.02
Std Dev	3.497

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

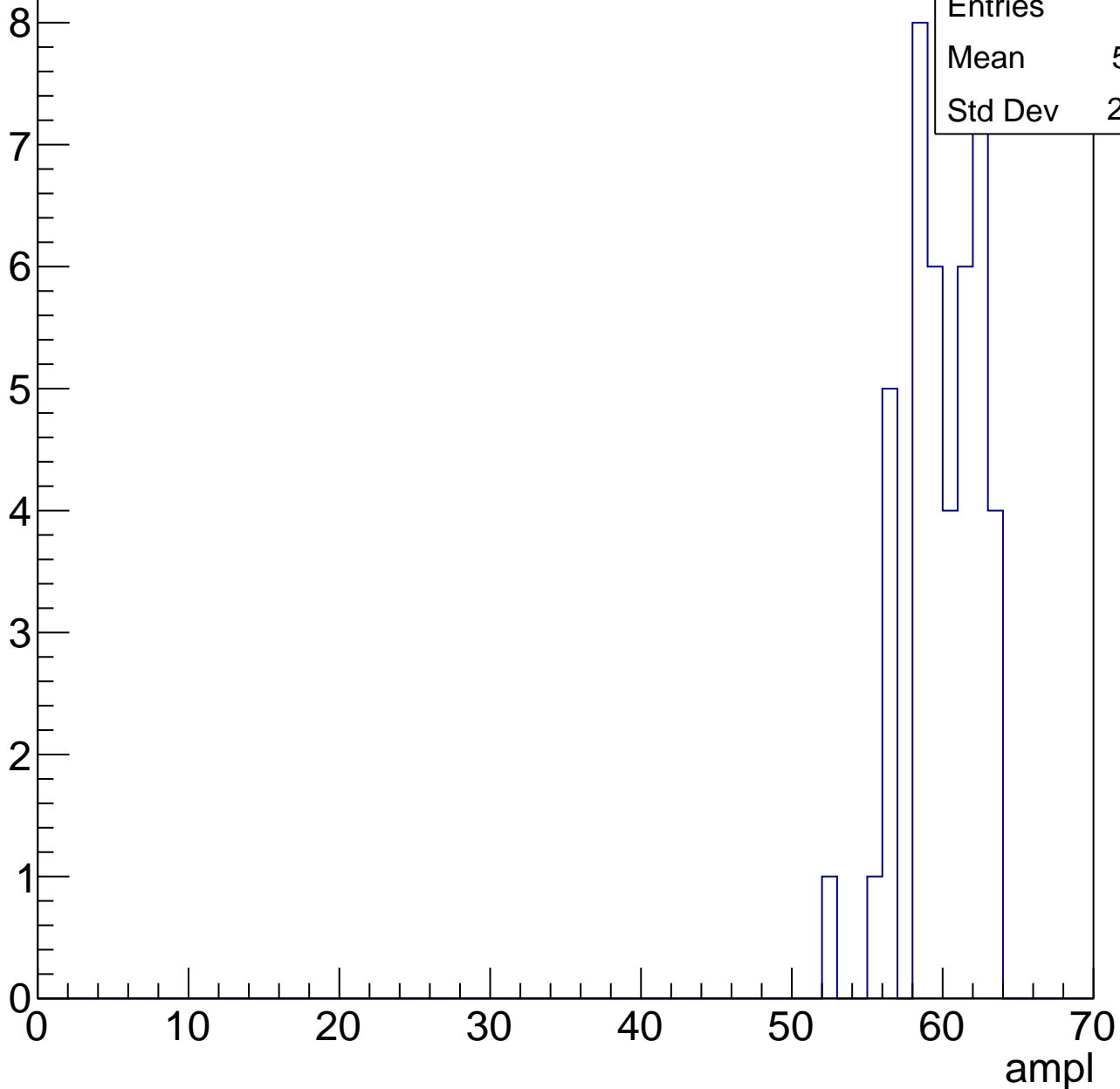


B1L003S, U21-ch91, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	59.51
Std Dev	2.509

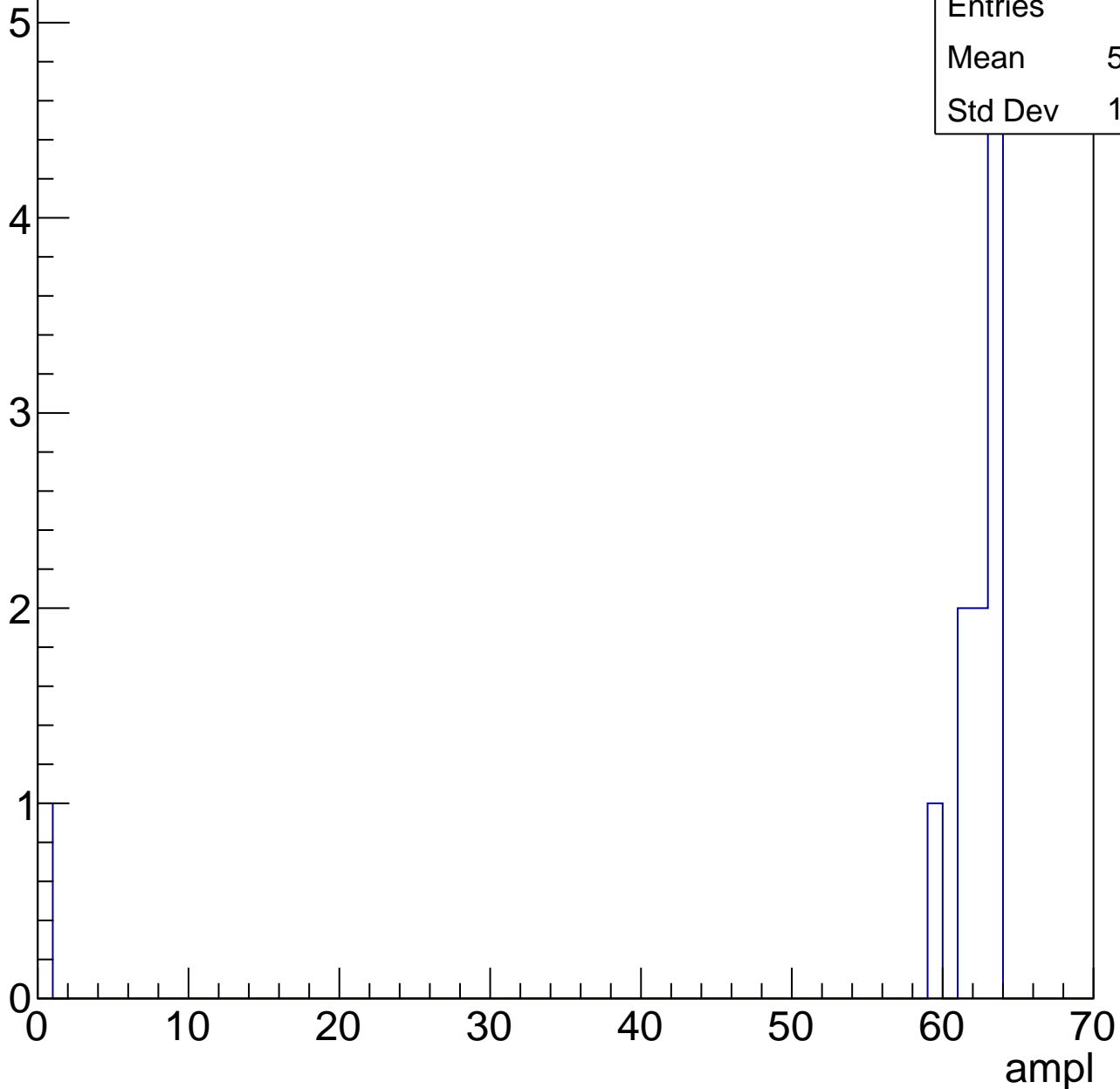


B1L003S, U21-ch91, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.36
Std Dev	17.86



B1L003S, U21-ch91, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch92, adc0

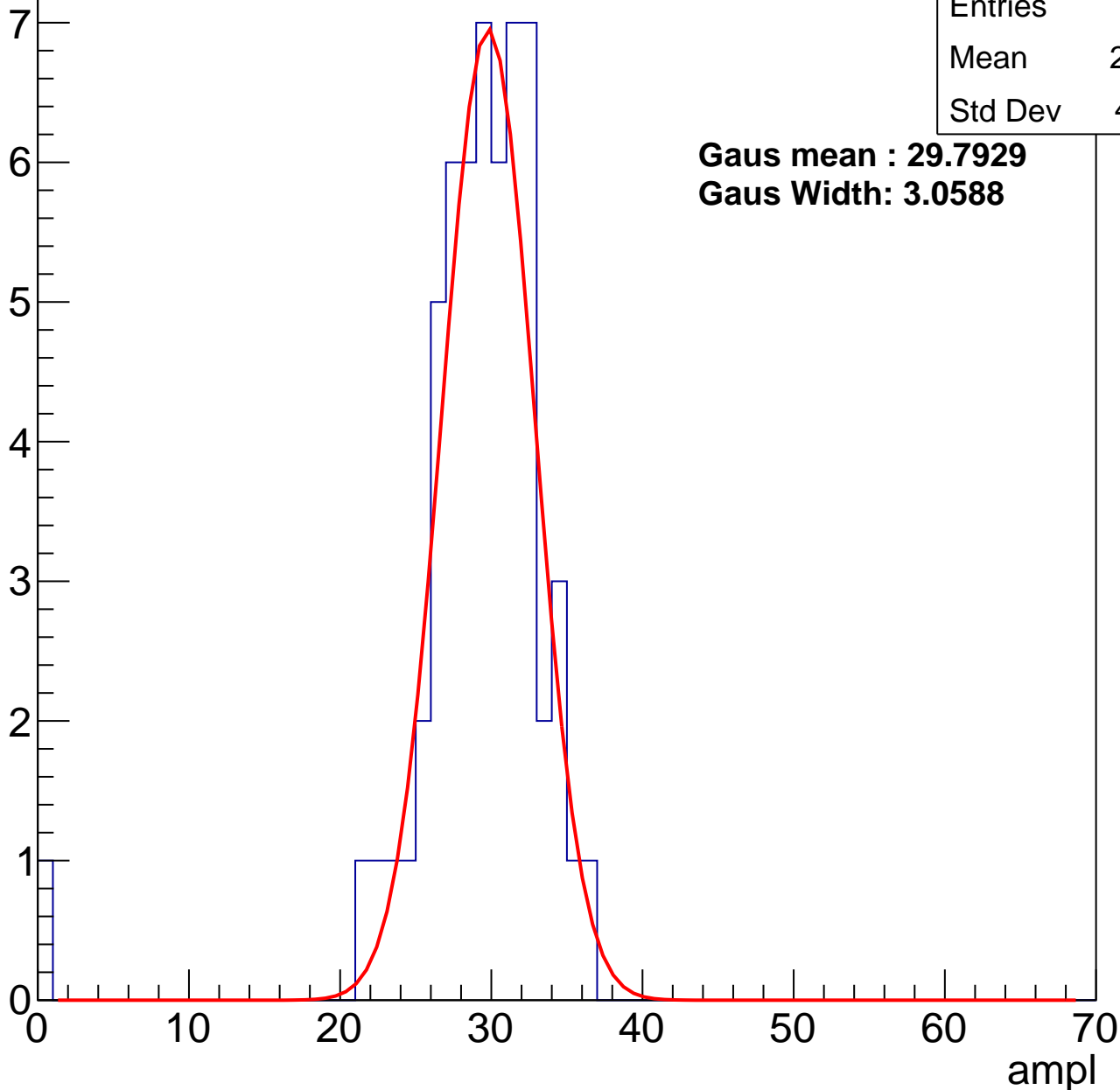
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	28.67
Std Dev	4.921

Gaus mean : 29.7929

Gaus Width: 3.0588



B1L003S, U21-ch92, adc1

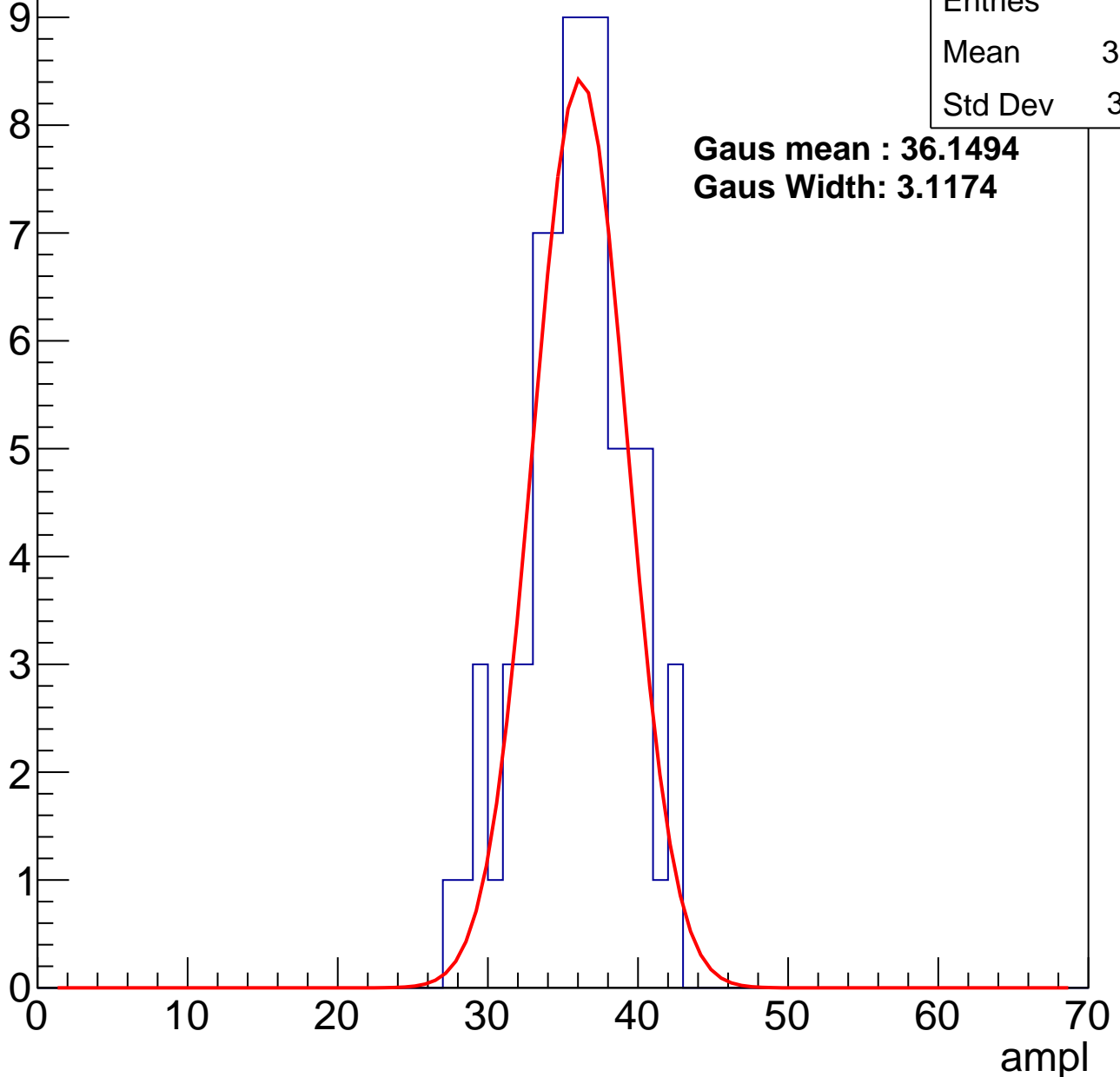
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	72
Mean	35.47
Std Dev	3.371

Gaus mean : 36.1494

Gaus Width: 3.1174



B1L003S, U21-ch92, adc2

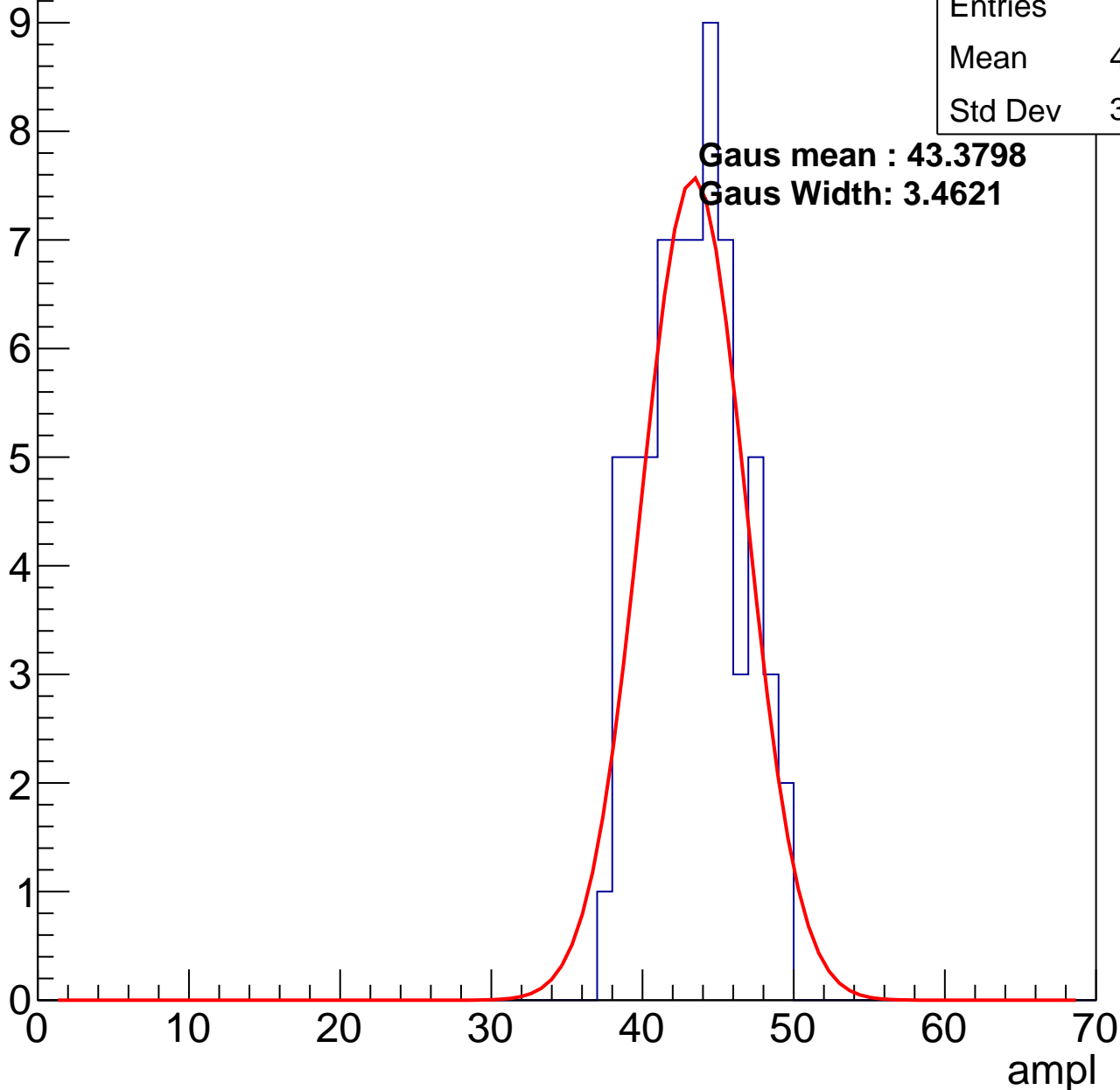
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	42.88
Std Dev	3.043

Gaus mean : 43.3798

Gaus Width: 3.4621

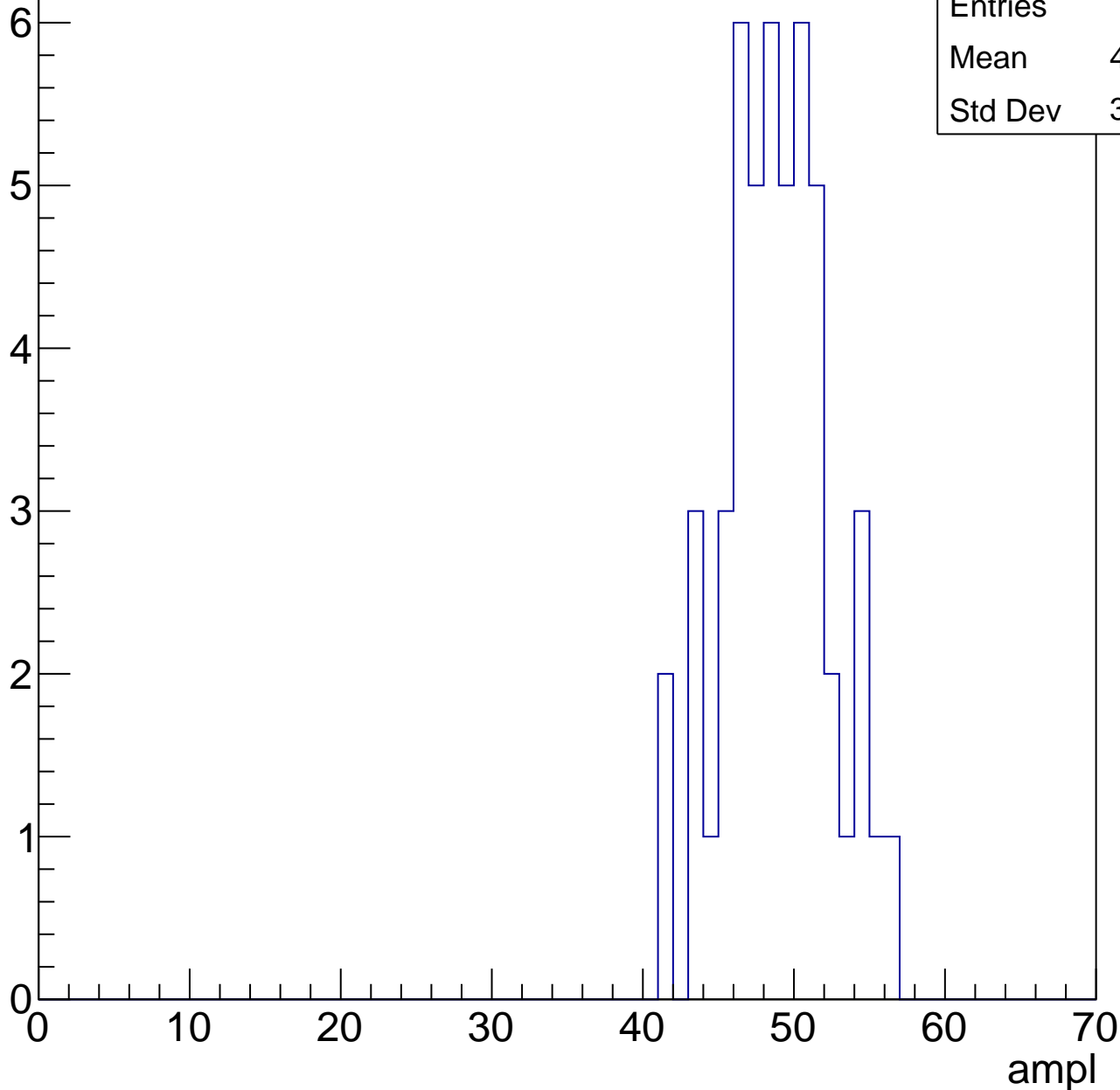


B1L003S, U21-ch92, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	50
Mean	48.38
Std Dev	3.434

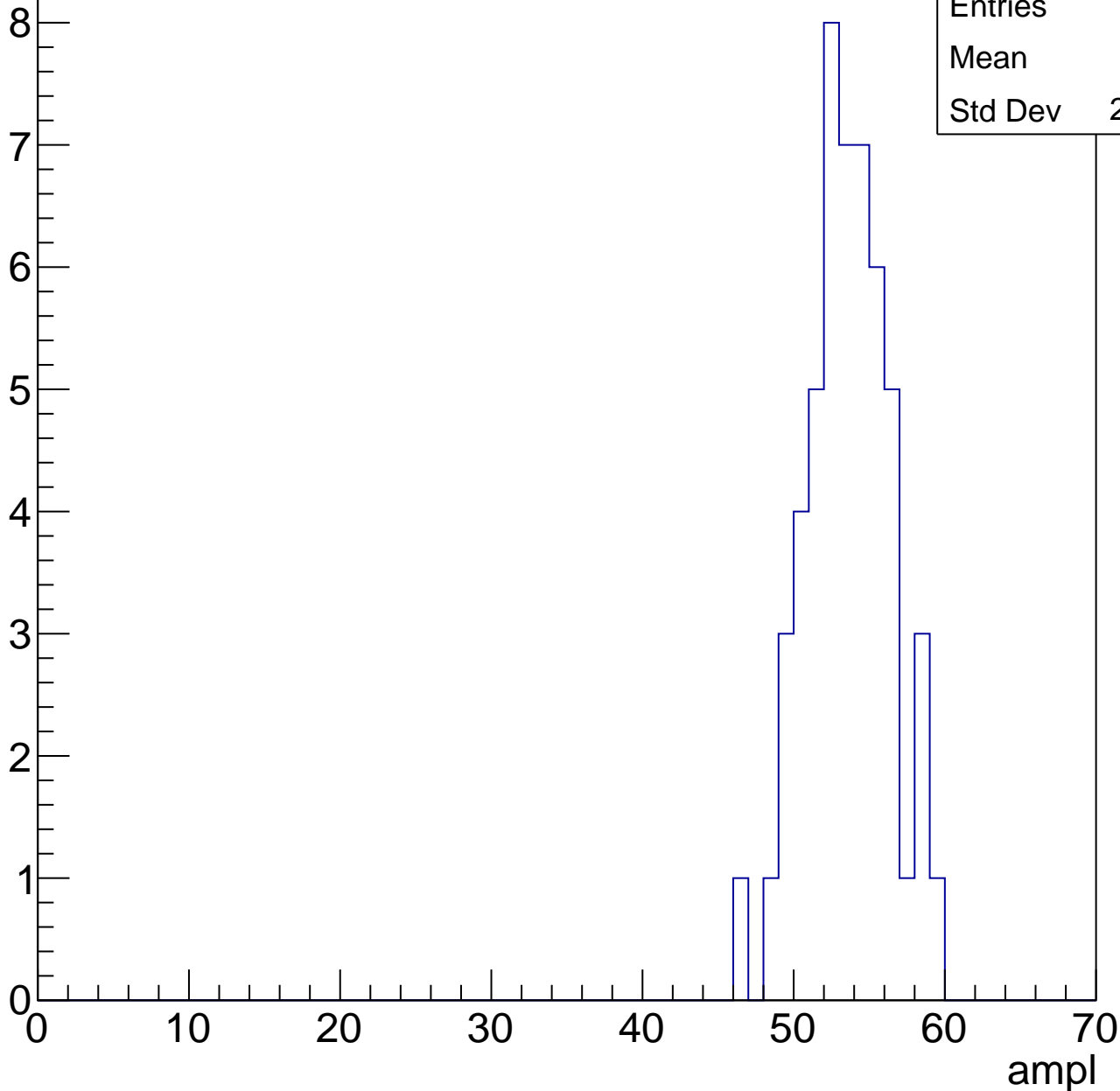


B1L003S, U21-ch92, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	53.1
Std Dev	2.733



B1L003S, U21-ch92, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

10

8

6

4

2

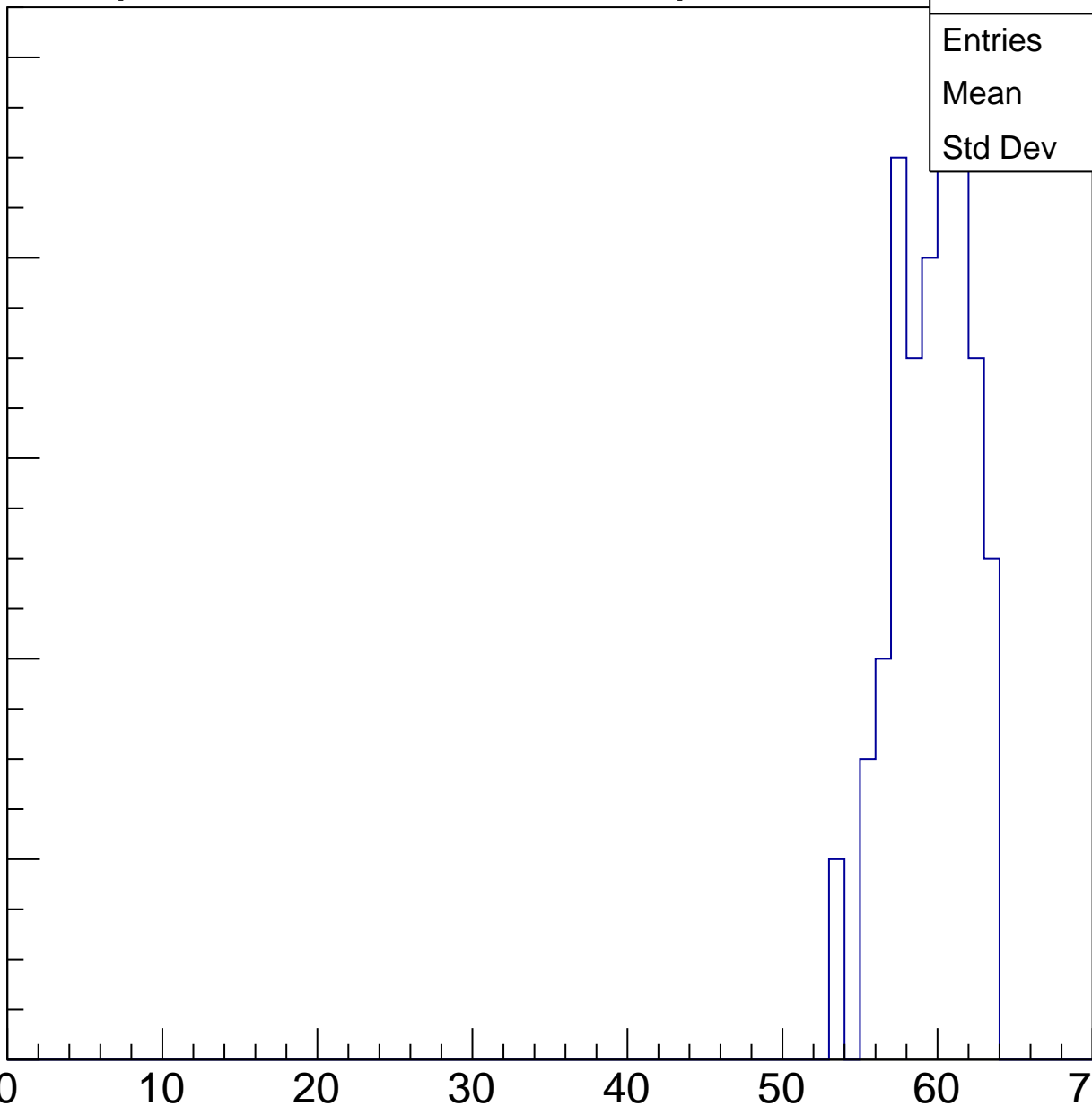
0

Entries 64

Mean 59.14

Std Dev 2.468

ampl

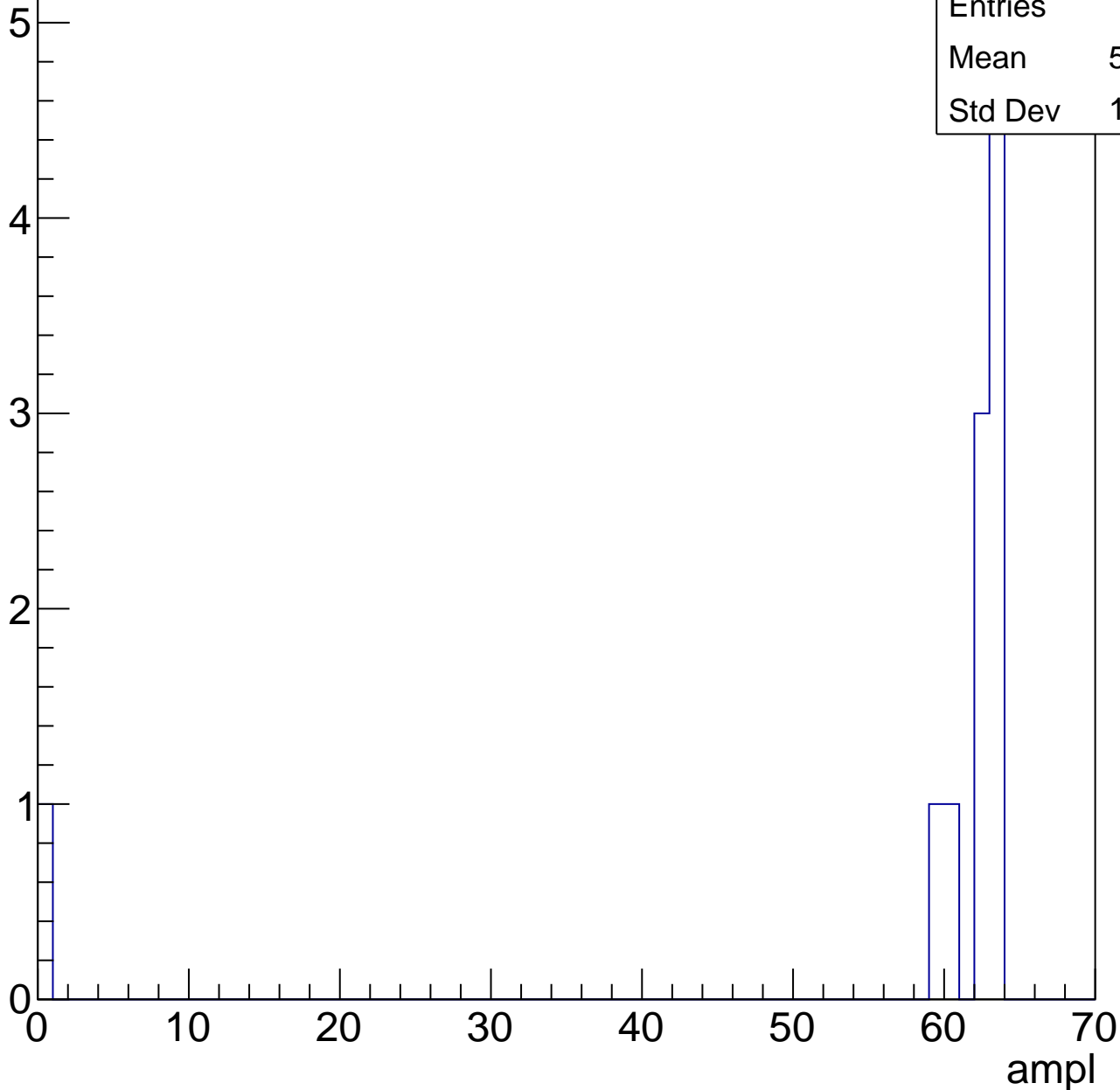


B1L003S, U21-ch92, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	11
Mean	56.36
Std Dev	17.87



B1L003S, U21-ch92, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch93, adc0

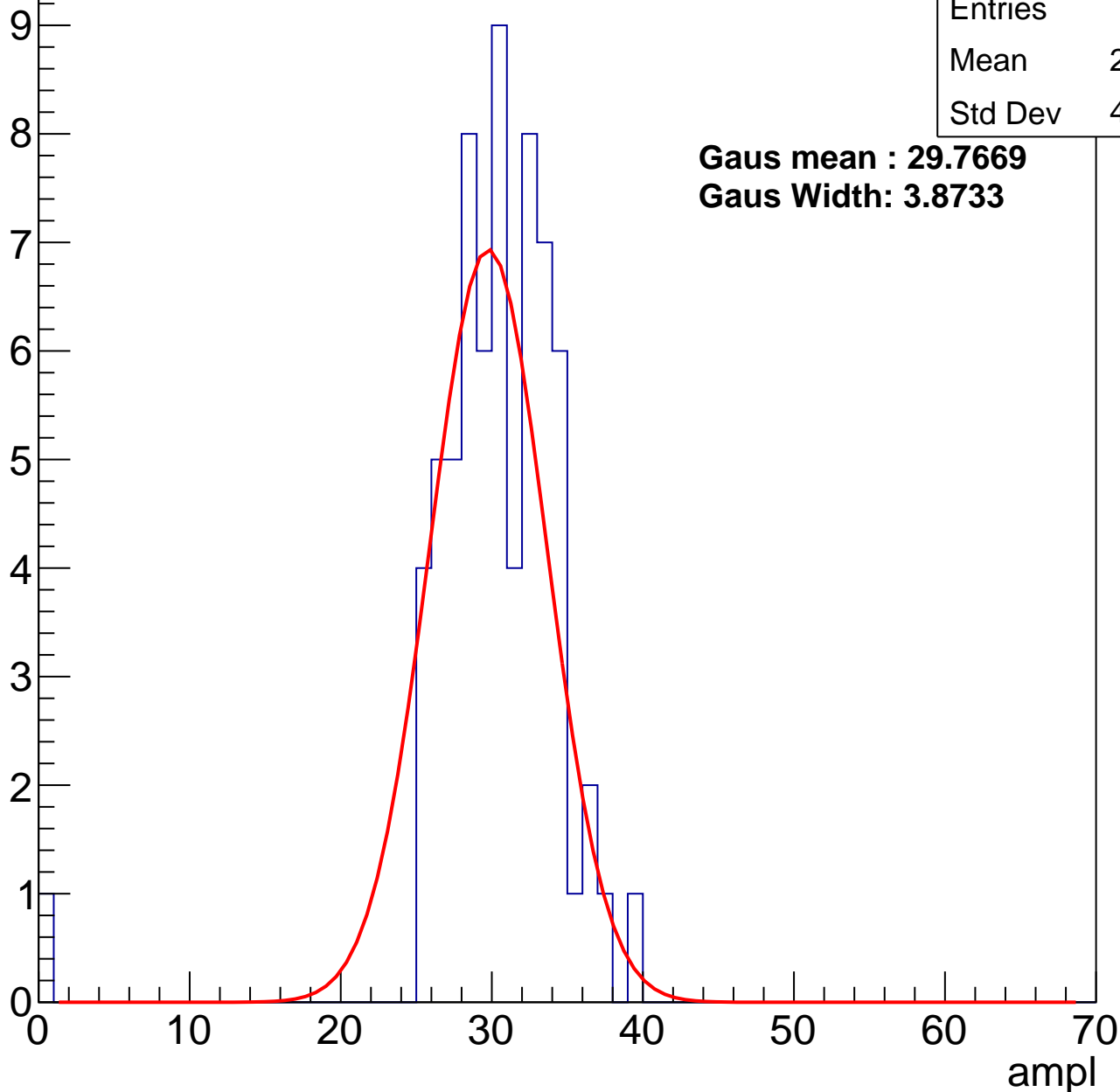
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	29.87
Std Dev	4.826

Gaus mean : 29.7669

Gaus Width: 3.8733



B1L003S, U21-ch93, adc1

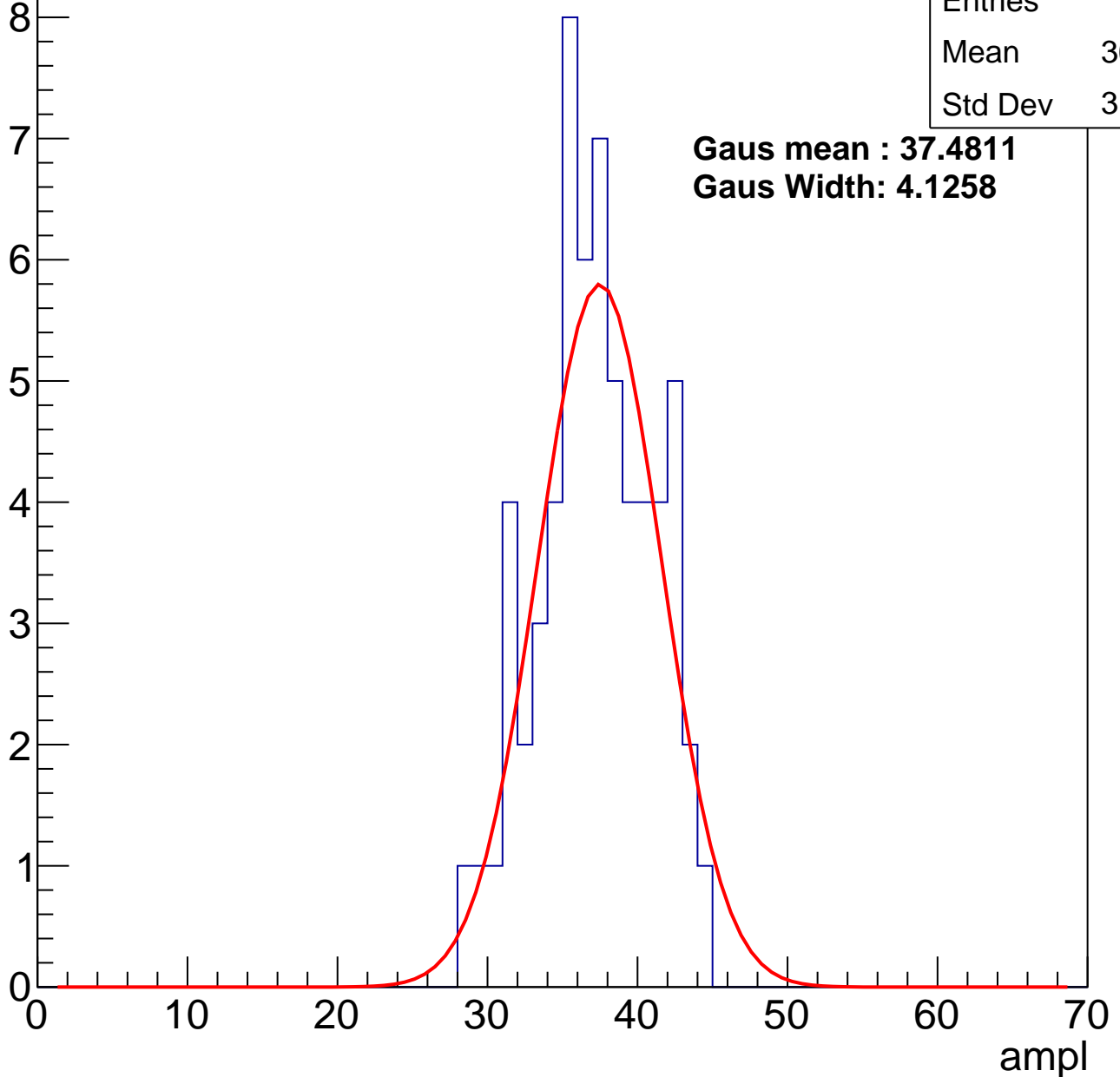
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.69
Std Dev	3.748

Gaus mean : 37.4811

Gaus Width: 4.1258



B1L003S, U21-ch93, adc2

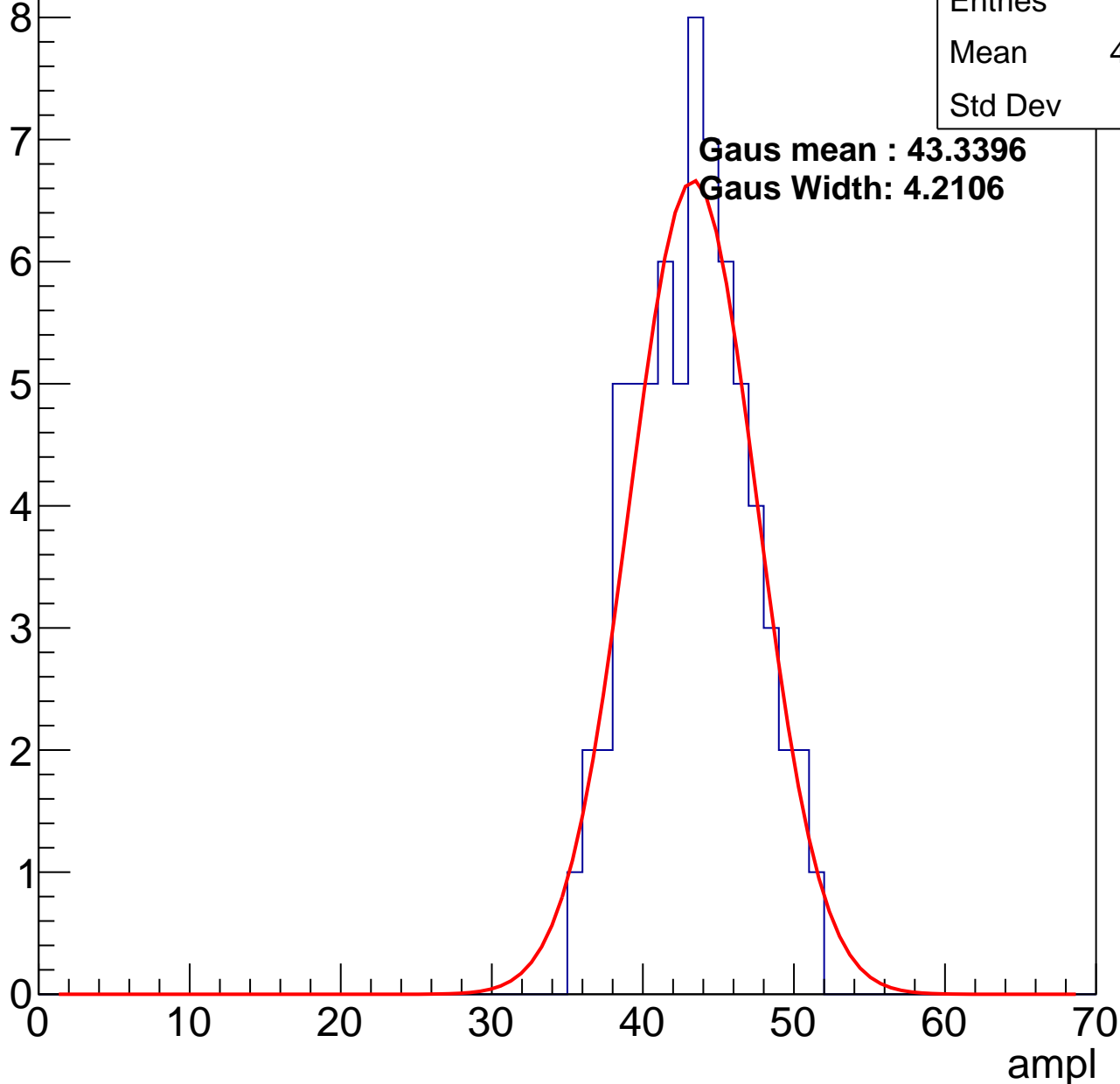
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.83
Std Dev	3.73

Gaus mean : 43.3396

Gaus Width: 4.2106

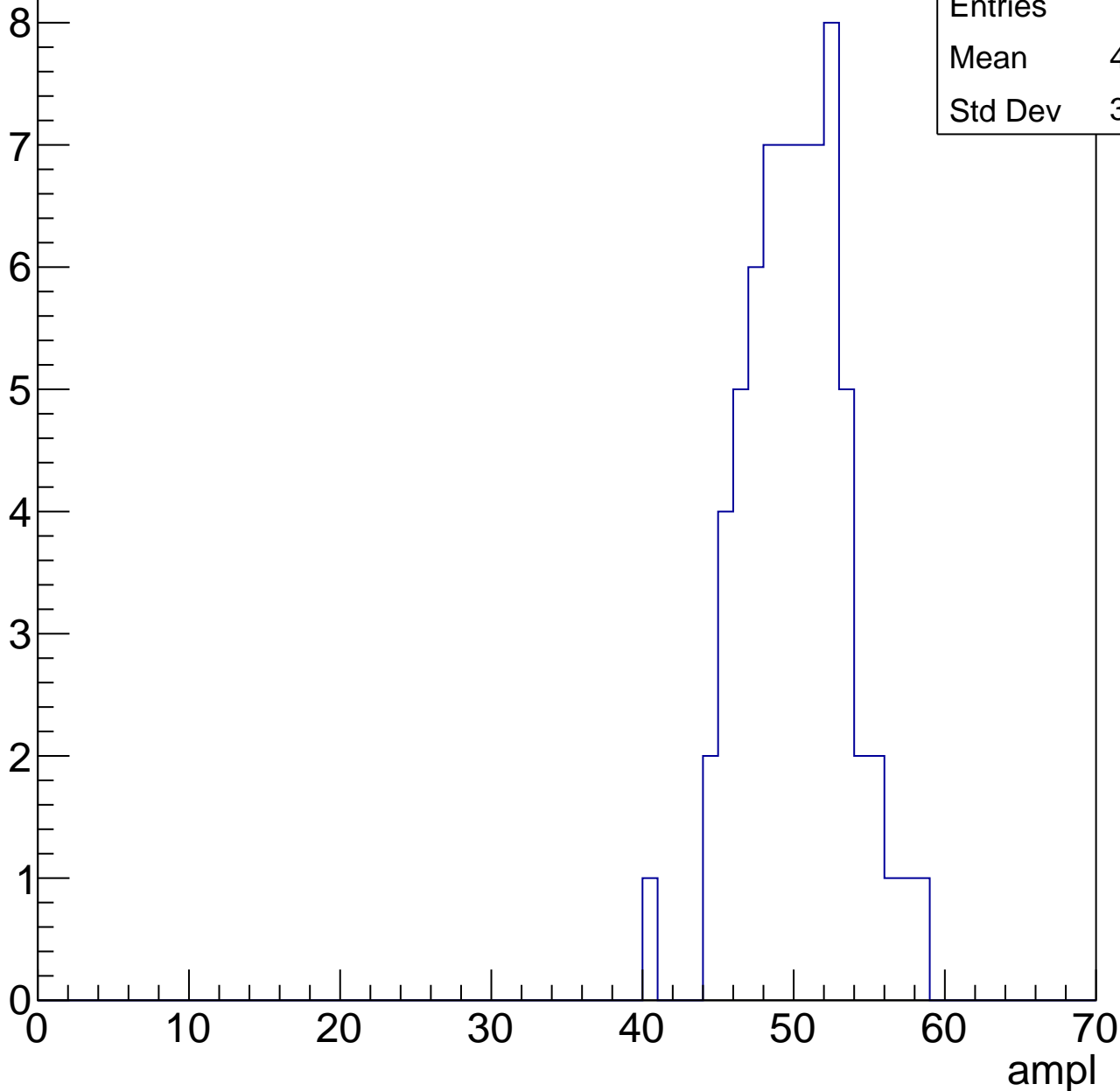


B1L003S, U21-ch93, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	49.64
Std Dev	3.365

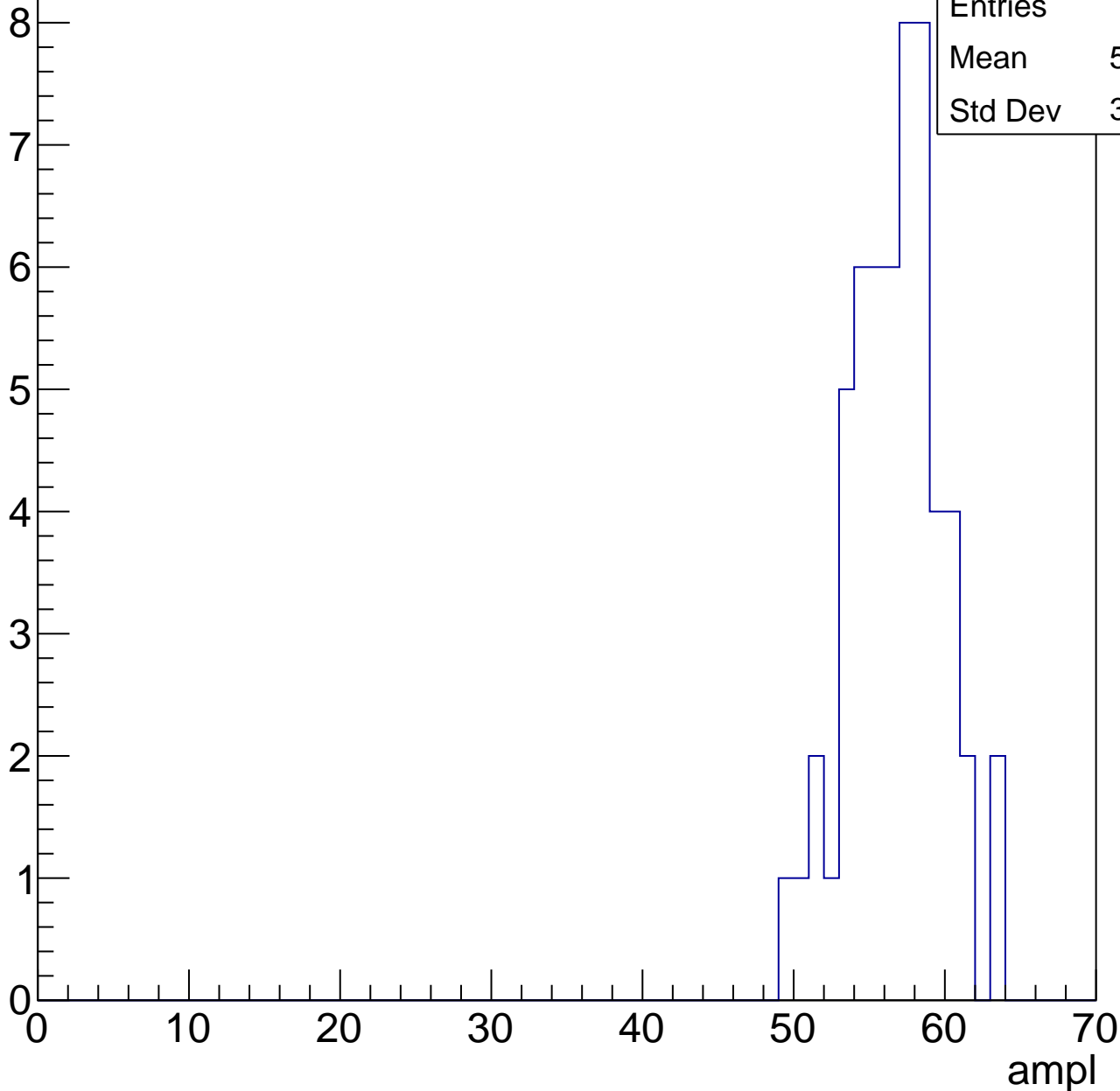


B1L003S, U21-ch93, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

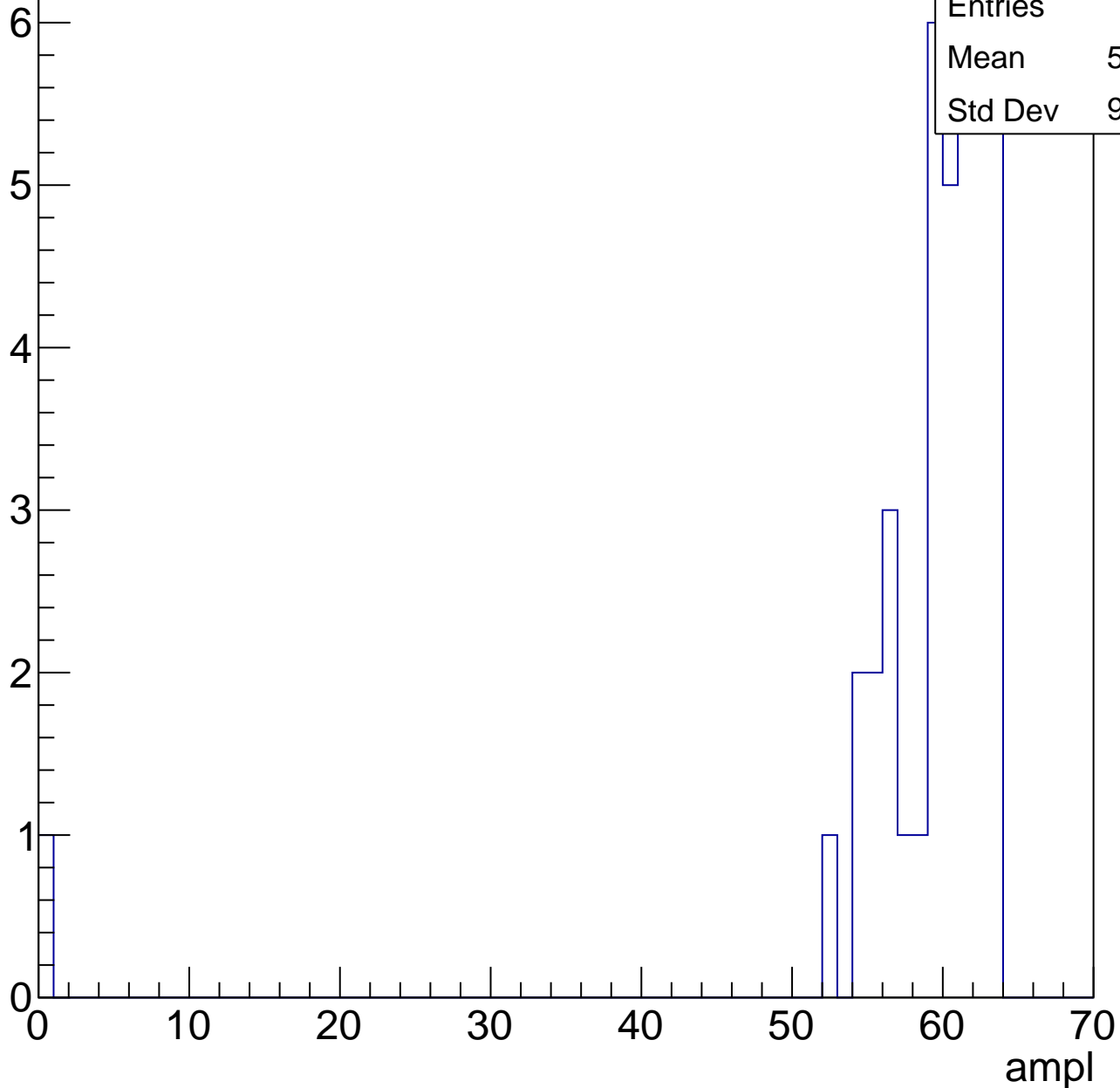
Entries	56
Mean	56.29
Std Dev	3.016



B1L003S, U21-ch93, adc5

calib_packv5_042523_0143.root, FC#13, port D2

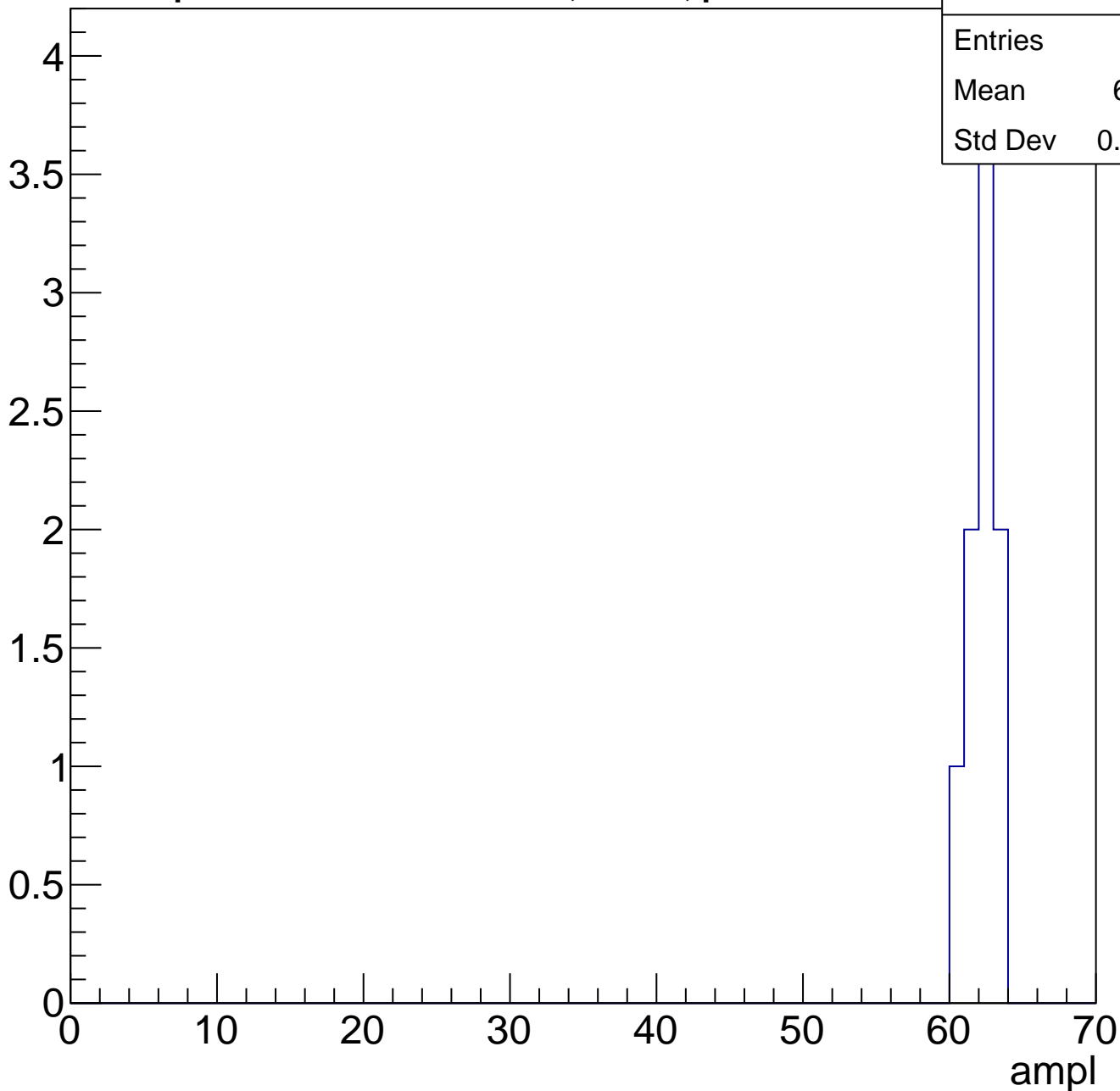
Entry



B1L003S, U21-ch93, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

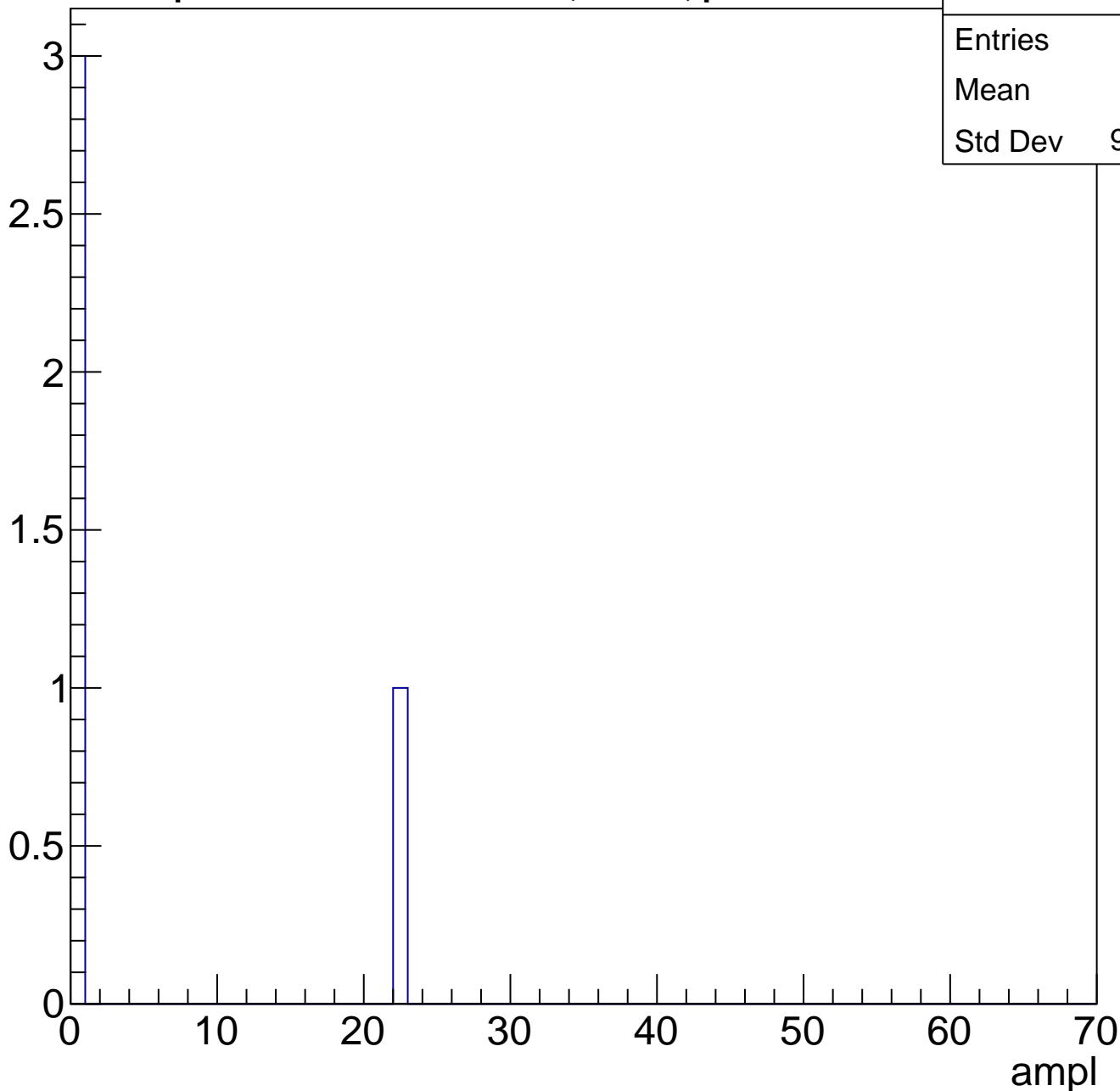


Entries	9
Mean	61.78
Std Dev	0.9162

B1L003S, U21-ch93, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	4
Mean	5.5
Std Dev	9.526

B1L003S, U21-ch94, adc0

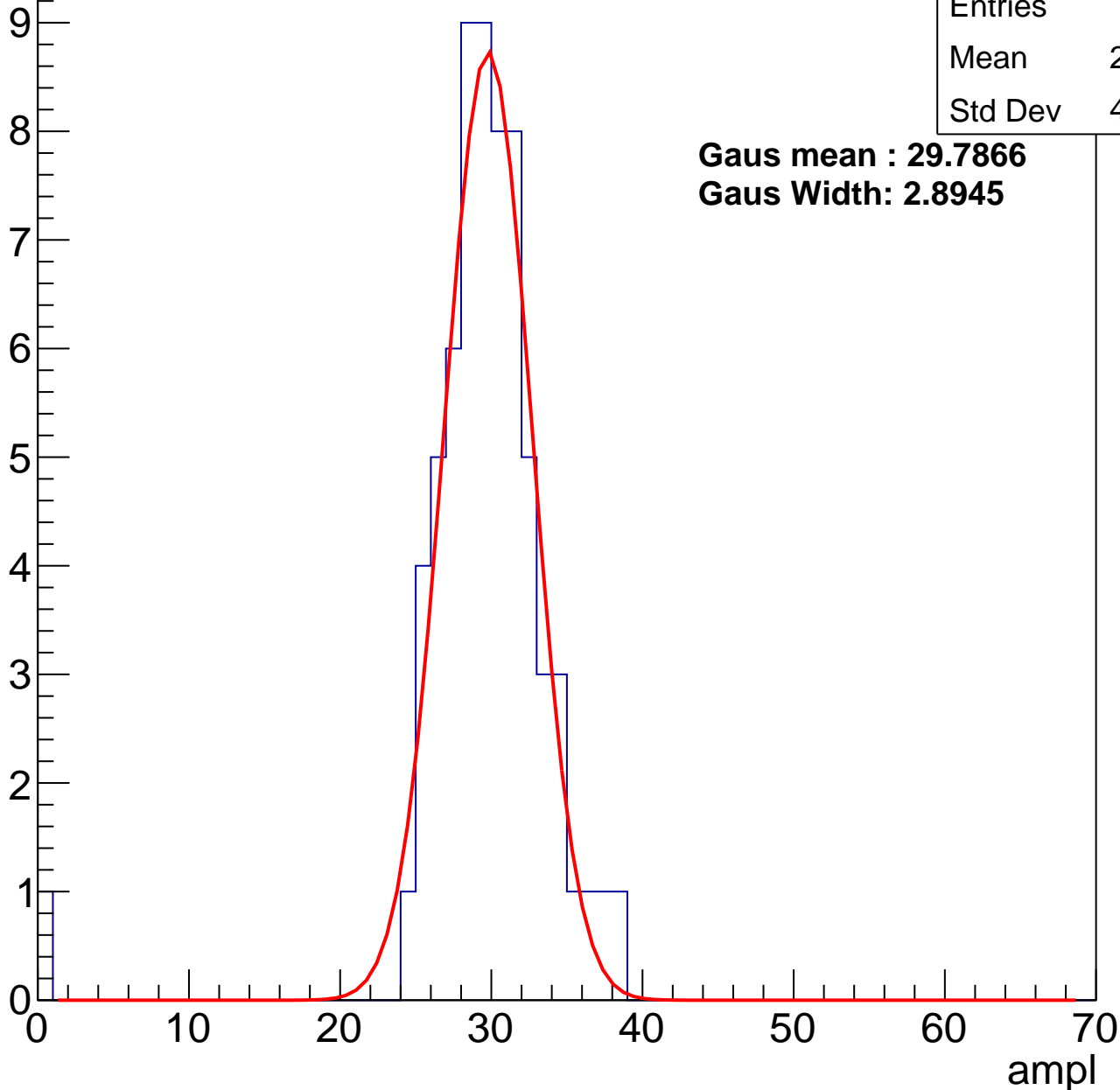
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.15
Std Dev	4.678

Gaus mean : 29.7866

Gaus Width: 2.8945



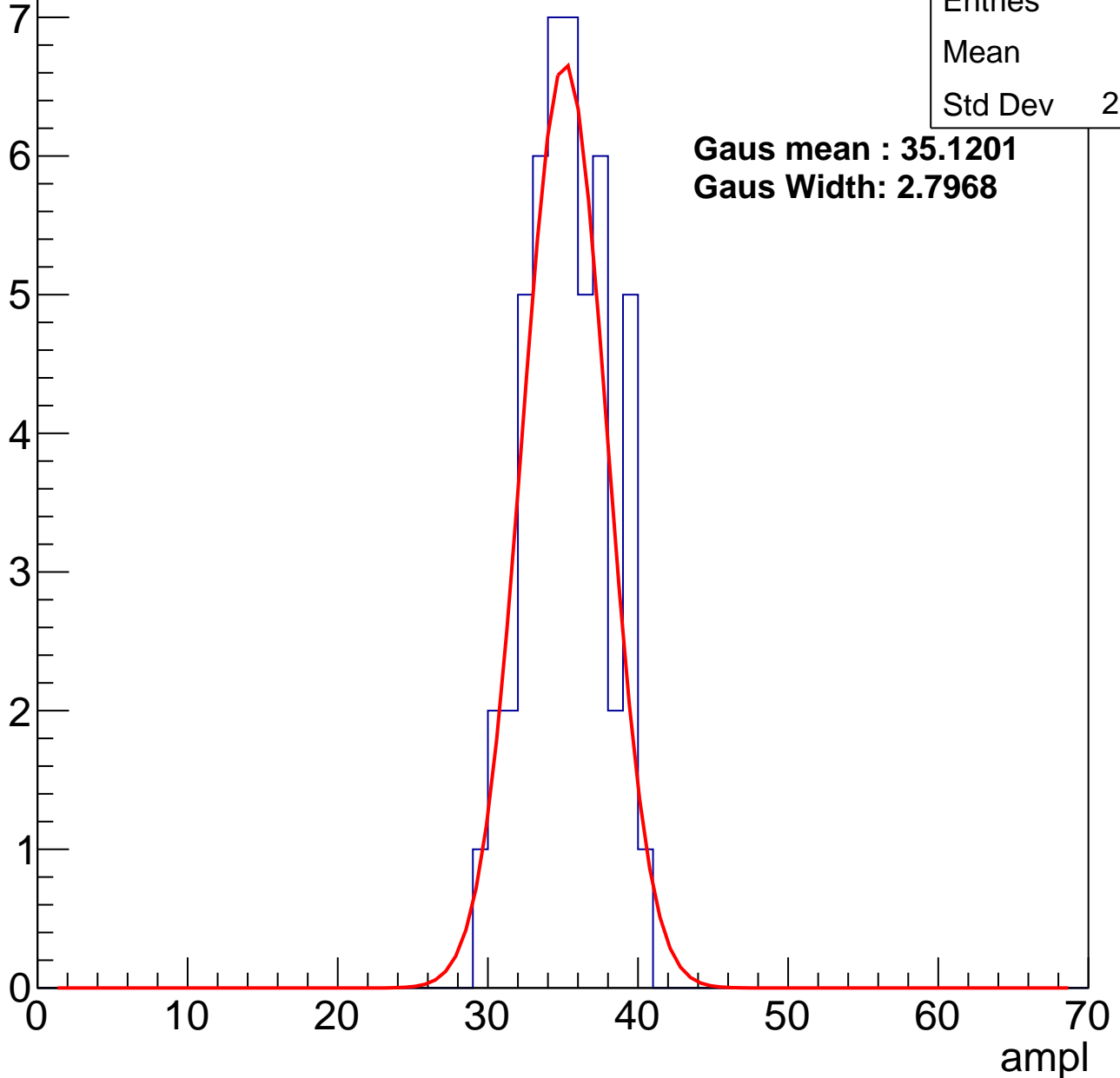
B1L003S, U21-ch94, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	34.8
Std Dev	2.649

Gaus mean : 35.1201
Gaus Width: 2.7968



B1L003S, U21-ch94, adc2

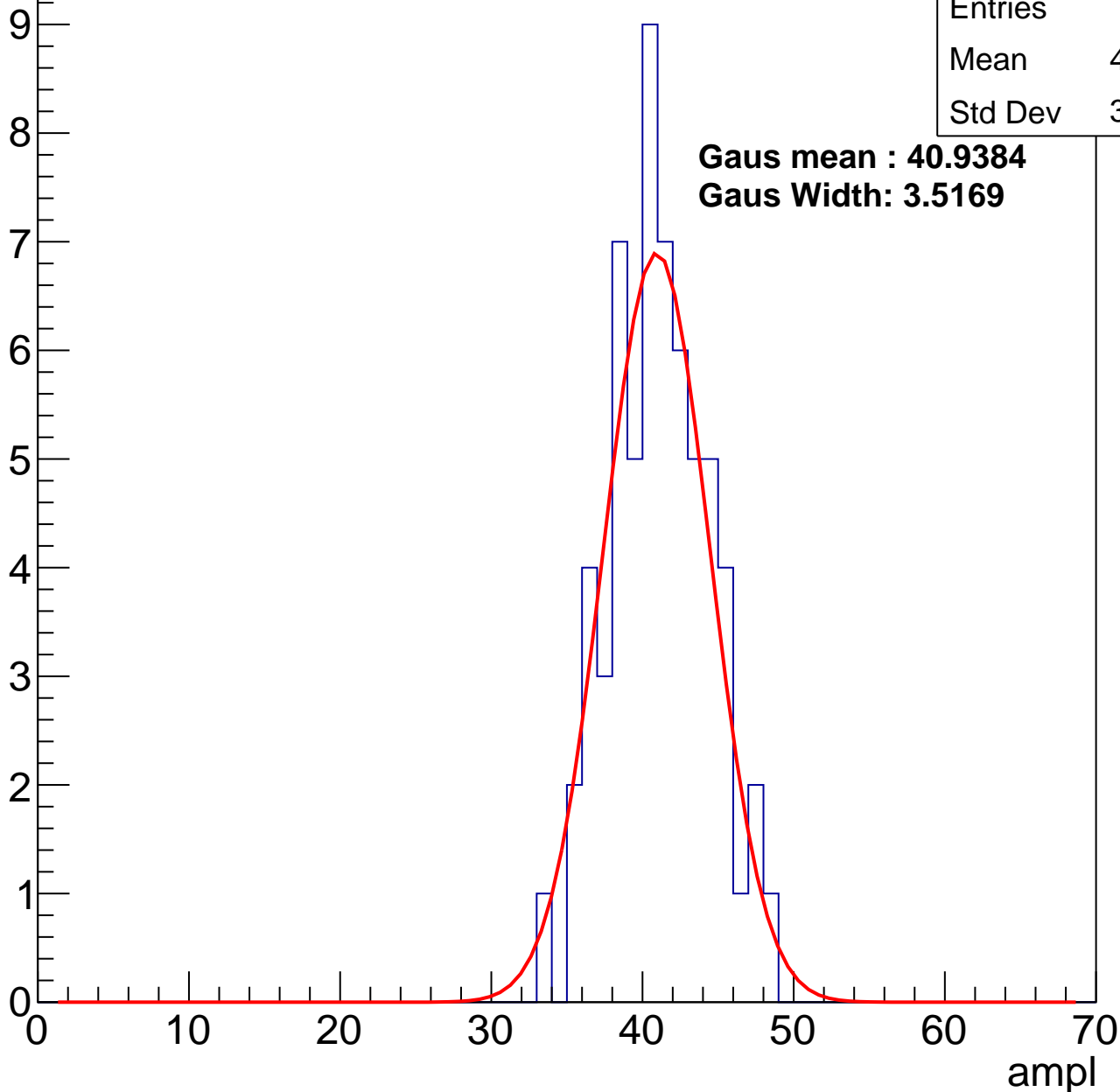
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	40.66
Std Dev	3.238

Gaus mean : 40.9384

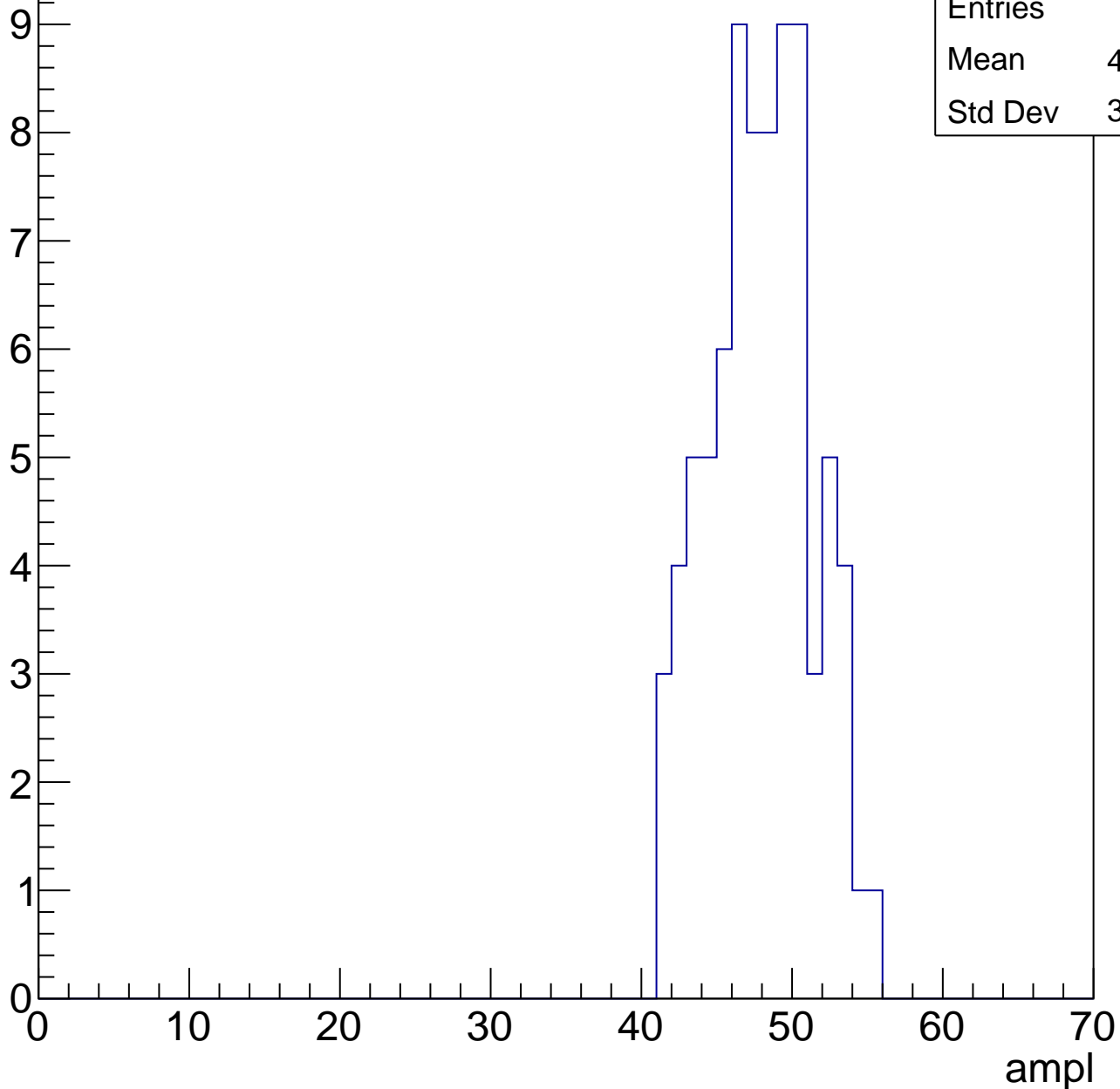
Gaus Width: 3.5169



B1L003S, U21-ch94, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

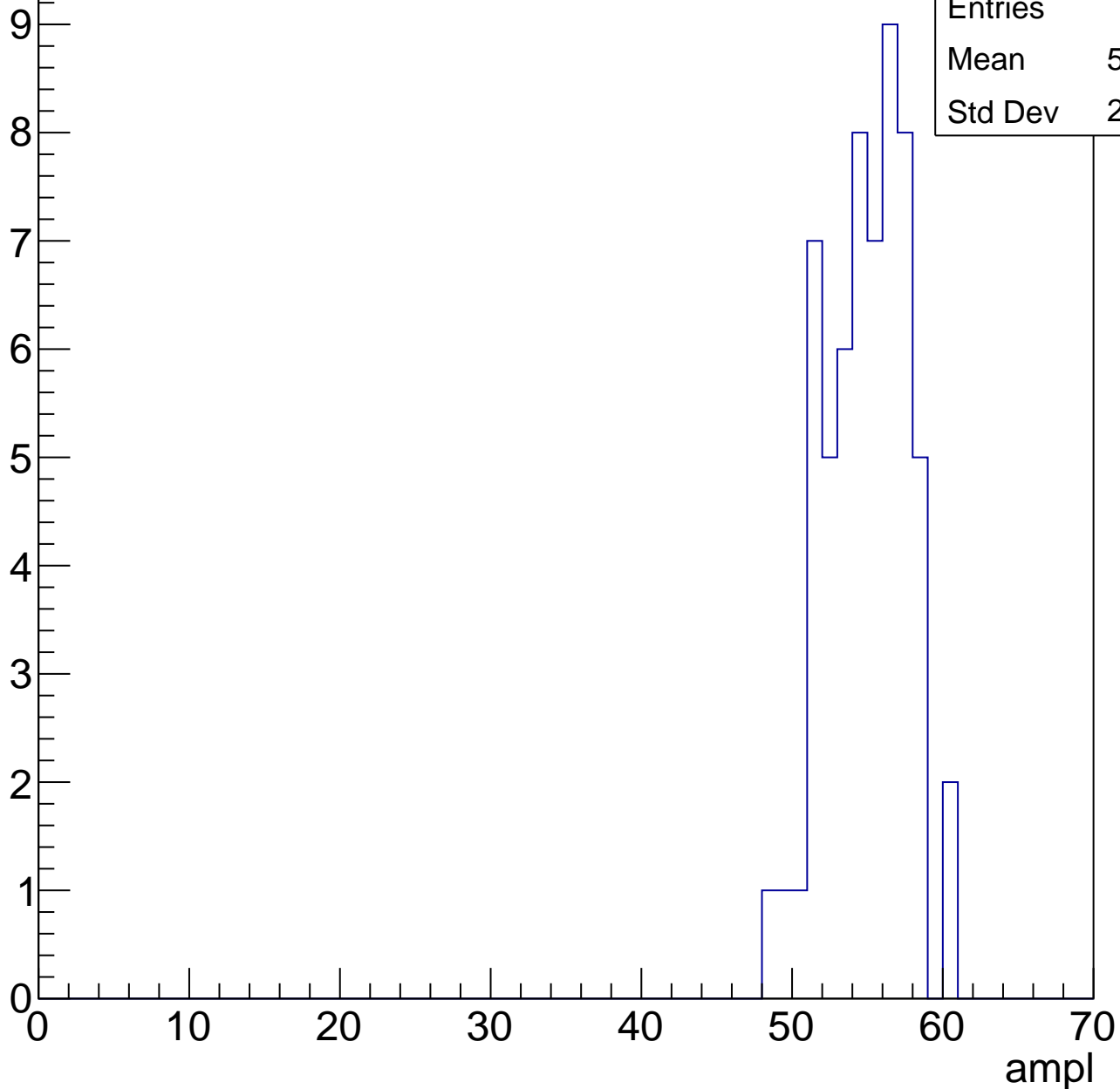


Entries	80
Mean	47.44
Std Dev	3.365

B1L003S, U21-ch94, adc4

calib_packv5_042523_0143.root, FC#13, port D2

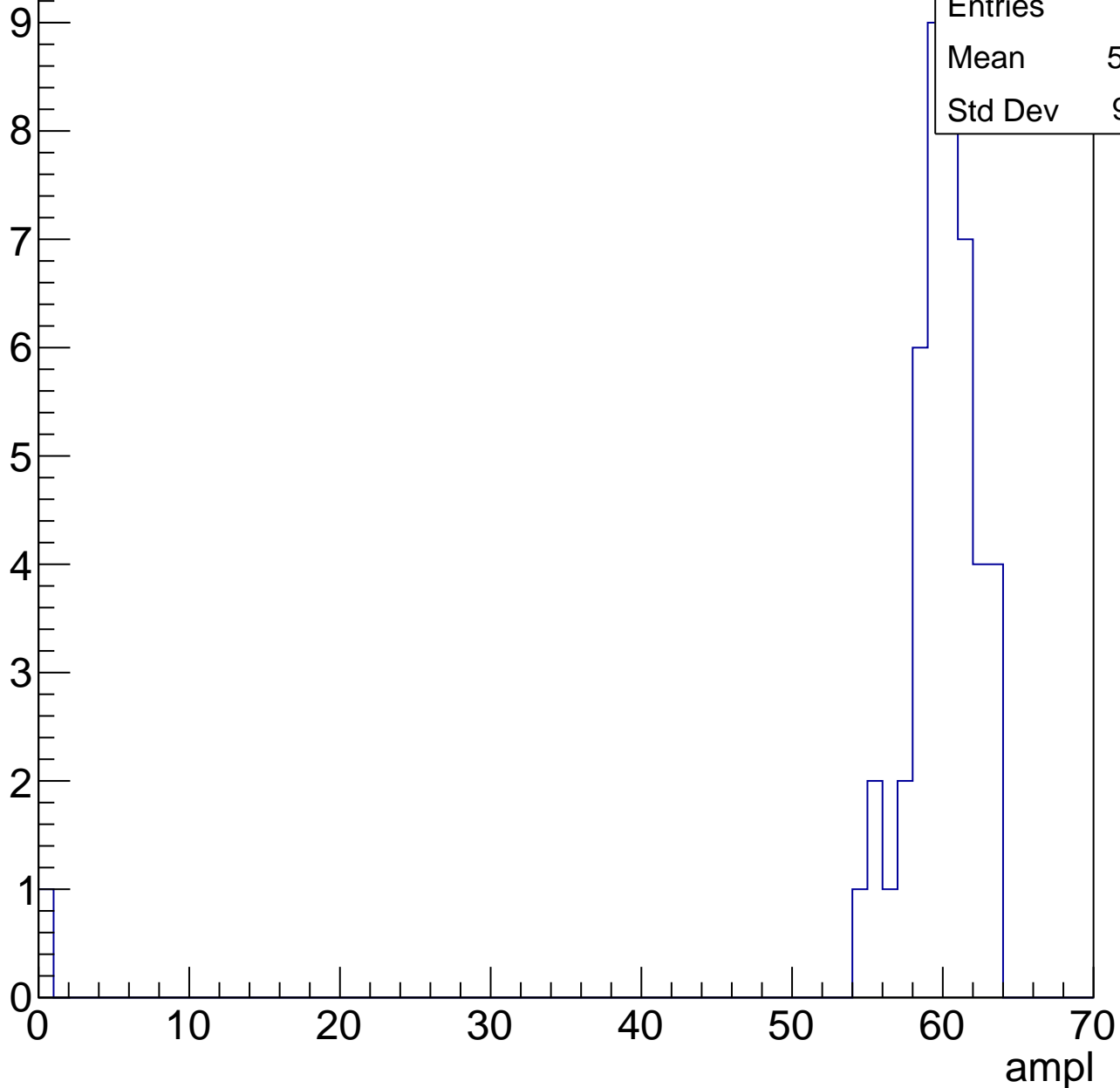
Entry



B1L003S, U21-ch94, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

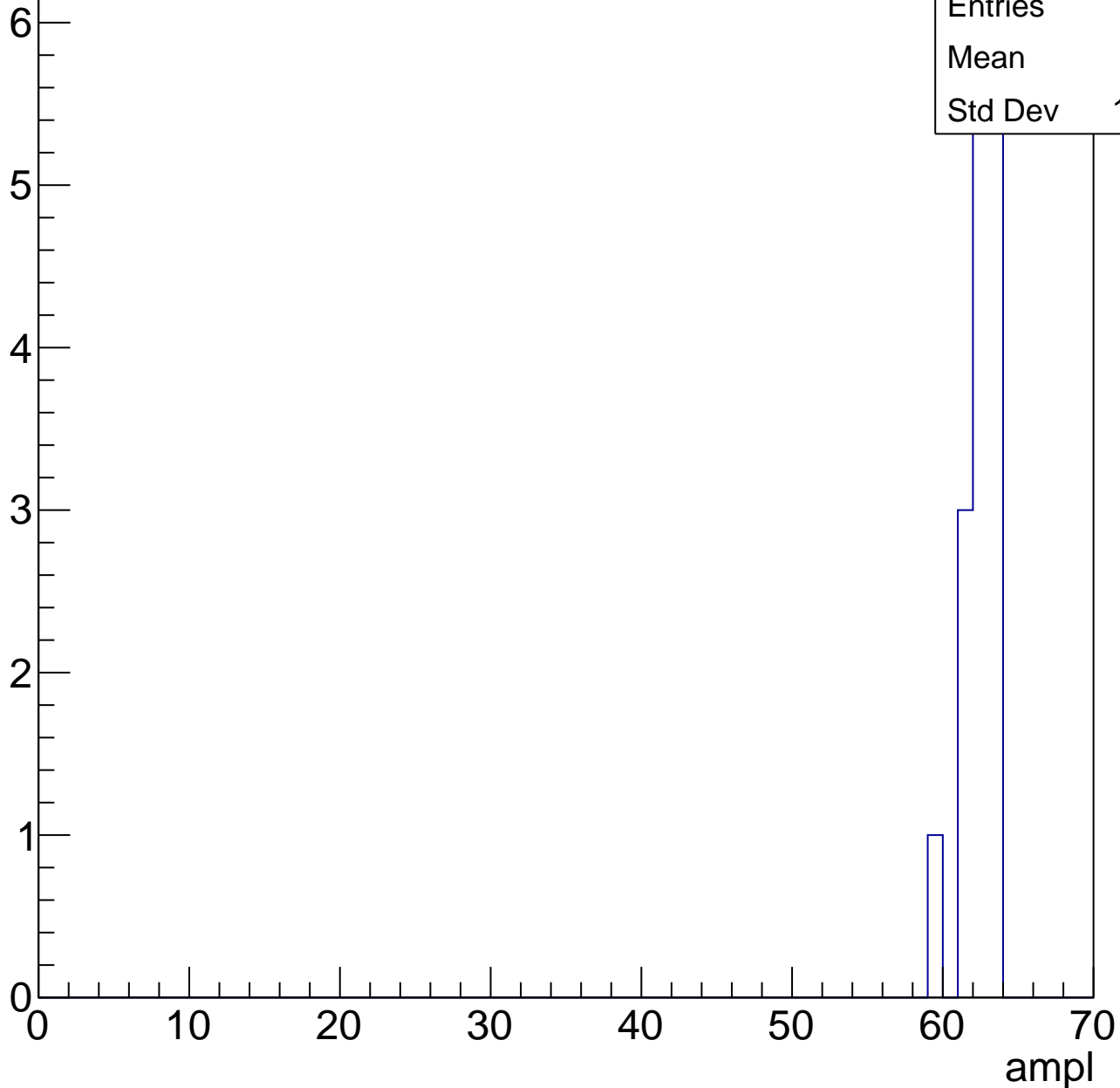


B1L003S, U21-ch94, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

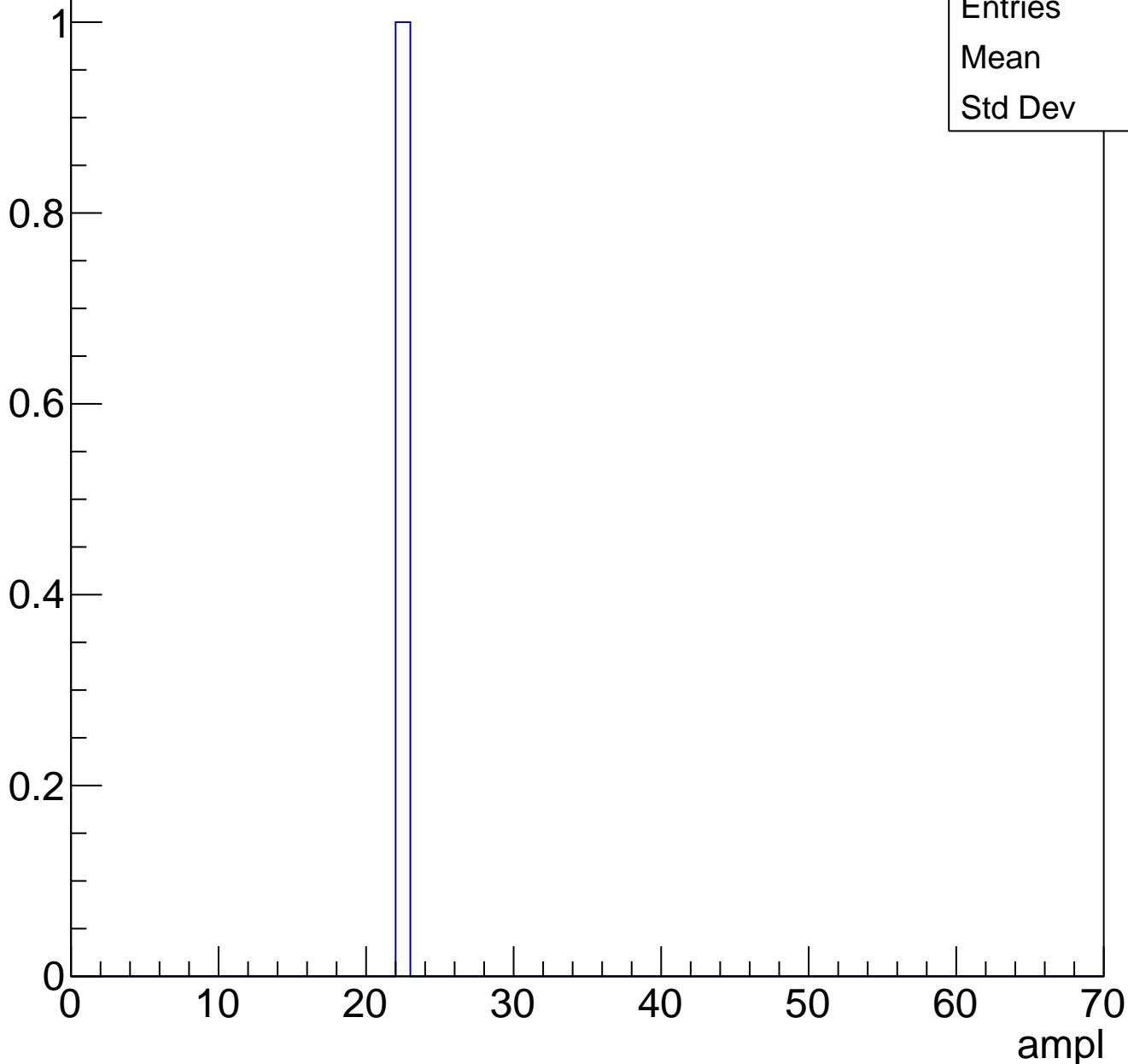
Entries	16
Mean	62
Std Dev	1.061



B1L003S, U21-ch94, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch95, adc0

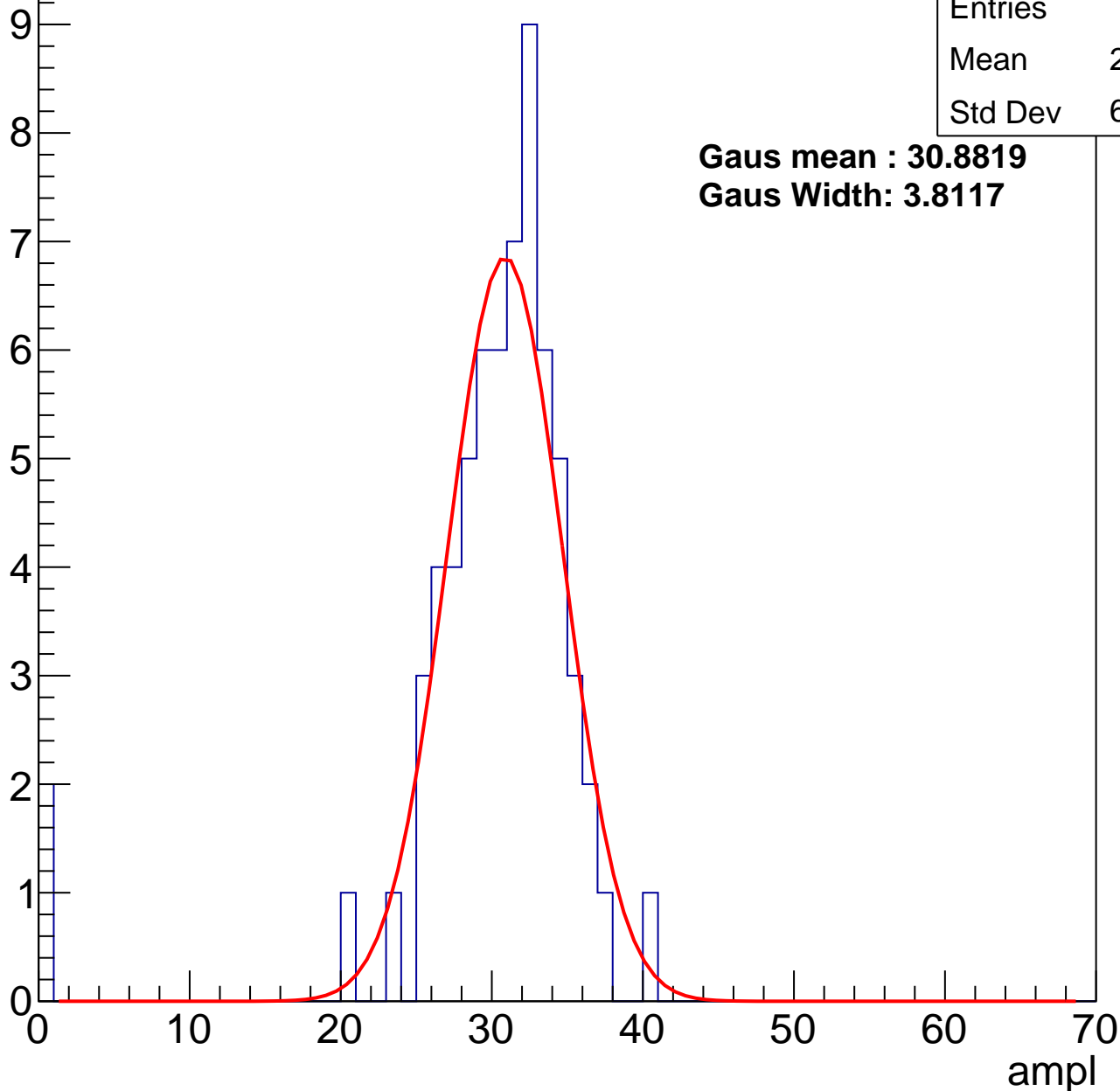
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	29.56
Std Dev	6.289

Gaus mean : 30.8819

Gaus Width: 3.8117



B1L003S, U21-ch95, adc1

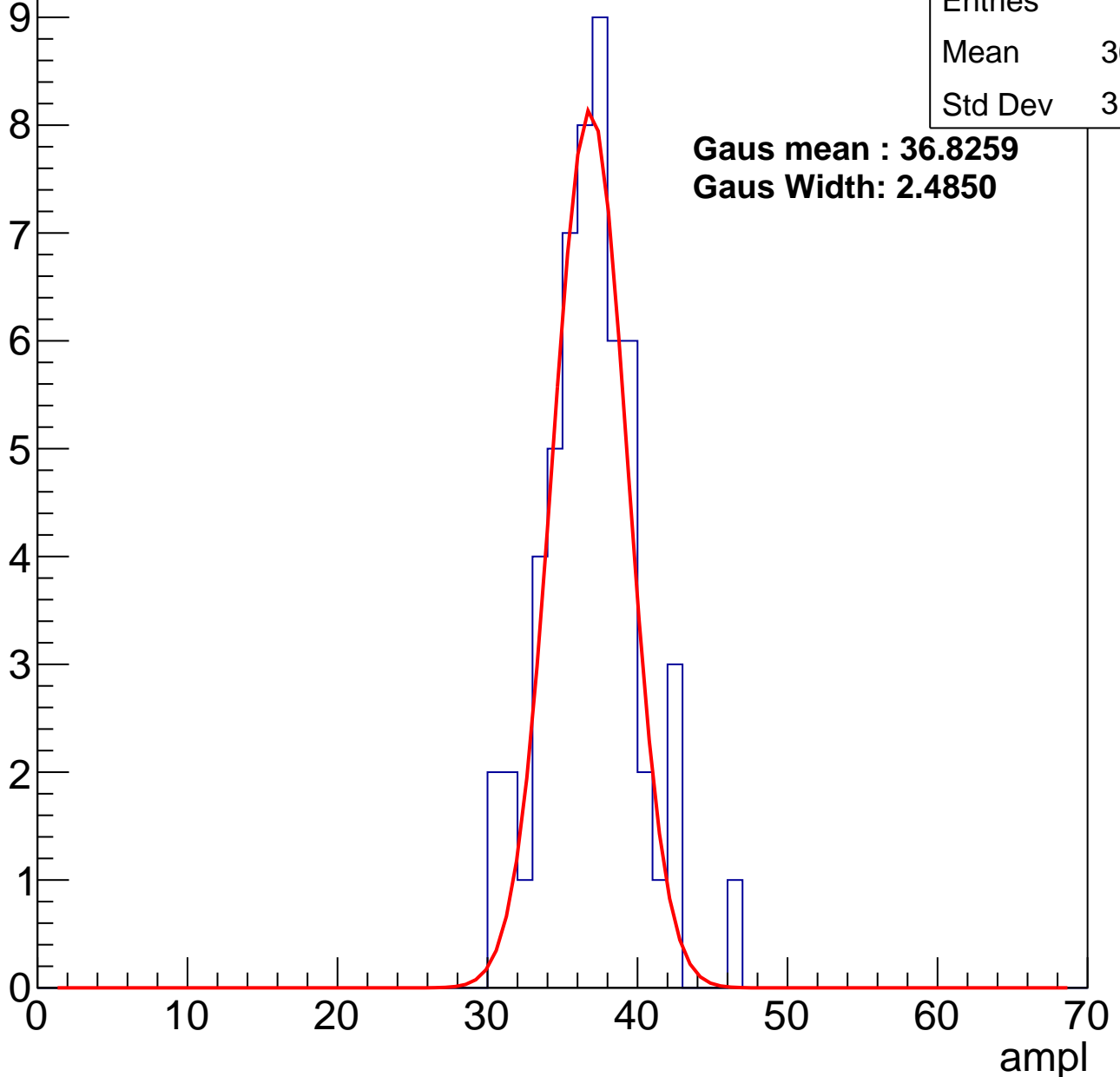
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	36.44
Std Dev	3.084

Gaus mean : 36.8259

Gaus Width: 2.4850



B1L003S, U21-ch95, adc2

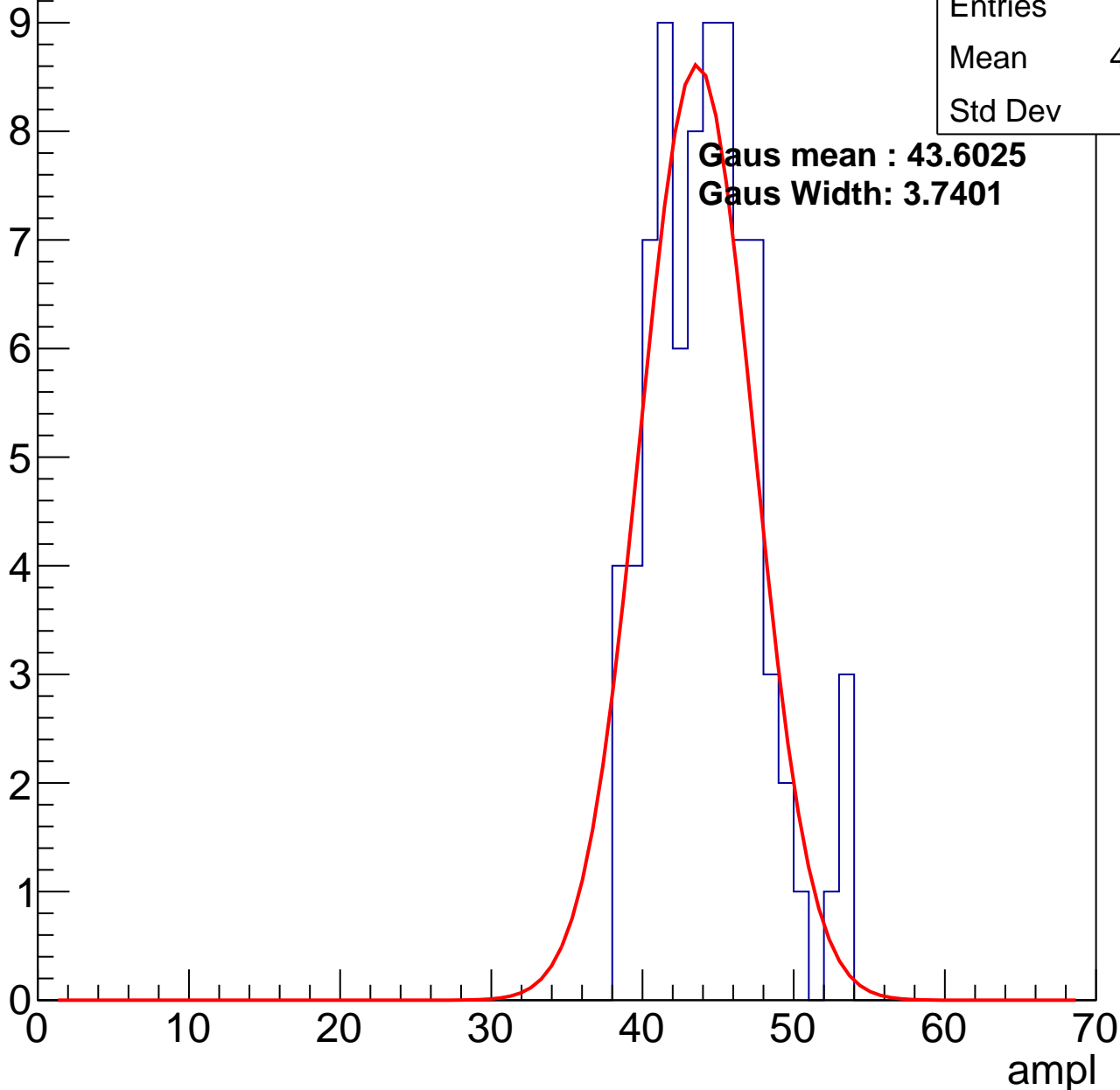
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	43.85
Std Dev	3.55

Gaus mean : 43.6025

Gaus Width: 3.7401

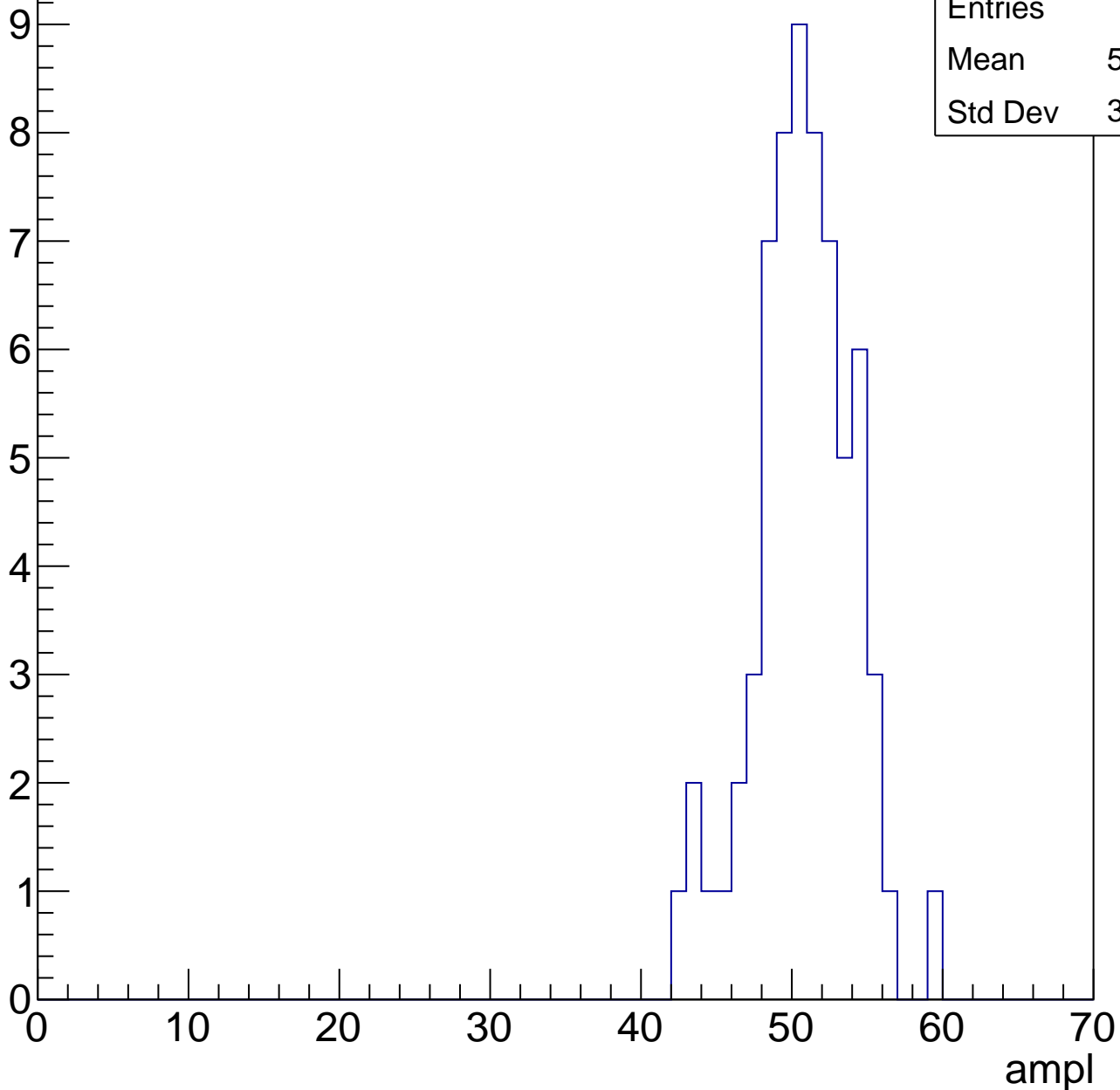


B1L003S, U21-ch95, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	50.29
Std Dev	3.238

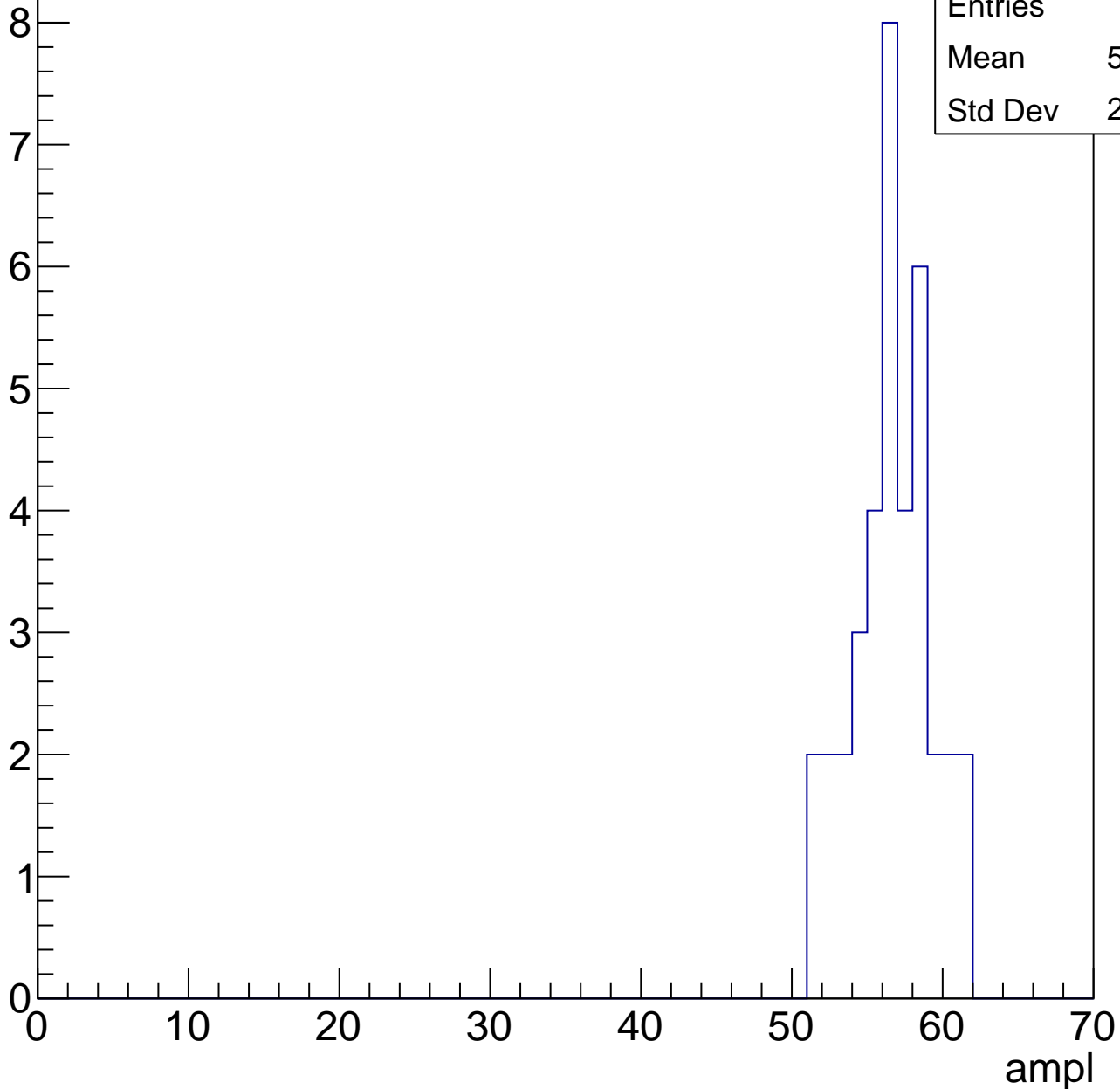


B1L003S, U21-ch95, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	37
Mean	56.16
Std Dev	2.563

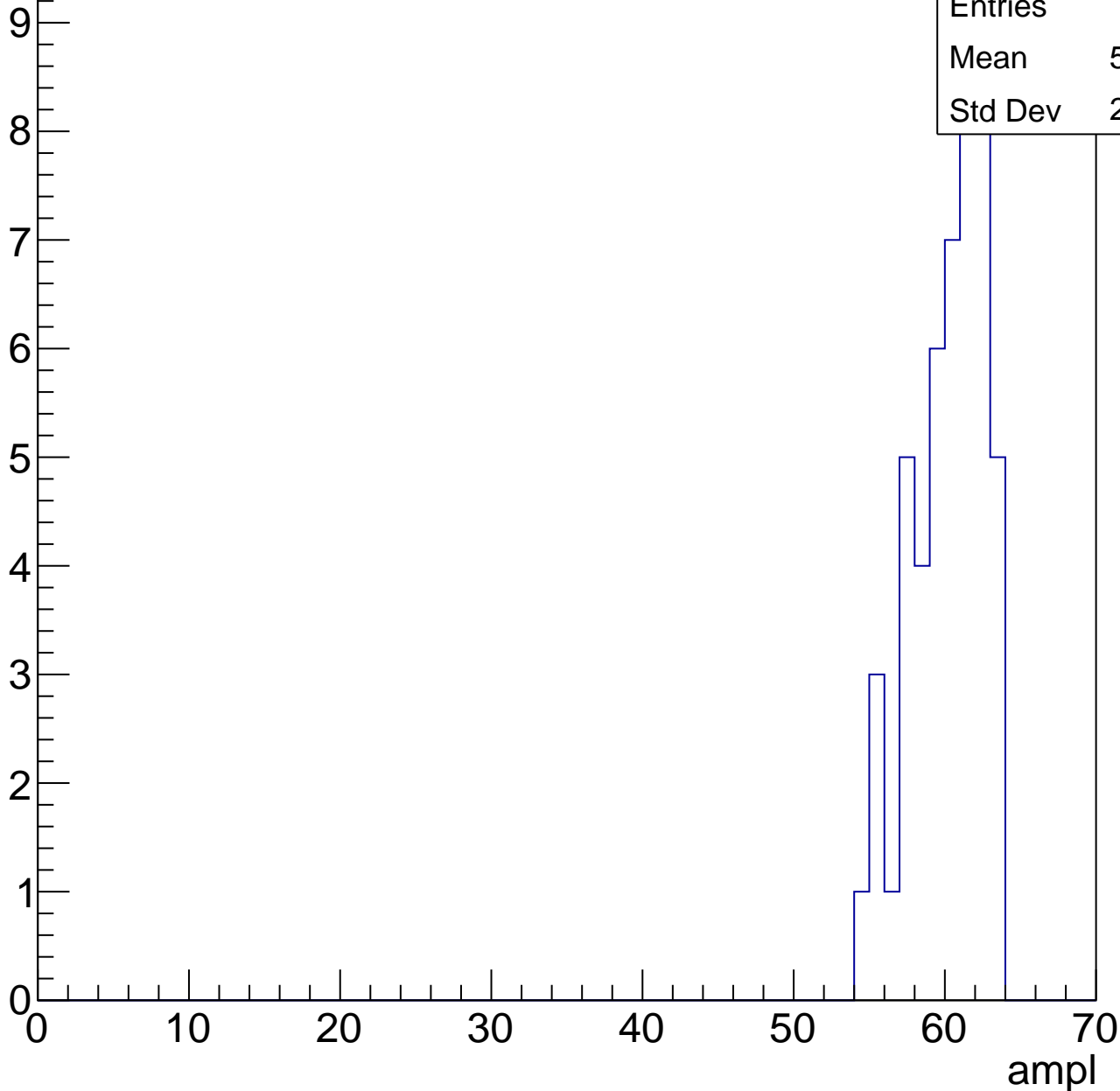


B1L003S, U21-ch95, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	59.73
Std Dev	2.389

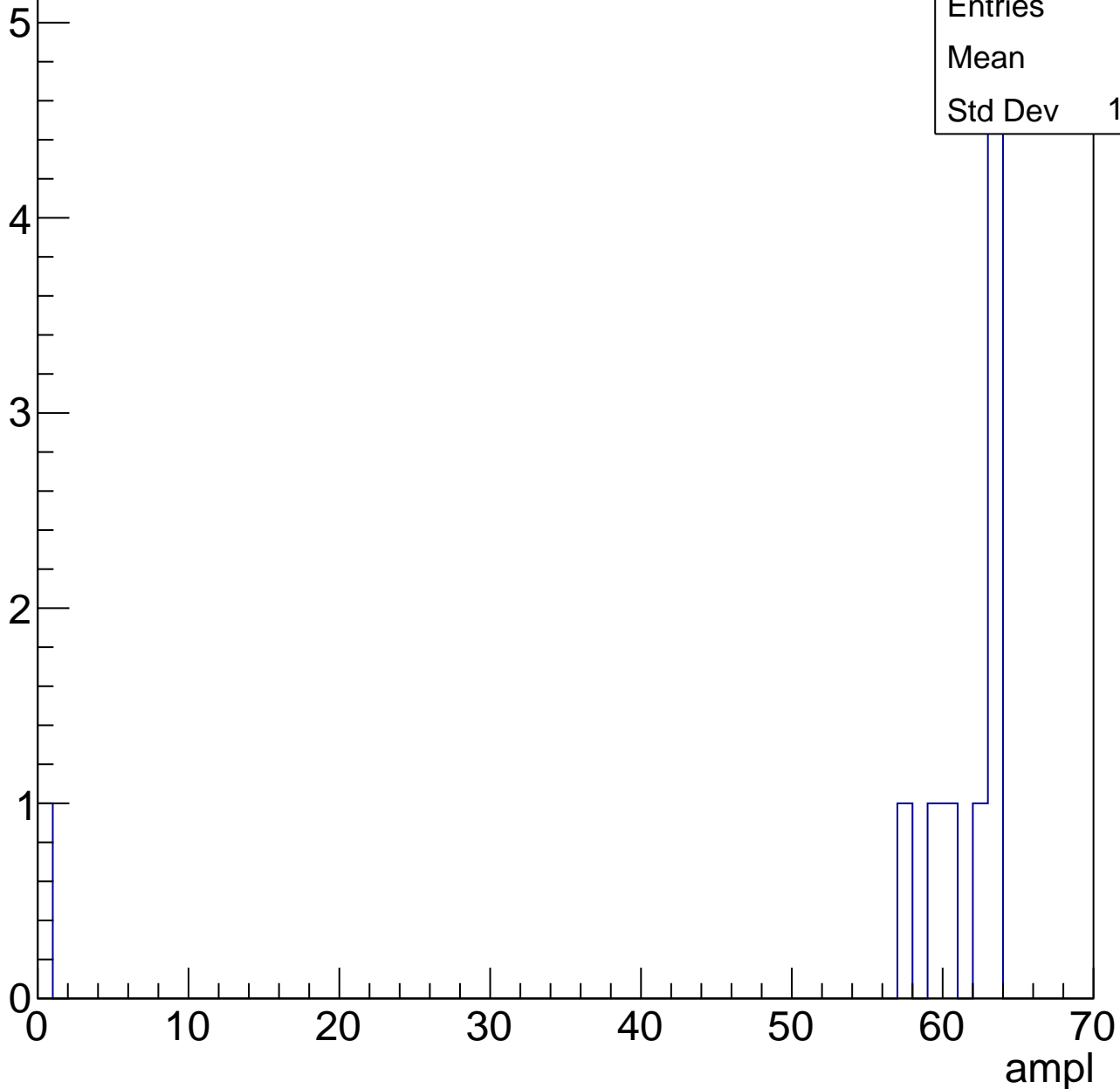


B1L003S, U21-ch95, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	55.3
Std Dev	18.54



B1L003S, U21-ch95, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch96, adc0

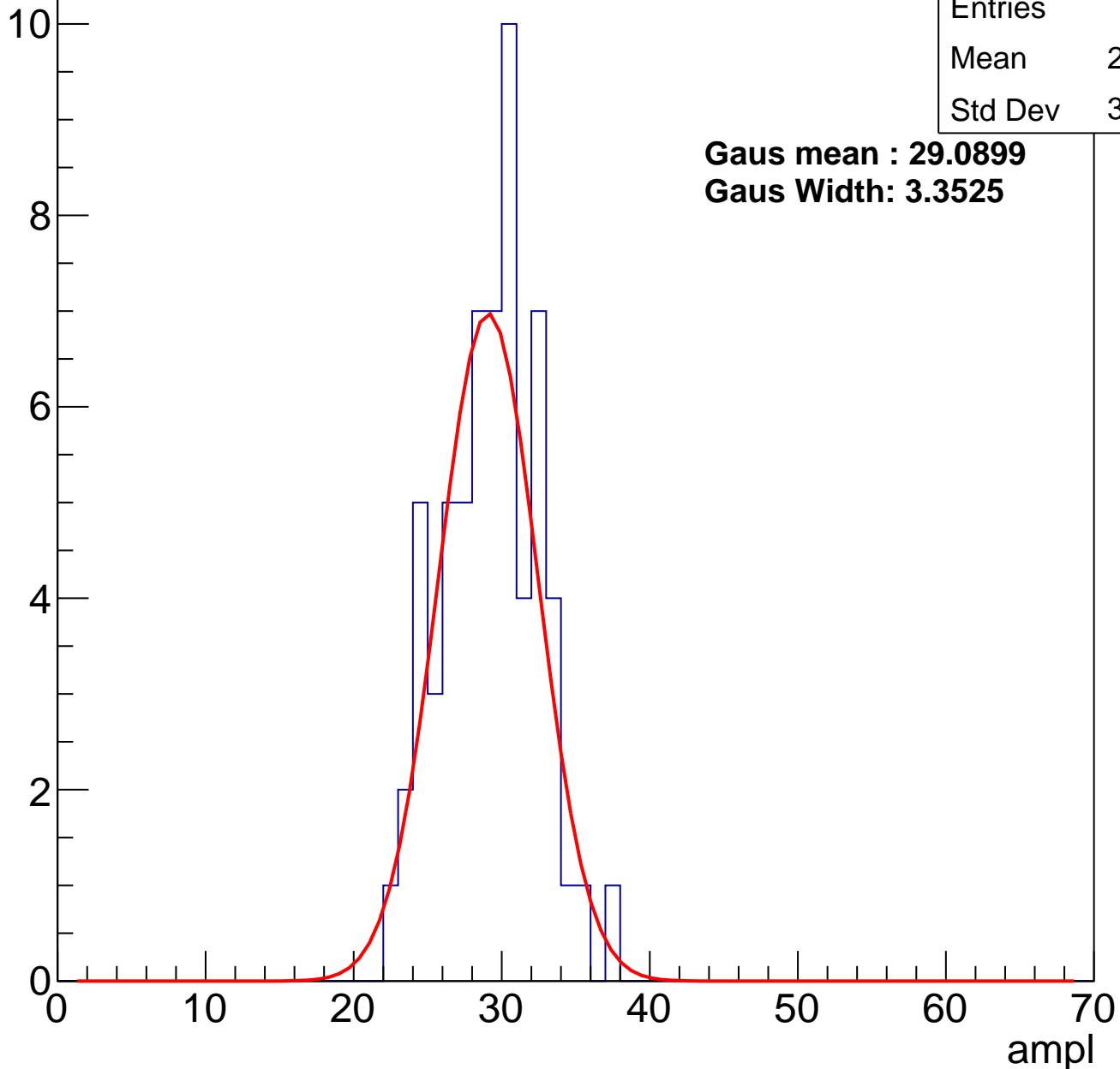
calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	28.78
Std Dev	3.189

Gaus mean : 29.0899

Gaus Width: 3.3525

Entry



B1L003S, U21-ch96, adc1

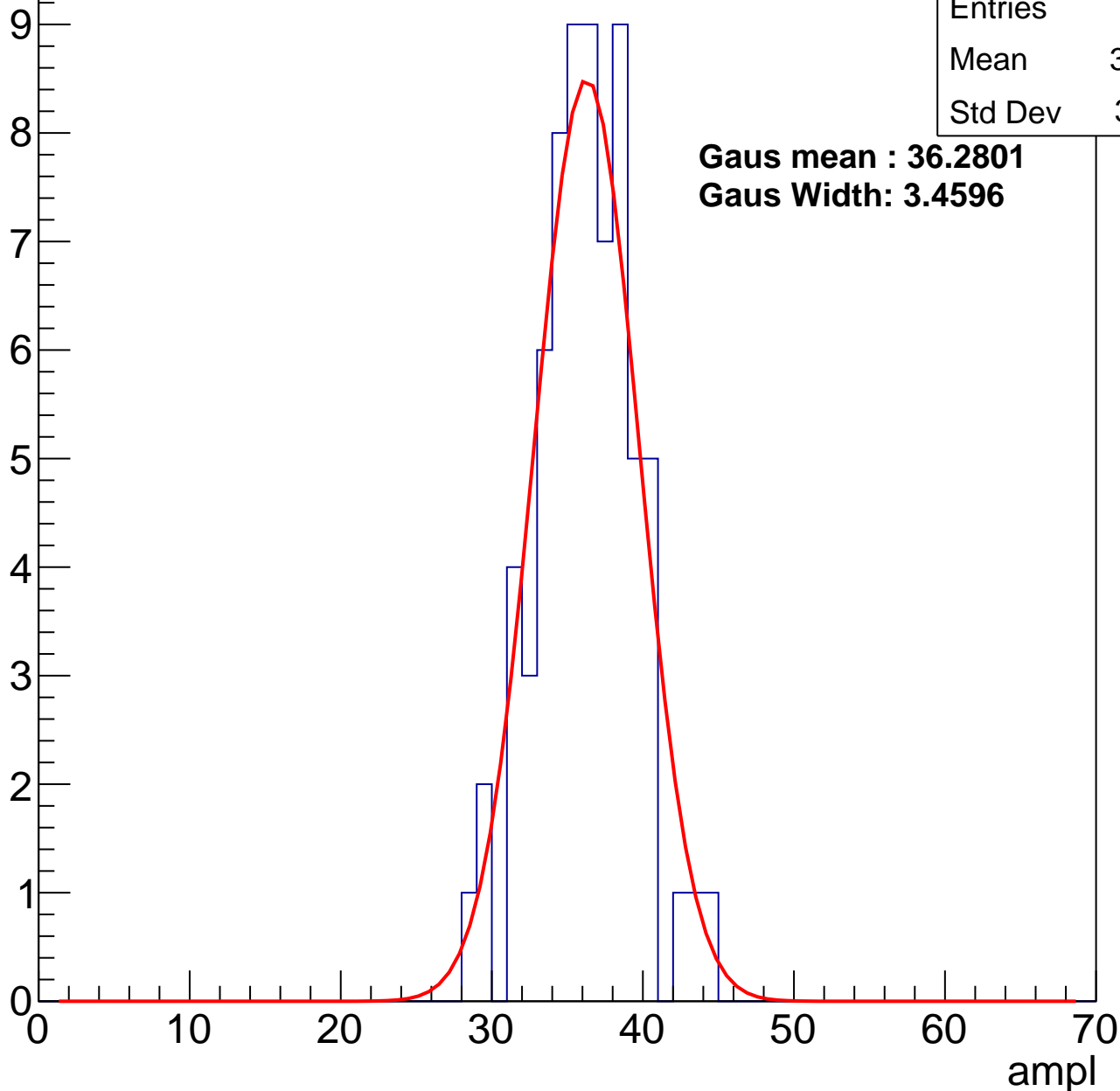
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.77
Std Dev	3.181

Gaus mean : 36.2801

Gaus Width: 3.4596



B1L003S, U21-ch96, adc2

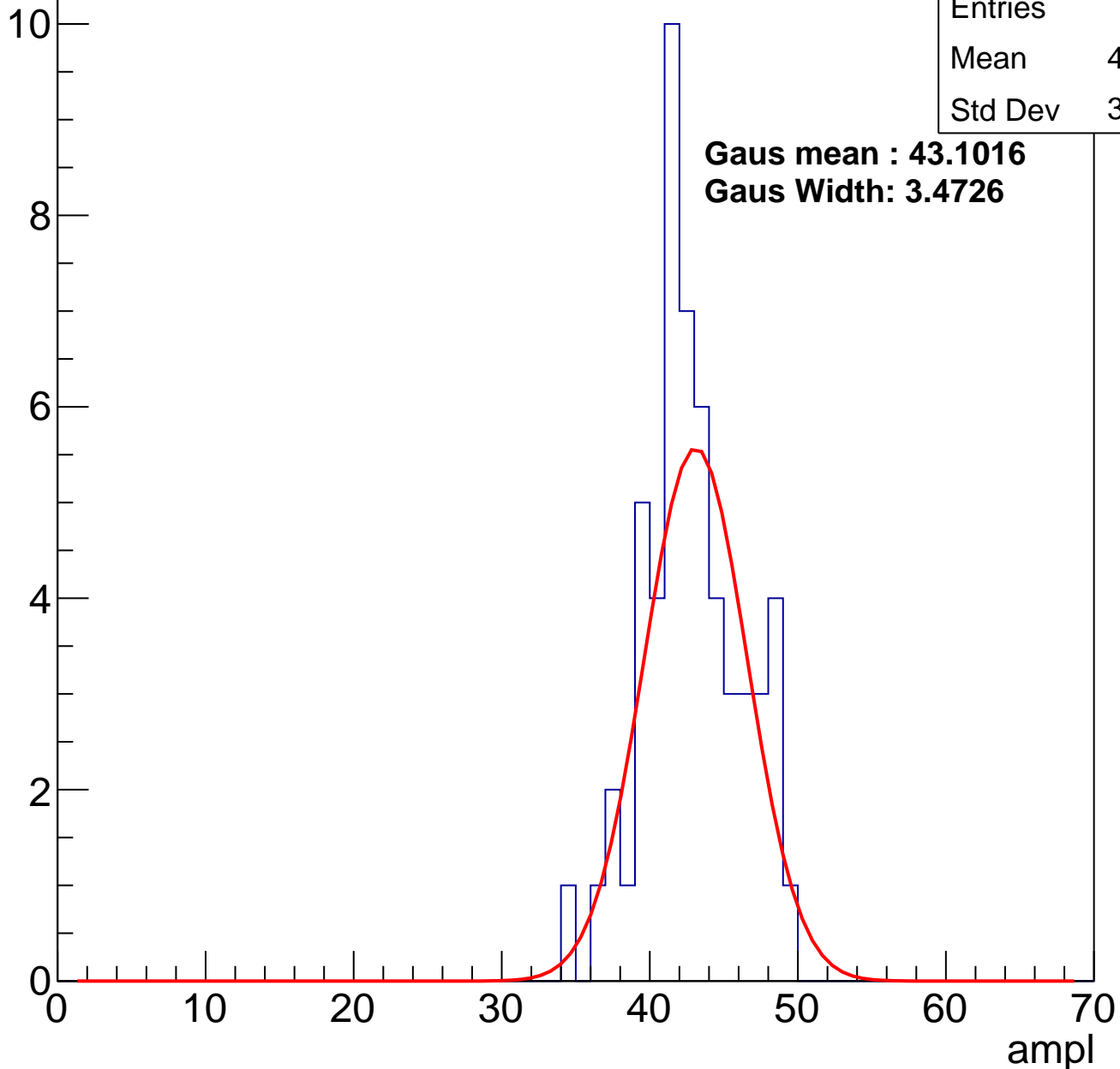
calib_packv5_042523_0143.root, FC#13, port D2

Entries	55
Mean	42.36
Std Dev	3.288

Gaus mean : 43.1016

Gaus Width: 3.4726

Entry

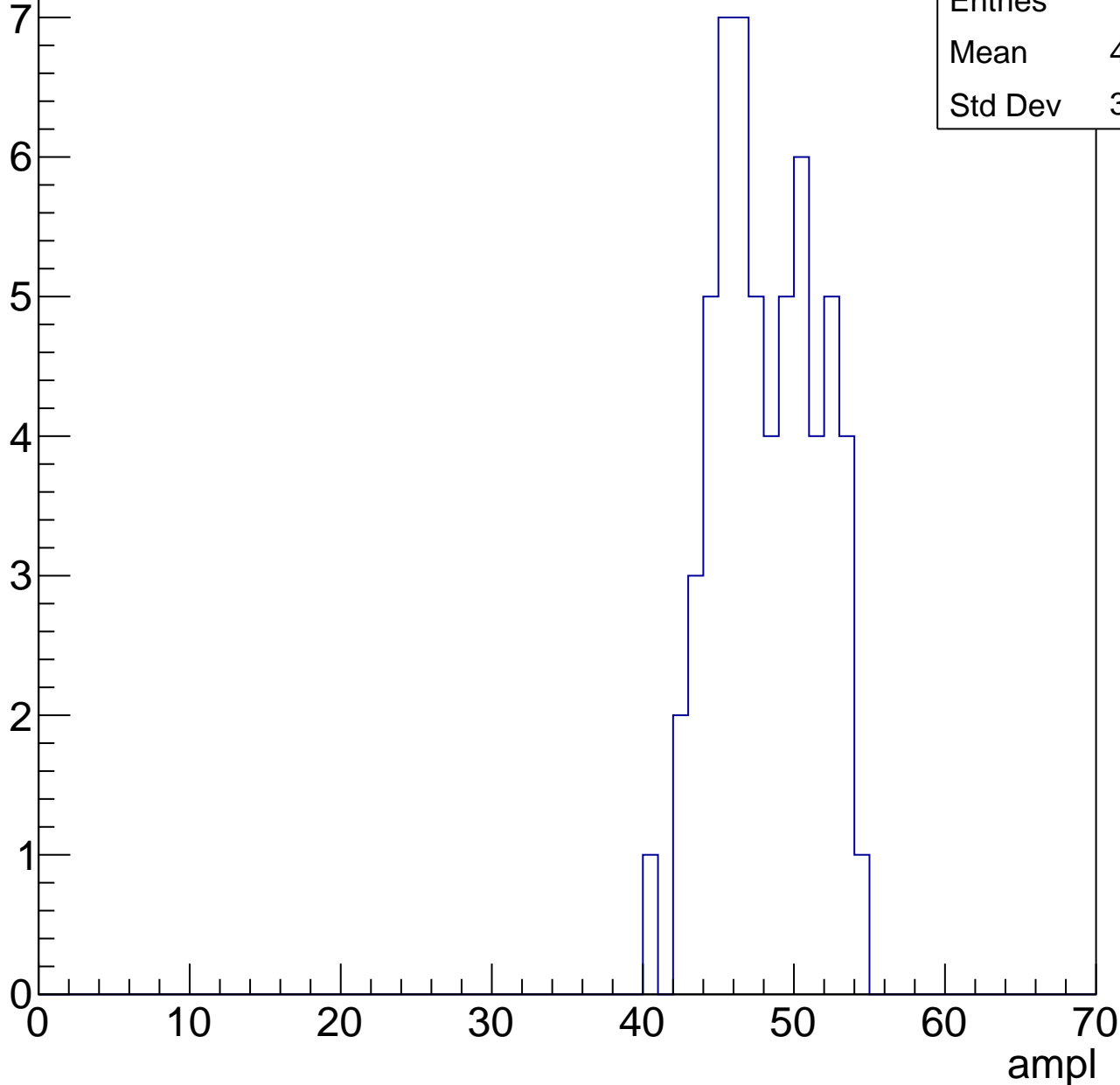


B1L003S, U21-ch96, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

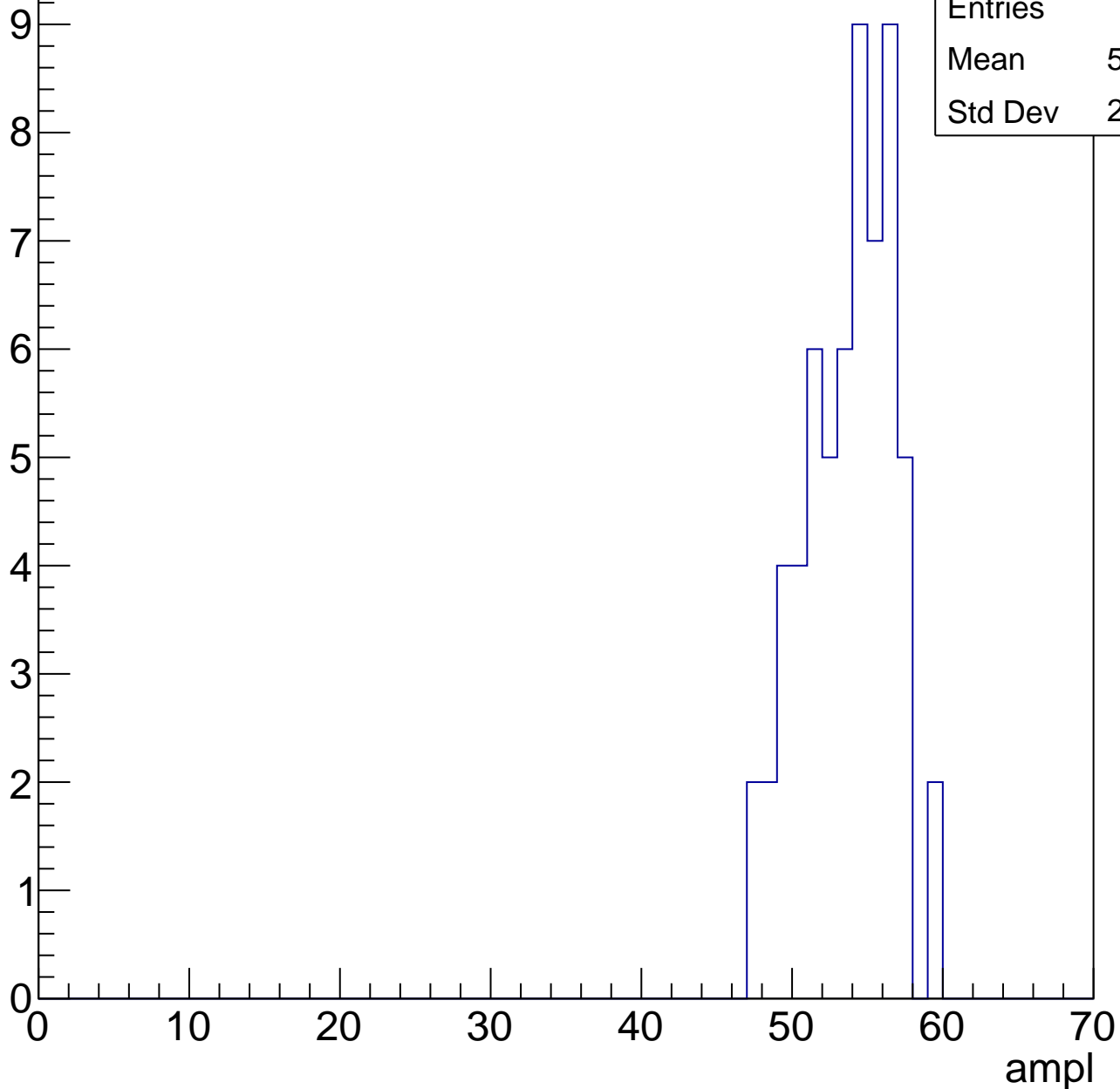
Entries	59
Mean	47.66
Std Dev	3.348



B1L003S, U21-ch96, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch96, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	58
Mean	58.6
Std Dev	8.051

Entry

10

8

6

4

2

0

0

10

20

30

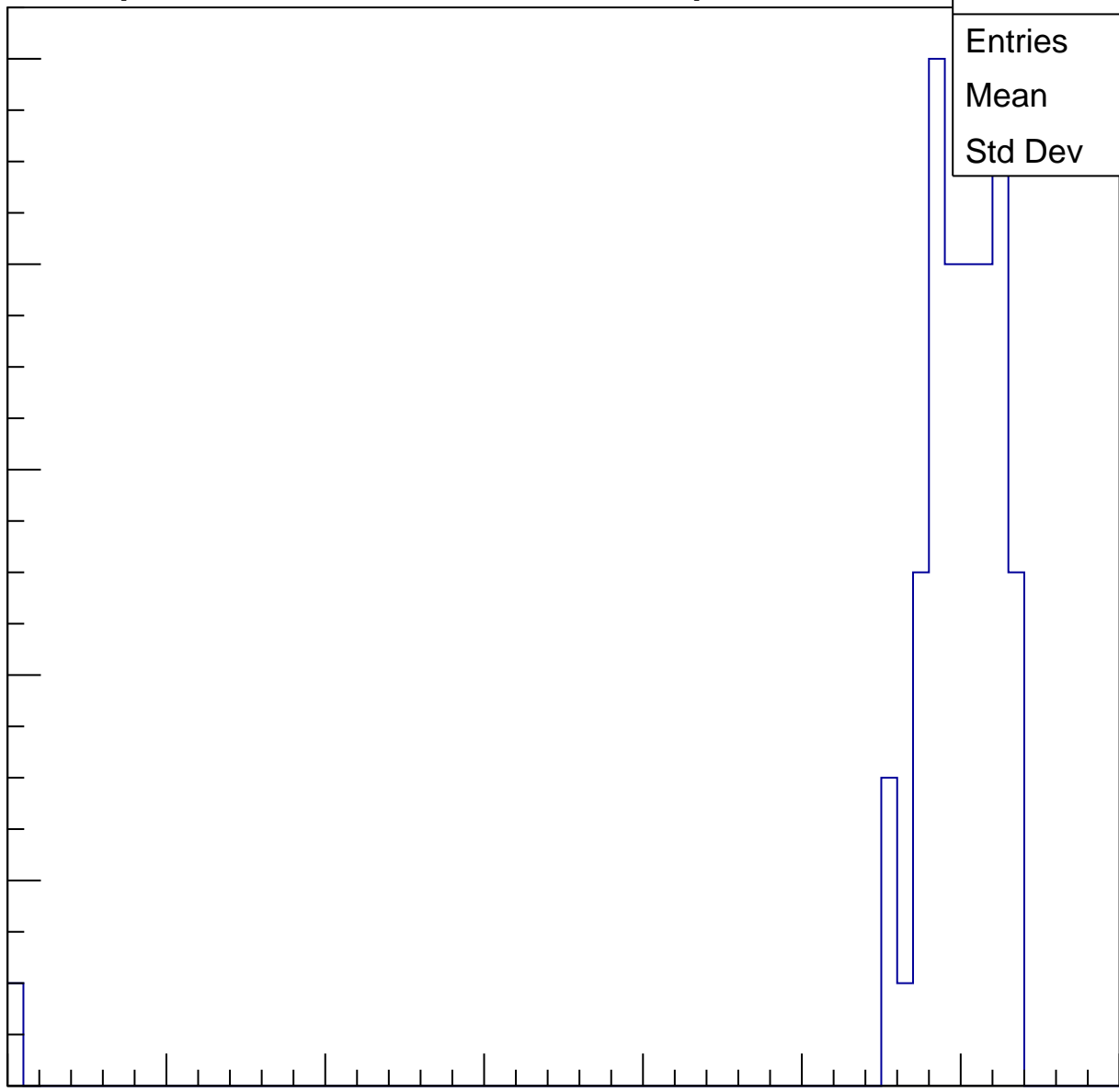
40

50

60

70

ampl

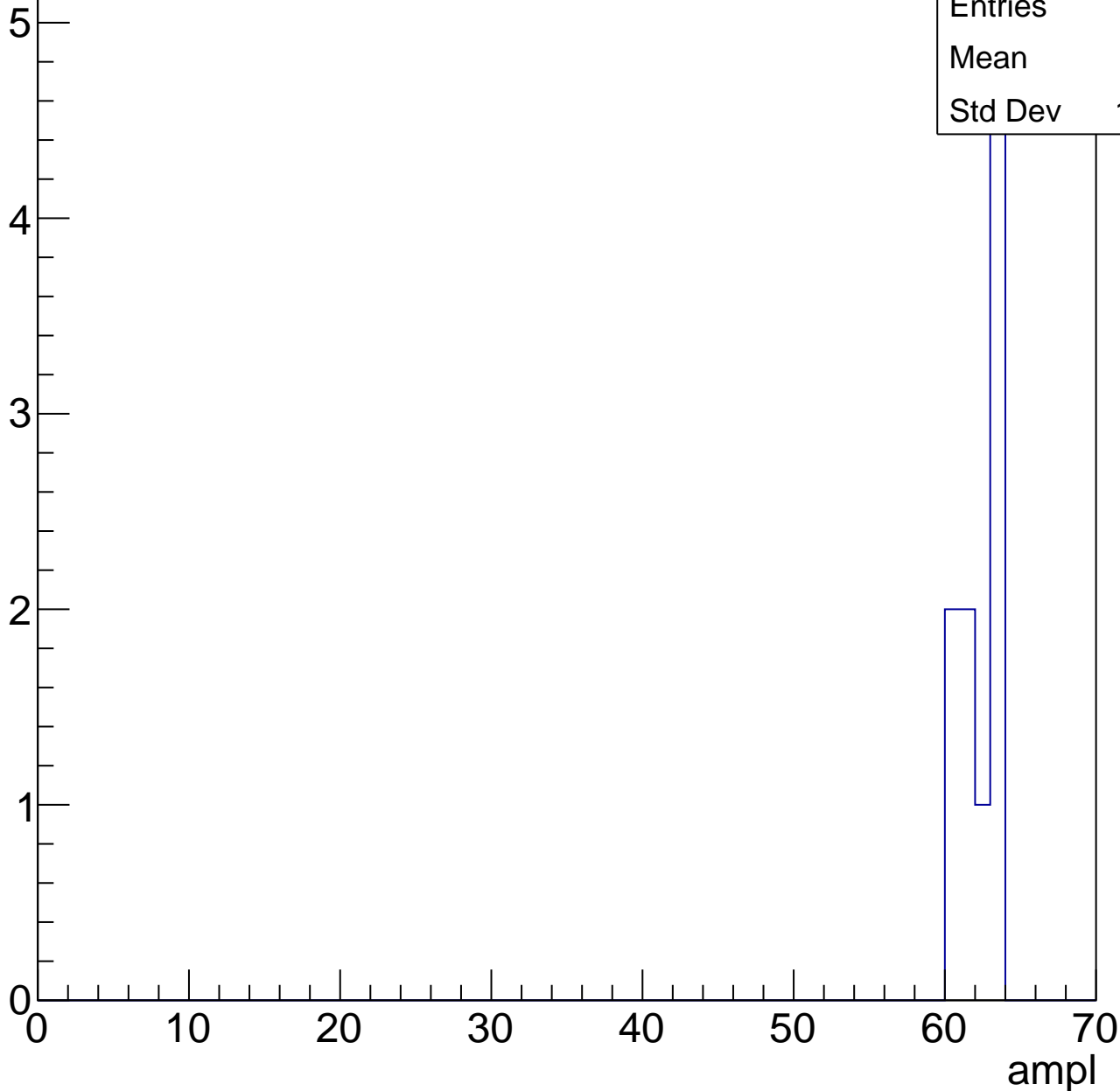


B1L003S, U21-ch96, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.9
Std Dev	1.221



B1L003S, U21-ch96, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch97, adc0

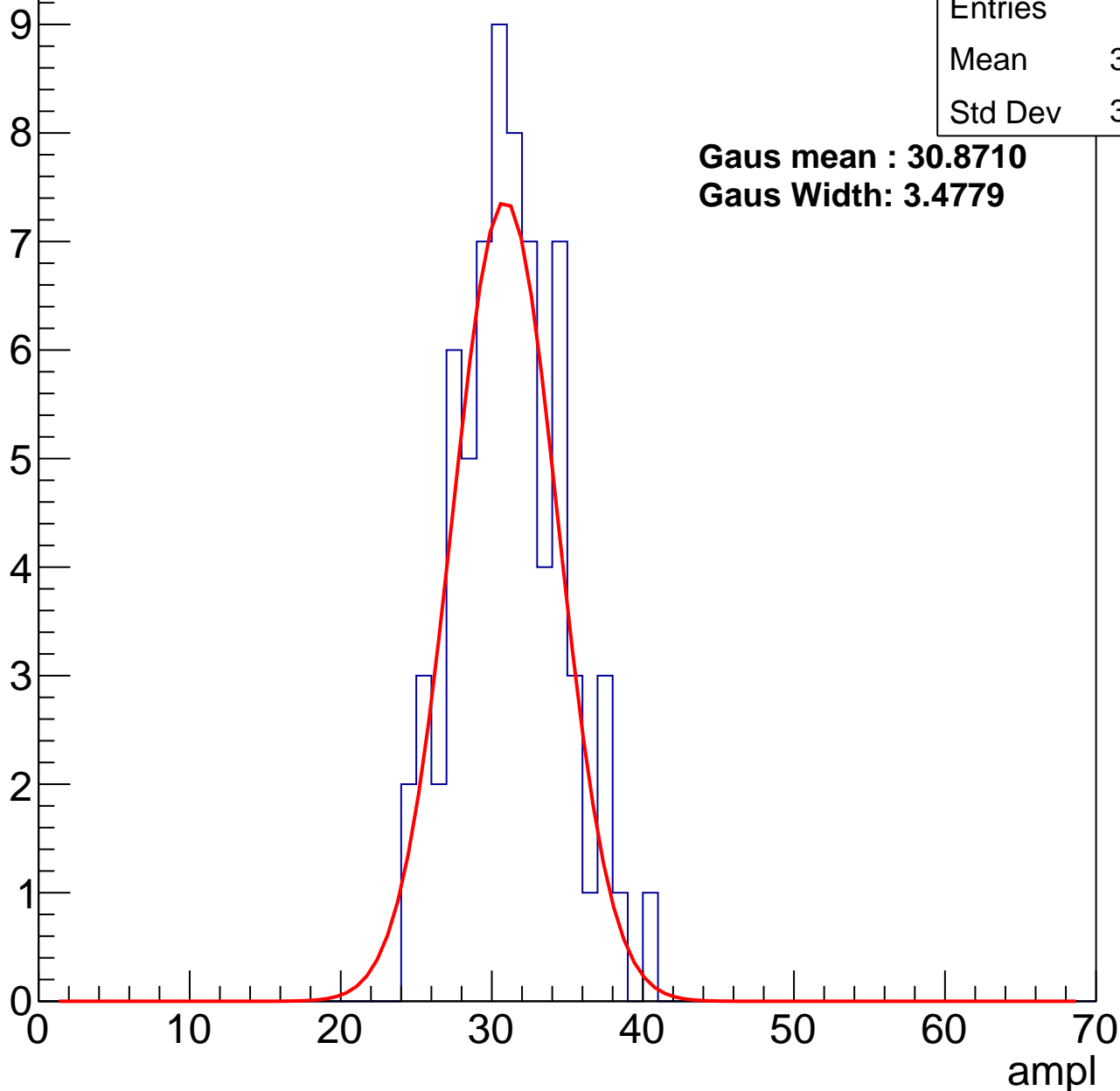
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	30.75
Std Dev	3.462

Gaus mean : 30.8710

Gaus Width: 3.4779



B1L003S, U21-ch97, adc1

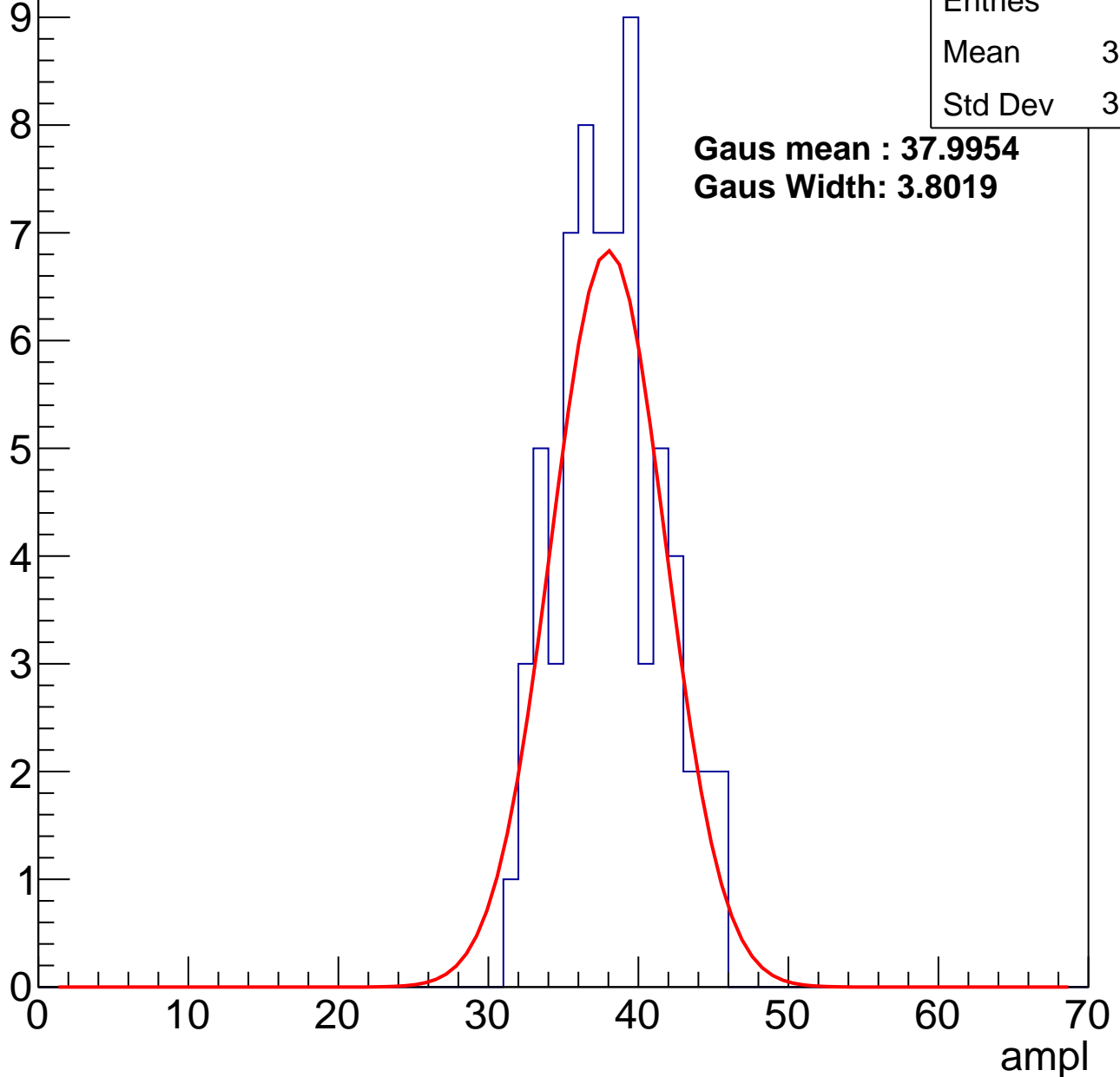
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	37.65
Std Dev	3.373

Gaus mean : 37.9954

Gaus Width: 3.8019



B1L003S, U21-ch97, adc2

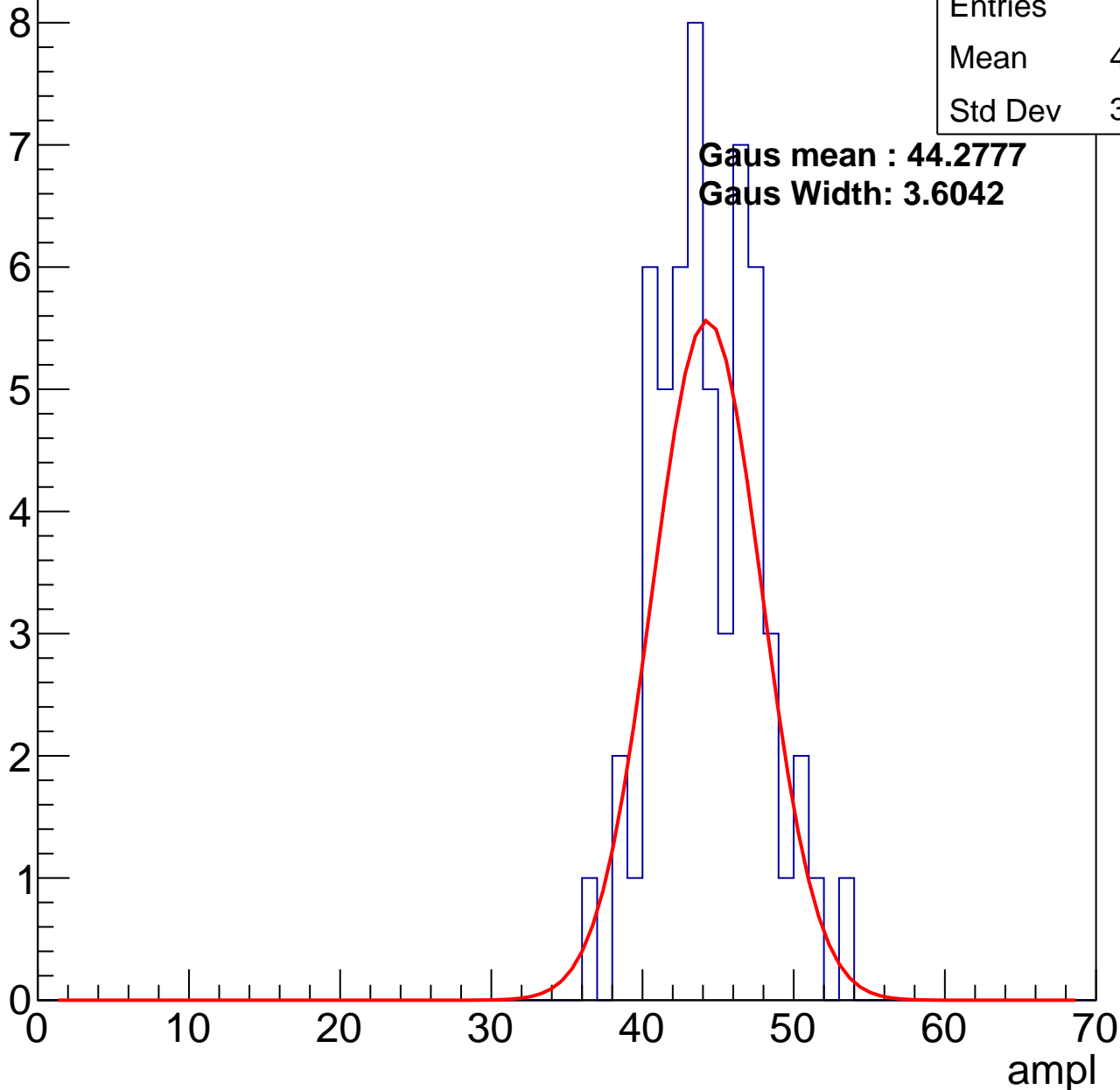
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	43.93
Std Dev	3.458

Gaus mean : 44.2777

Gaus Width: 3.6042

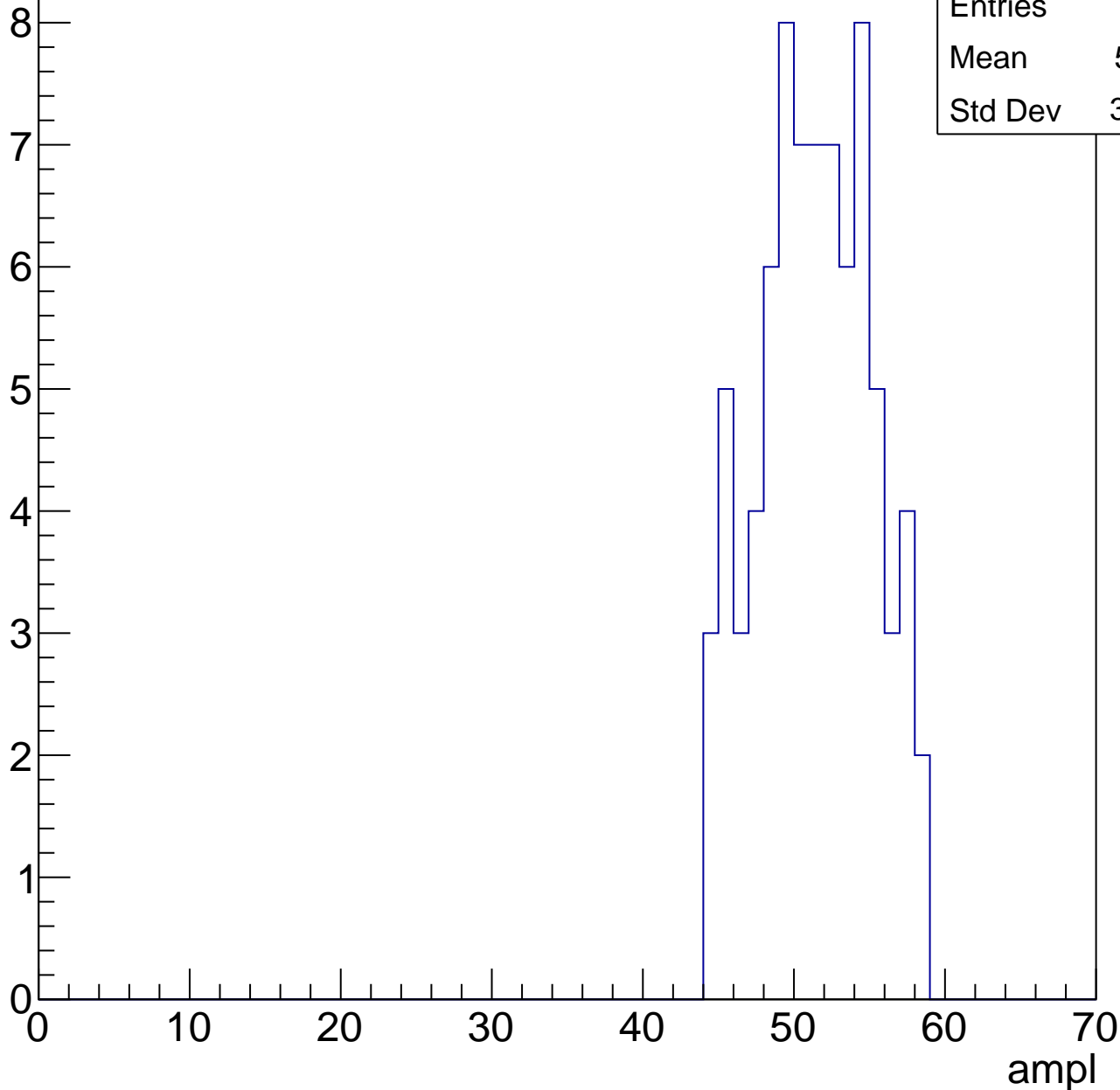


B1L003S, U21-ch97, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	50.91
Std Dev	3.684

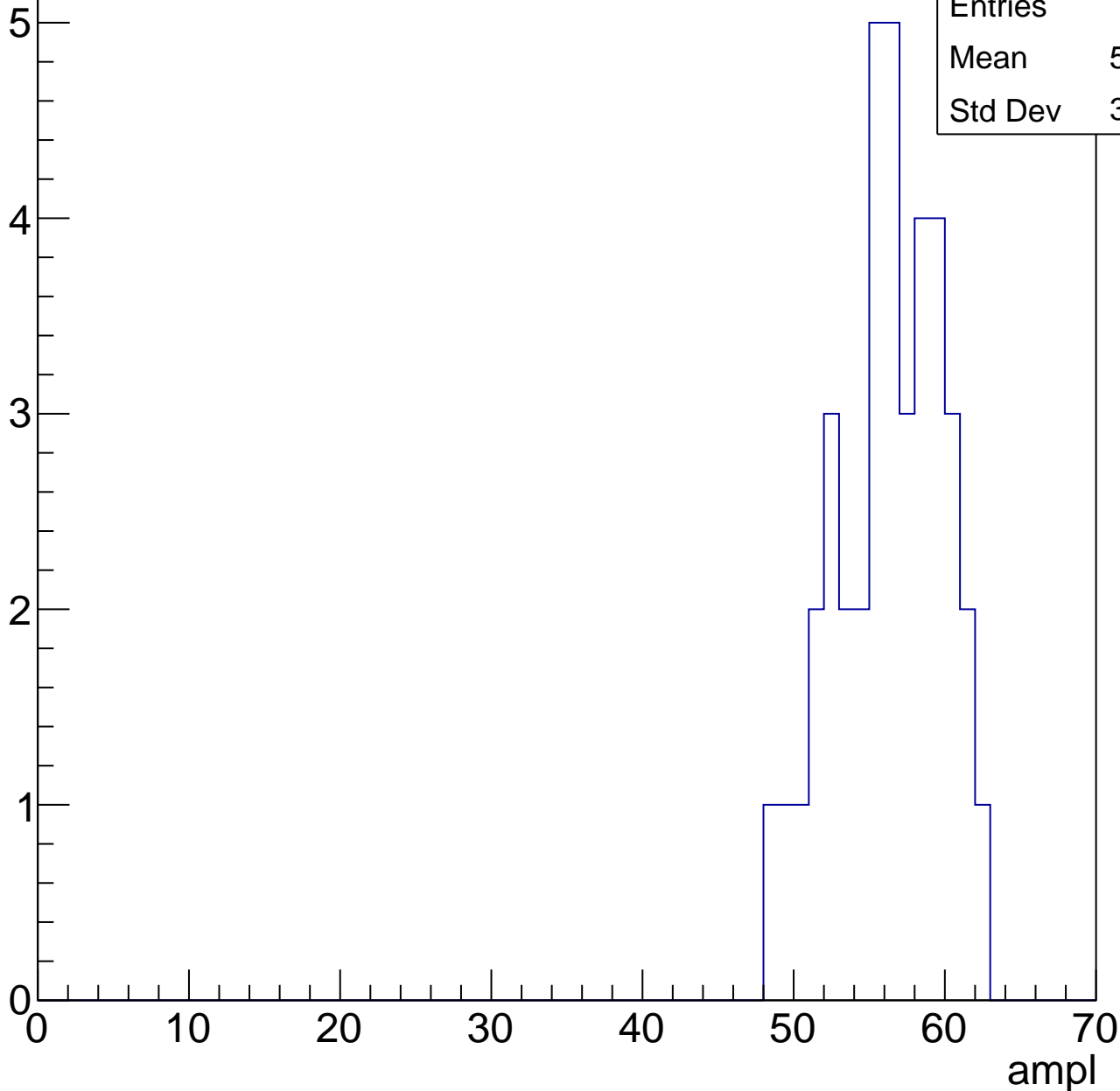


B1L003S, U21-ch97, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	55.82
Std Dev	3.456

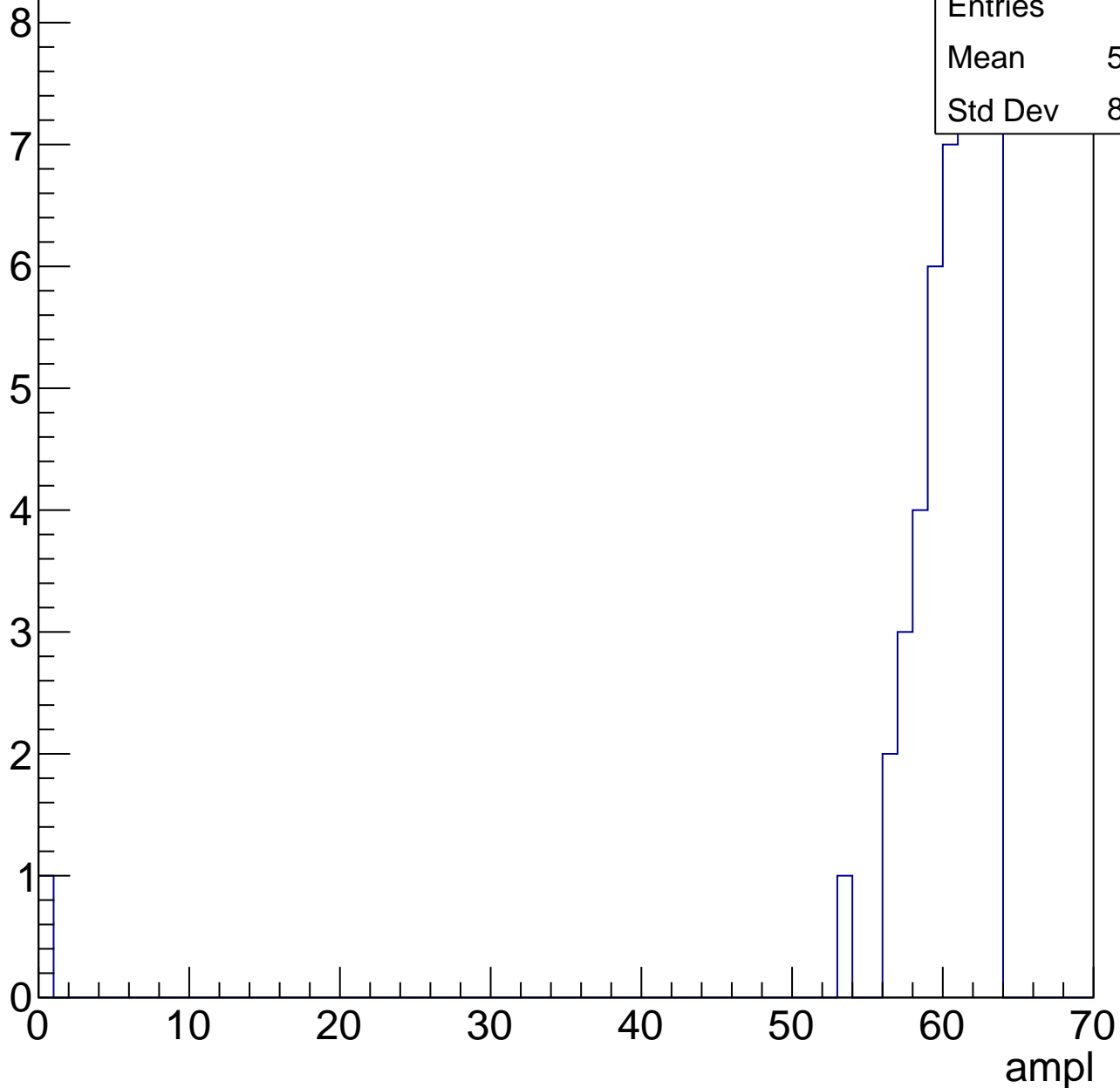


B1L003S, U21-ch97, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	48
Mean	58.96
Std Dev	8.886



B1L003S, U21-ch97, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch97, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	23
Std Dev	0

ampl

B1L003S, U21-ch98, adc0

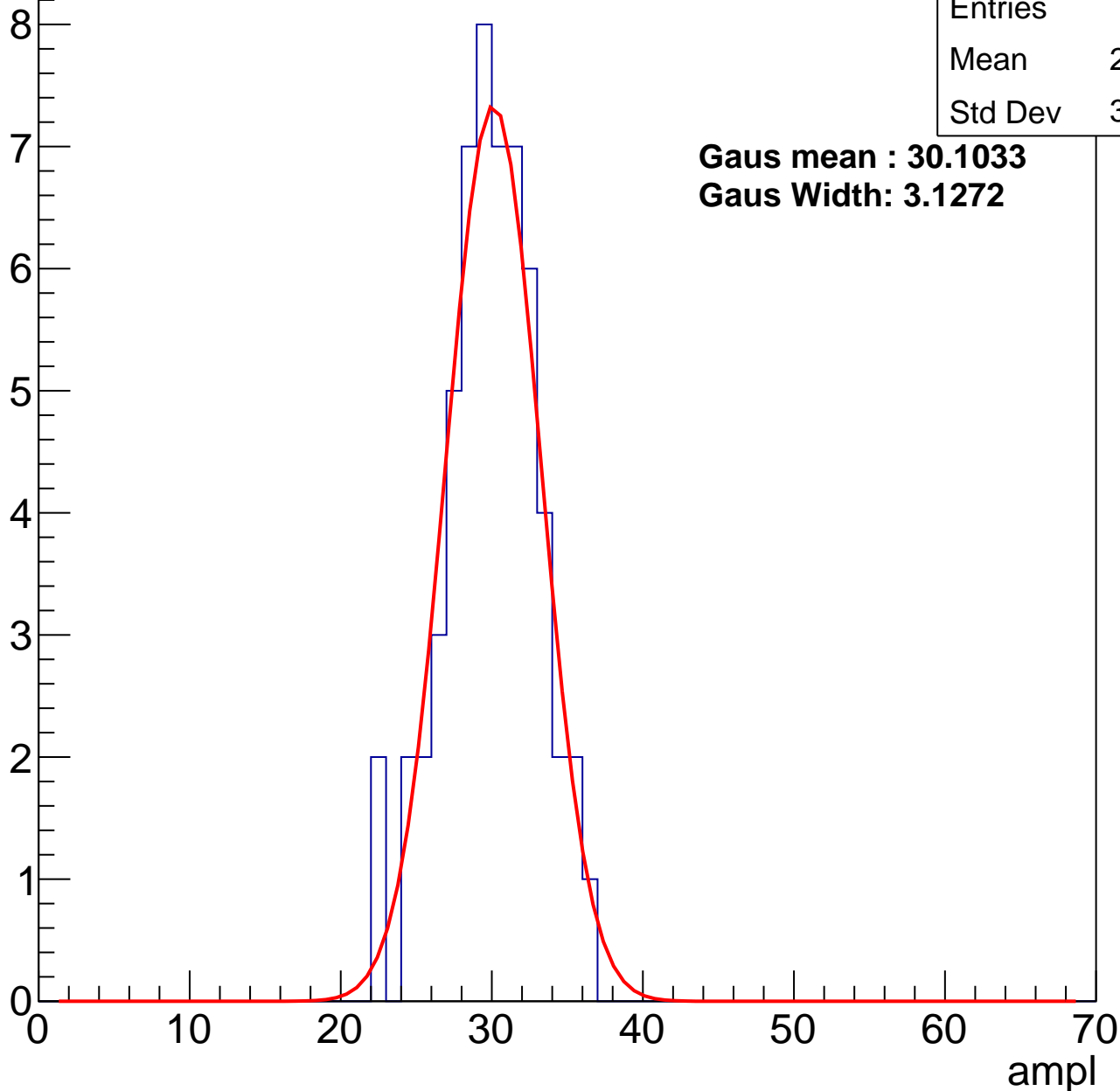
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	29.45
Std Dev	3.069

Gaus mean : 30.1033

Gaus Width: 3.1272



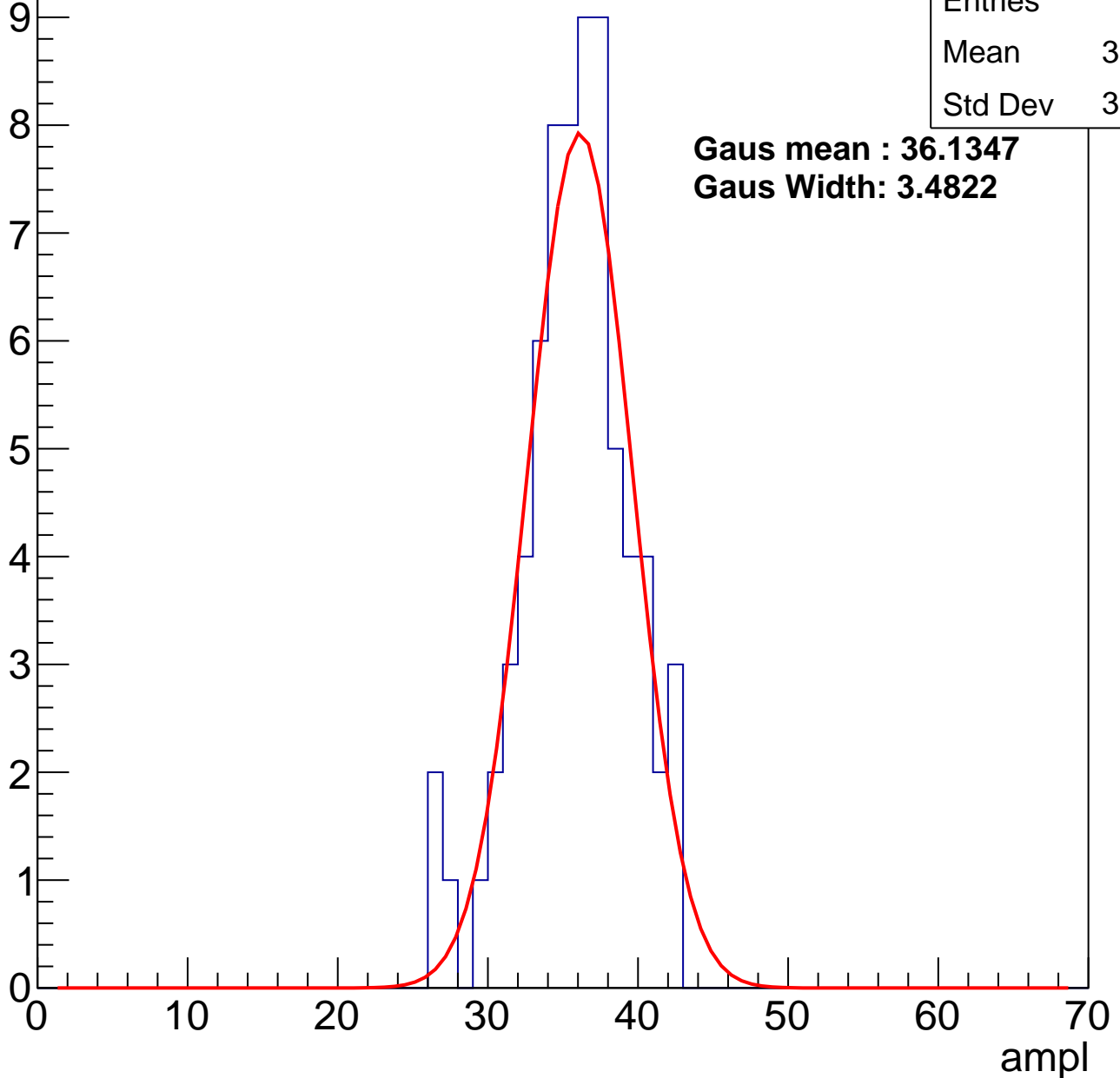
B1L003S, U21-ch98, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	35.35
Std Dev	3.545

Gaus mean : 36.1347
Gaus Width: 3.4822



B1L003S, U21-ch98, adc2

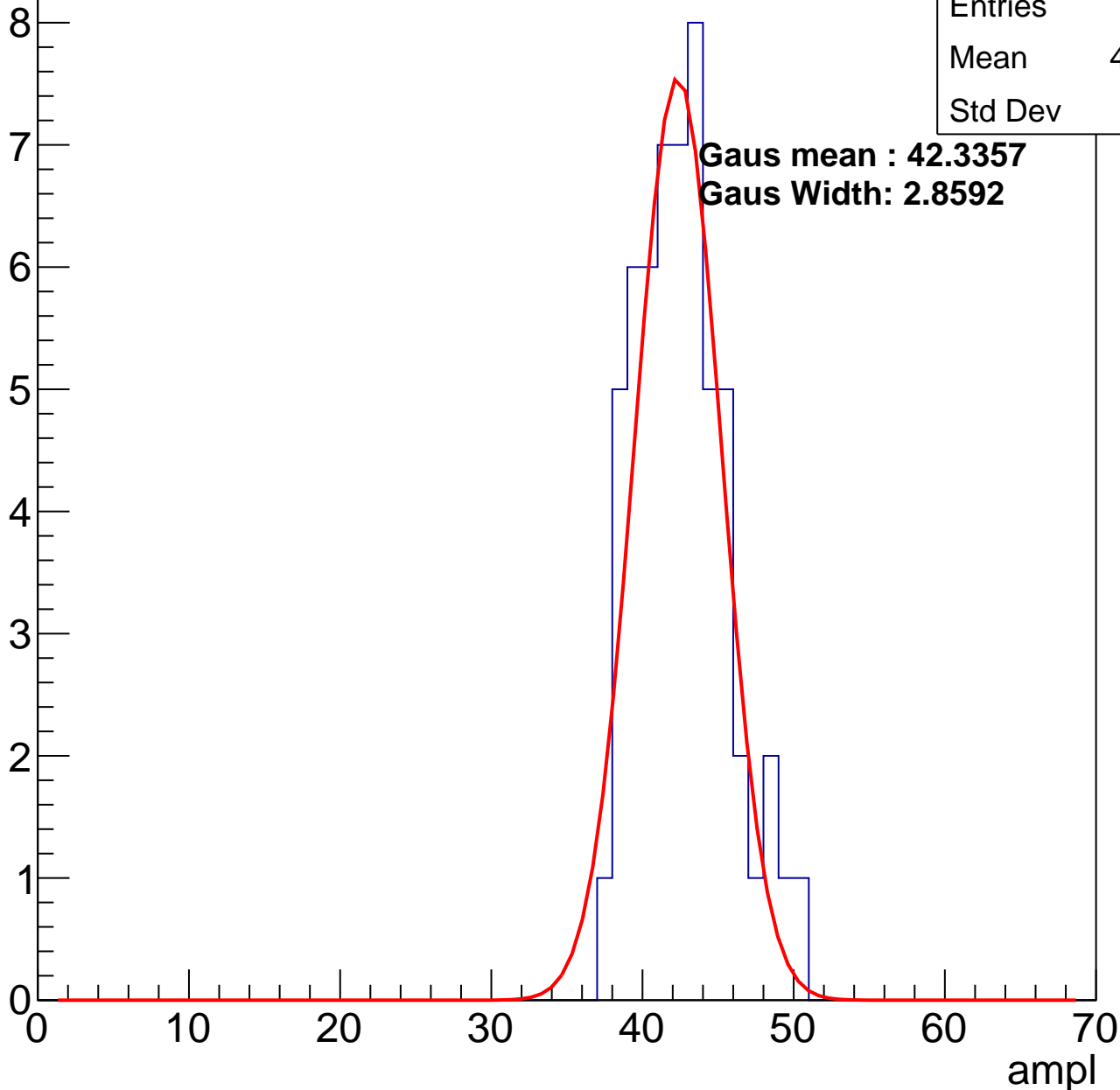
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.19
Std Dev	2.97

Gaus mean : 42.3357

Gaus Width: 2.8592

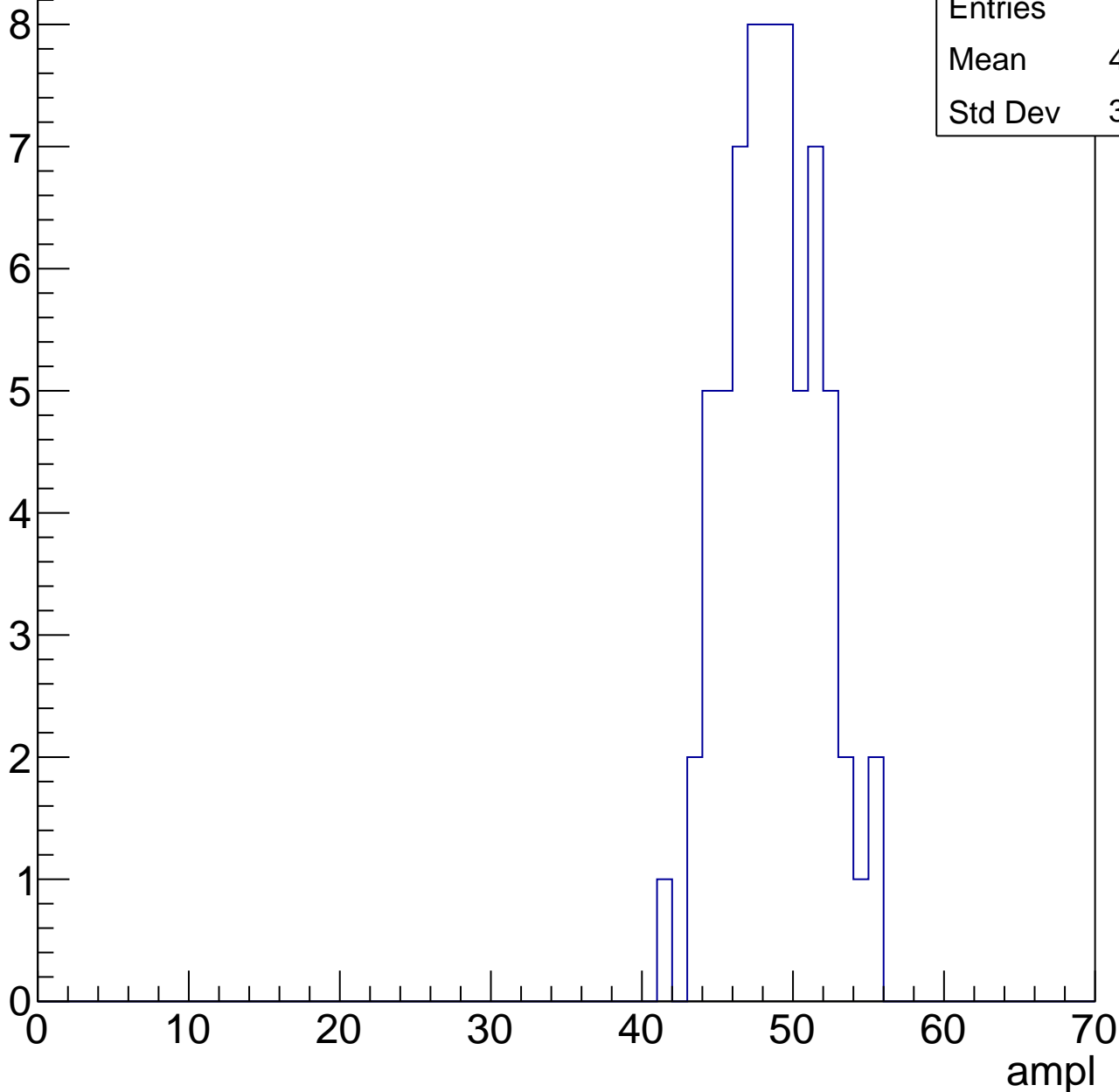


B1L003S, U21-ch98, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.23
Std Dev	3.044

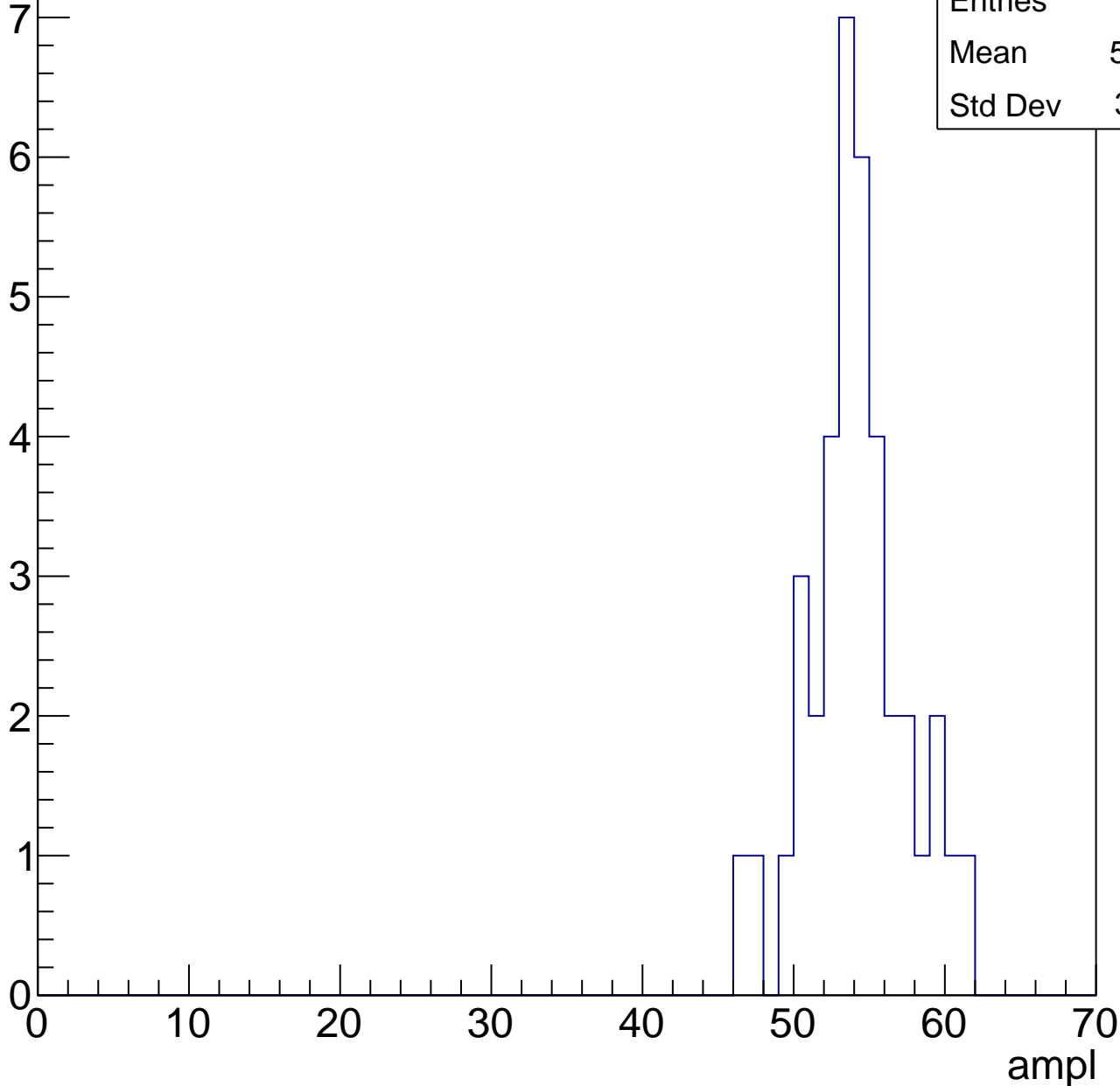


B1L003S, U21-ch98, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

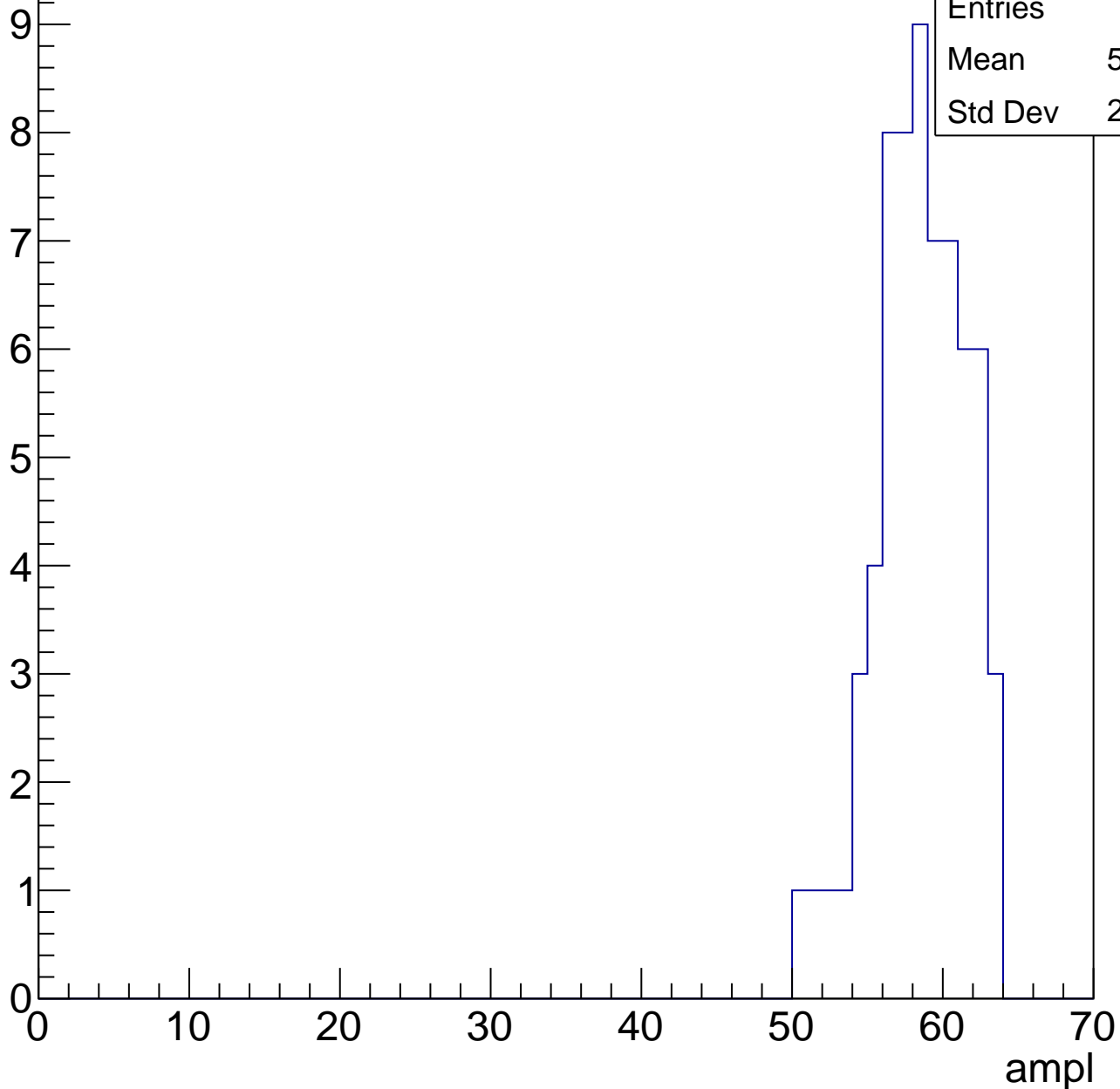
Entries	38
Mean	53.68
Std Dev	3.261



B1L003S, U21-ch98, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



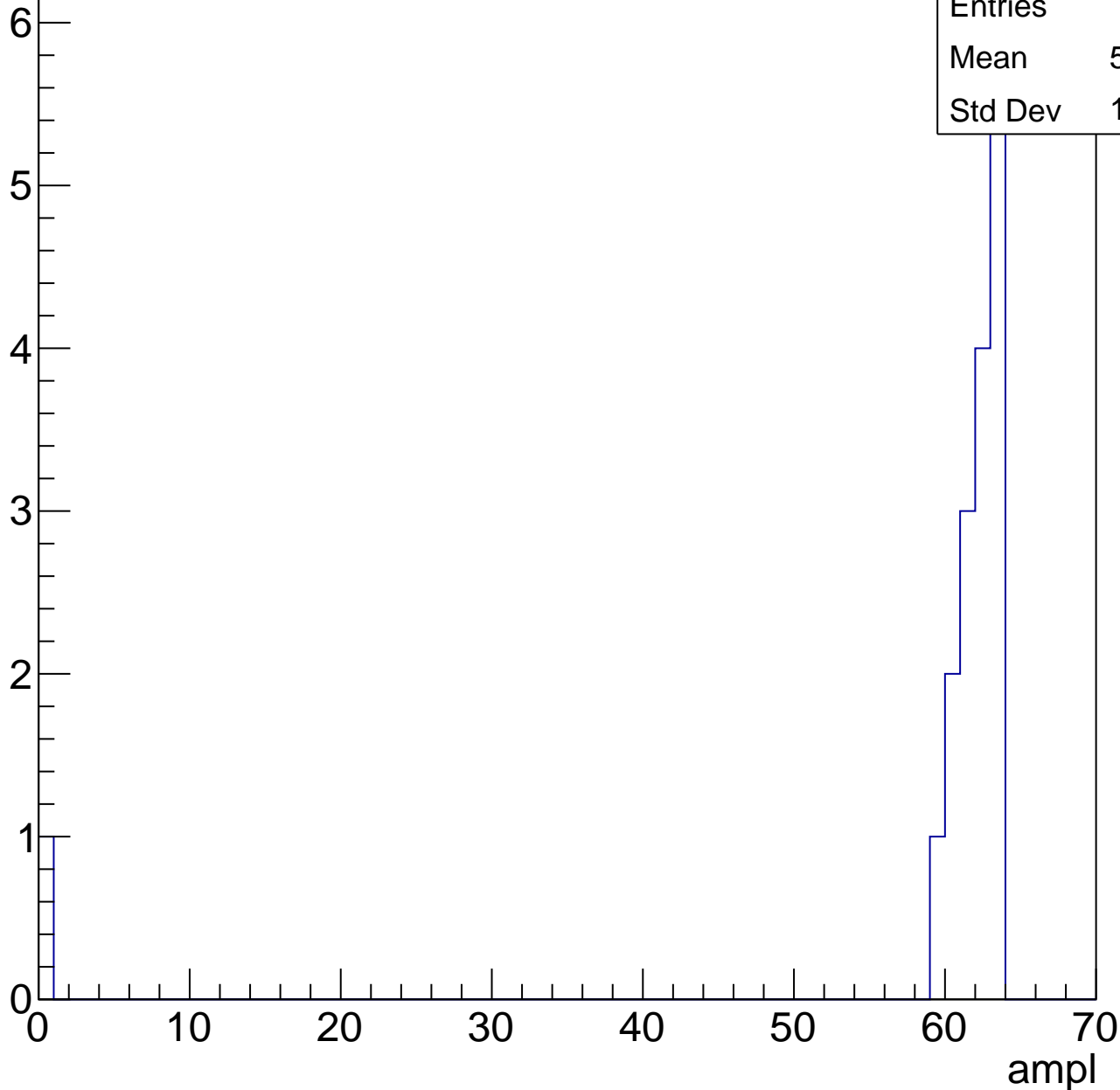
Entries	65
Mean	58.06
Std Dev	2.929

B1L003S, U21-ch98, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.12
Std Dev	14.58



B1L003S, U21-ch98, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	42
Std Dev	21

B1L003S, U21-ch99, adc0

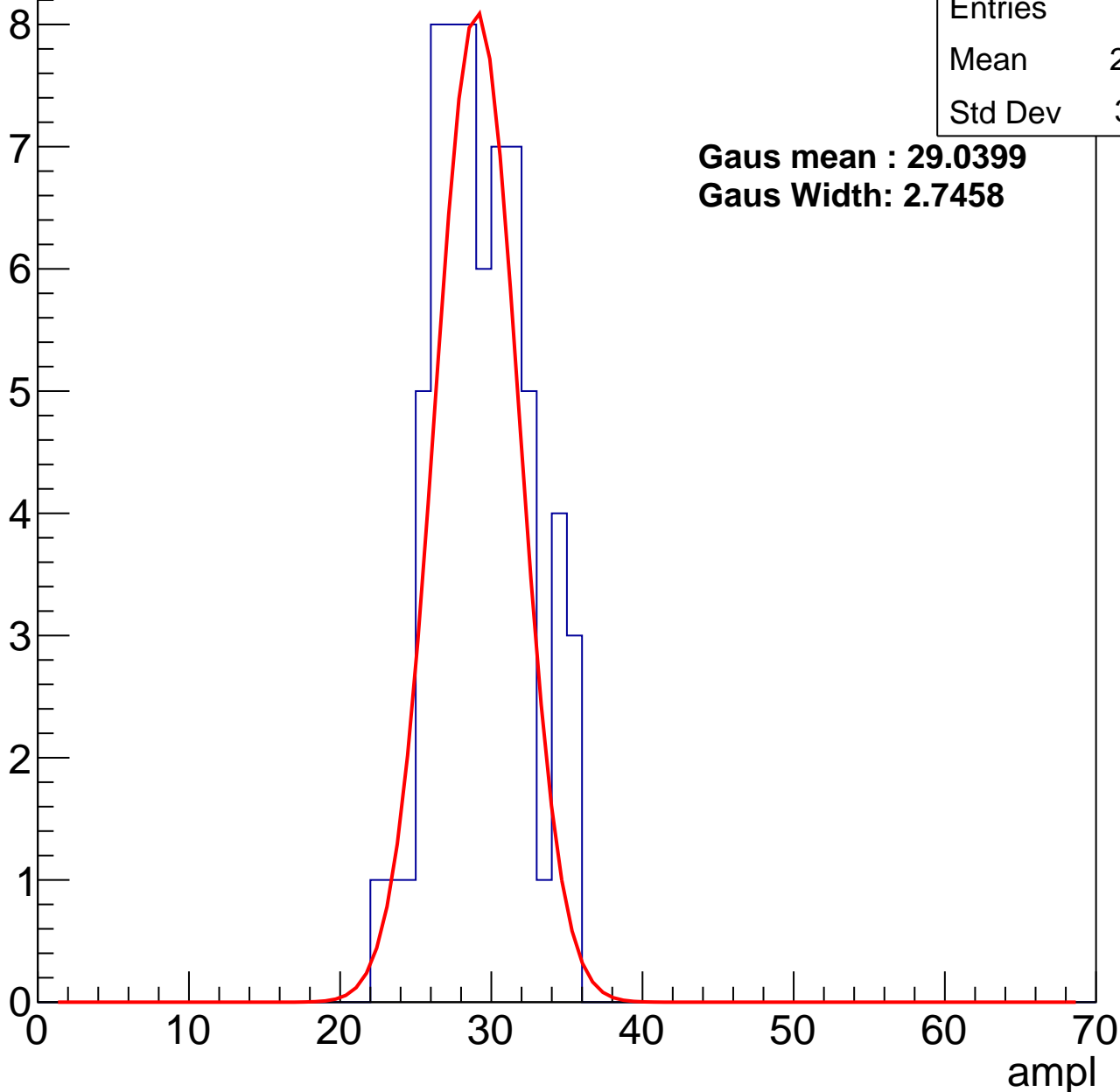
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	28.88
Std Dev	3.051

Gaus mean : 29.0399

Gaus Width: 2.7458



B1L003S, U21-ch99, adc1

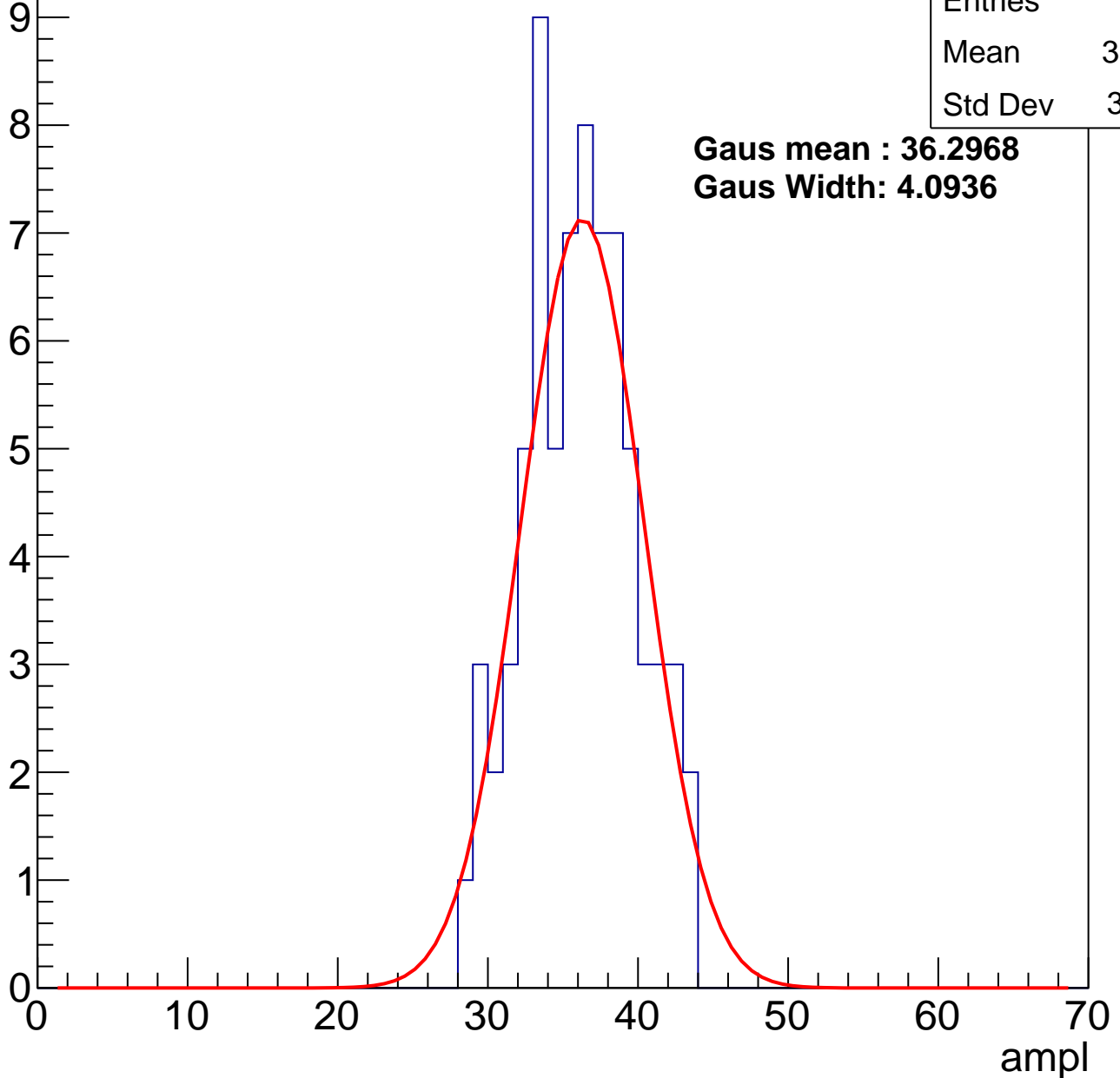
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	35.66
Std Dev	3.601

Gaus mean : 36.2968

Gaus Width: 4.0936



B1L003S, U21-ch99, adc2

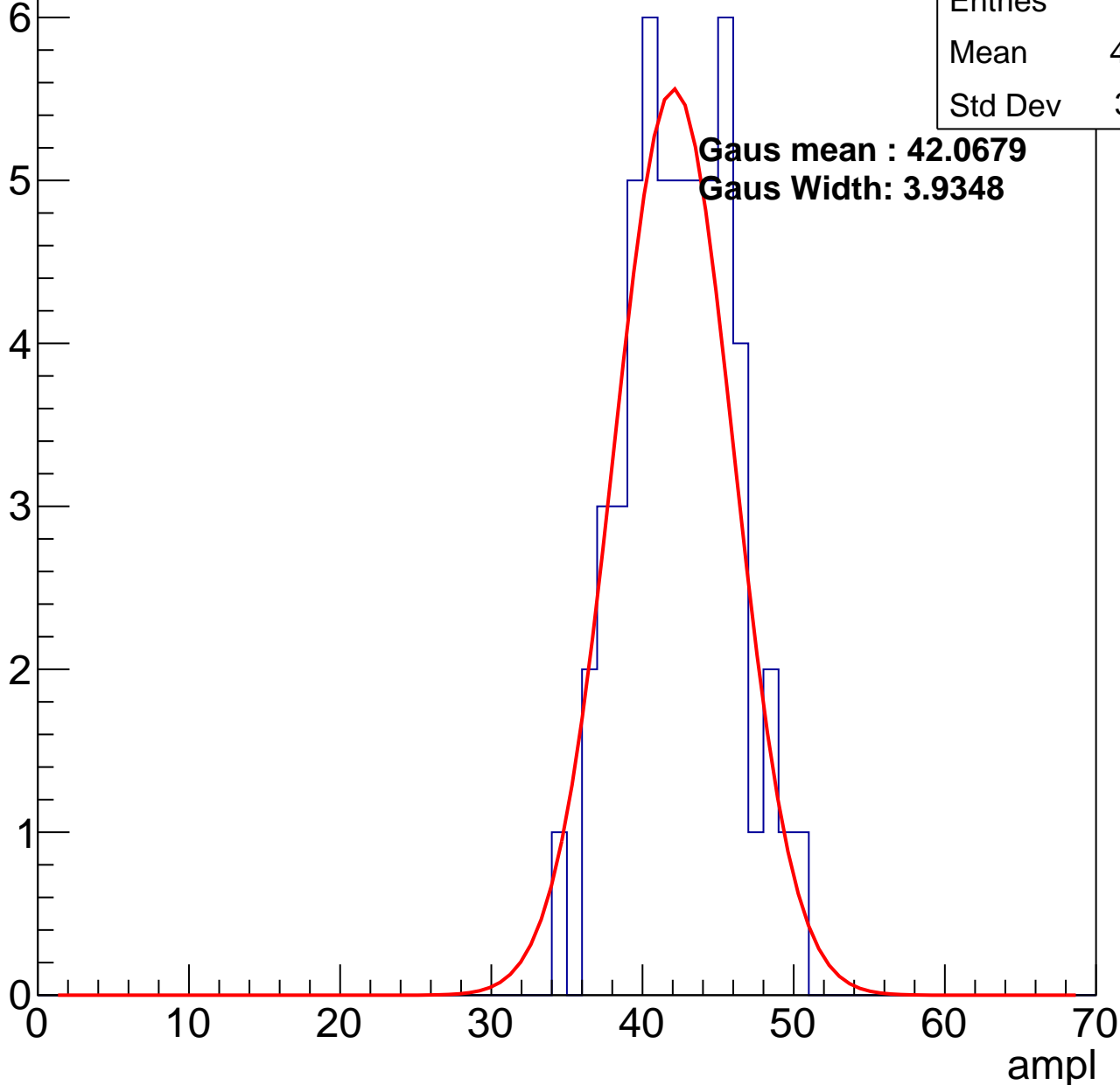
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	42.04
Std Dev	3.531

Gaus mean : 42.0679

Gaus Width: 3.9348

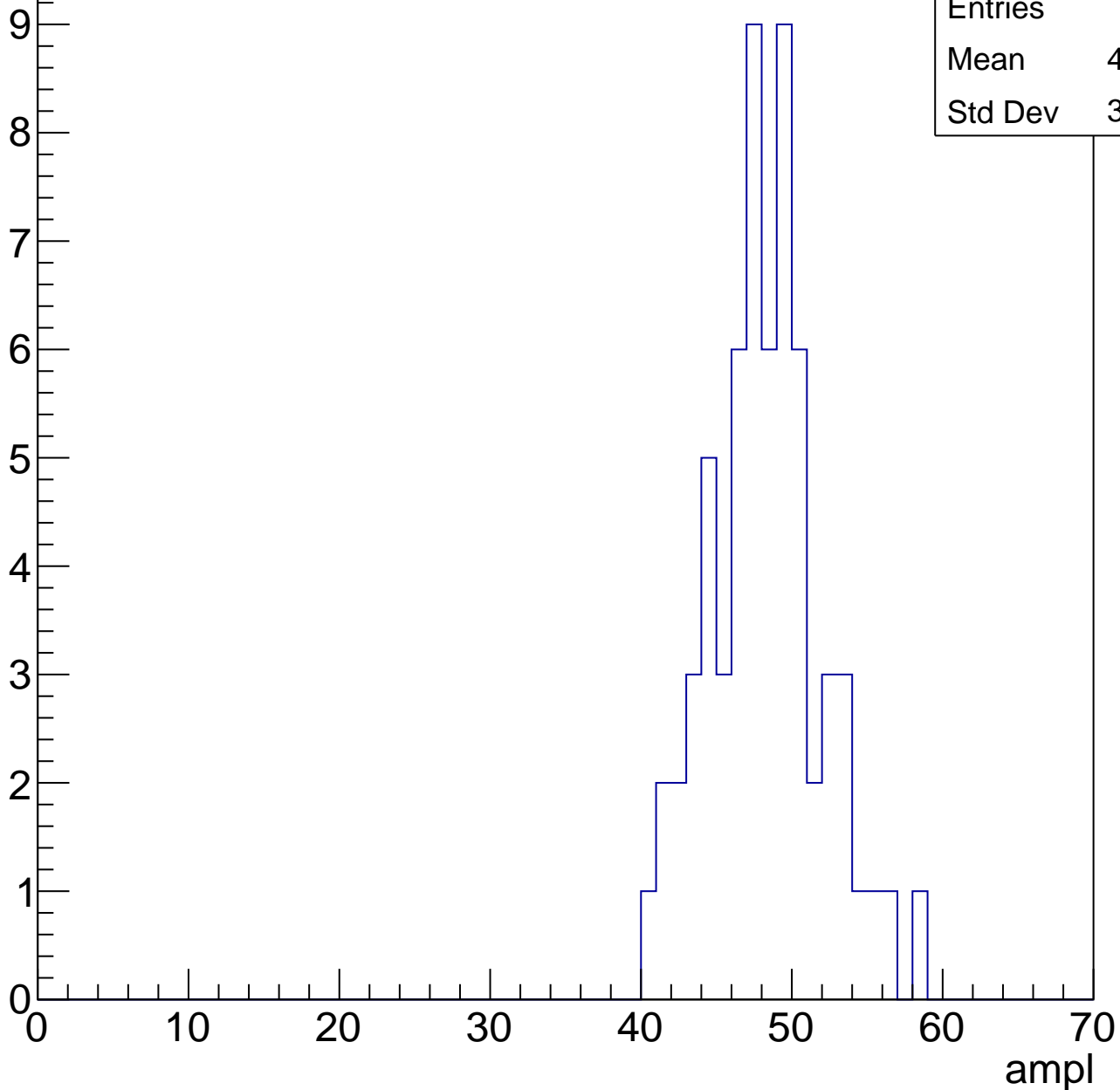


B1L003S, U21-ch99, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	47.78
Std Dev	3.685

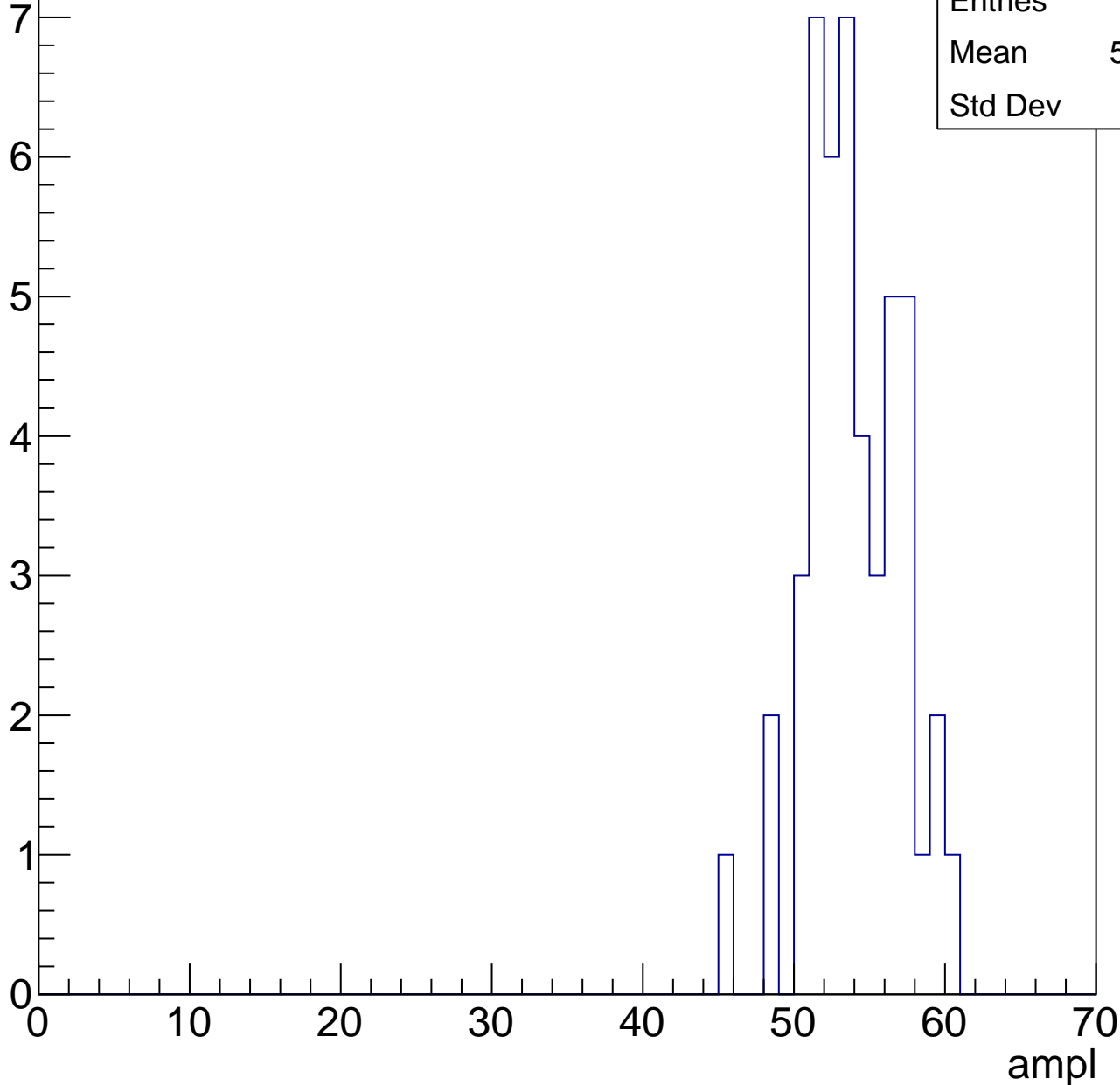


B1L003S, U21-ch99, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	53.47
Std Dev	3.1

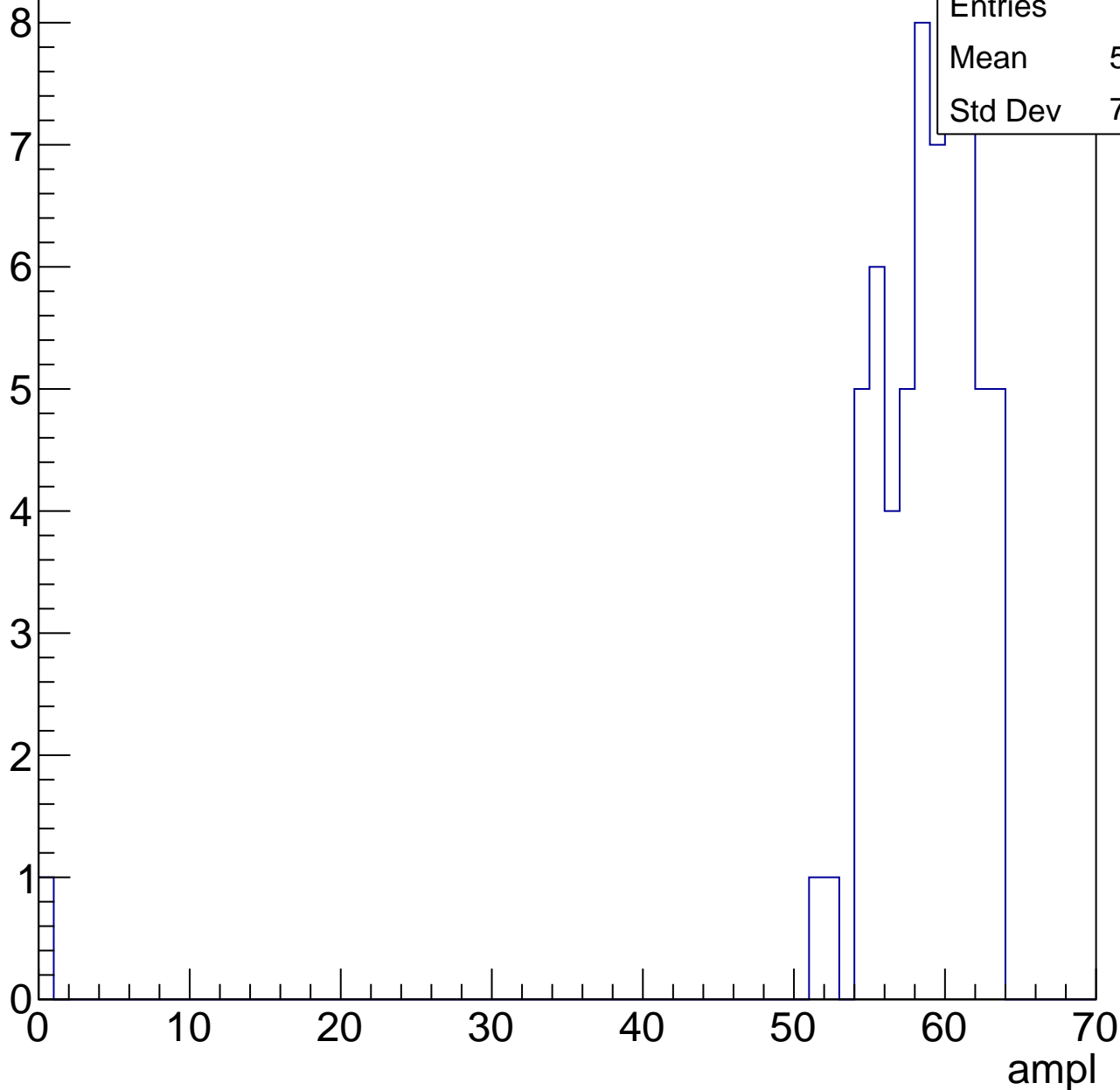


B1L003S, U21-ch99, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	57.53
Std Dev	7.812

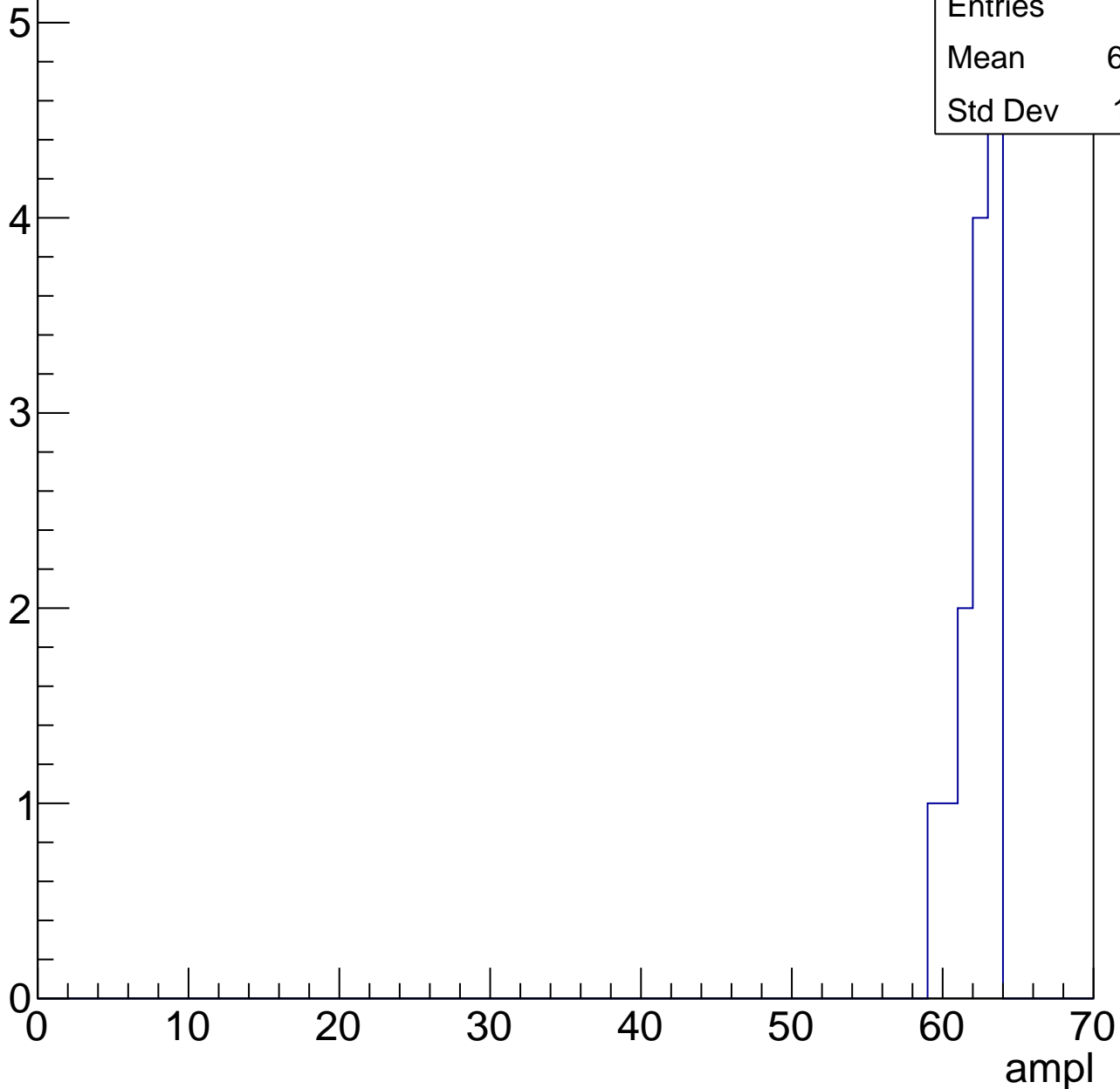


B1L003S, U21-ch99, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

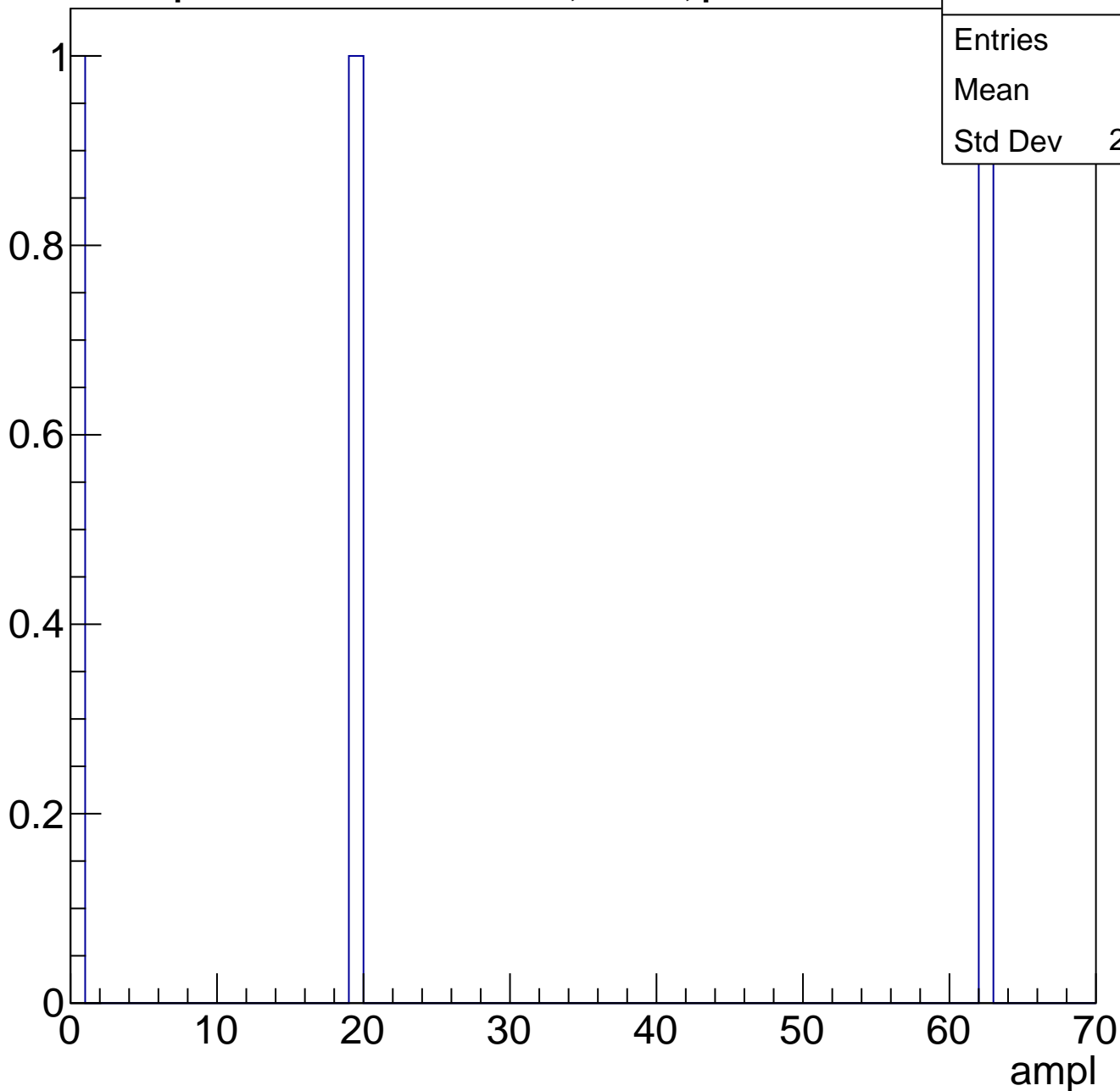
Entries	13
Mean	61.85
Std Dev	1.231



B1L003S, U21-ch99, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	27
Std Dev	25.94

B1L003S, U21-ch100, adc0

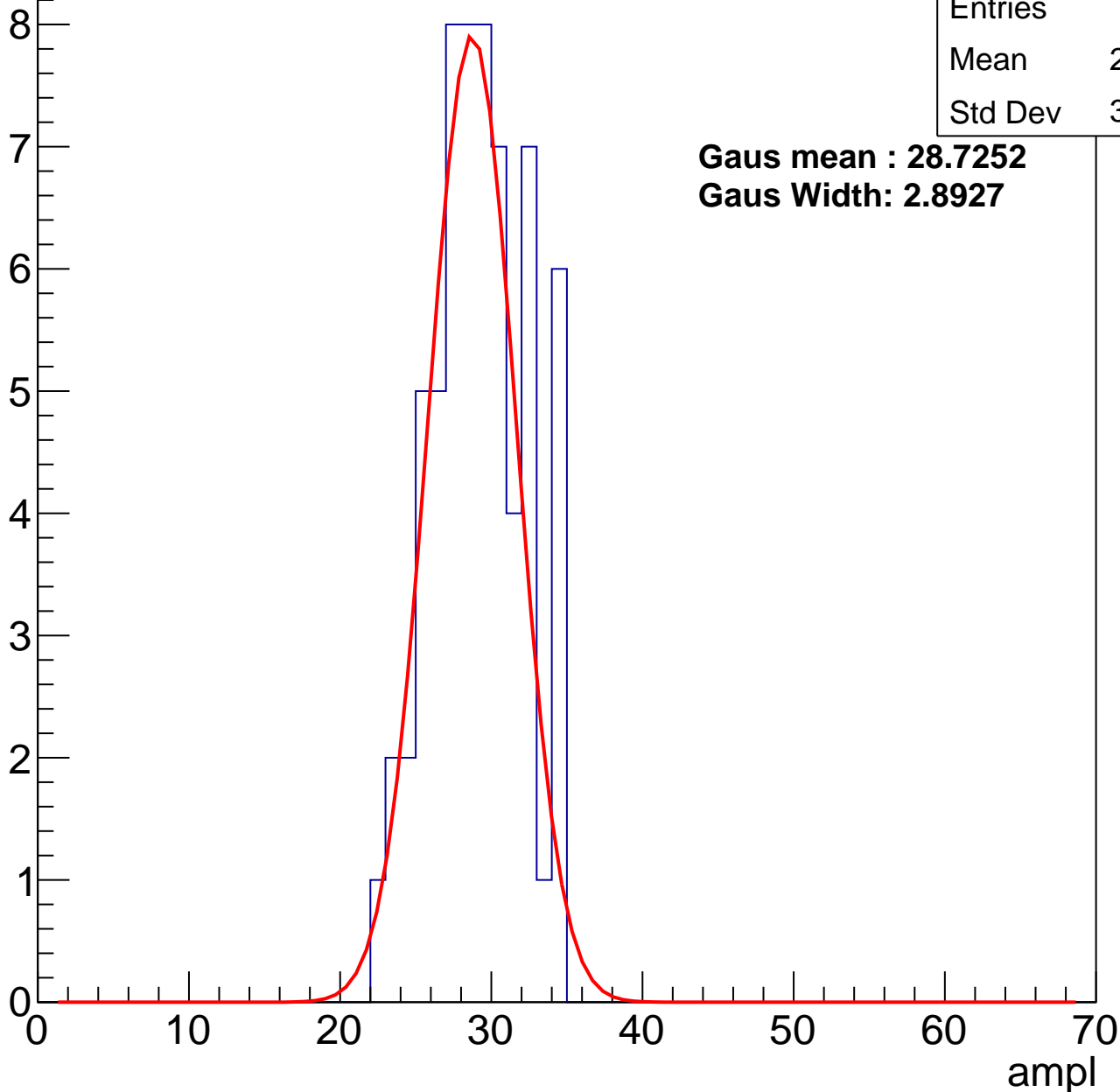
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	28.72
Std Dev	3.018

Gaus mean : 28.7252

Gaus Width: 2.8927



B1L003S, U21-ch100, adc1

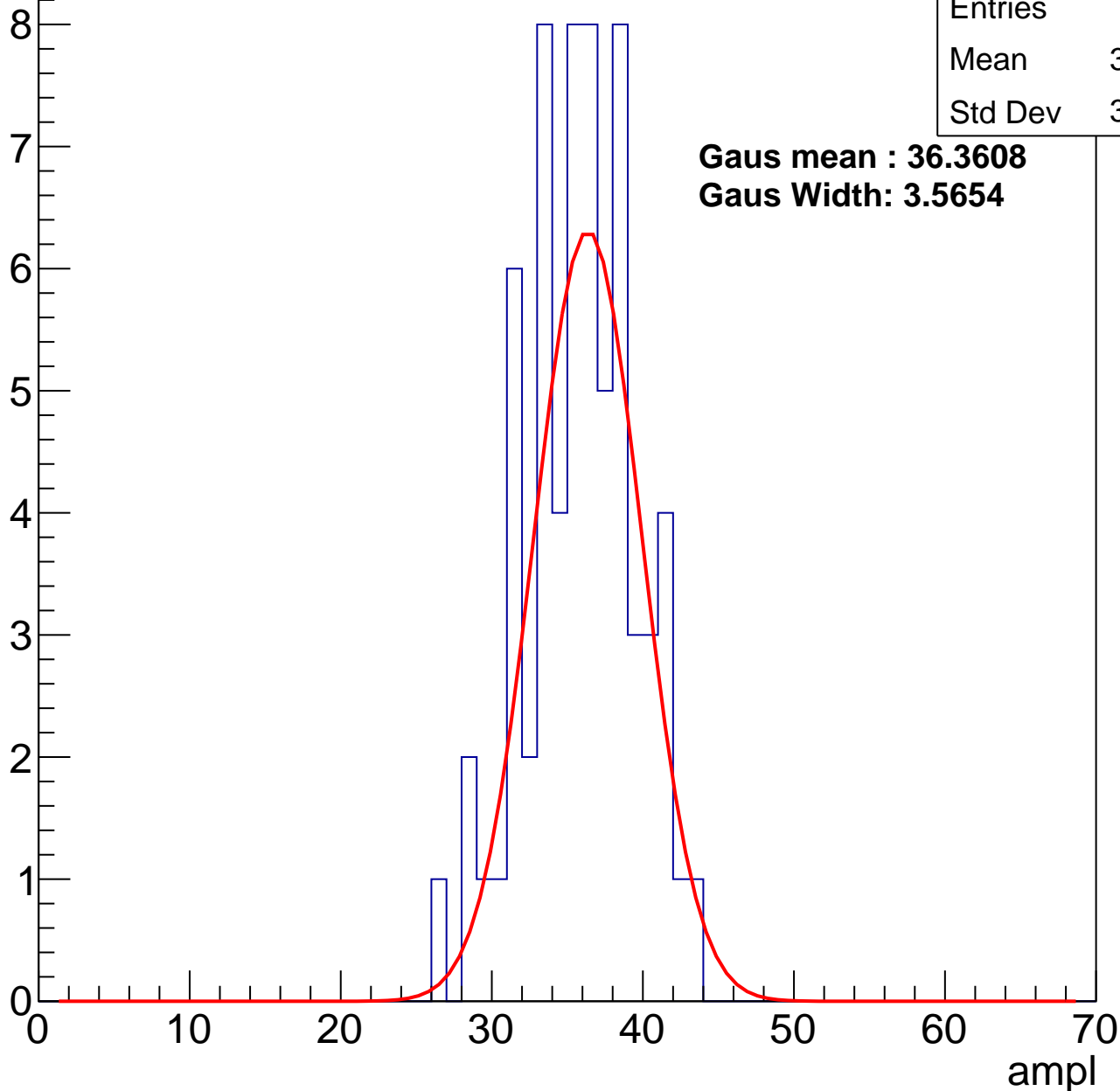
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	35.36
Std Dev	3.612

Gaus mean : 36.3608

Gaus Width: 3.5654



B1L003S, U21-ch100, adc2

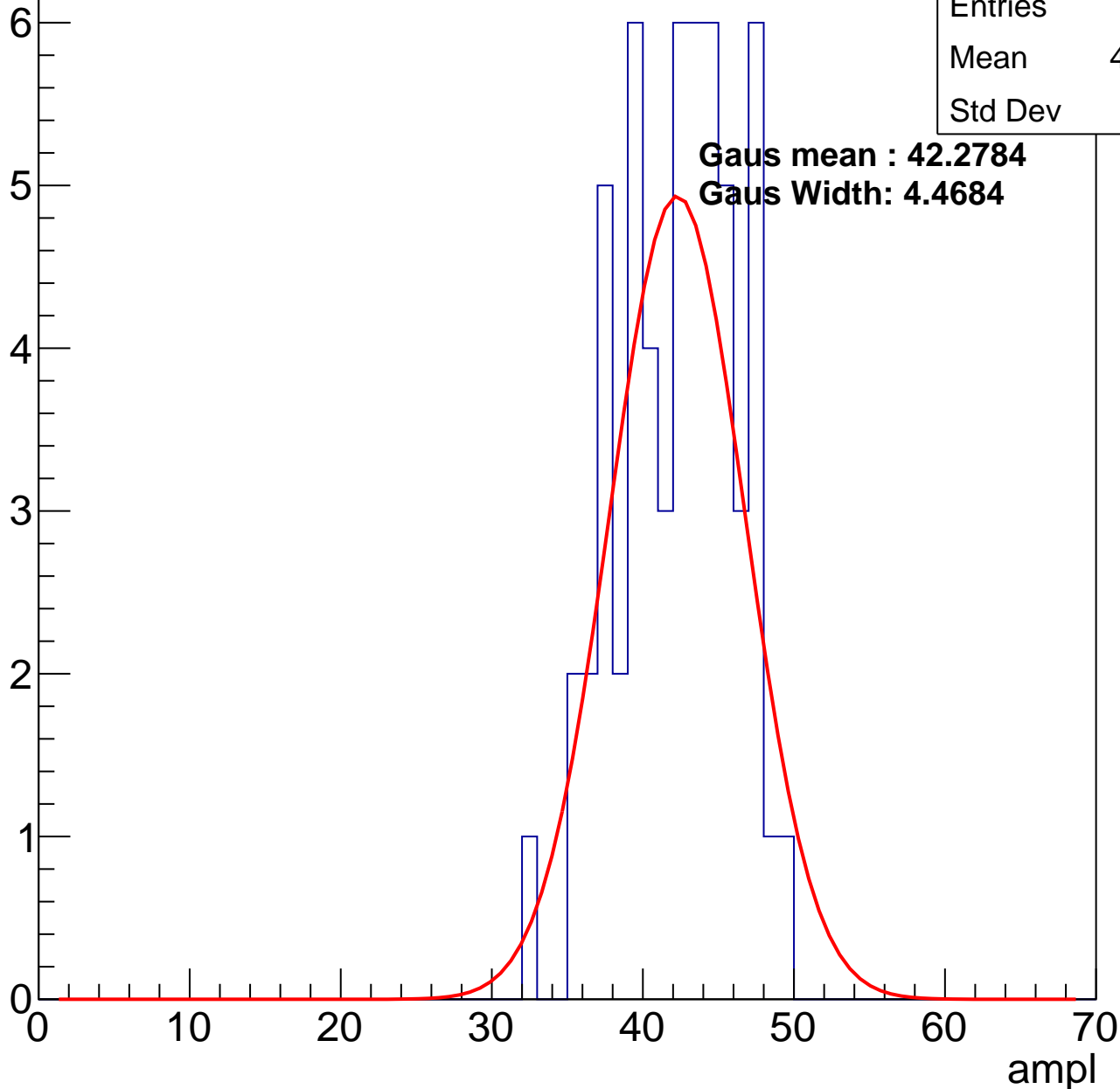
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	41.83
Std Dev	3.81

Gaus mean : 42.2784

Gaus Width: 4.4684

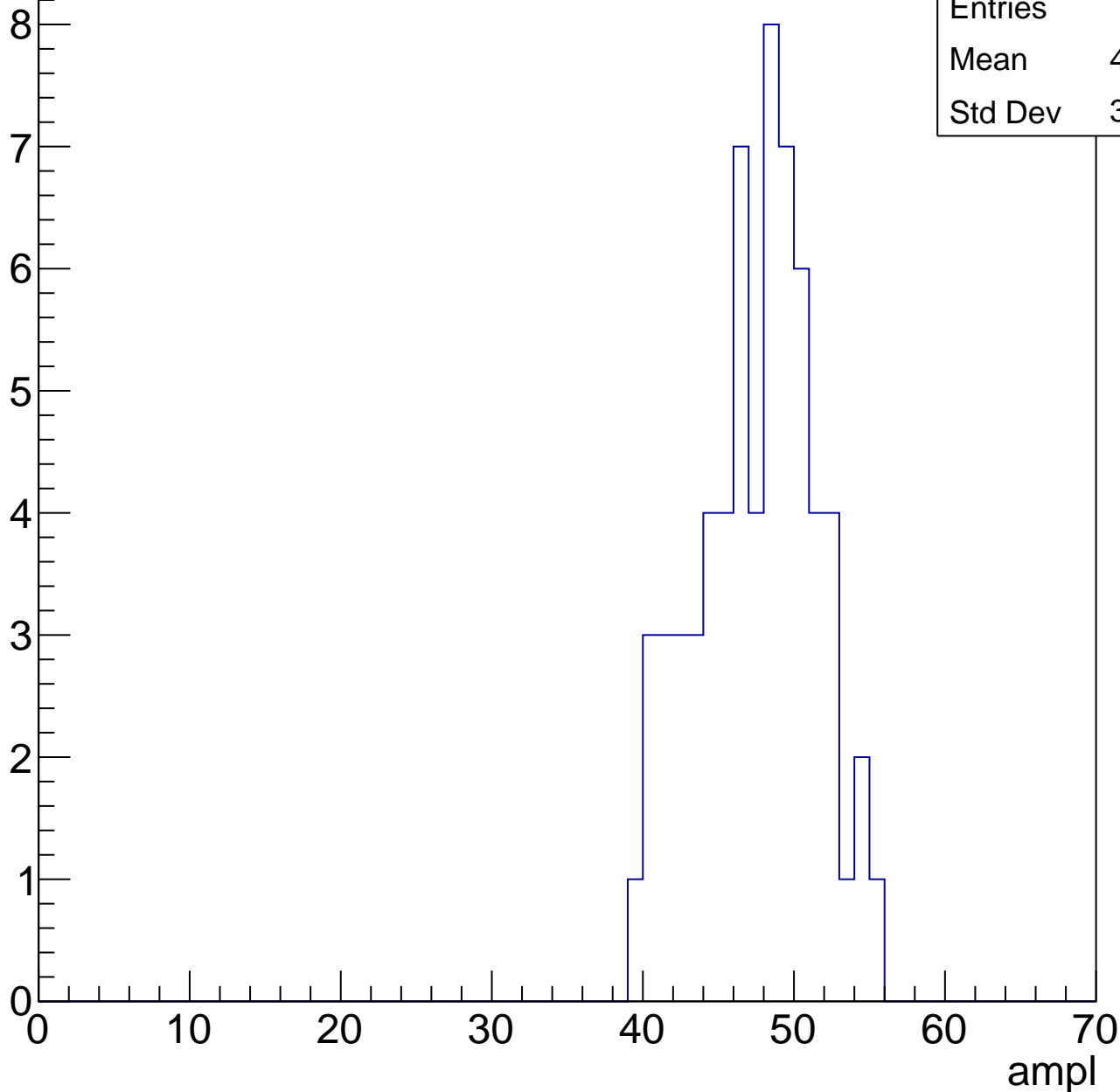


B1L003S, U21-ch100, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	47.05
Std Dev	3.829

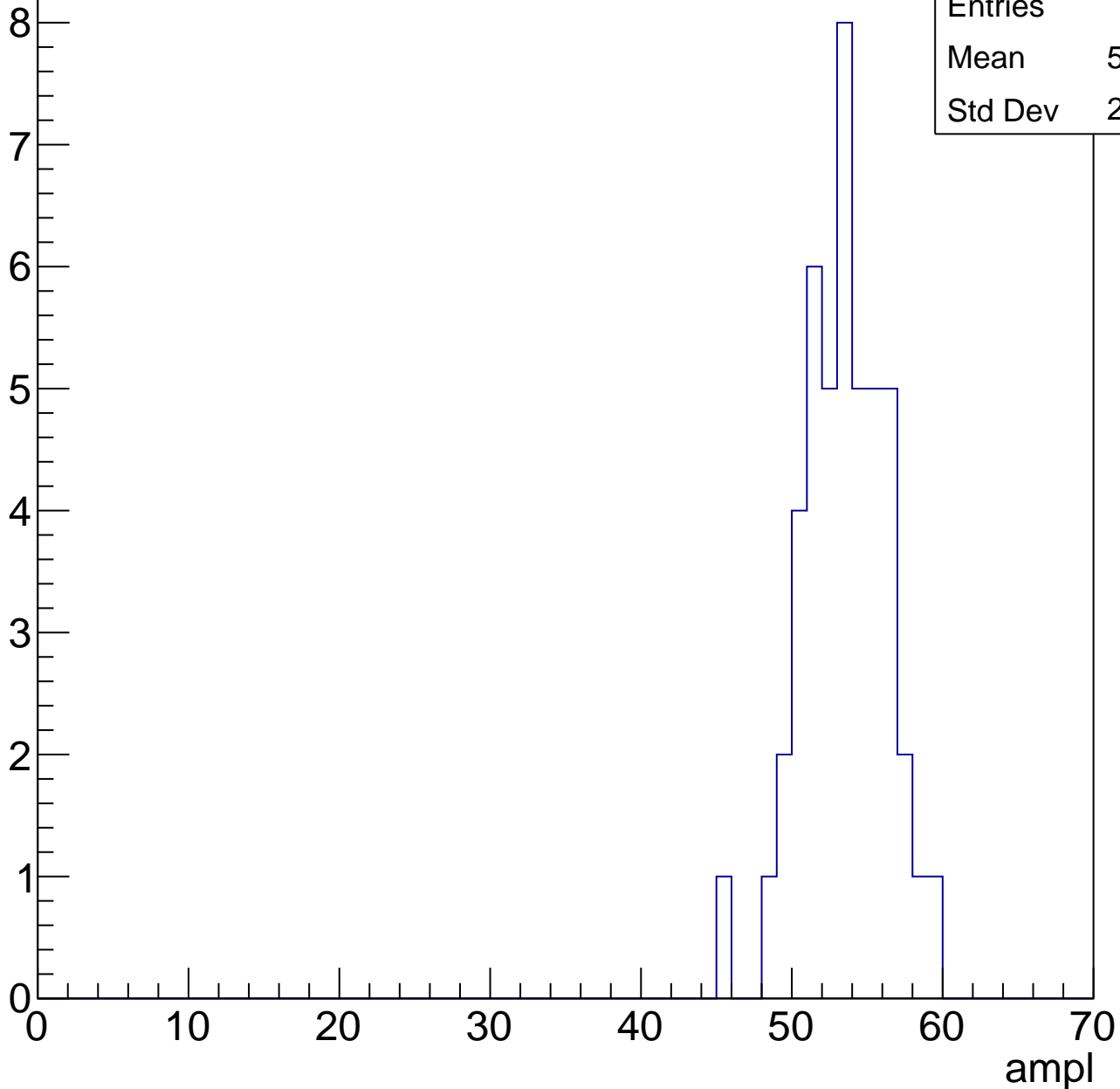


B1L003S, U21-ch100, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	46
Mean	52.98
Std Dev	2.754

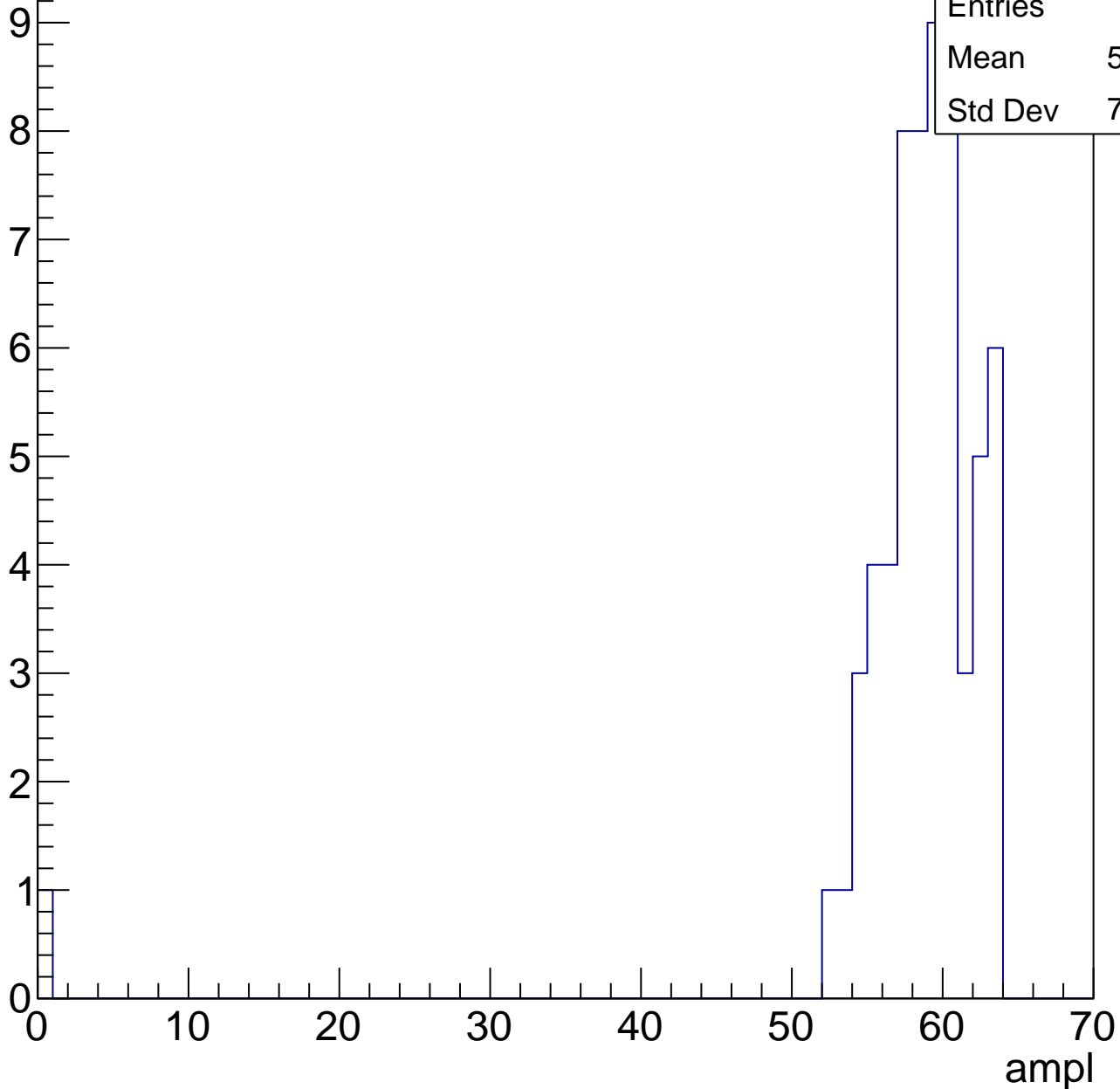


B1L003S, U21-ch100, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	57.63
Std Dev	7.856

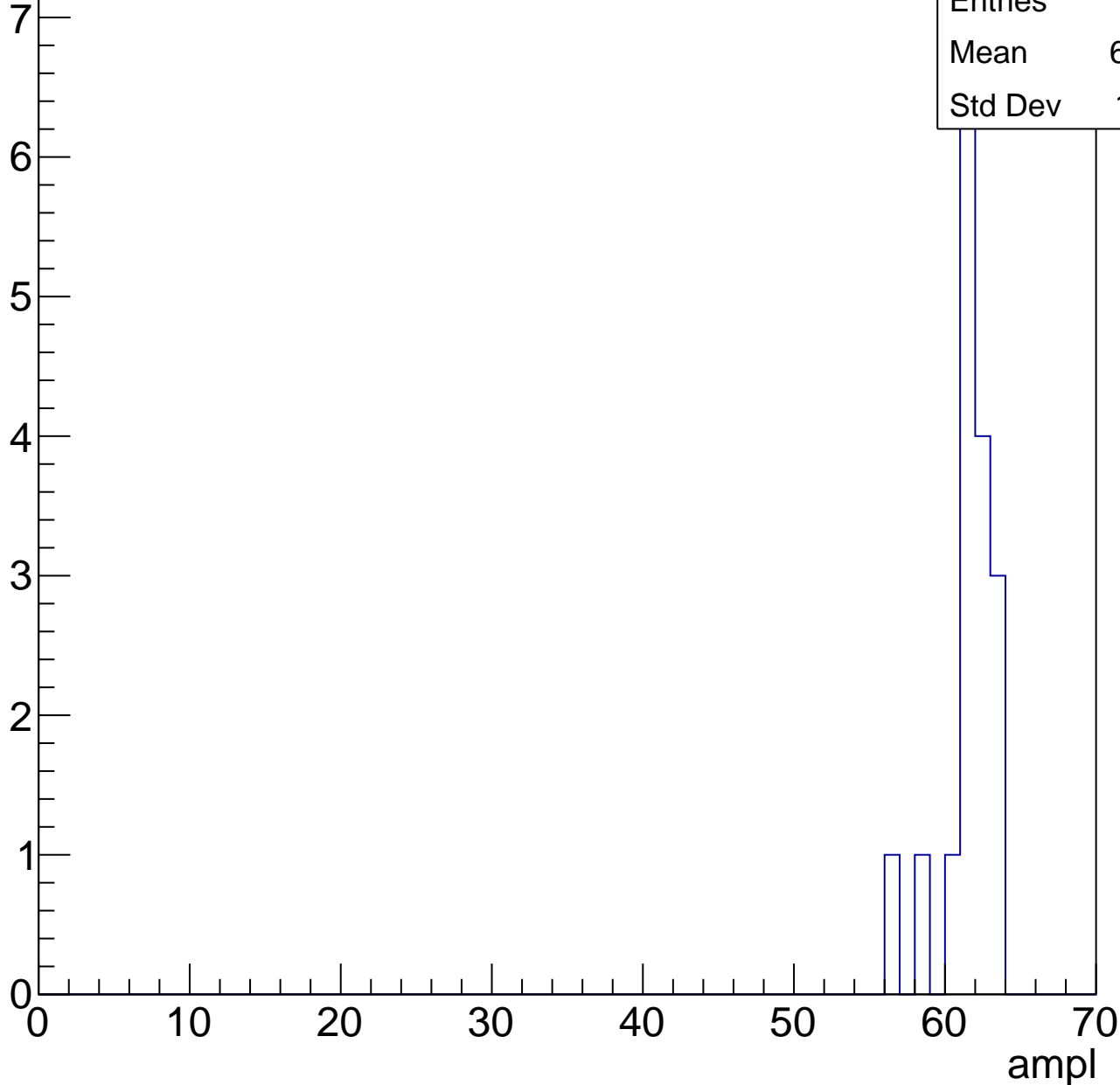


B1L003S, U21-ch100, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

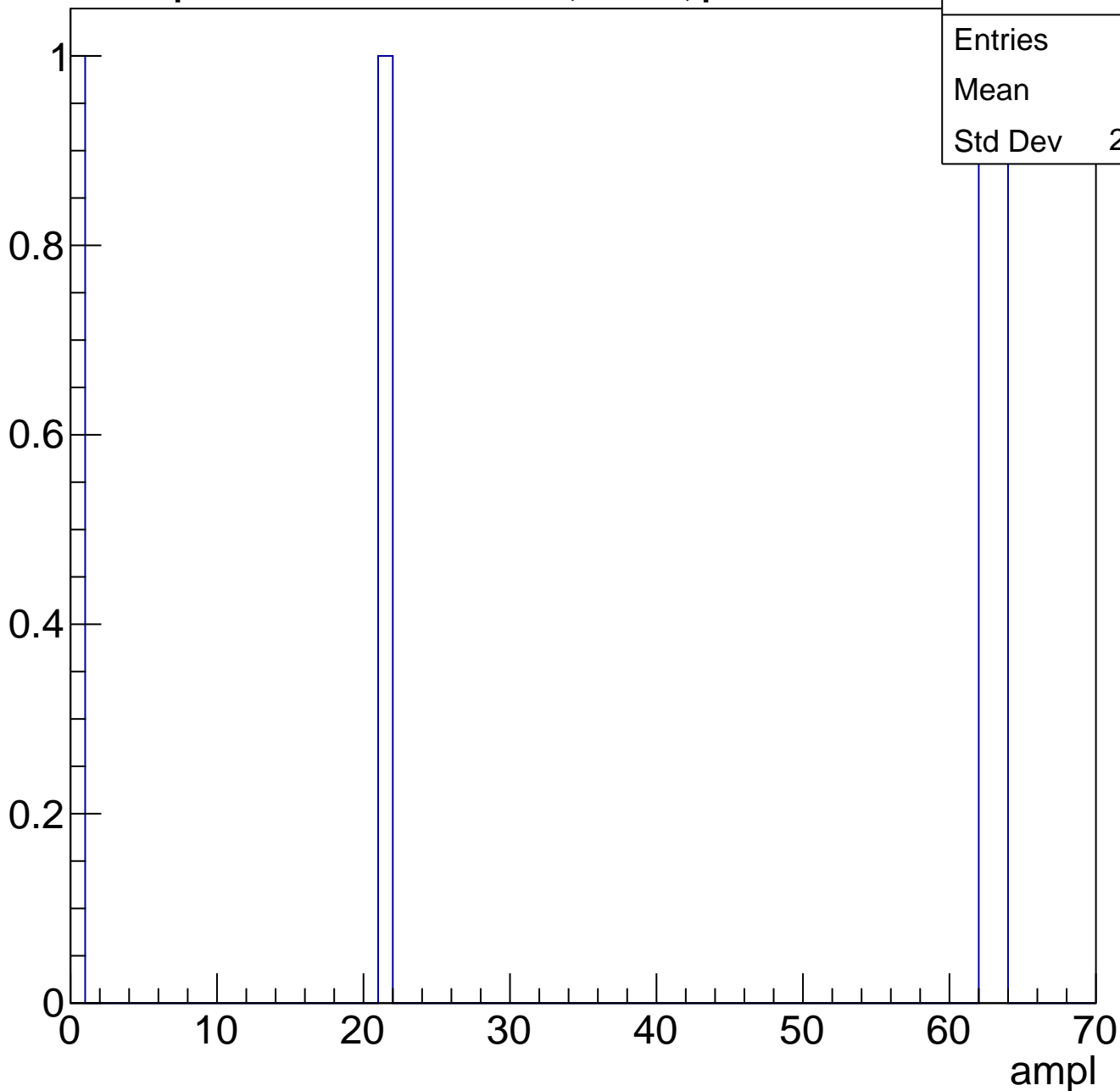
Entries	17
Mean	61.06
Std Dev	1.731



B1L003S, U21-ch100, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch101, adc0

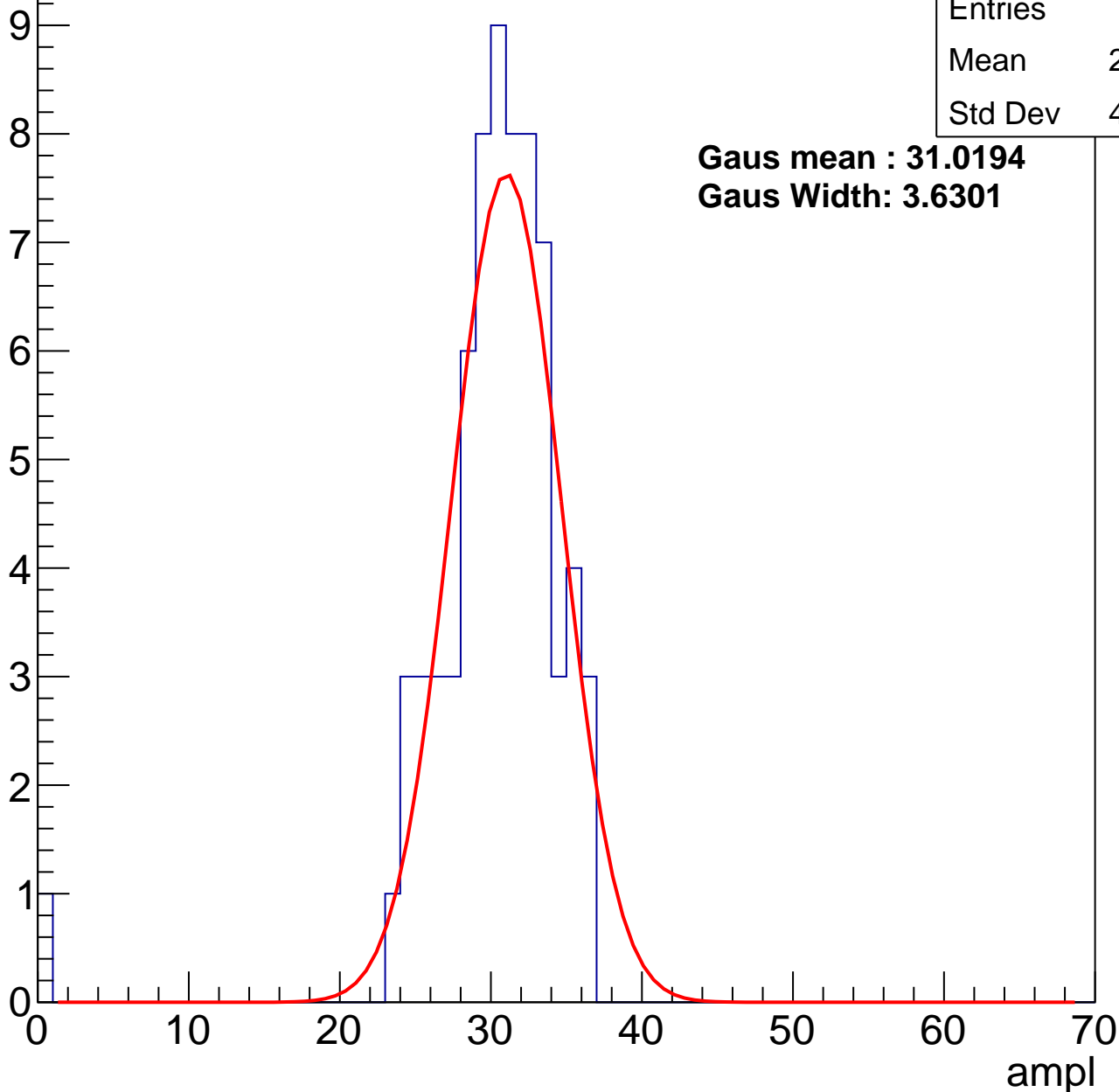
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.77
Std Dev	4.772

Gaus mean : 31.0194

Gaus Width: 3.6301



B1L003S, U21-ch101, adc1

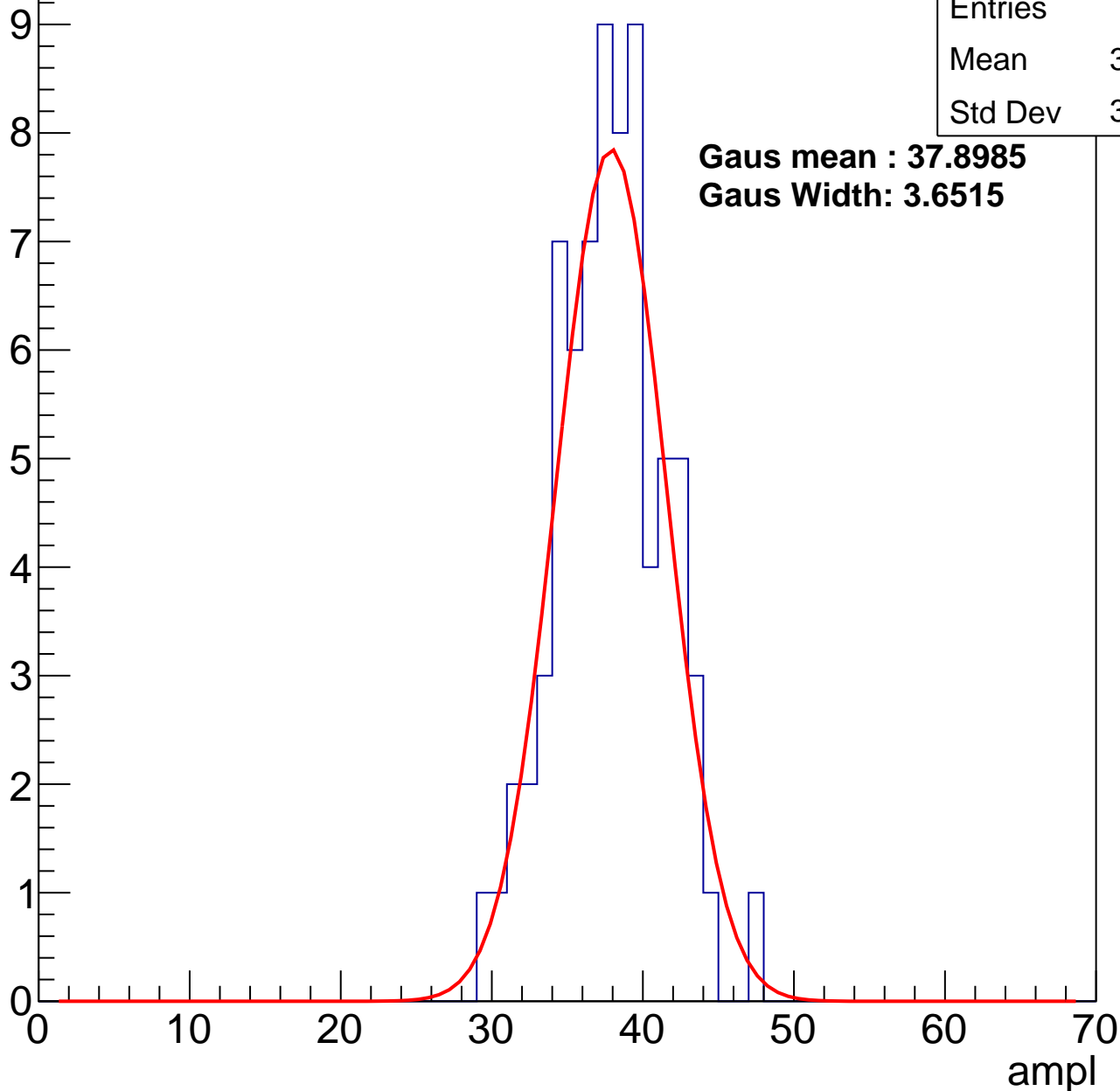
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	37.39
Std Dev	3.502

Gaus mean : 37.8985

Gaus Width: 3.6515

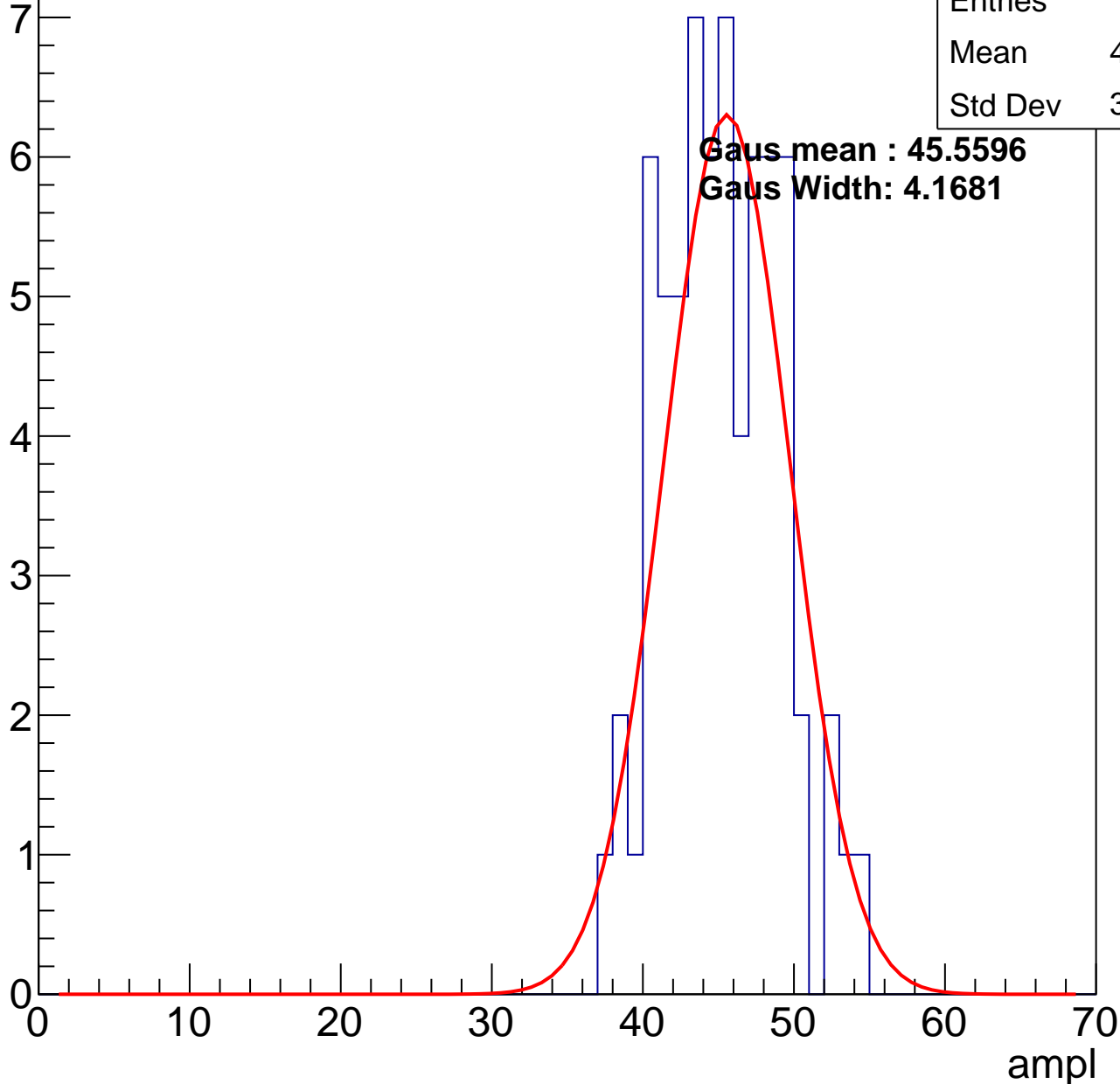


B1L003S, U21-ch101, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	44.79
Std Dev	3.787

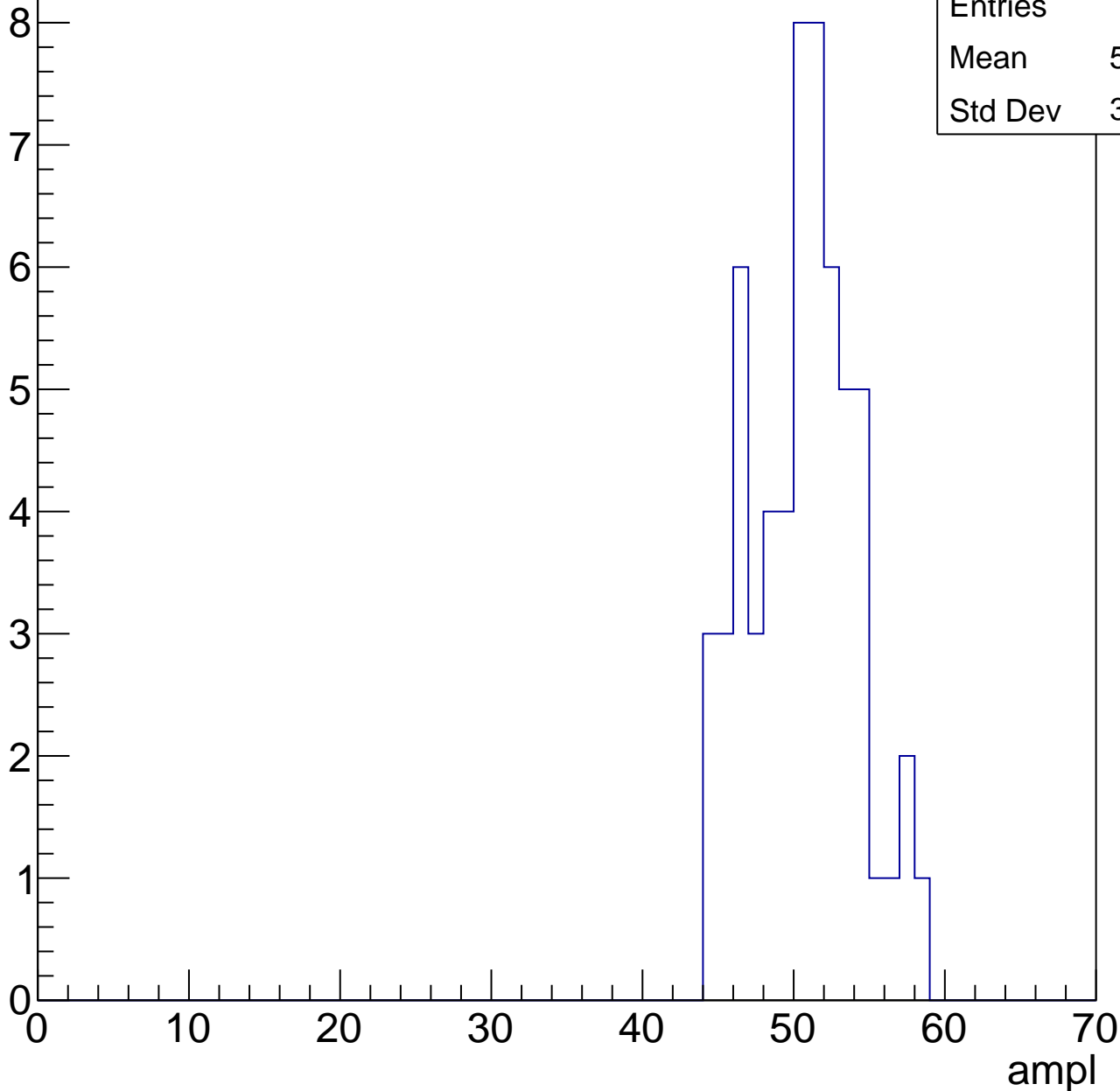


B1L003S, U21-ch101, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	50.17
Std Dev	3.426

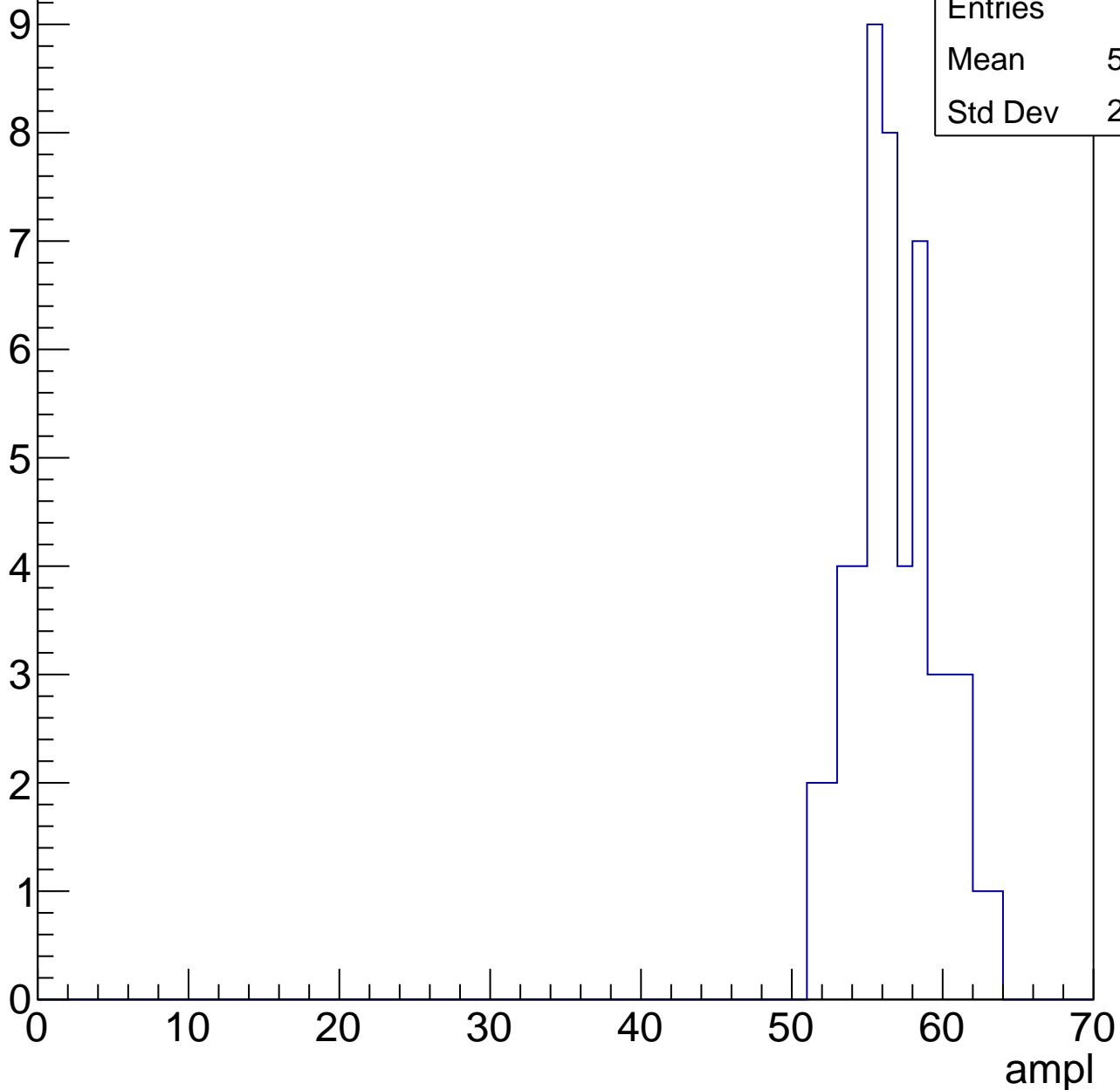


B1L003S, U21-ch101, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	56.39
Std Dev	2.808

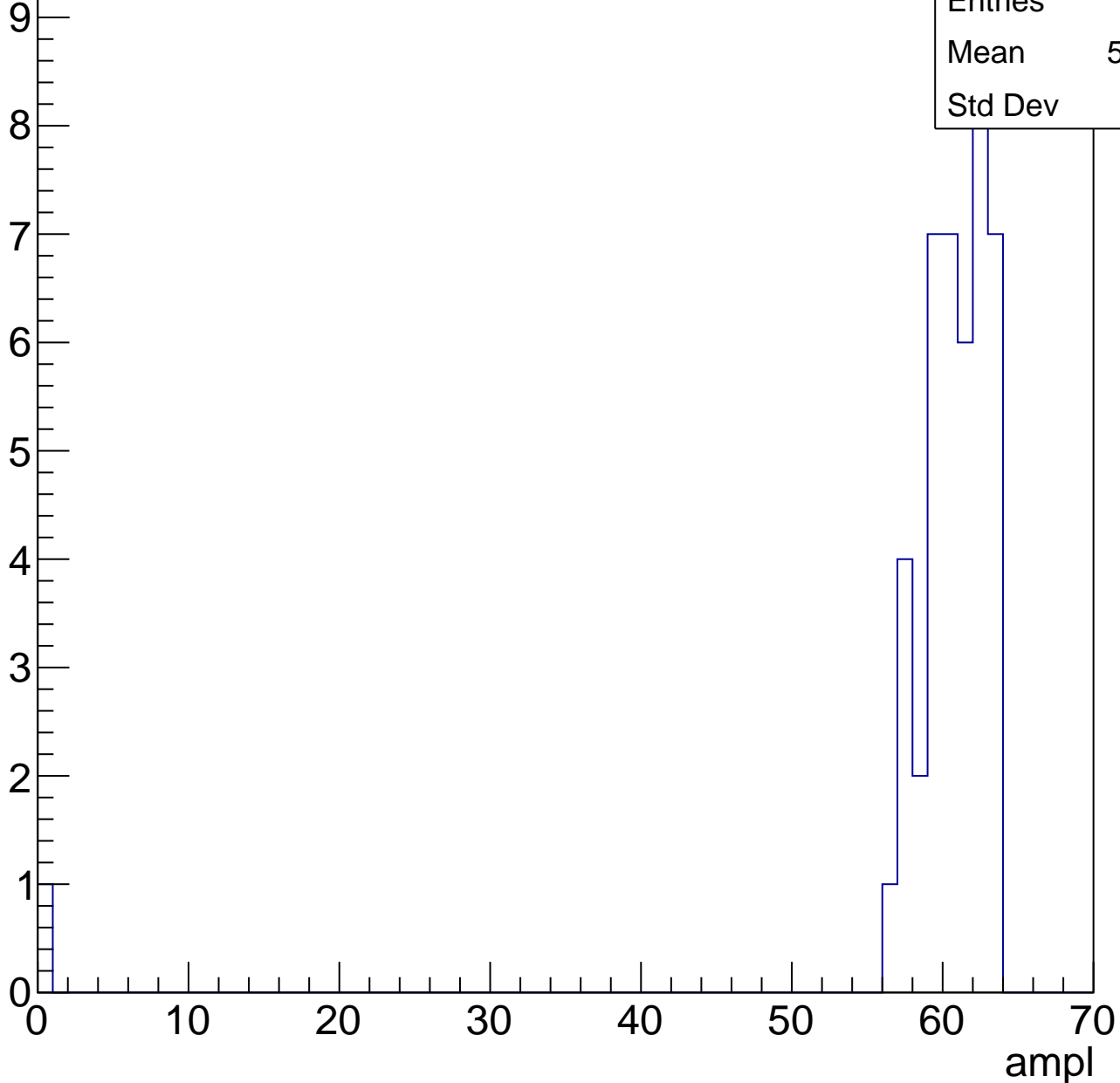


B1L003S, U21-ch101, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	44
Mean	59.05
Std Dev	9.21



B1L003S, U21-ch101, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch101, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch102, adc0

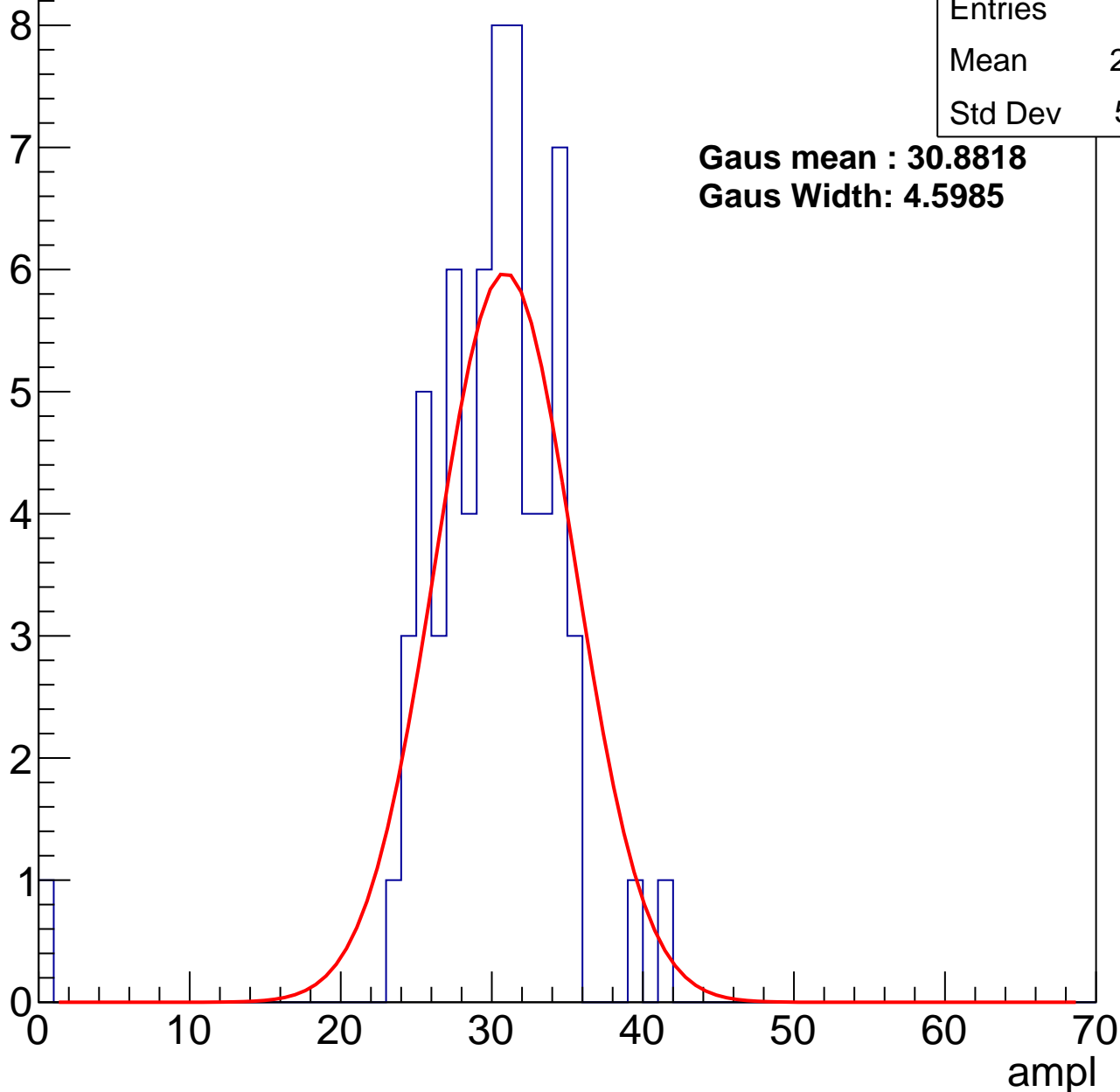
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	29.49
Std Dev	5.171

Gaus mean : 30.8818

Gaus Width: 4.5985



B1L003S, U21-ch102, adc1

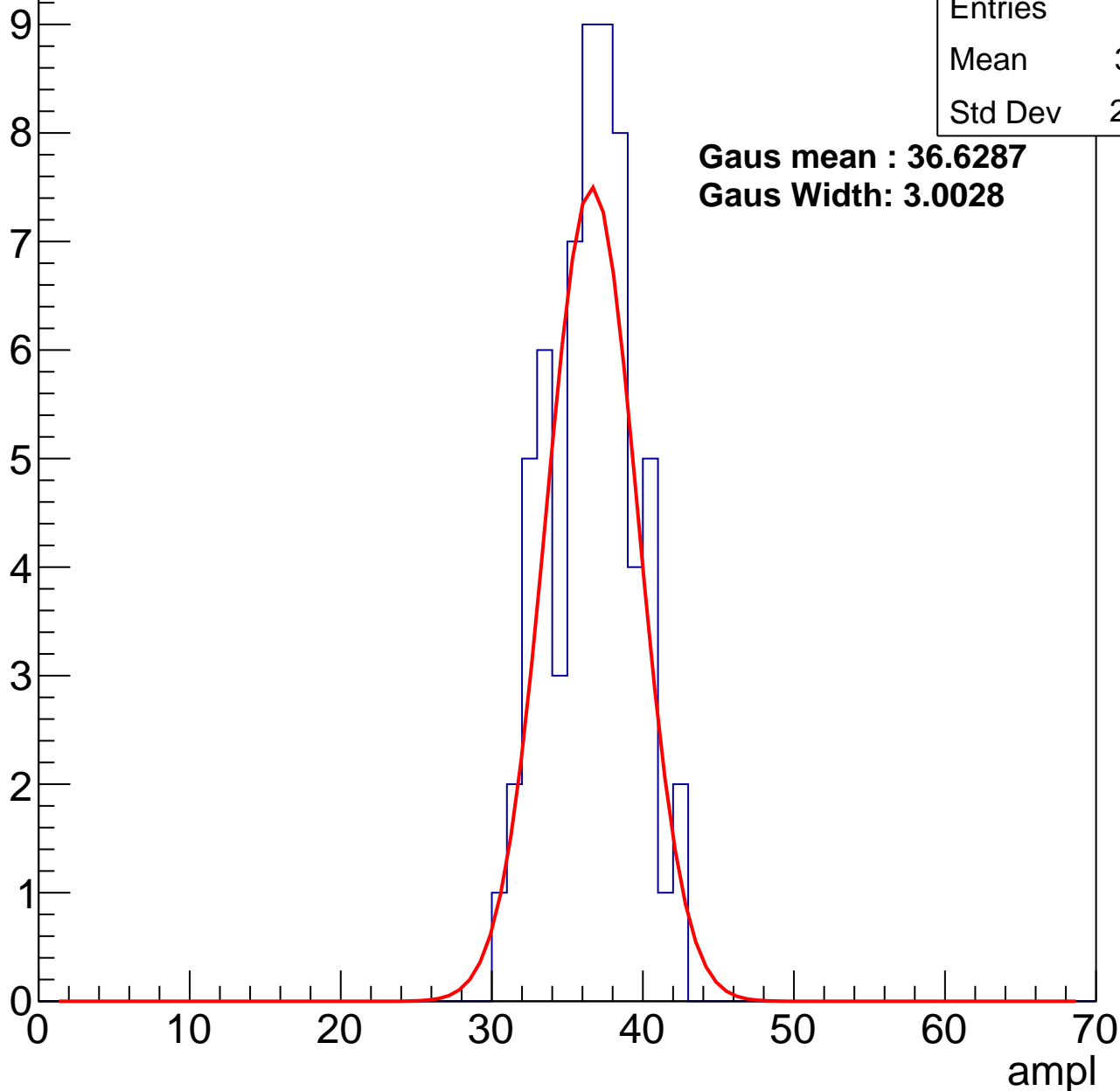
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	36.11
Std Dev	2.818

Gaus mean : 36.6287

Gaus Width: 3.0028



B1L003S, U21-ch102, adc2

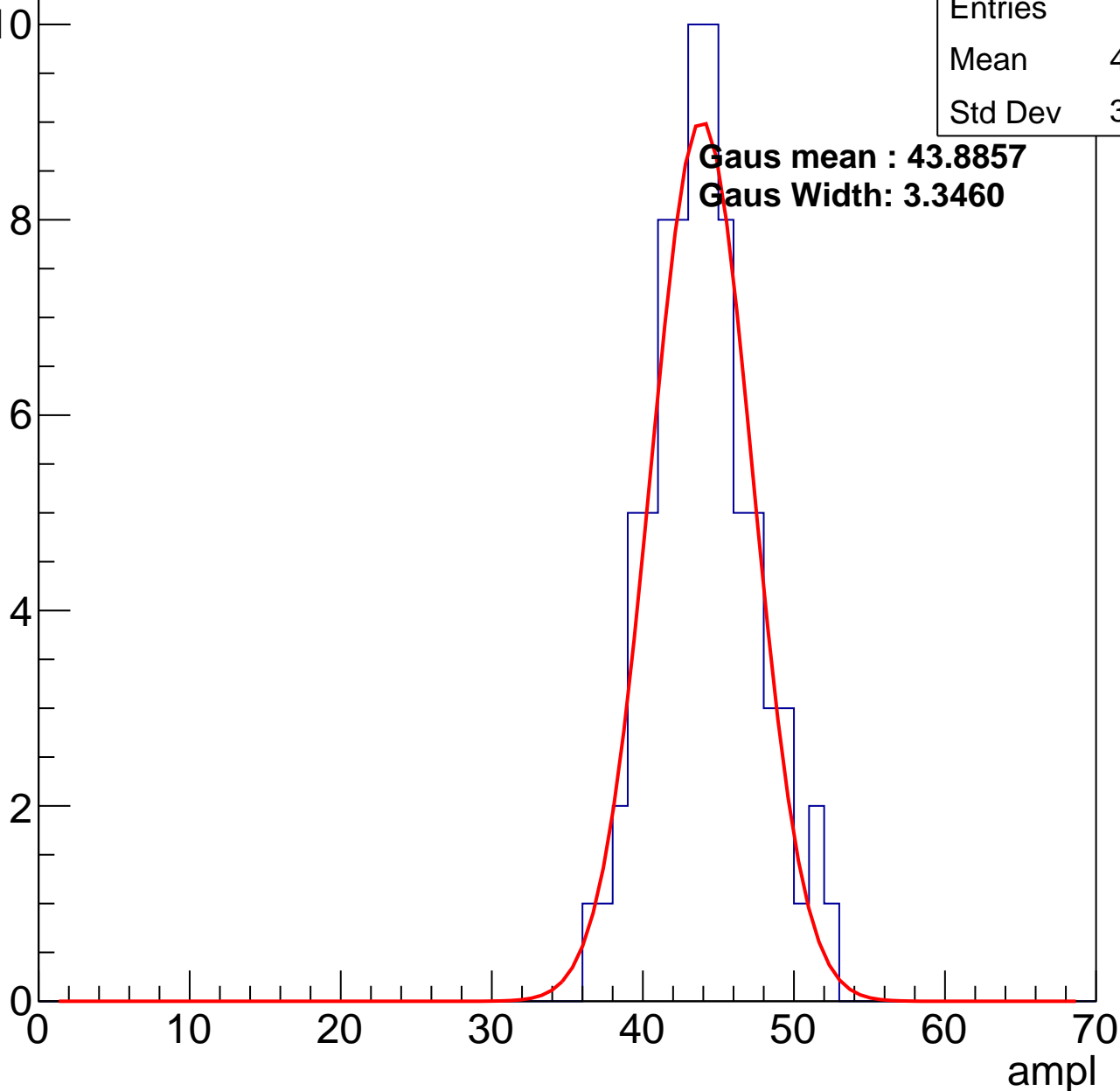
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	78
Mean	43.56
Std Dev	3.365

Gaus mean : 43.8857

Gaus Width: 3.3460

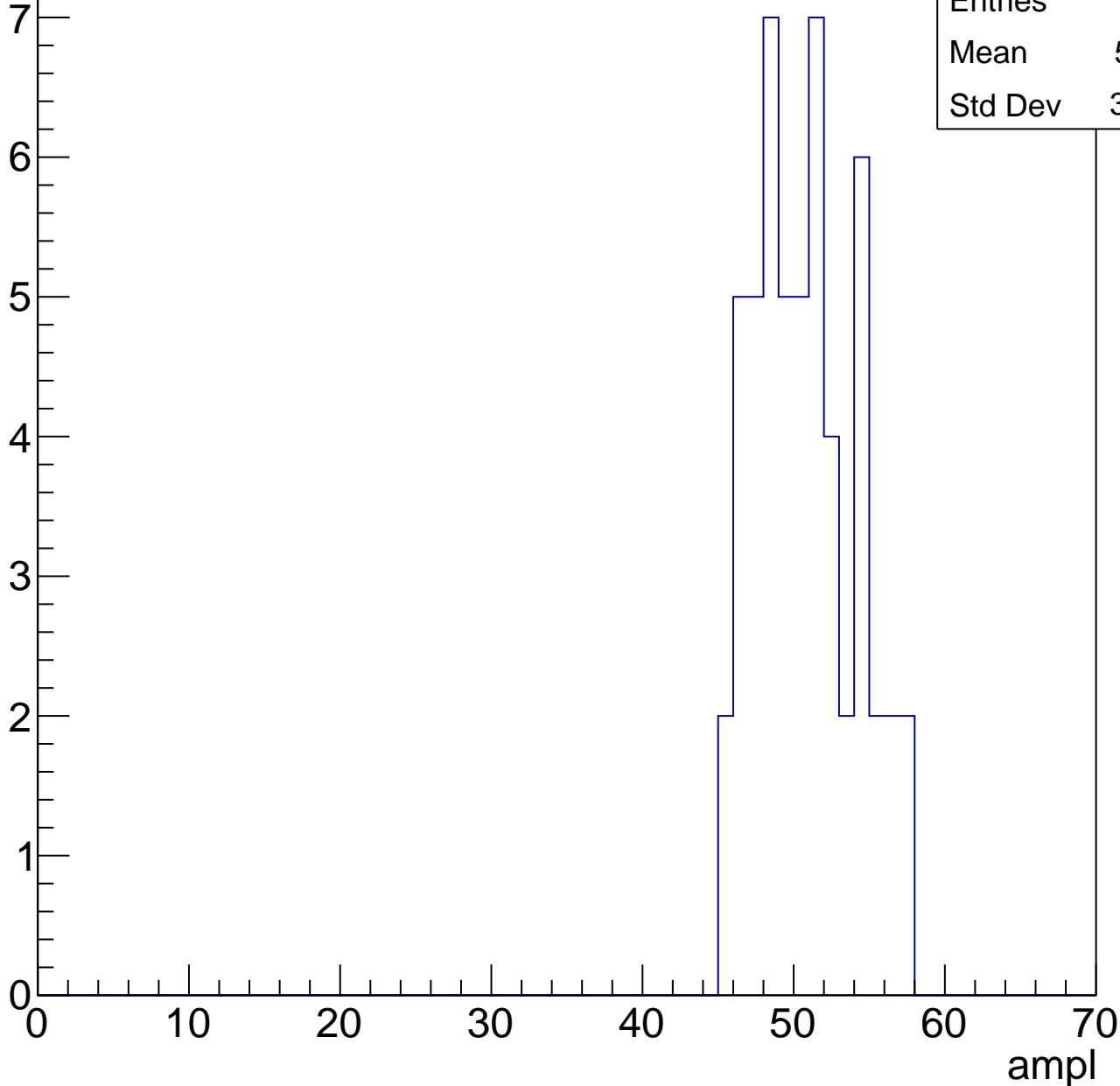


B1L003S, U21-ch102, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	50.31
Std Dev	3.219

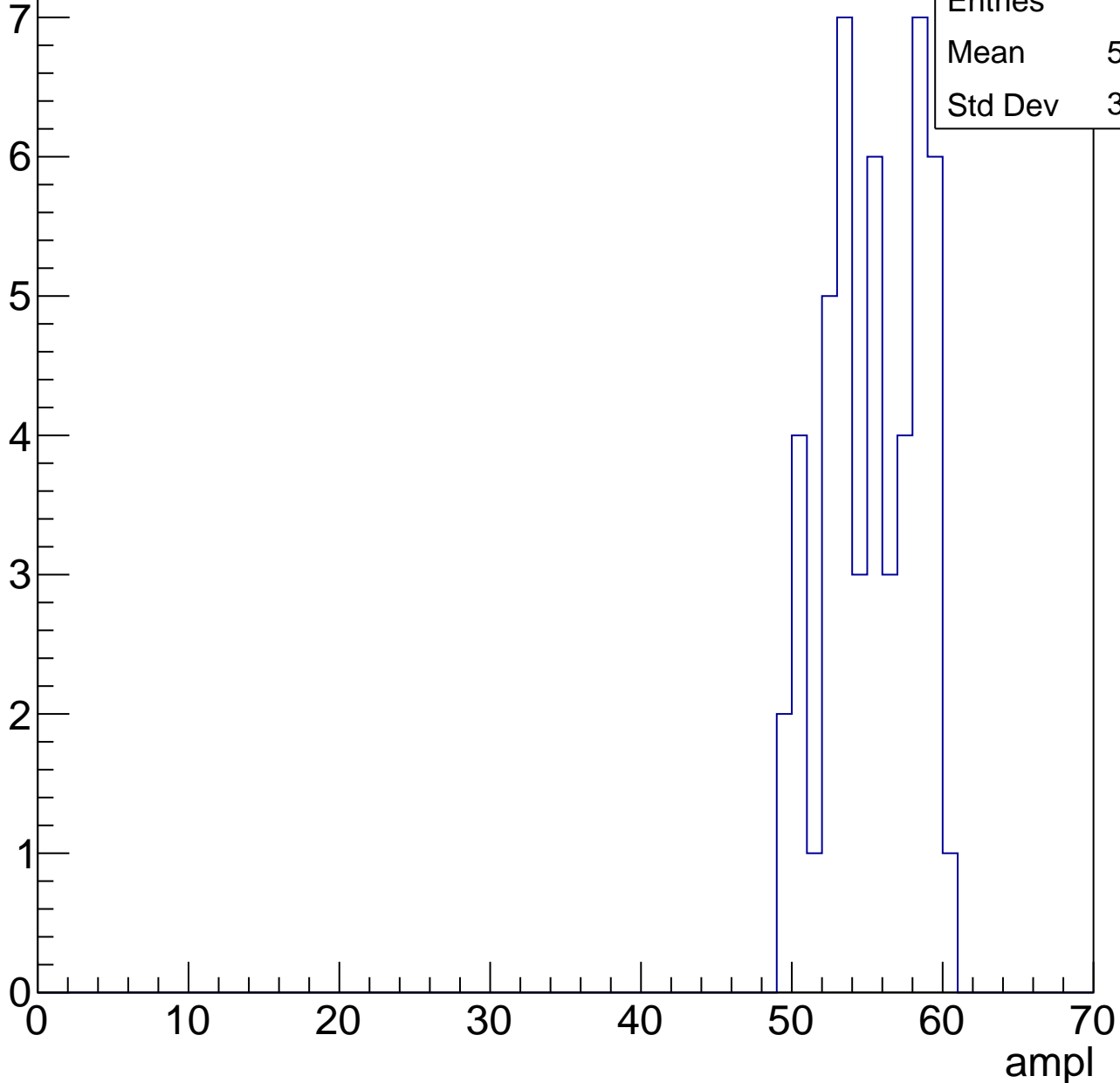


B1L003S, U21-ch102, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	54.86
Std Dev	3.084

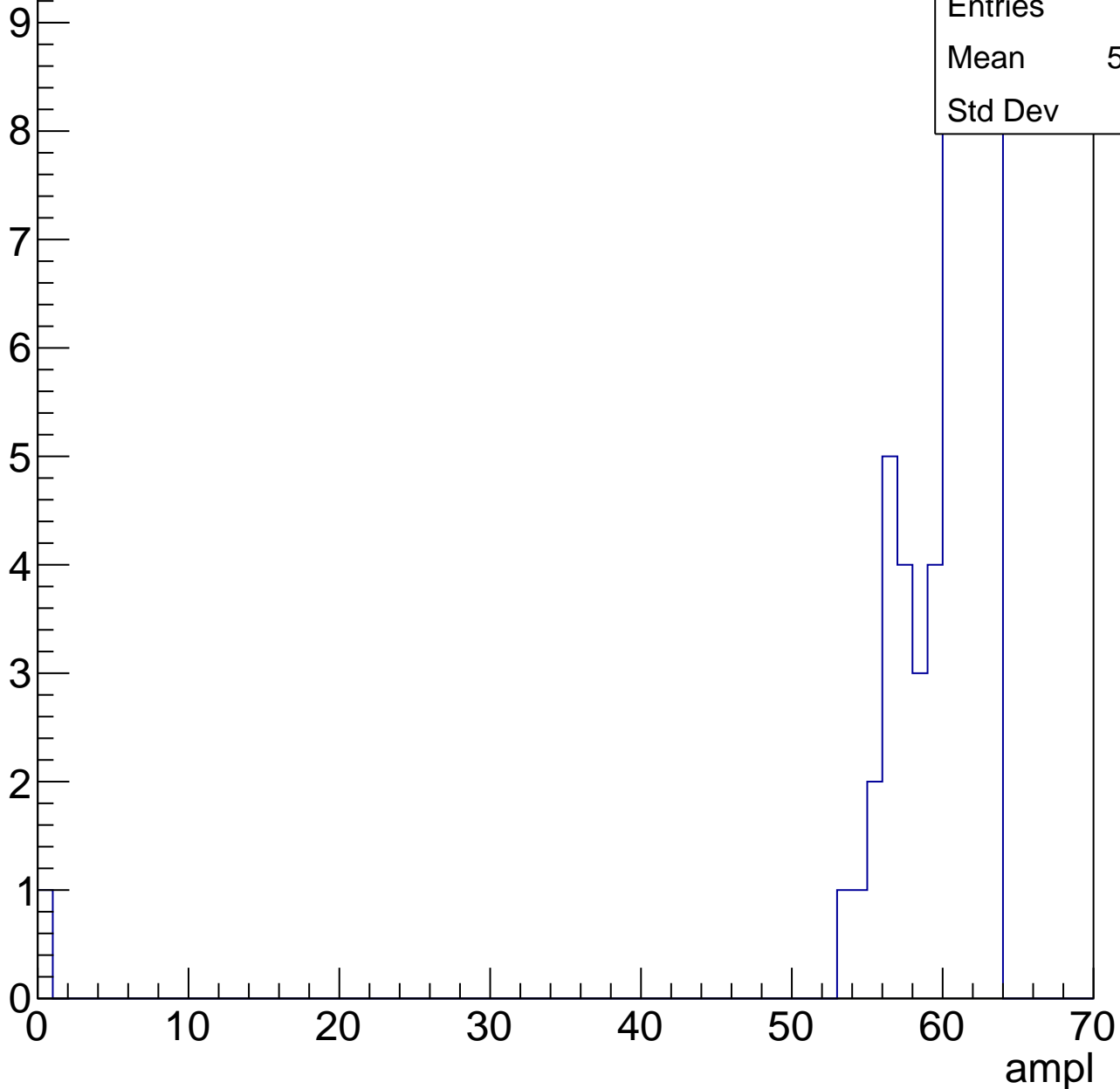


B1L003S, U21-ch102, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

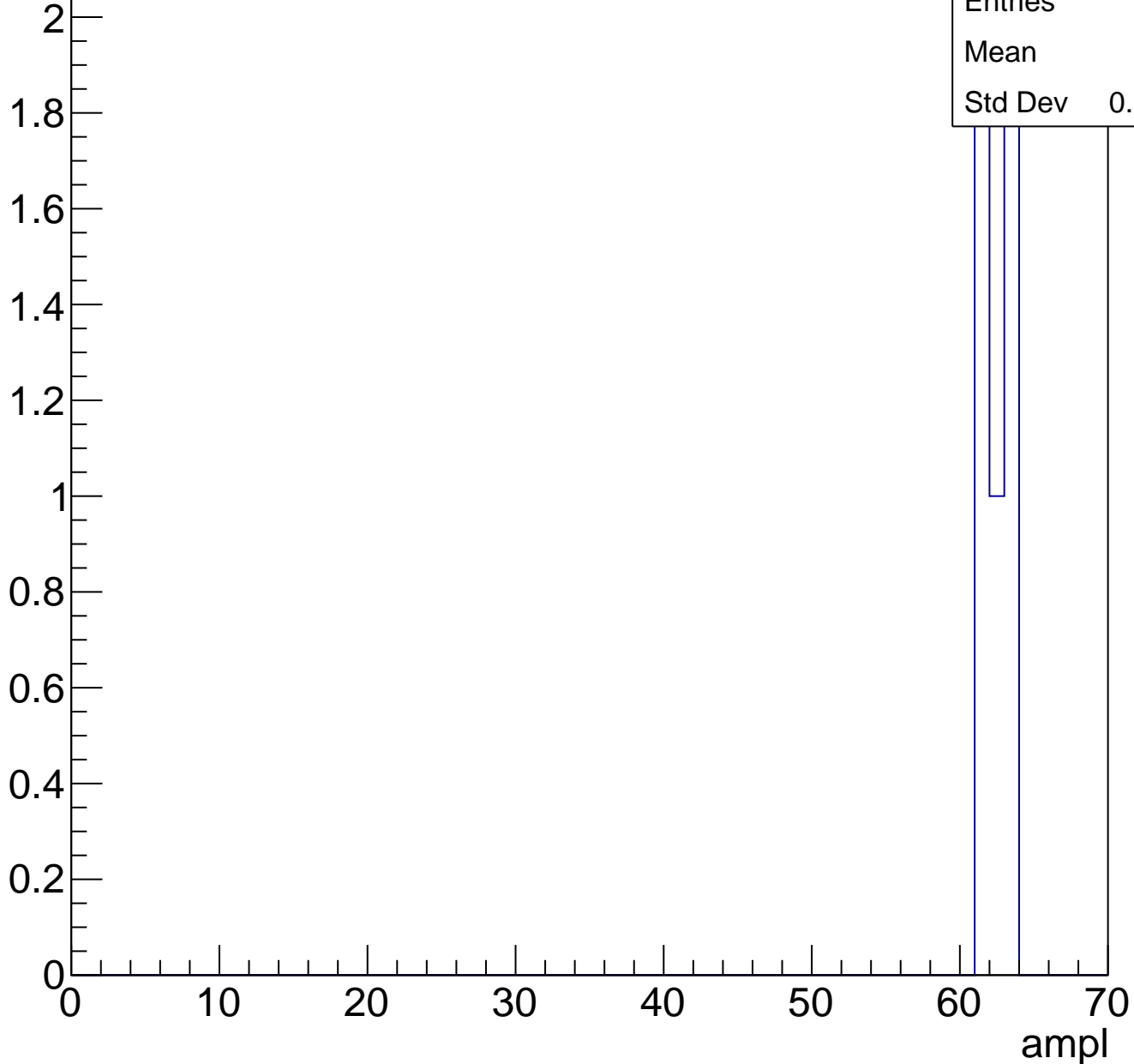
Entries	55
Mean	58.64
Std Dev	8.4



B1L003S, U21-ch102, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch102, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	3
Mean	0
Std Dev	0

B1L003S, U21-ch103, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	68
Mean	29.15
Std Dev	4.43

Gaus mean : 29.9060

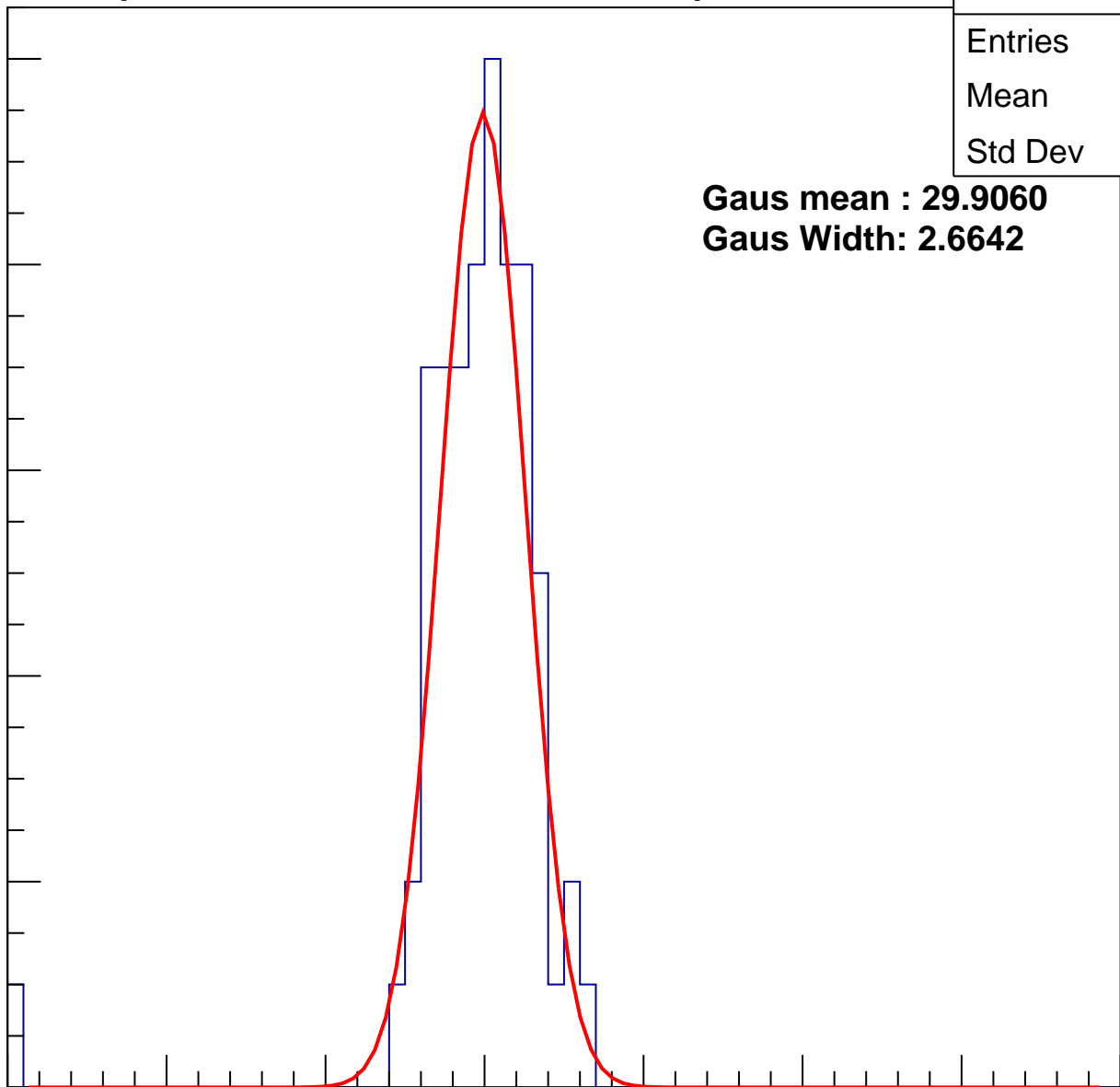
Gaus Width: 2.6642

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch103, adc1

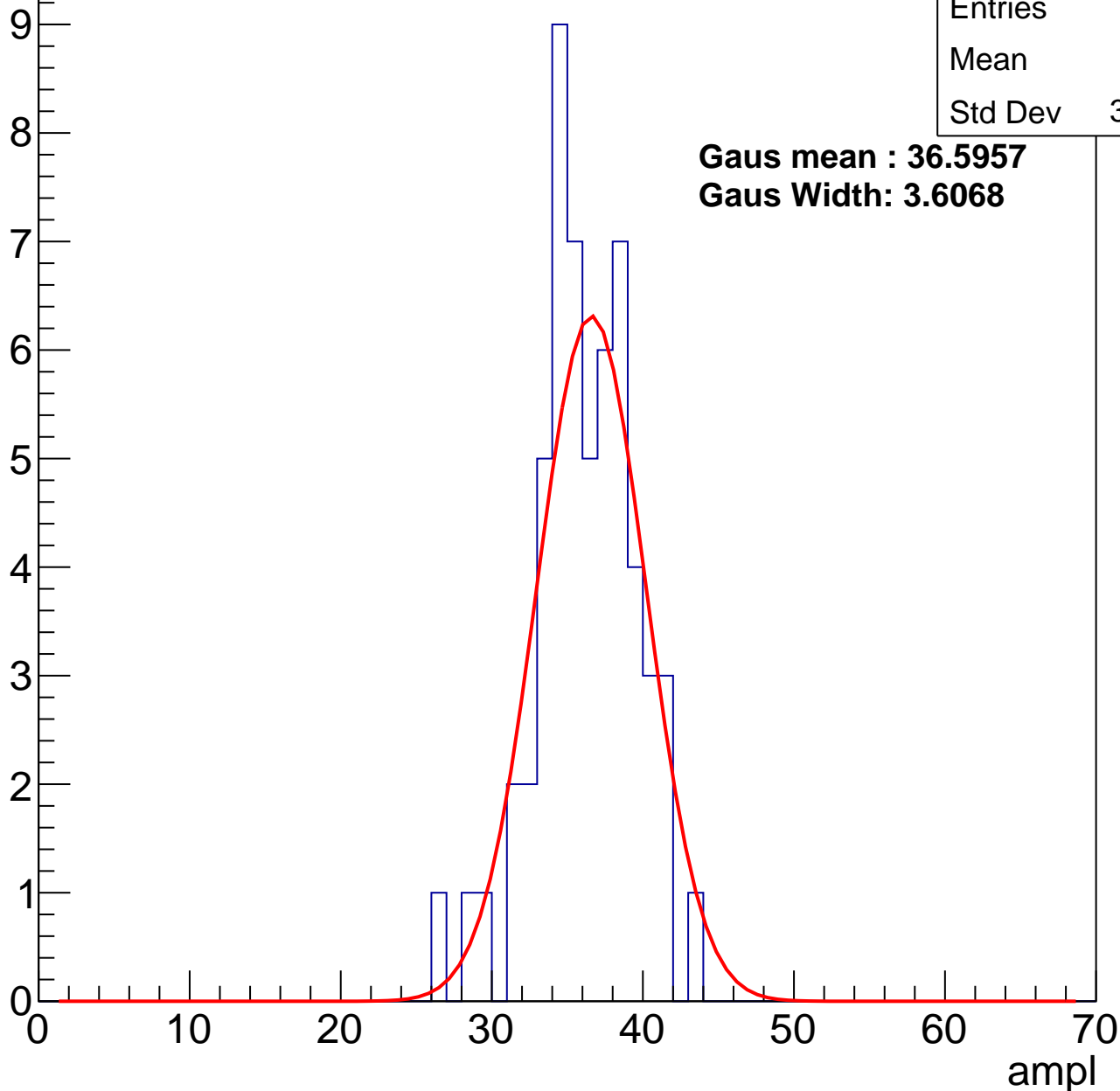
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	35.7
Std Dev	3.298

Gaus mean : 36.5957

Gaus Width: 3.6068



B1L003S, U21-ch103, adc2

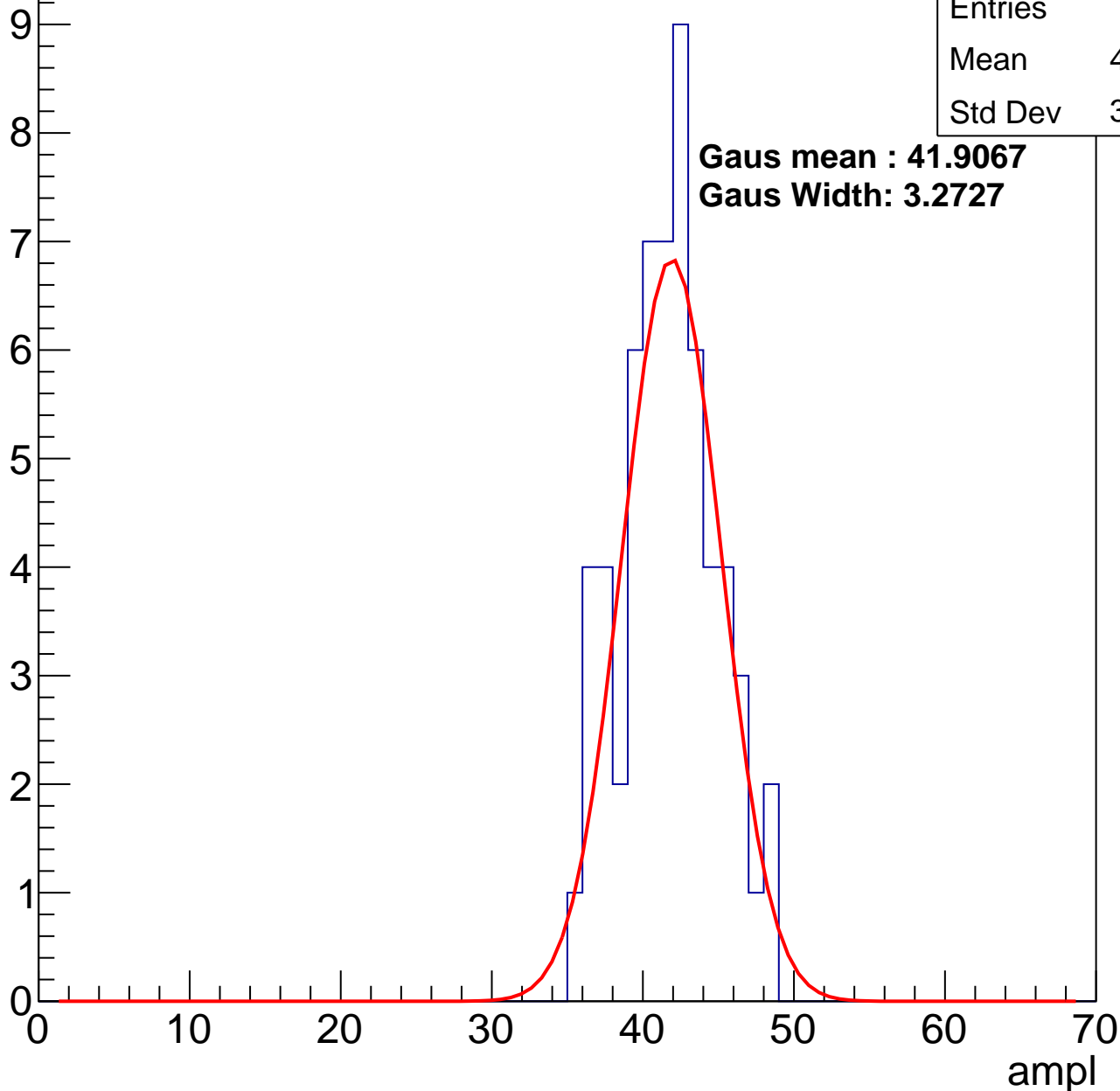
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	41.28
Std Dev	3.126

Gaus mean : 41.9067

Gaus Width: 3.2727

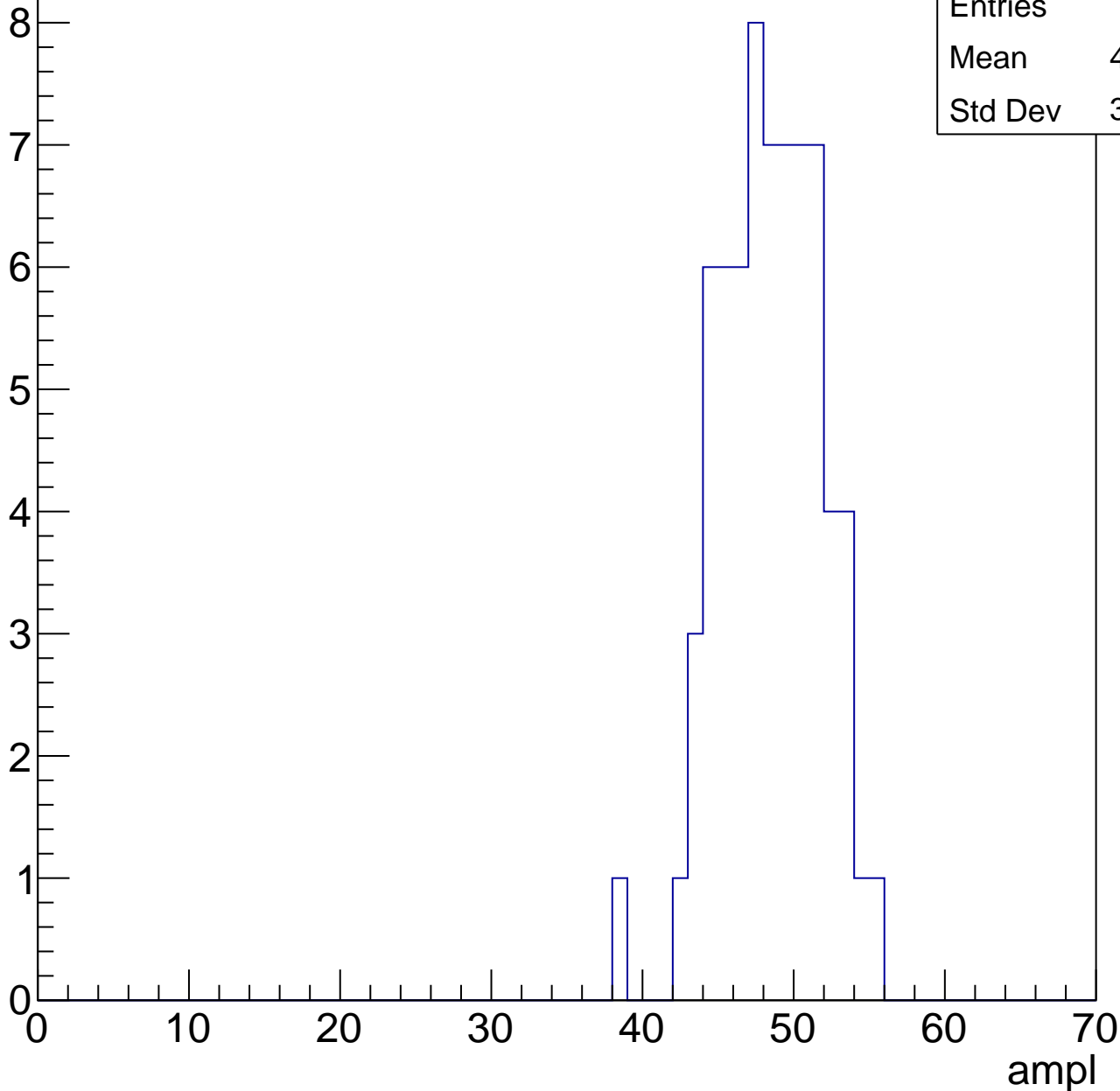


B1L003S, U21-ch103, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	47.97
Std Dev	3.275

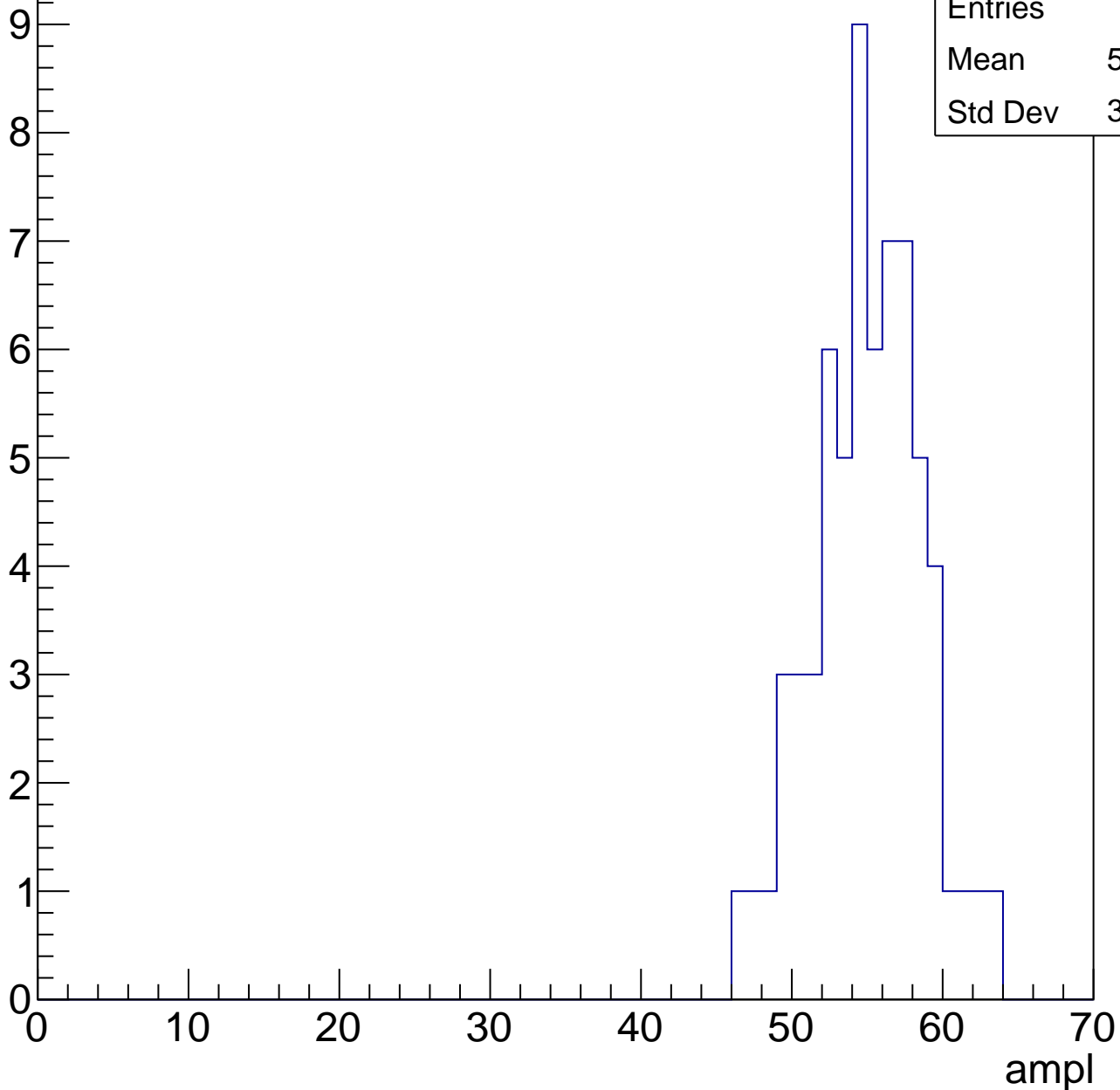


B1L003S, U21-ch103, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.57
Std Dev	3.539

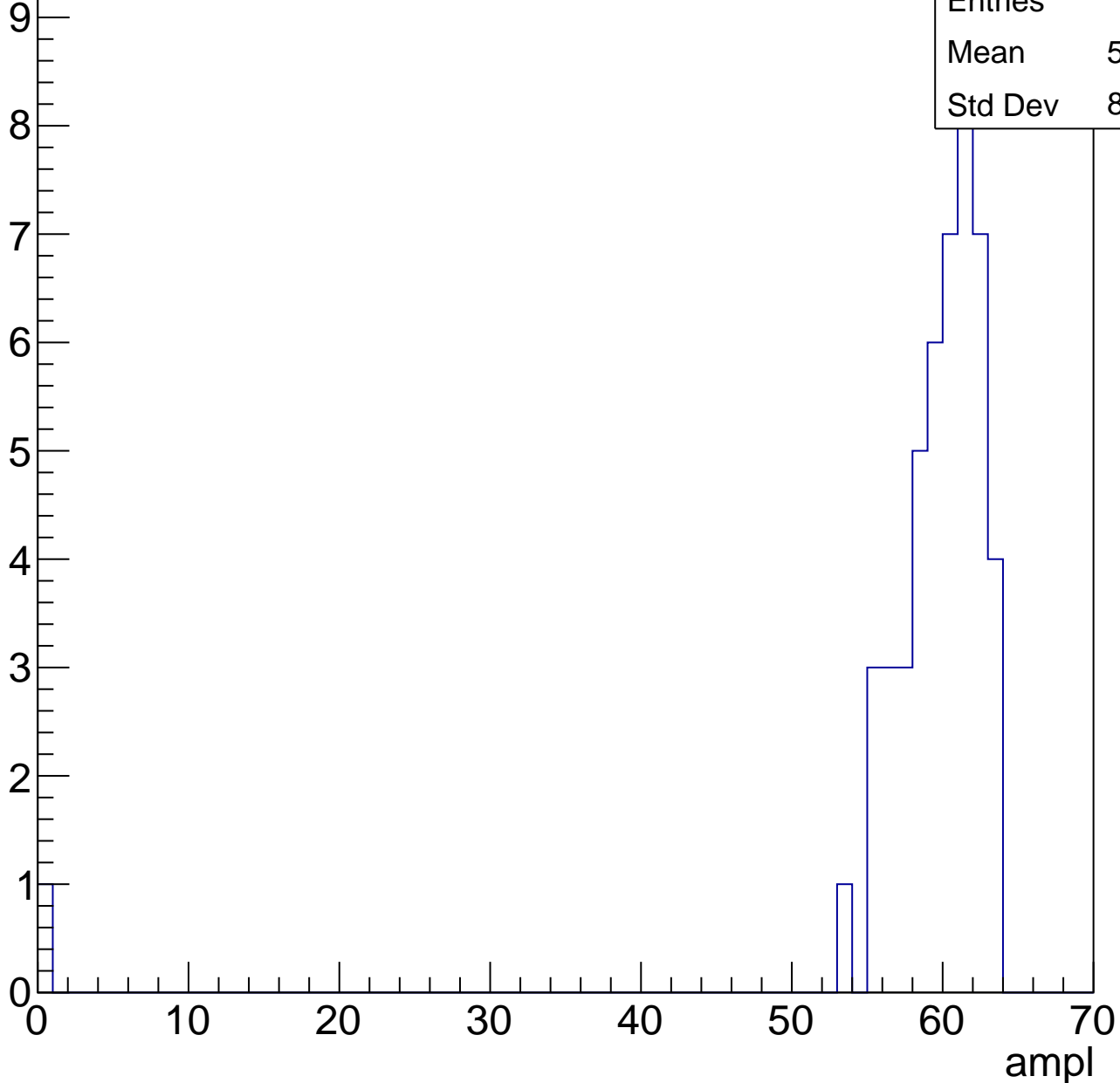


B1L003S, U21-ch103, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	58.29
Std Dev	8.753

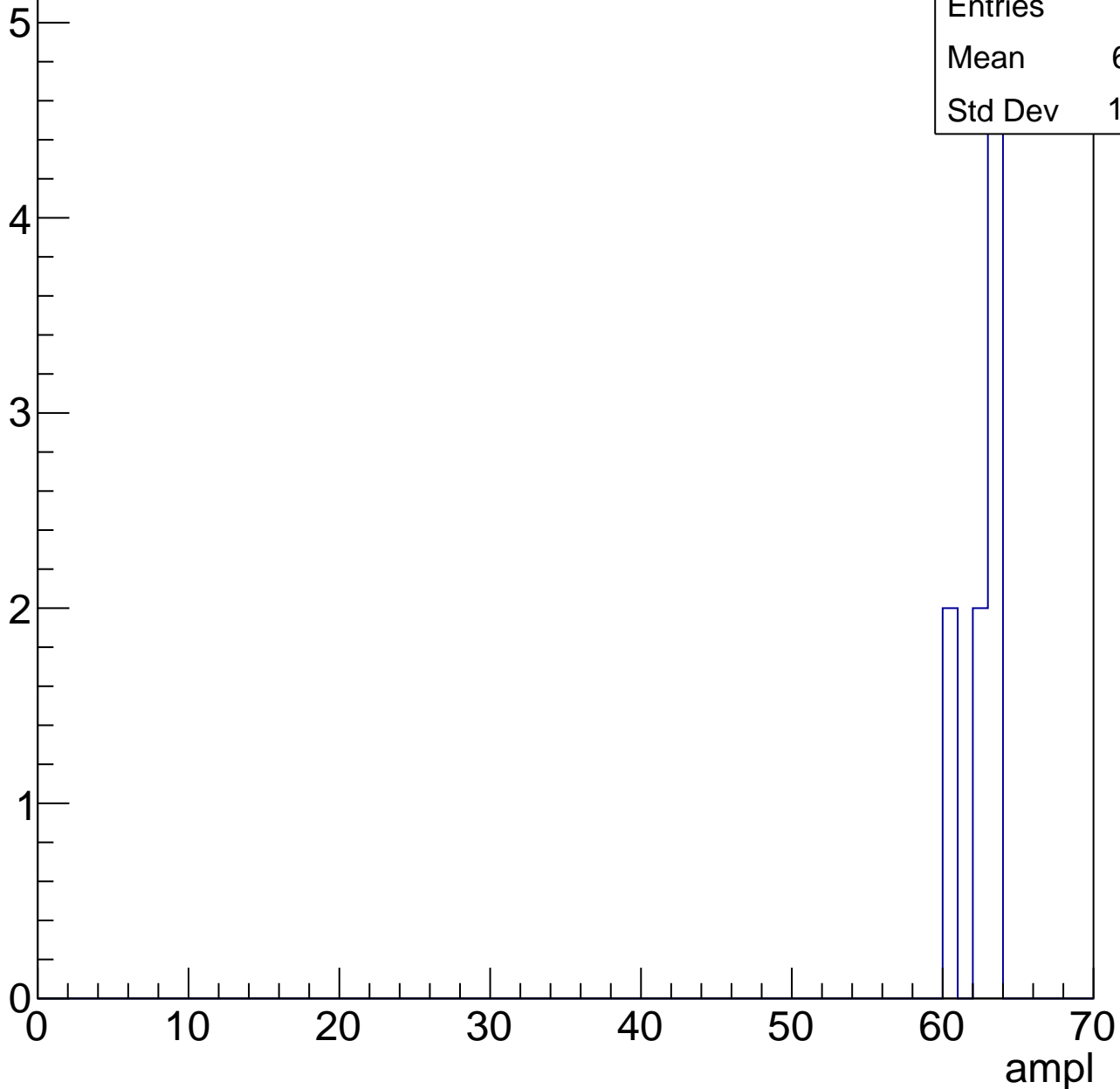


B1L003S, U21-ch103, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

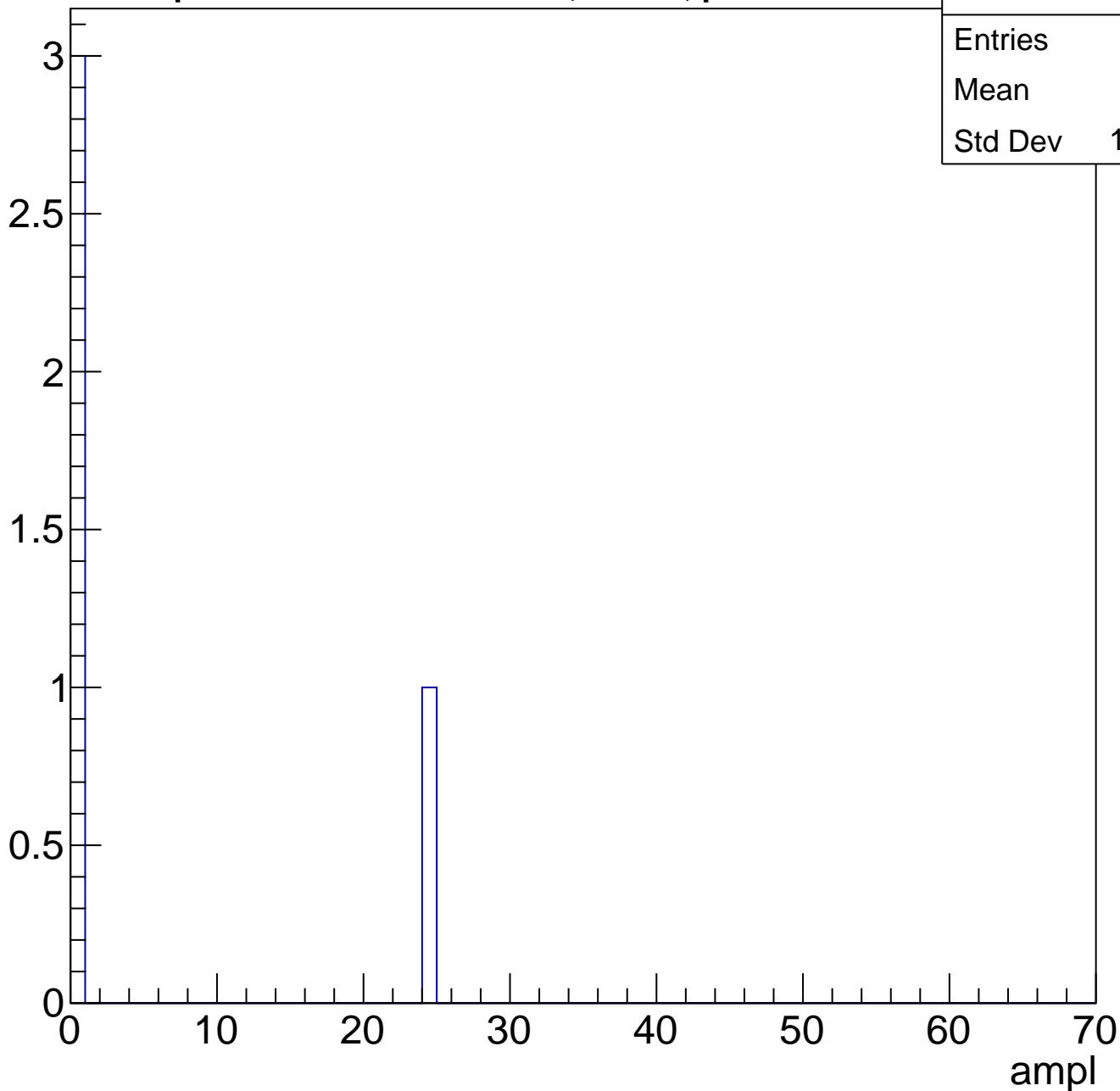
Entries	9
Mean	62.11
Std Dev	1.197



B1L003S, U21-ch103, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch104, adc0

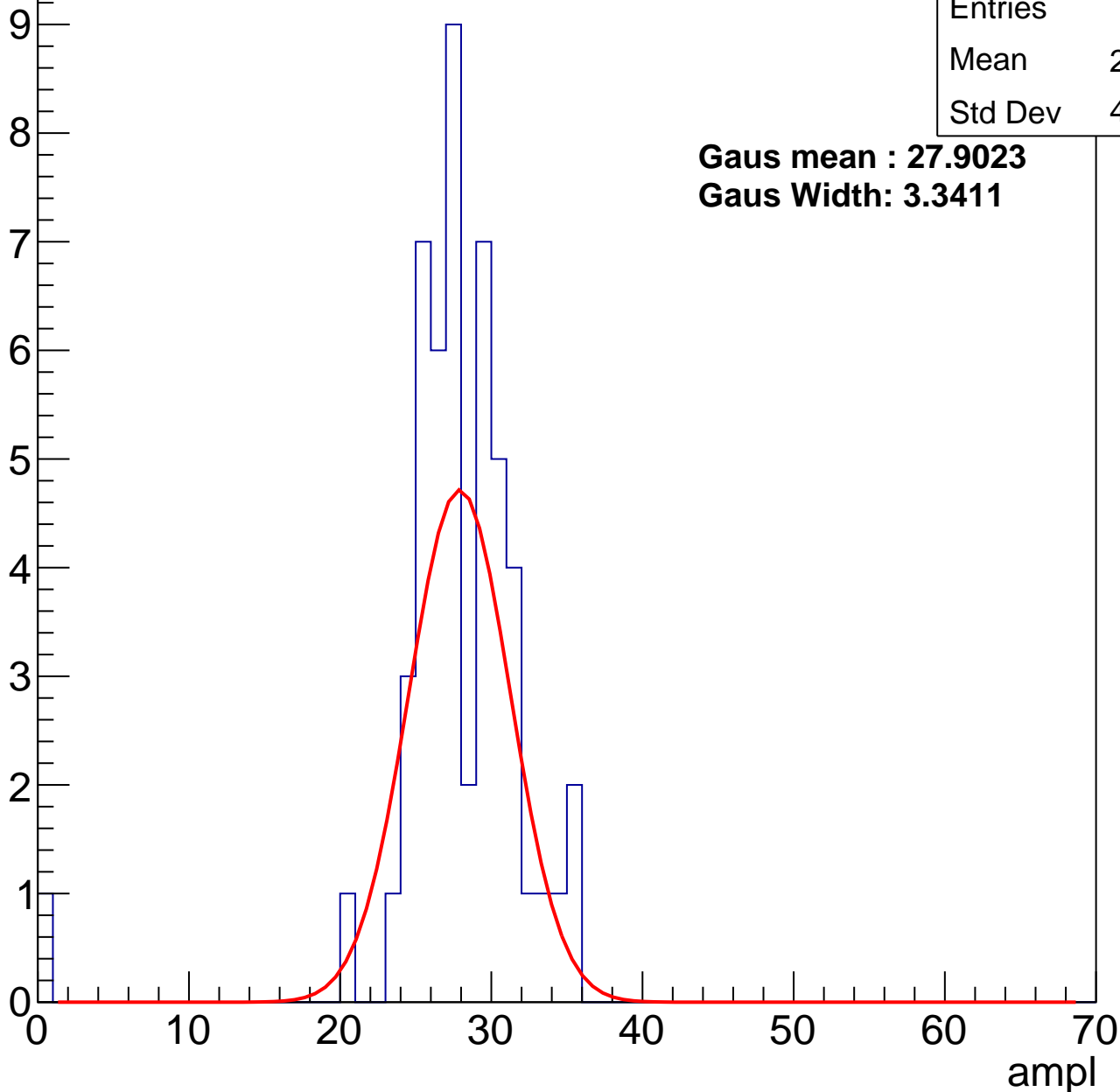
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	27.27
Std Dev	4.907

Gaus mean : 27.9023

Gaus Width: 3.3411



B1L003S, U21-ch104, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	33.89
Std Dev	3.143

Gaus mean : 33.8047

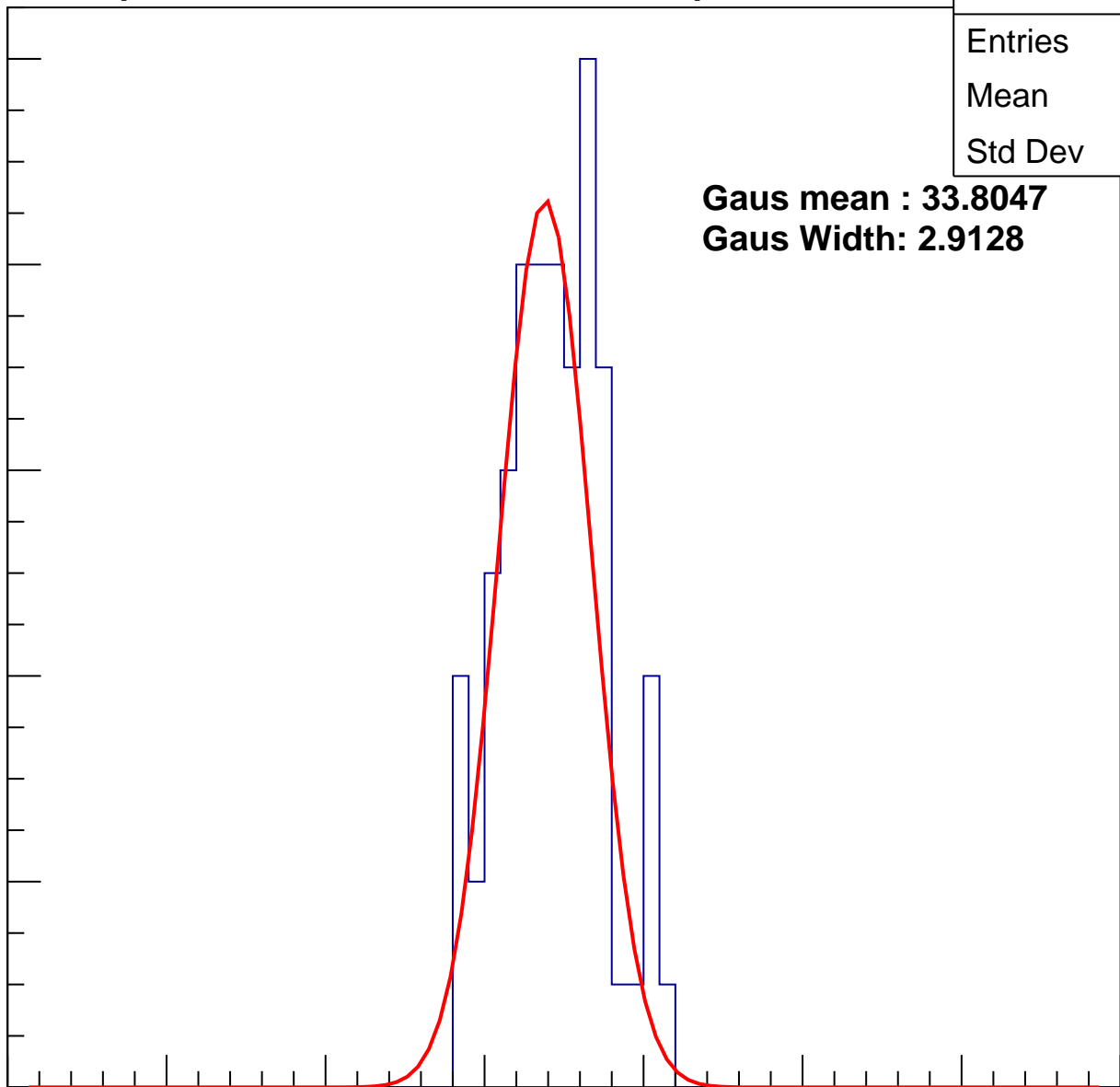
Gaus Width: 2.9128

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch104, adc2

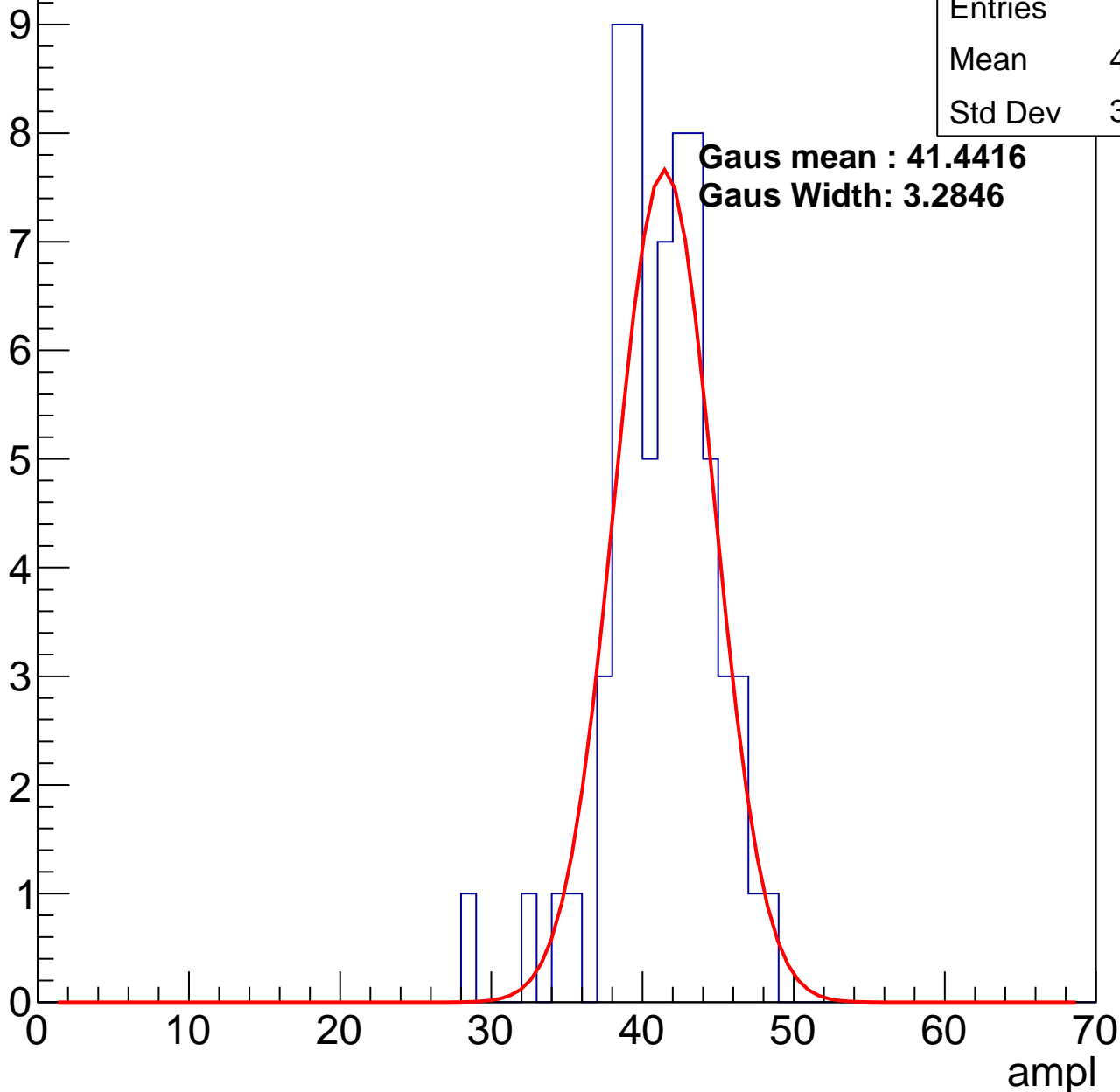
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	40.73
Std Dev	3.466

Gaus mean : 41.4416

Gaus Width: 3.2846

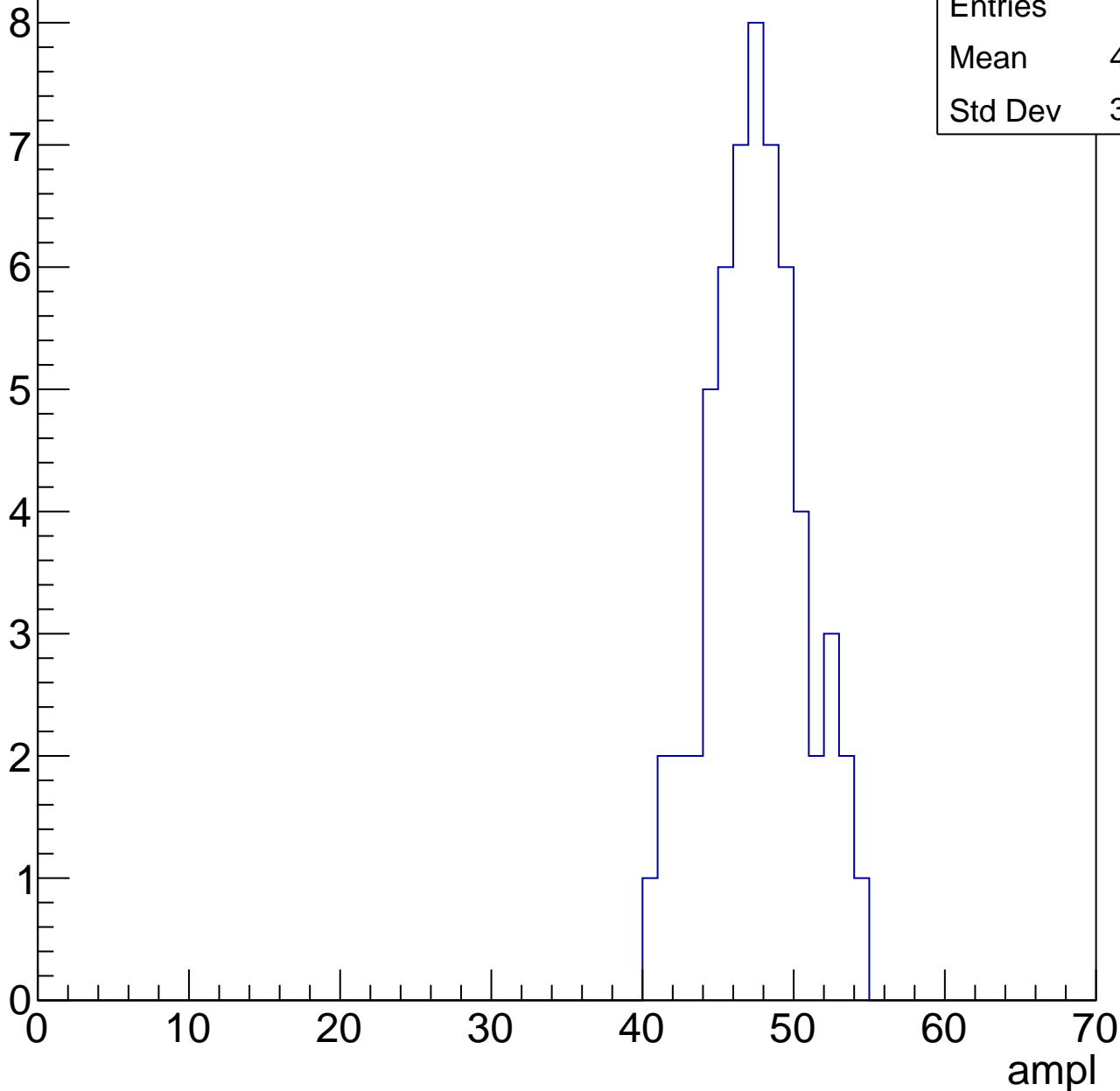


B1L003S, U21-ch104, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	47.03
Std Dev	3.146

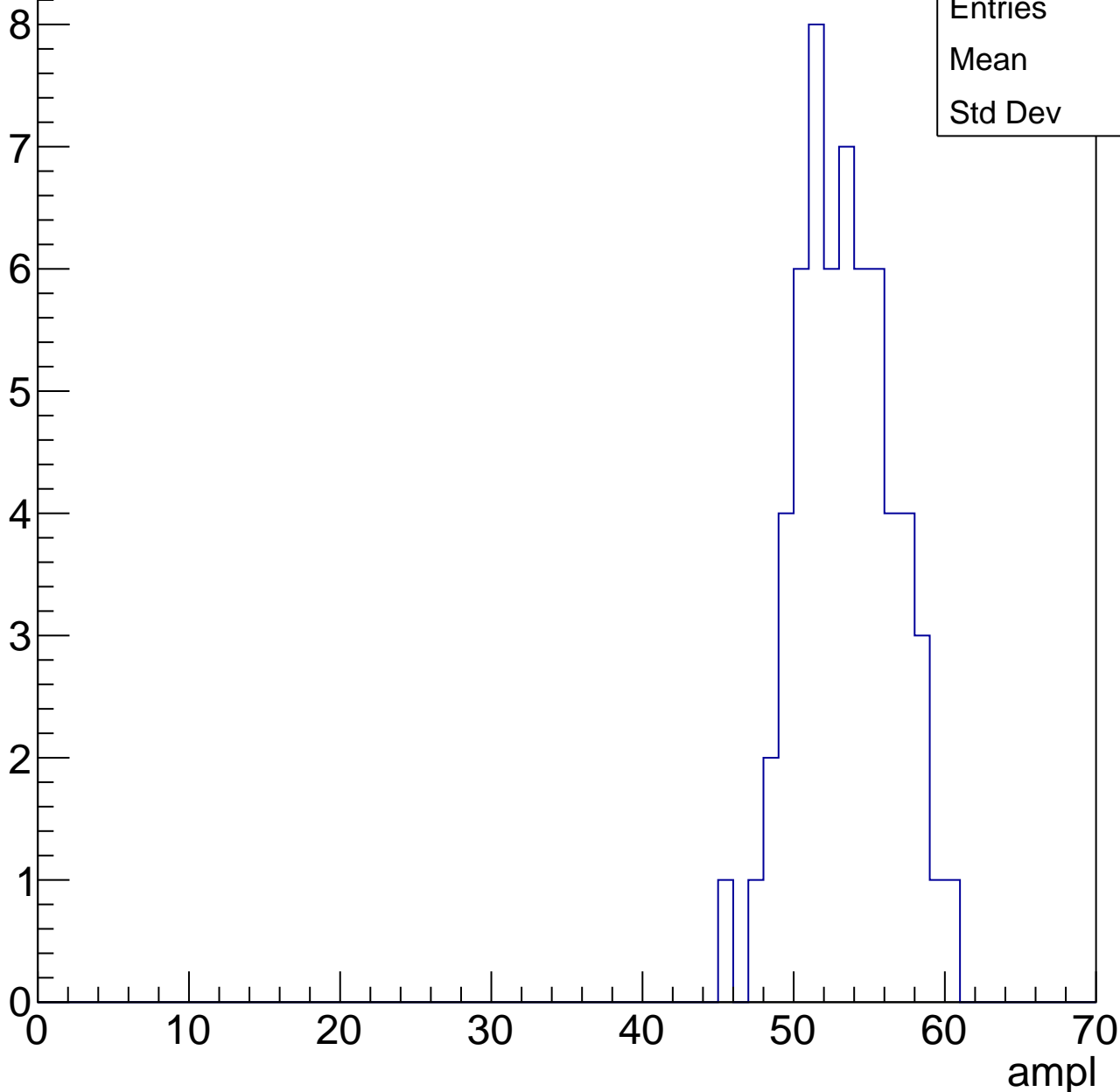


B1L003S, U21-ch104, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	52.9
Std Dev	3.15

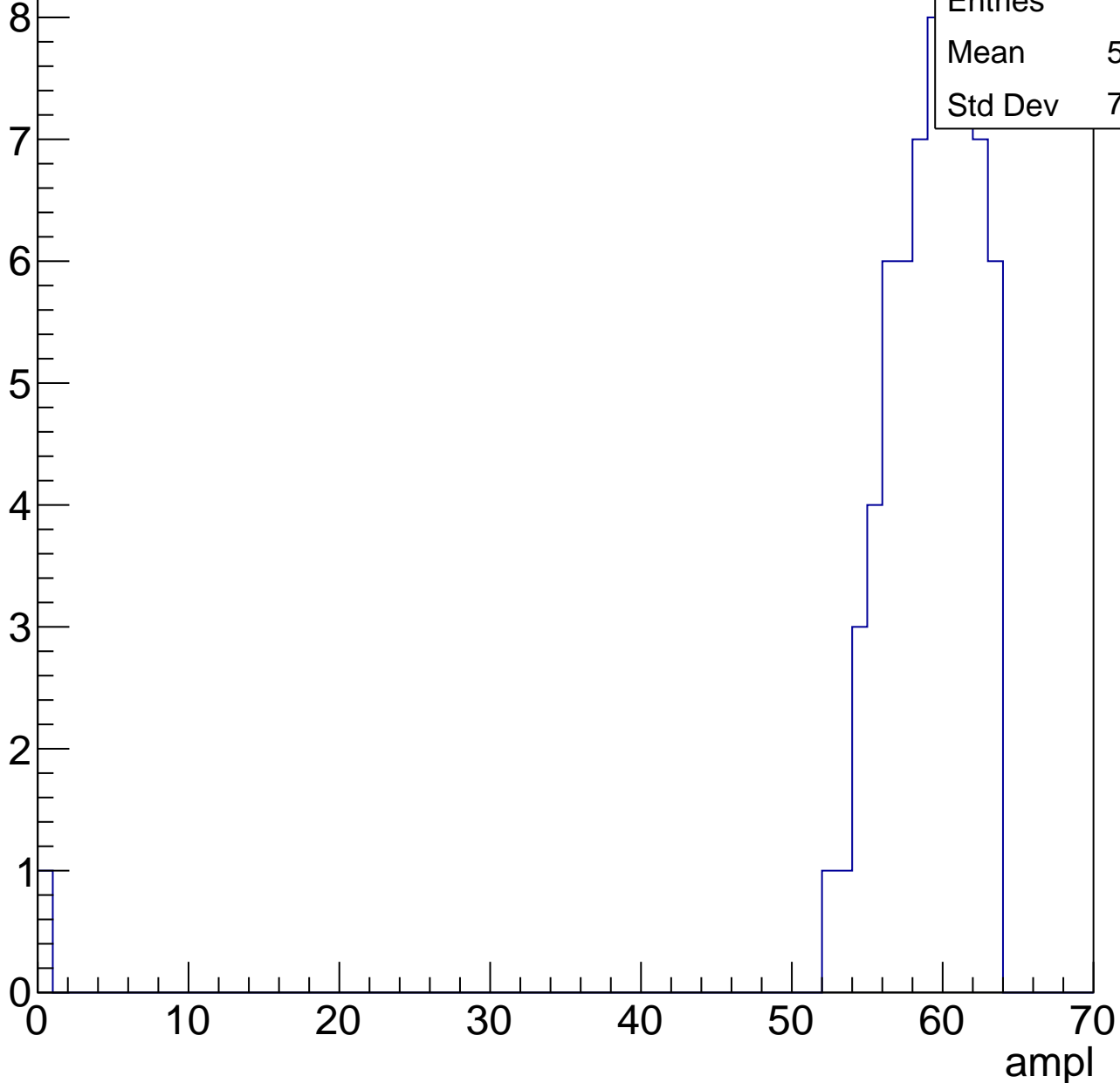


B1L003S, U21-ch104, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

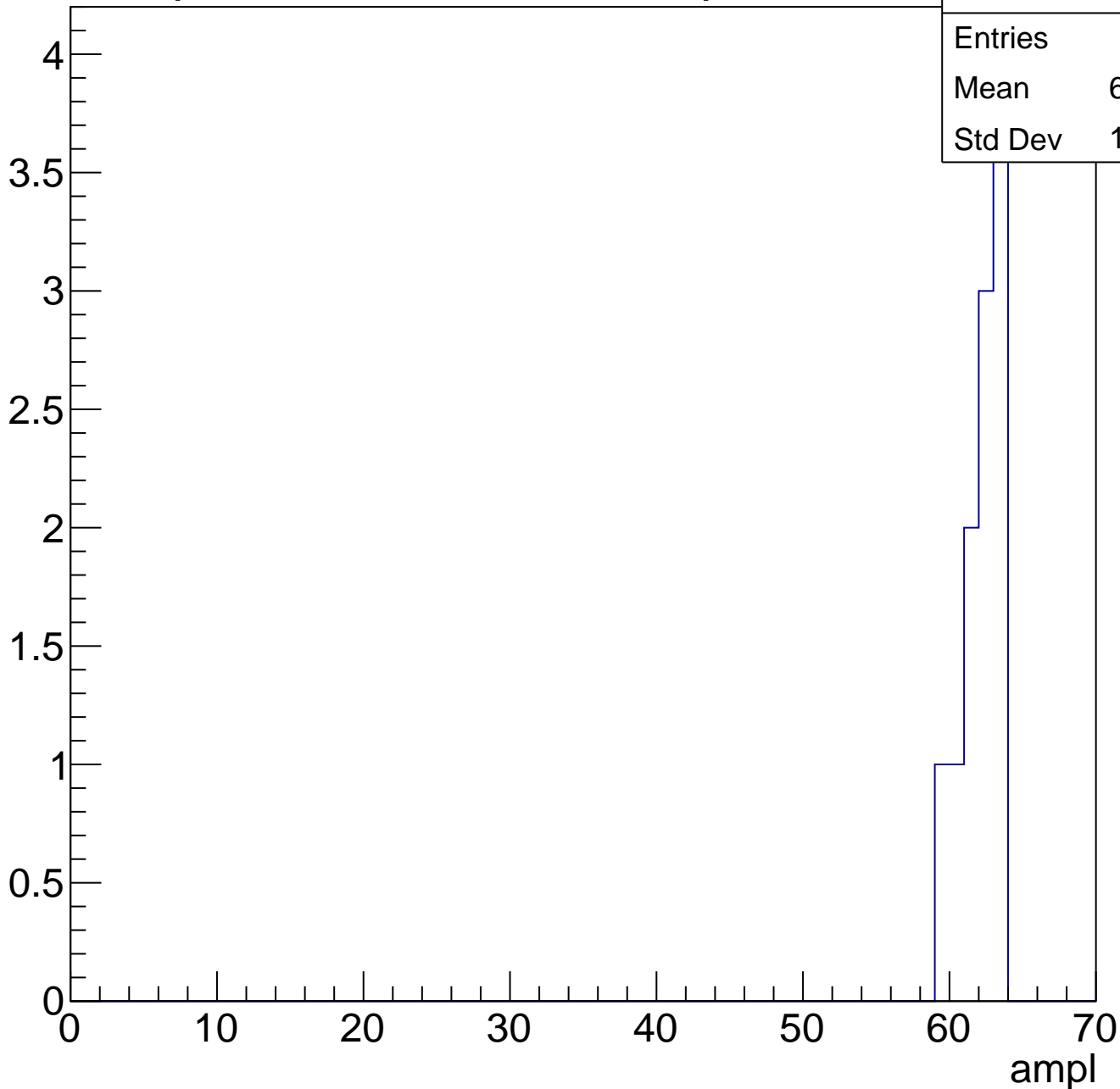
Entries	66
Mean	57.92
Std Dev	7.699



B1L003S, U21-ch104, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch104, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch105, adc0

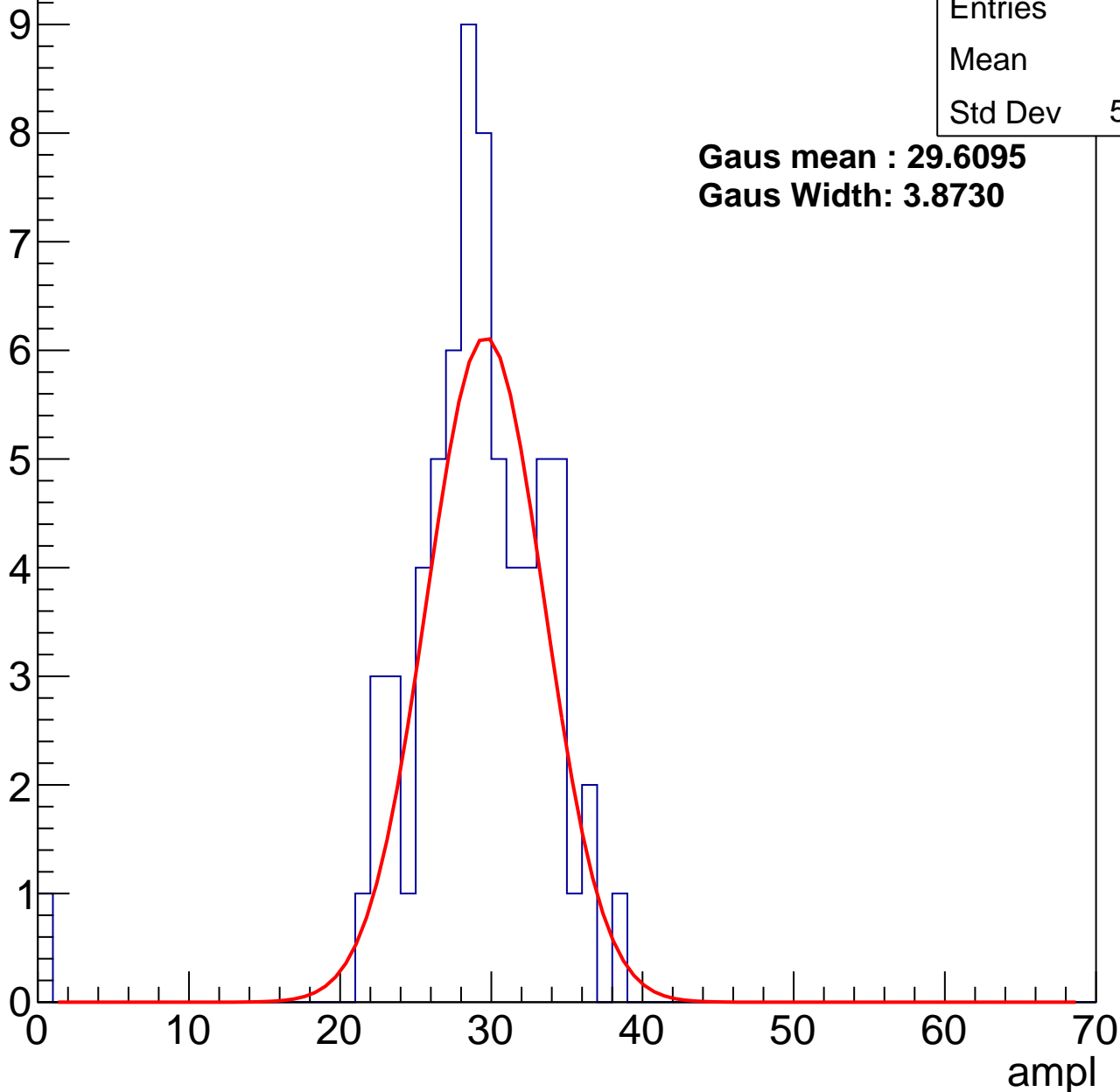
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	28.5
Std Dev	5.126

Gaus mean : 29.6095

Gaus Width: 3.8730



B1L003S, U21-ch105, adc1

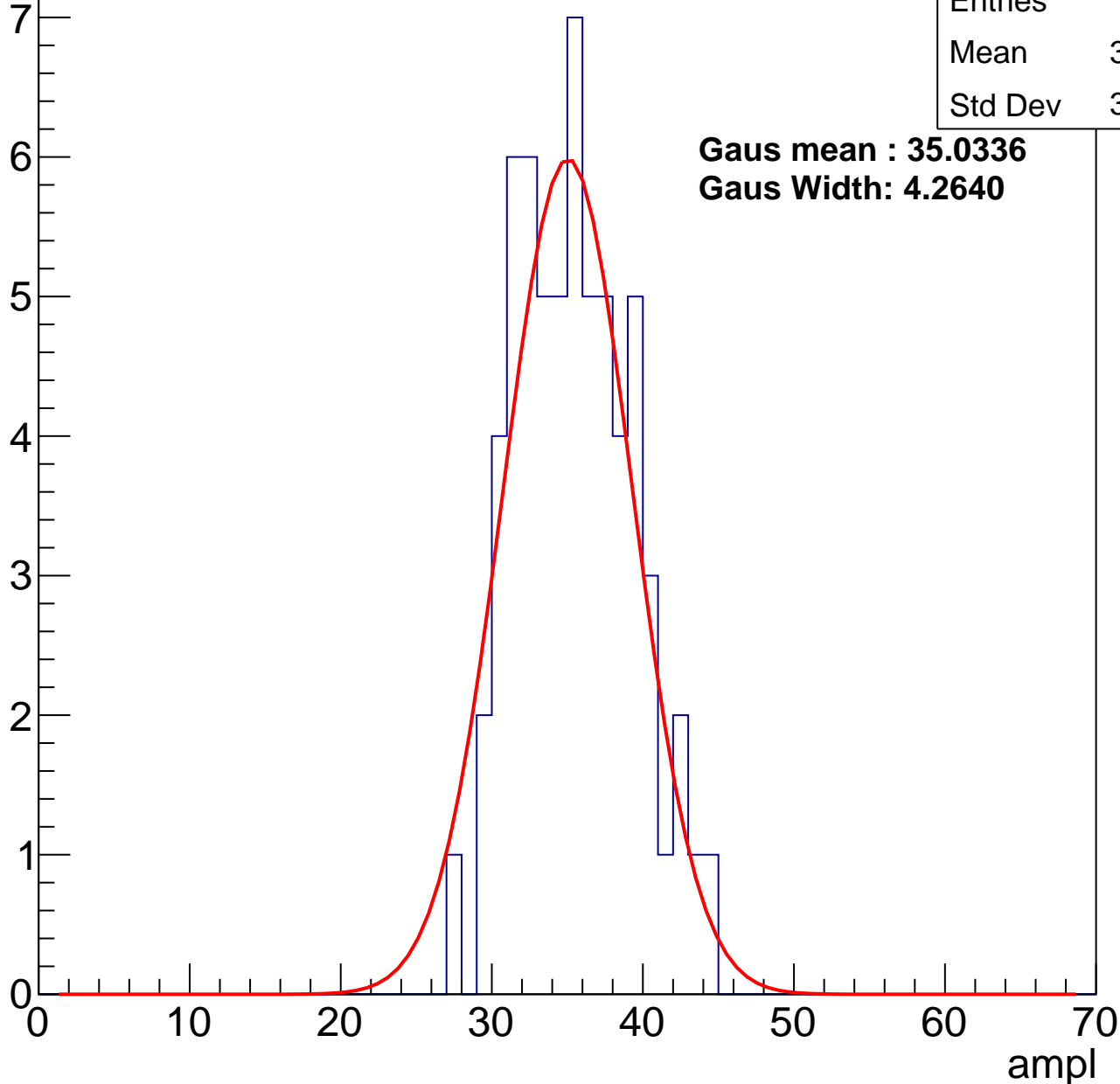
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	35.03
Std Dev	3.792

Gaus mean : 35.0336

Gaus Width: 4.2640



B1L003S, U21-ch105, adc2

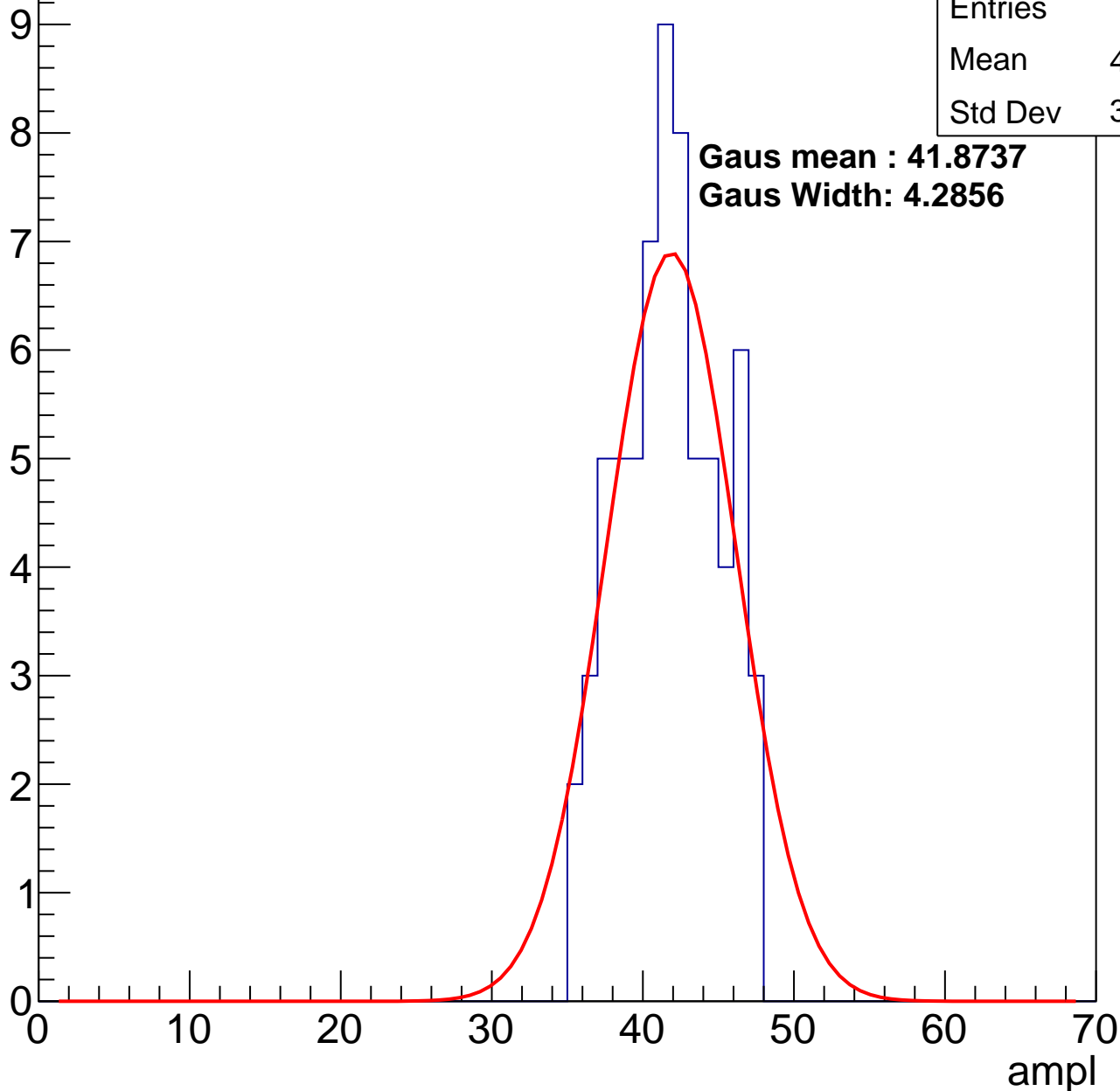
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.27
Std Dev	3.207

Gaus mean : 41.8737

Gaus Width: 4.2856

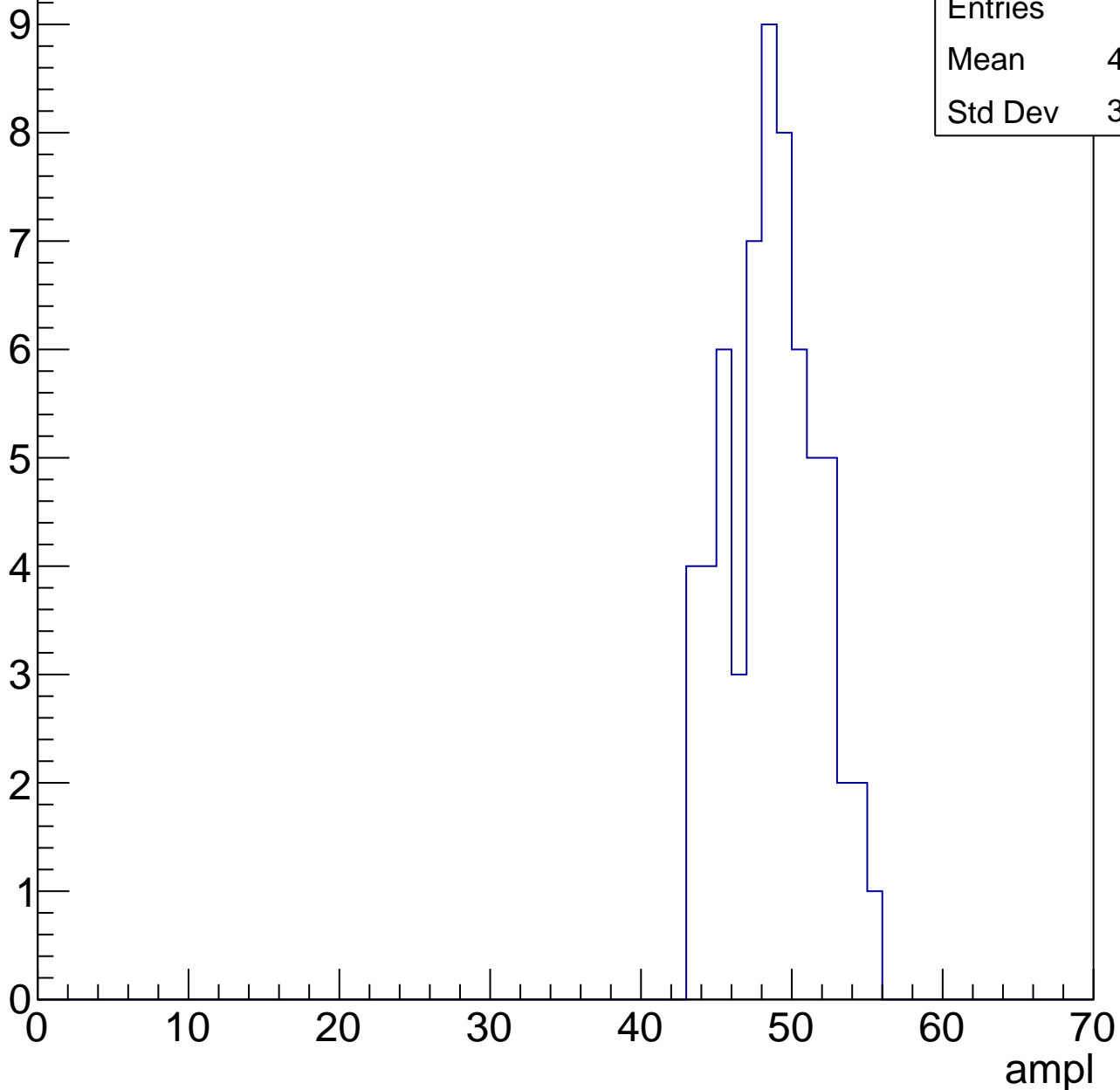


B1L003S, U21-ch105, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.27
Std Dev	3.006

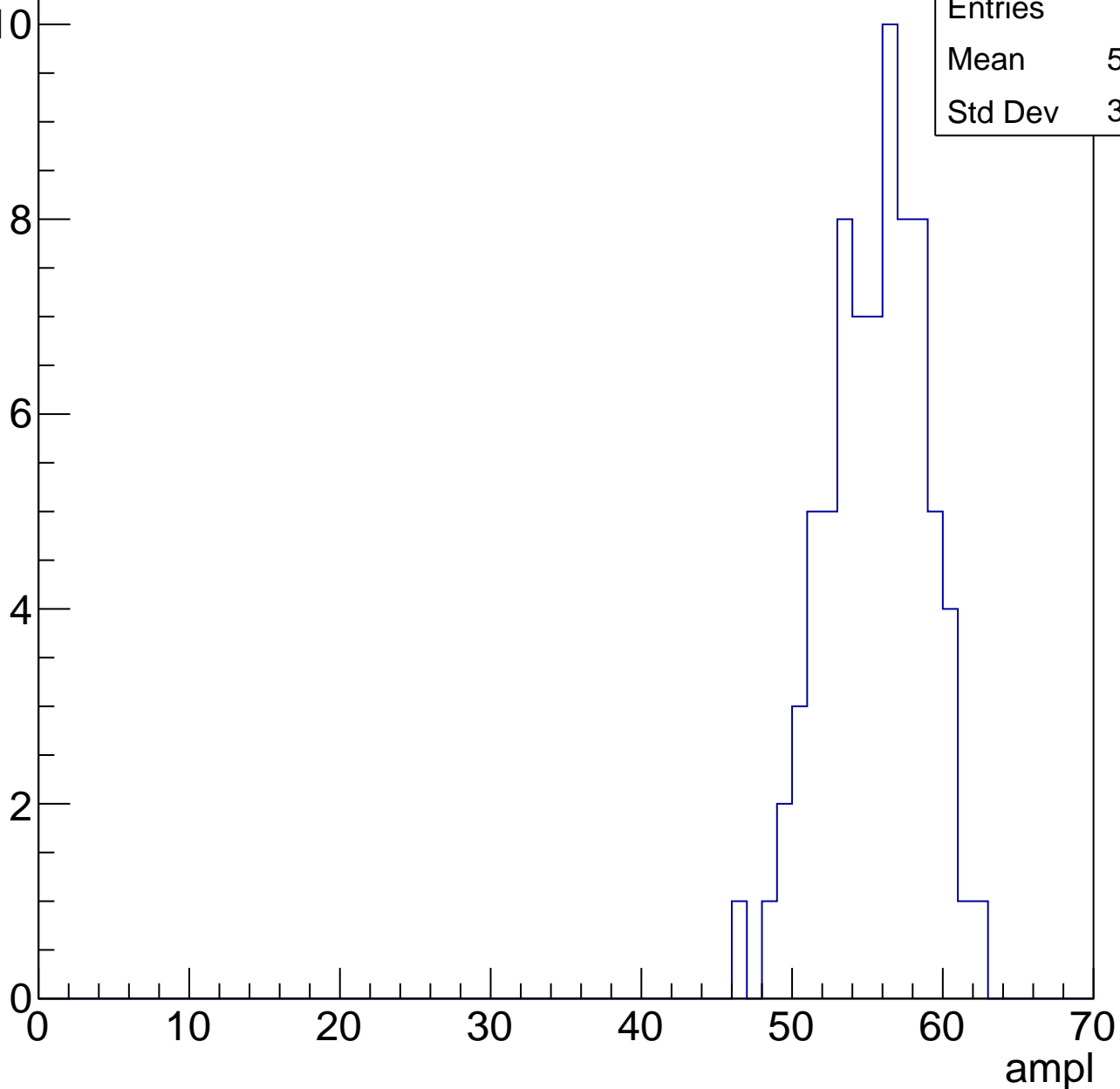


B1L003S, U21-ch105, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	55.03
Std Dev	3.285

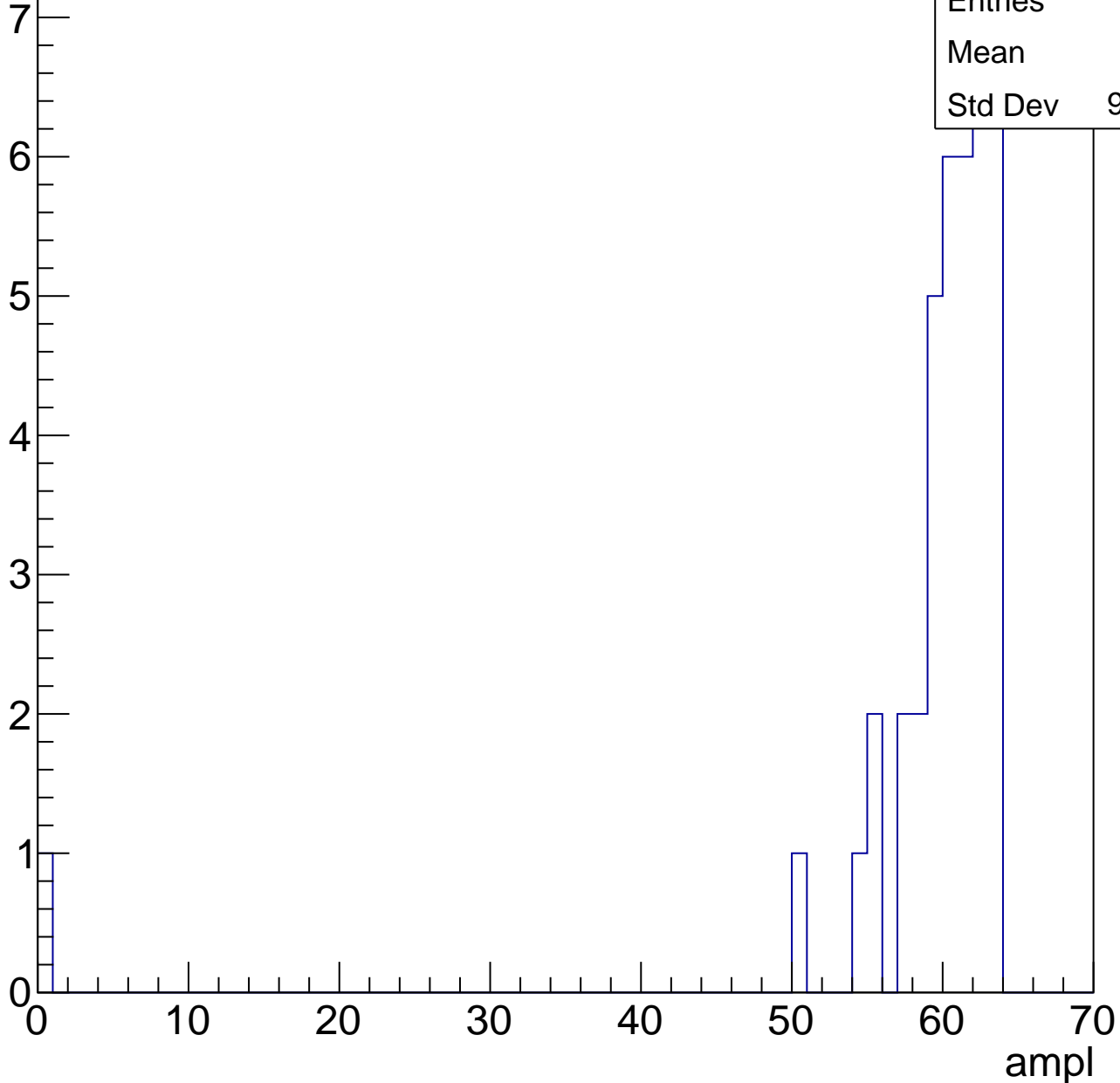


B1L003S, U21-ch105, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

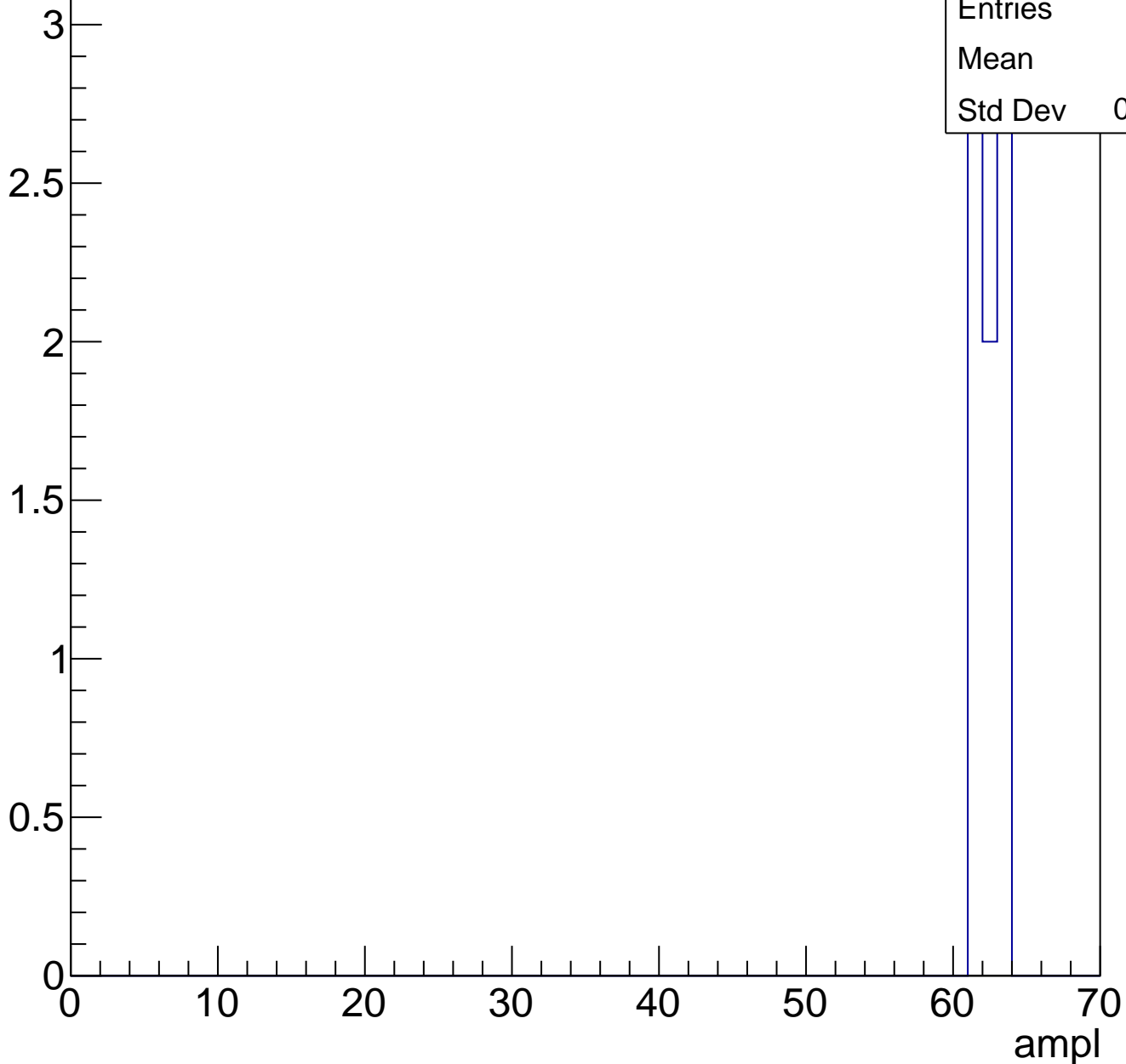
Entries	40
Mean	58.5
Std Dev	9.778



B1L003S, U21-ch105, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch105, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch106, adc0

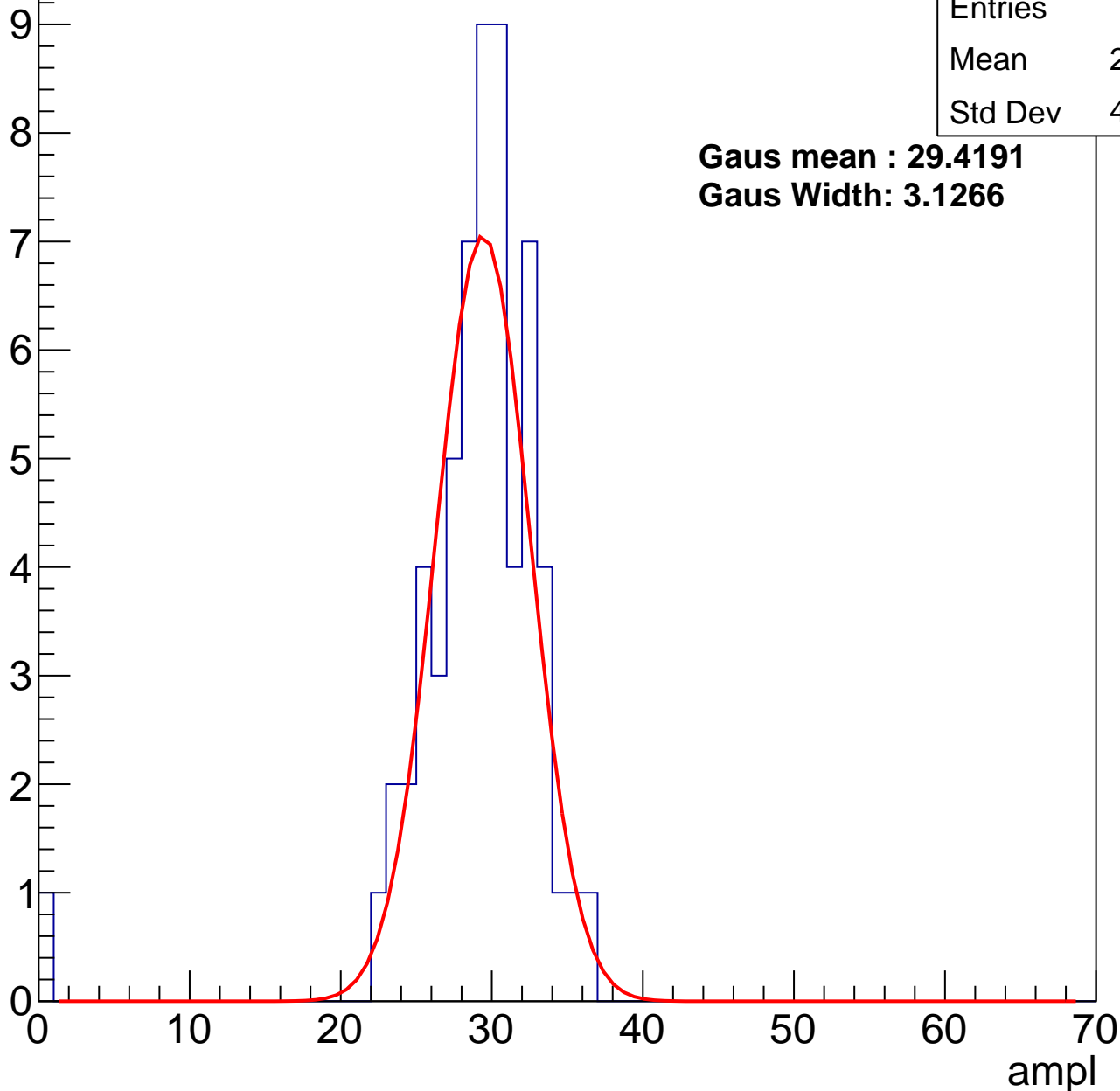
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	28.54
Std Dev	4.755

Gaus mean : 29.4191

Gaus Width: 3.1266



B1L003S, U21-ch106, adc1

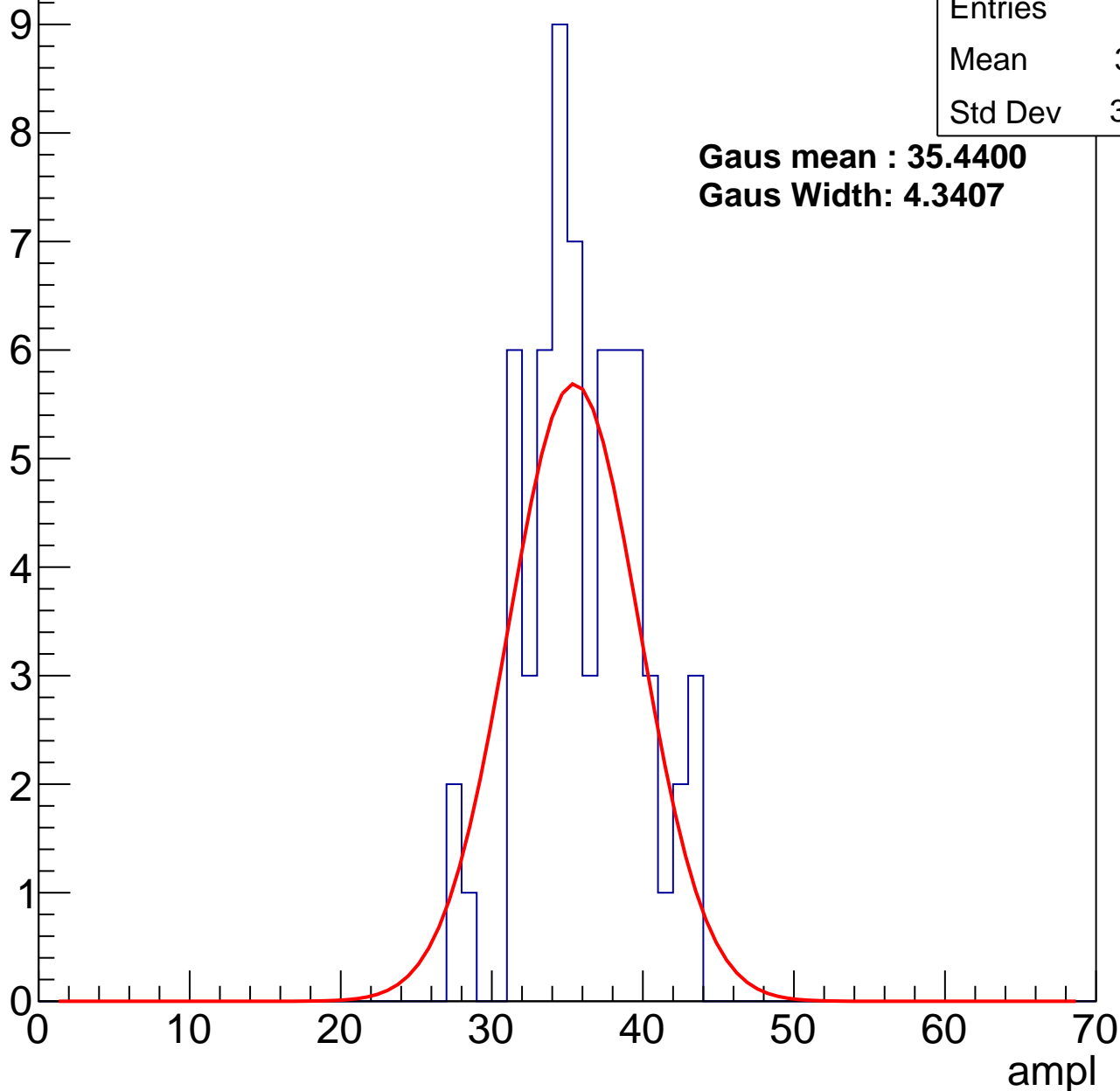
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.61
Std Dev	3.723

Gaus mean : 35.4400

Gaus Width: 4.3407



B1L003S, U21-ch106, adc2

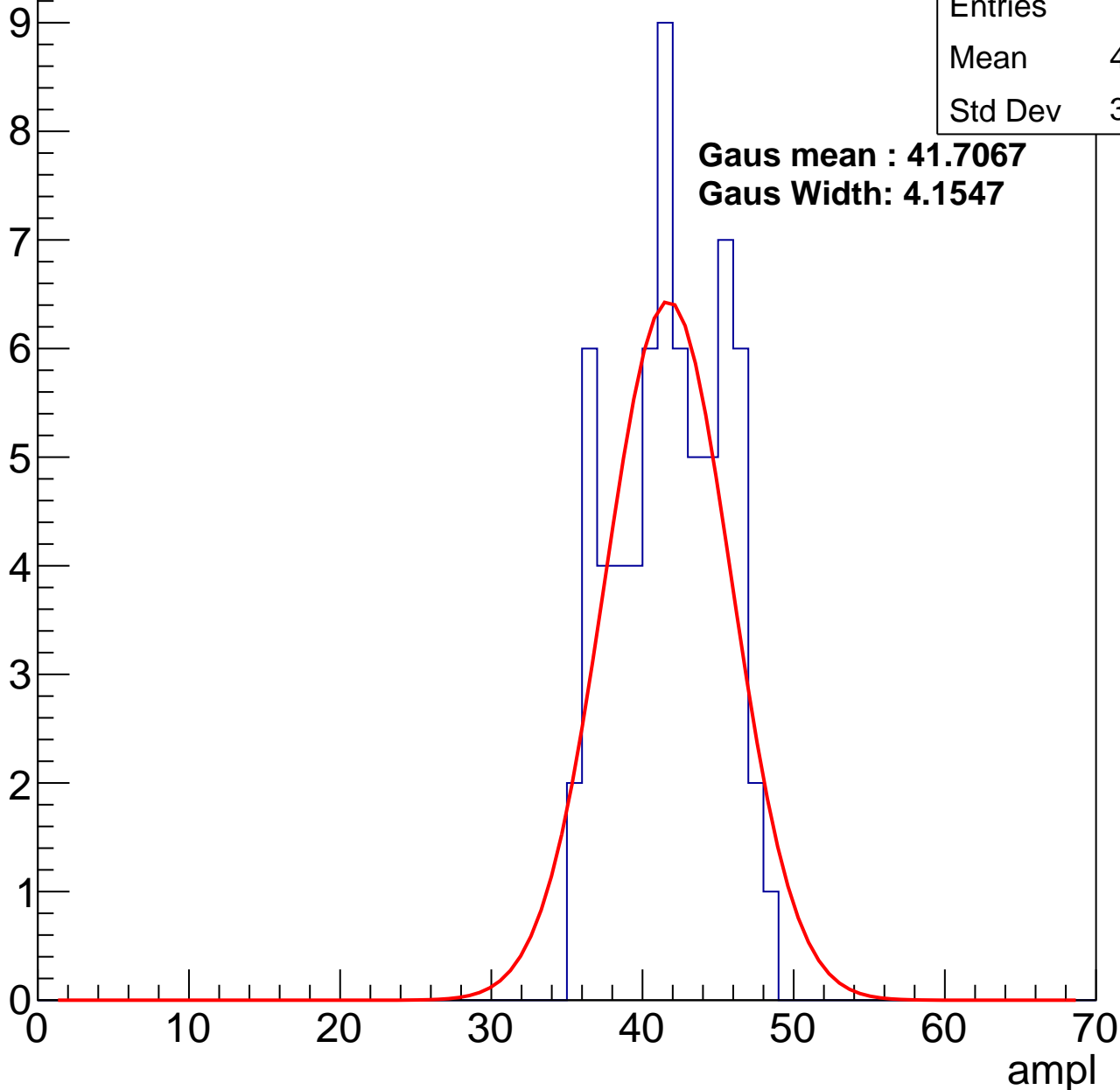
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	41.36
Std Dev	3.433

Gaus mean : 41.7067

Gaus Width: 4.1547

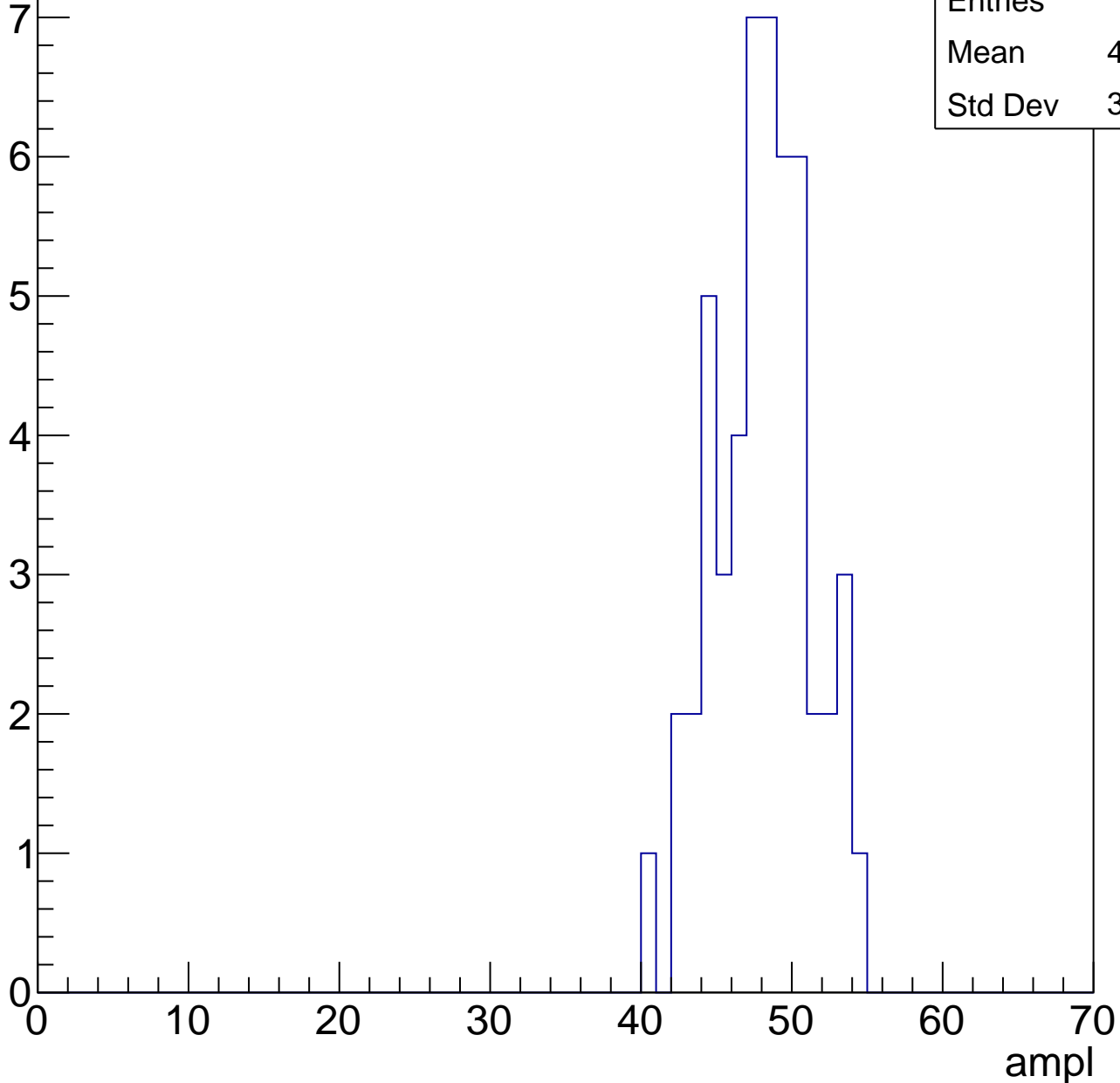


B1L003S, U21-ch106, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	47.59
Std Dev	3.126

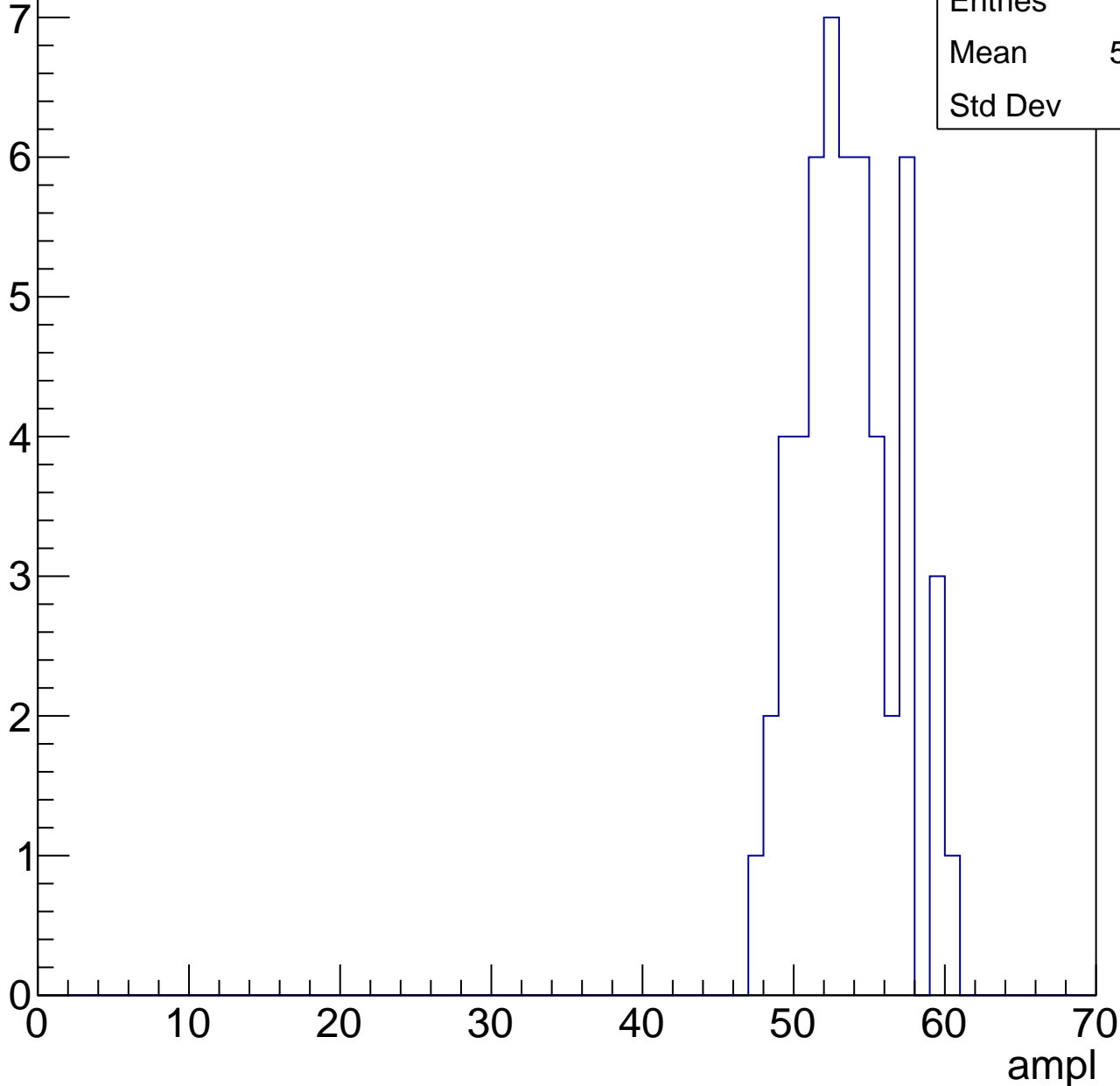


B1L003S, U21-ch106, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	53.12
Std Dev	3.13

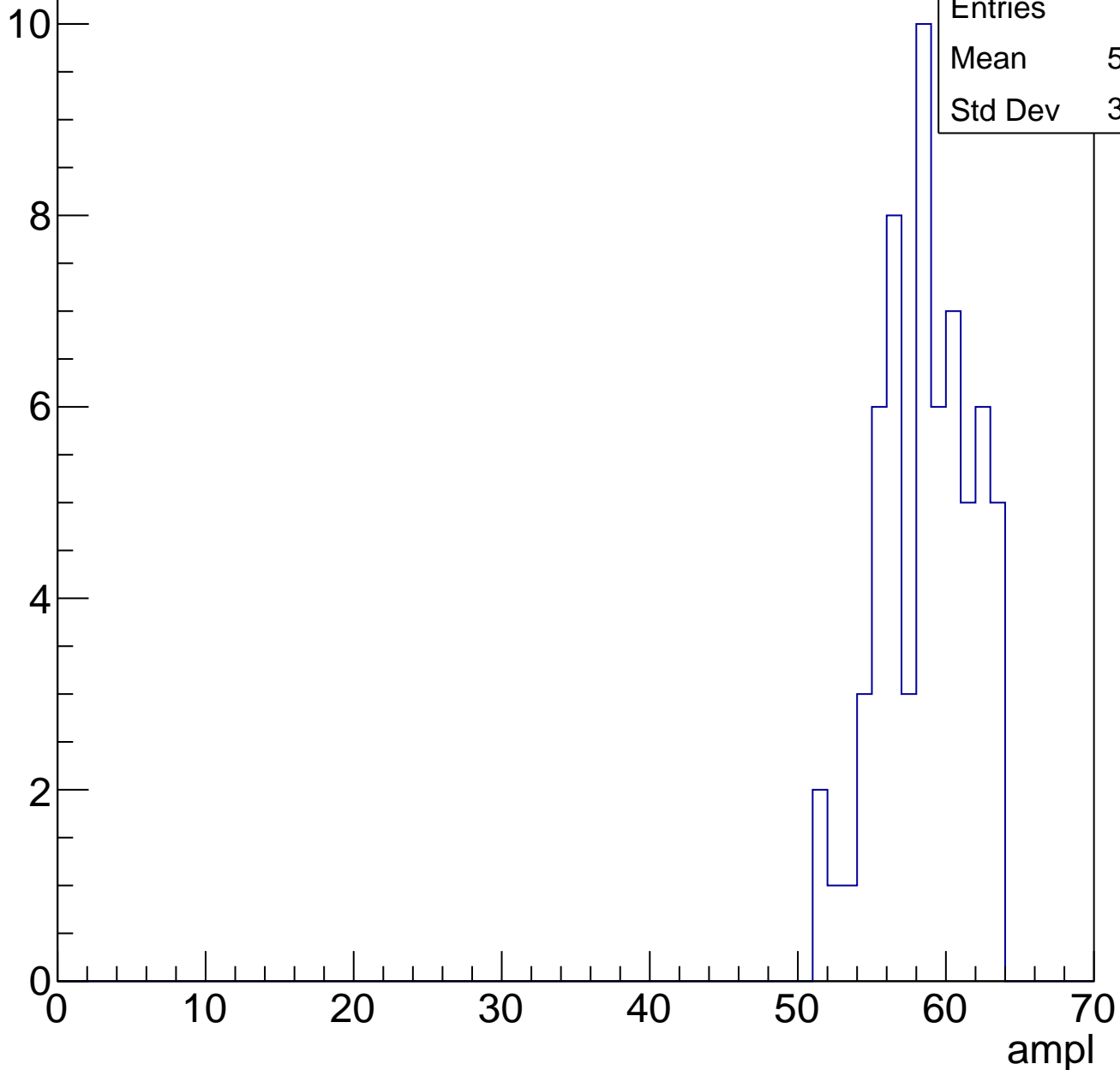


B1L003S, U21-ch106, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	63
Mean	58.16
Std Dev	3.072

Entry

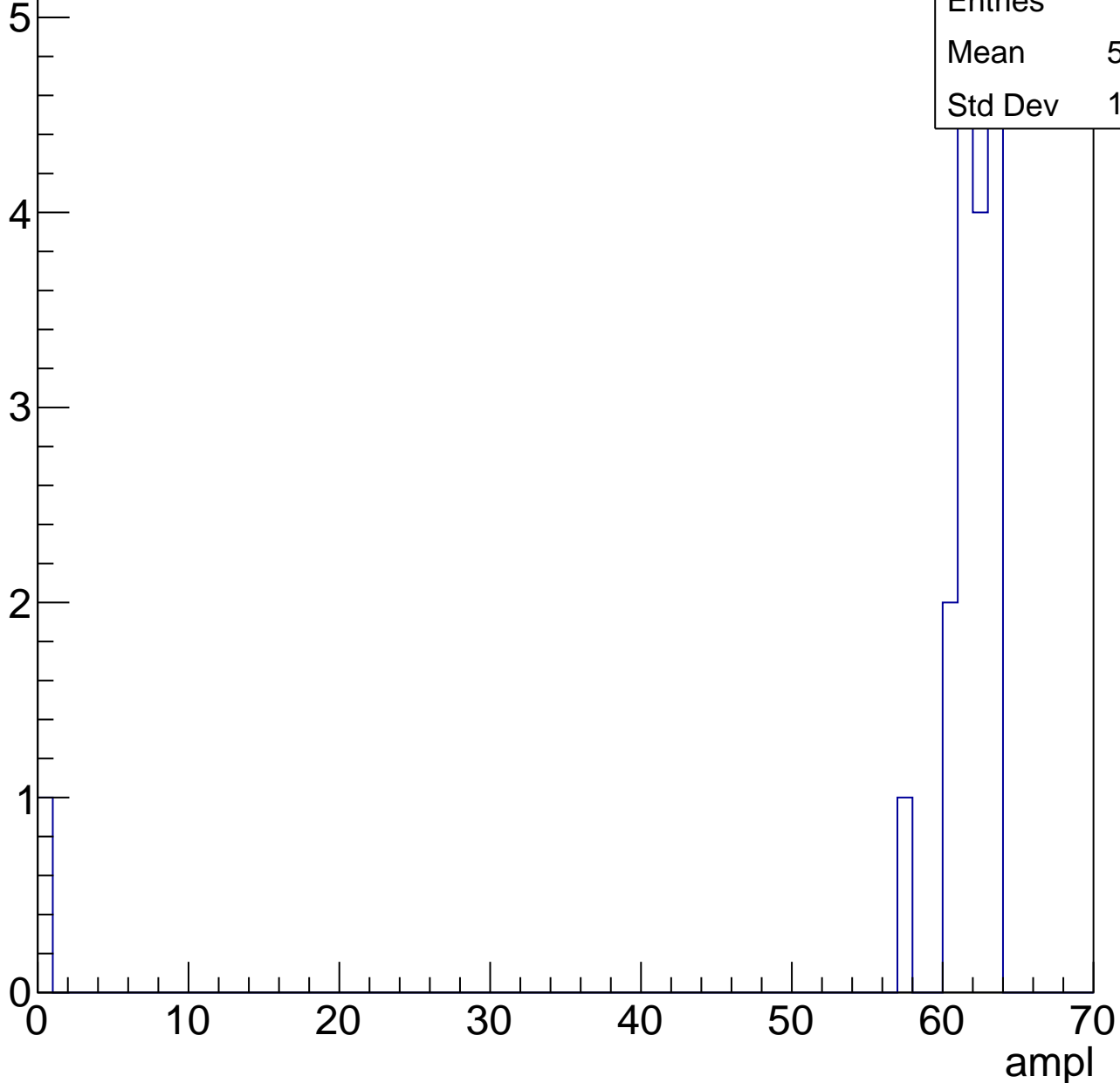


B1L003S, U21-ch106, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

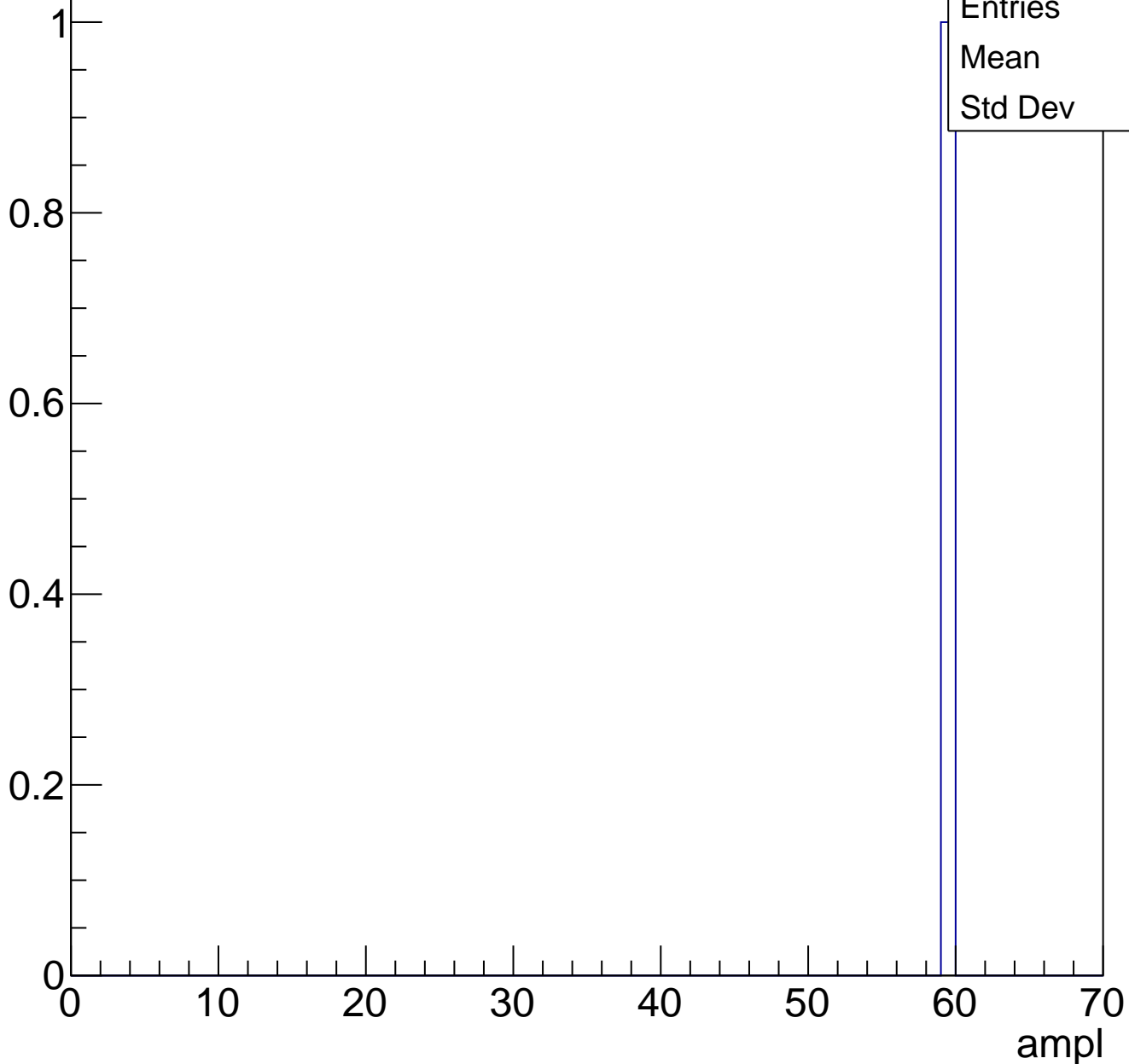
Entries	18
Mean	58.06
Std Dev	14.16



B1L003S, U21-ch106, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	59
Std Dev	0

B1L003S, U21-ch107, adc0

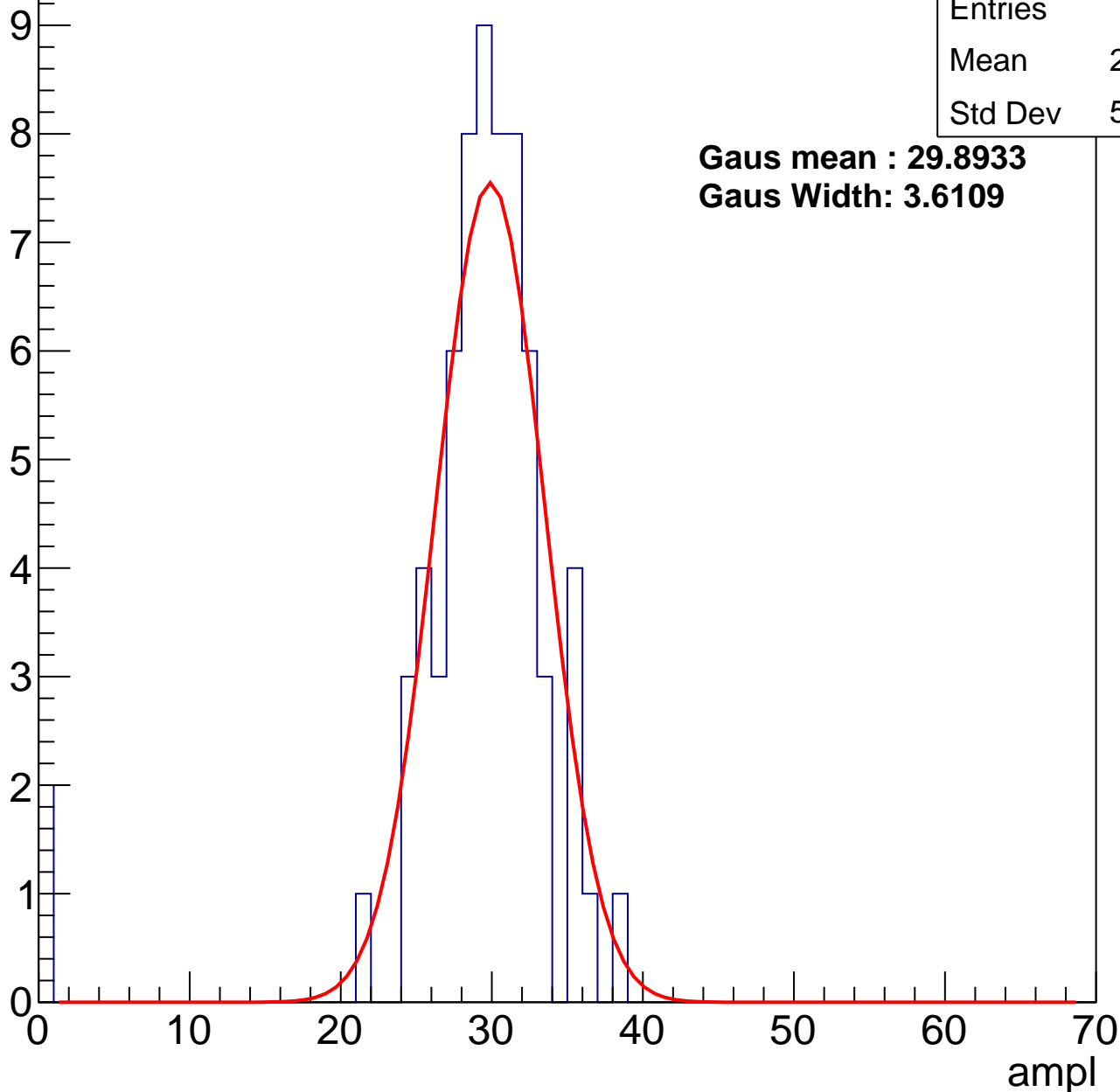
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	67
Mean	28.52
Std Dev	5.916

Gaus mean : 29.8933

Gaus Width: 3.6109



B1L003S, U21-ch107, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	66
Mean	36.11
Std Dev	3.061

Gaus mean : 36.4510

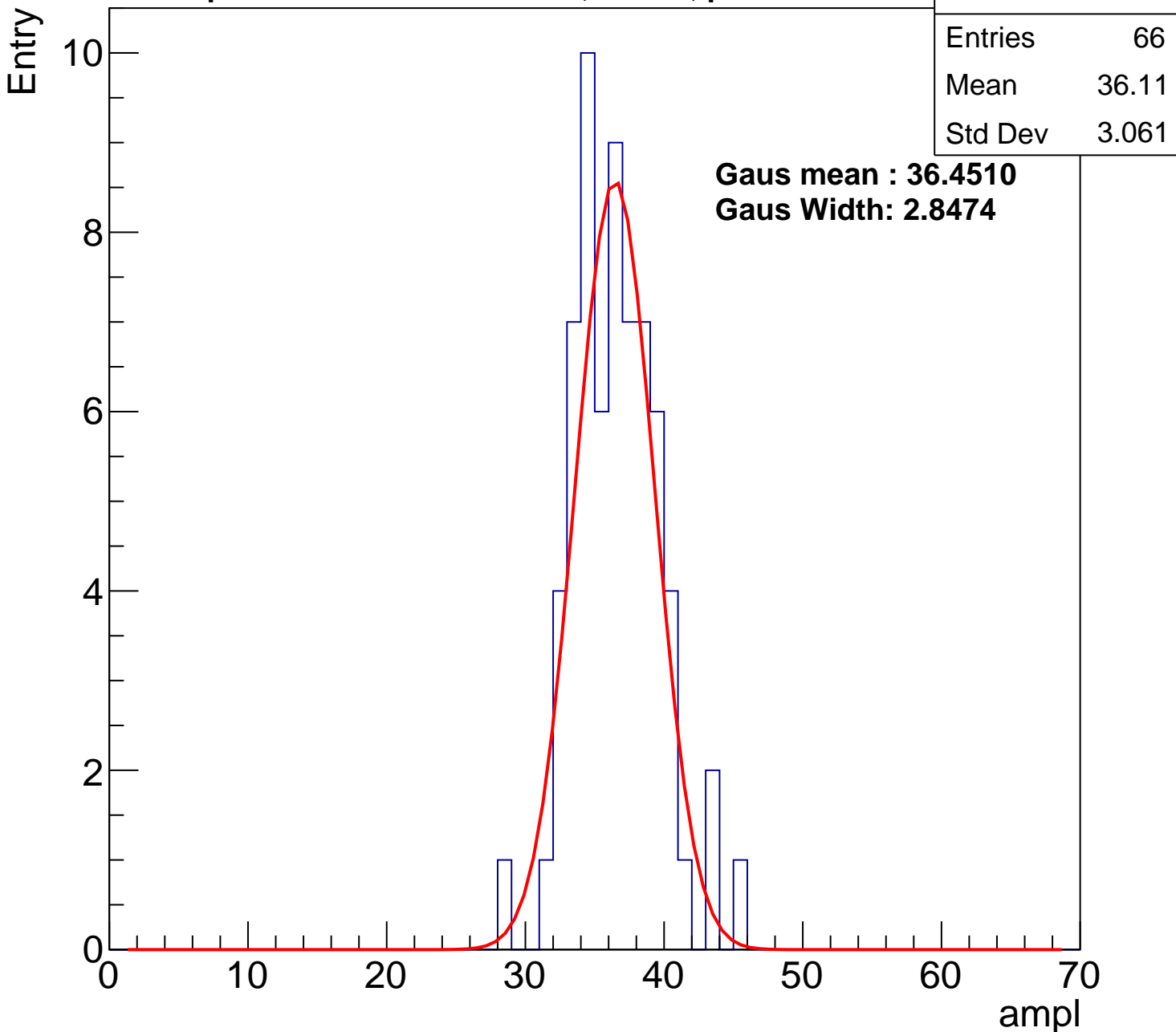
Gaus Width: 2.8474

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch107, adc2

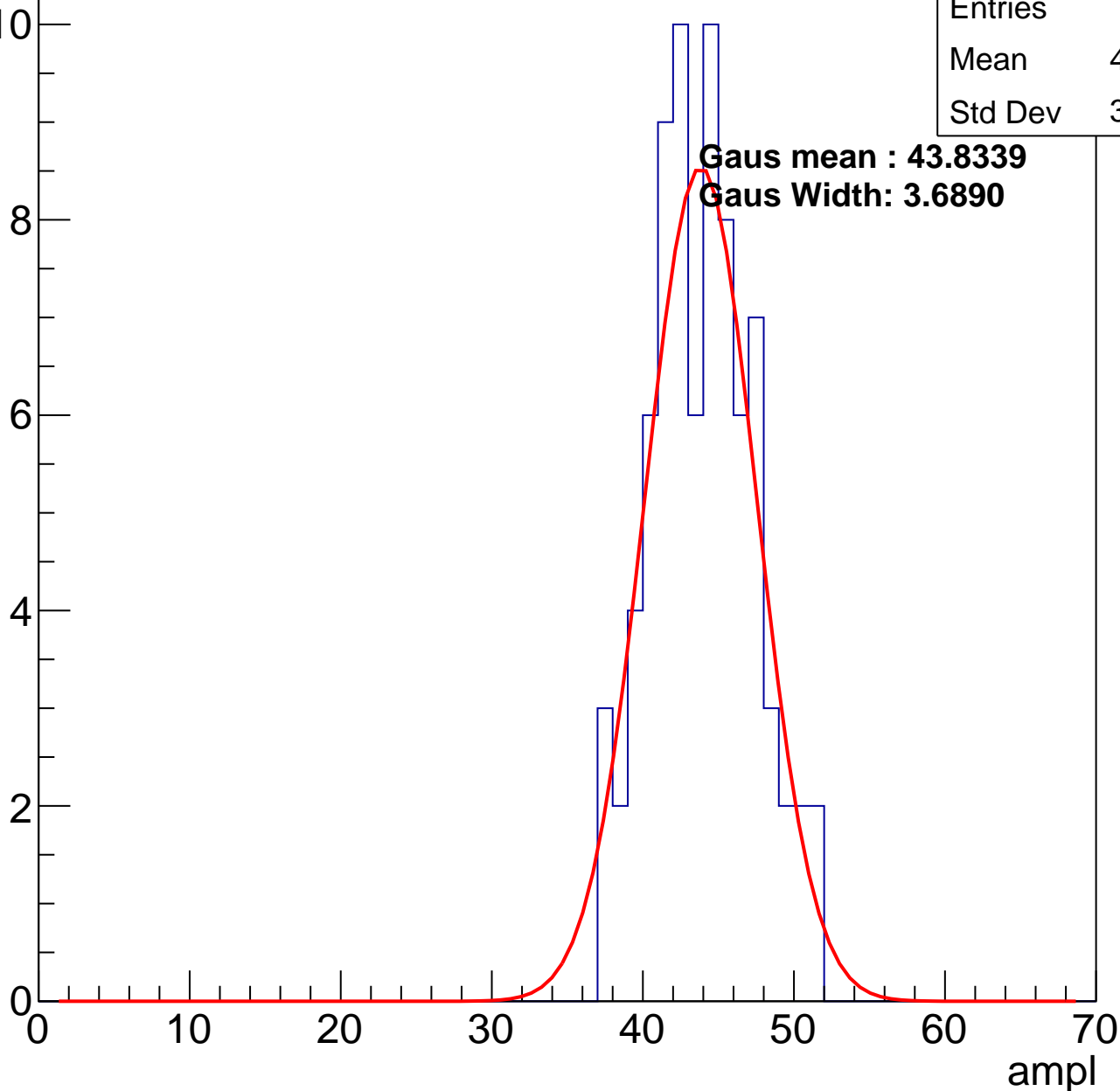
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	43.49
Std Dev	3.324

Gaus mean : 43.8339

Gaus Width: 3.6890

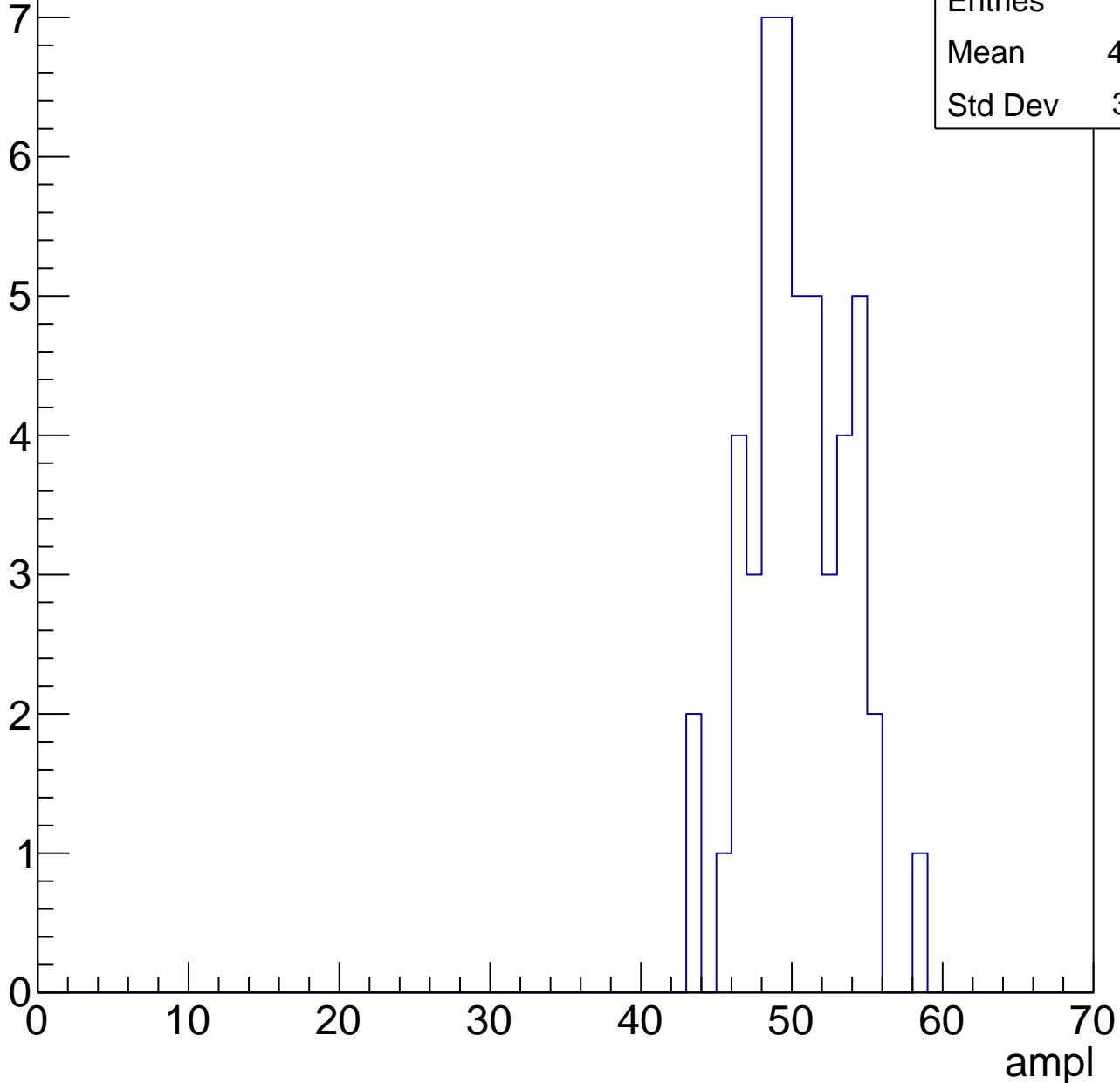


B1L003S, U21-ch107, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	49
Mean	49.92
Std Dev	3.181

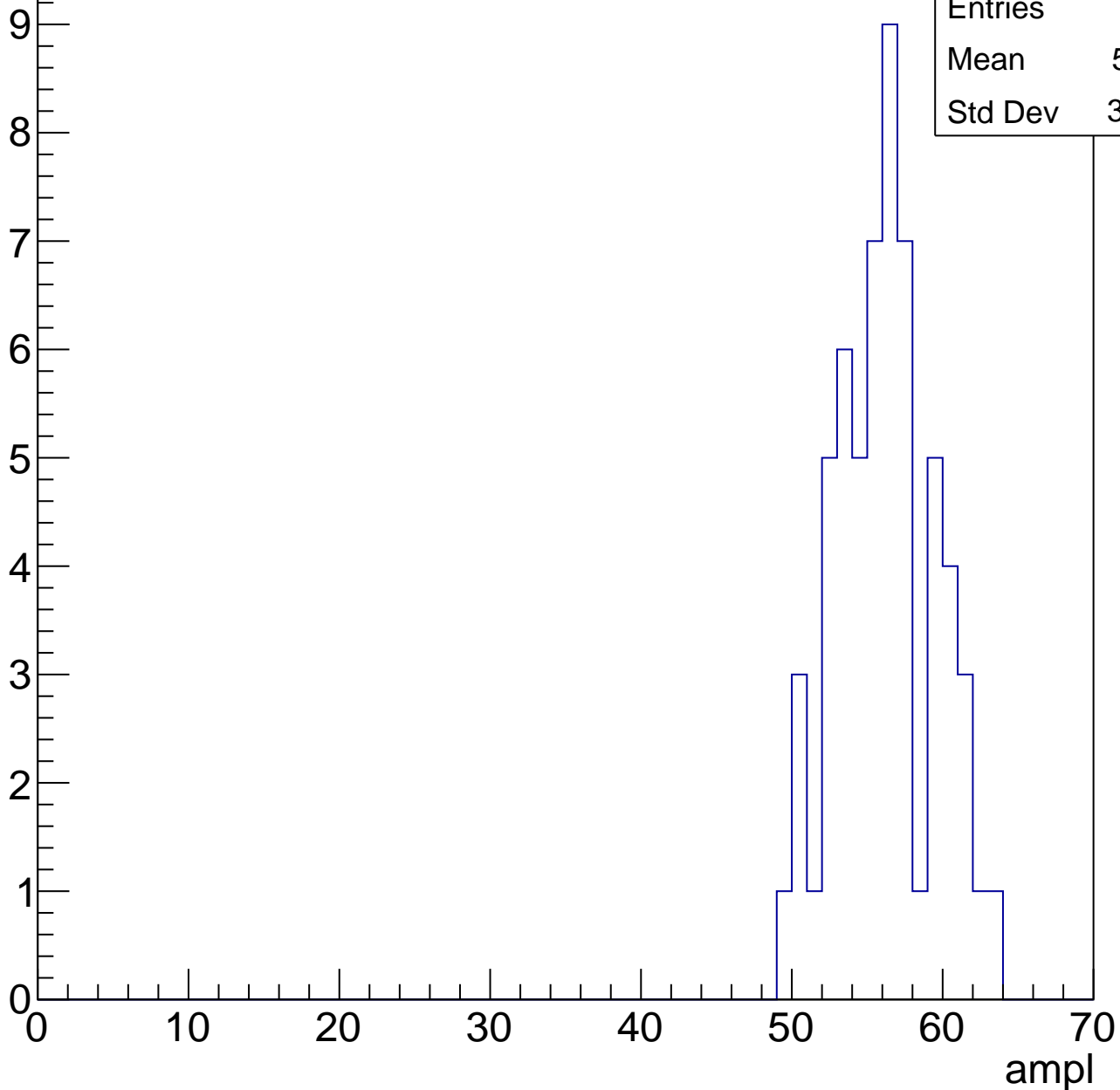


B1L003S, U21-ch107, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	55.71
Std Dev	3.237

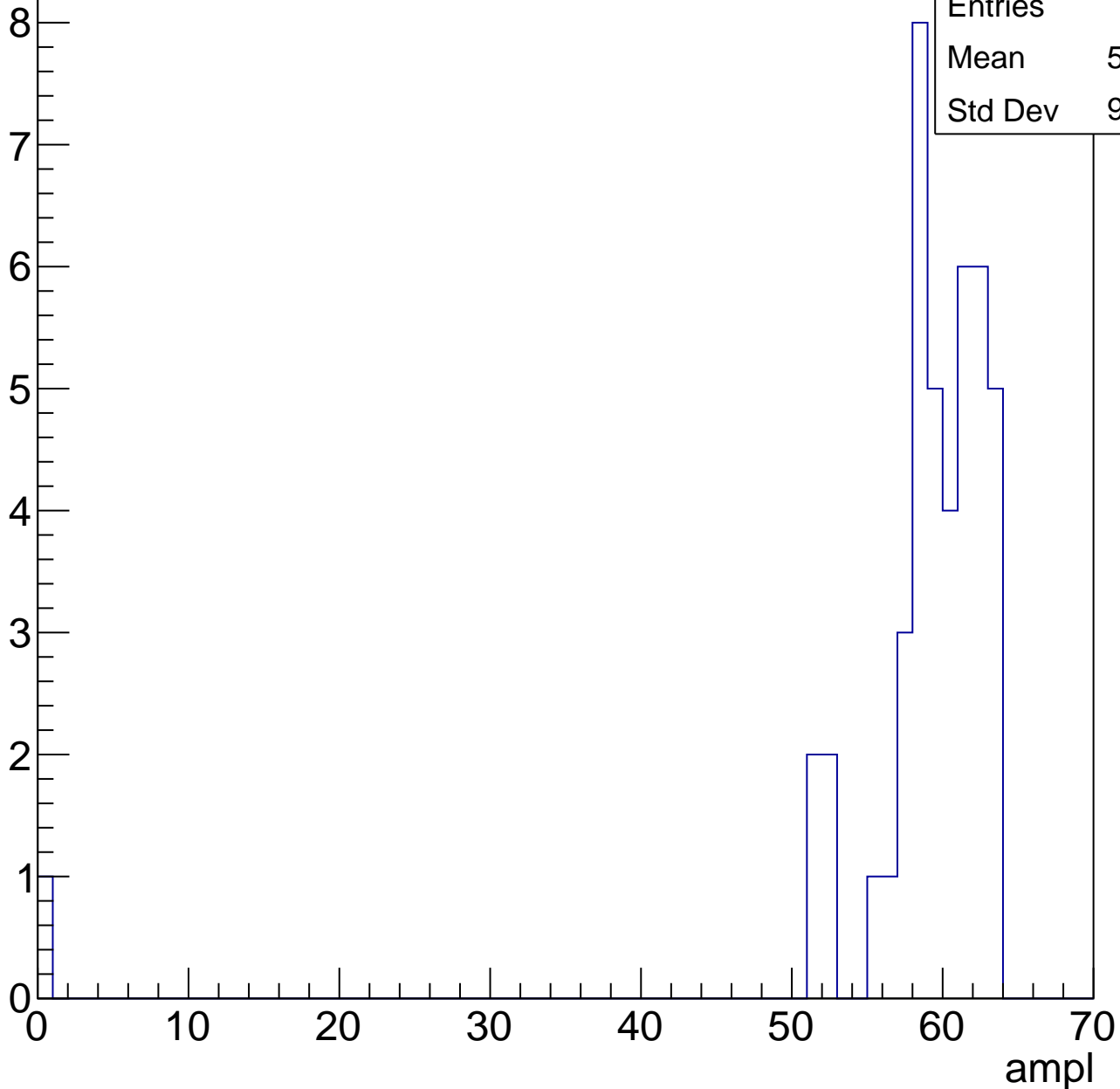


B1L003S, U21-ch107, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

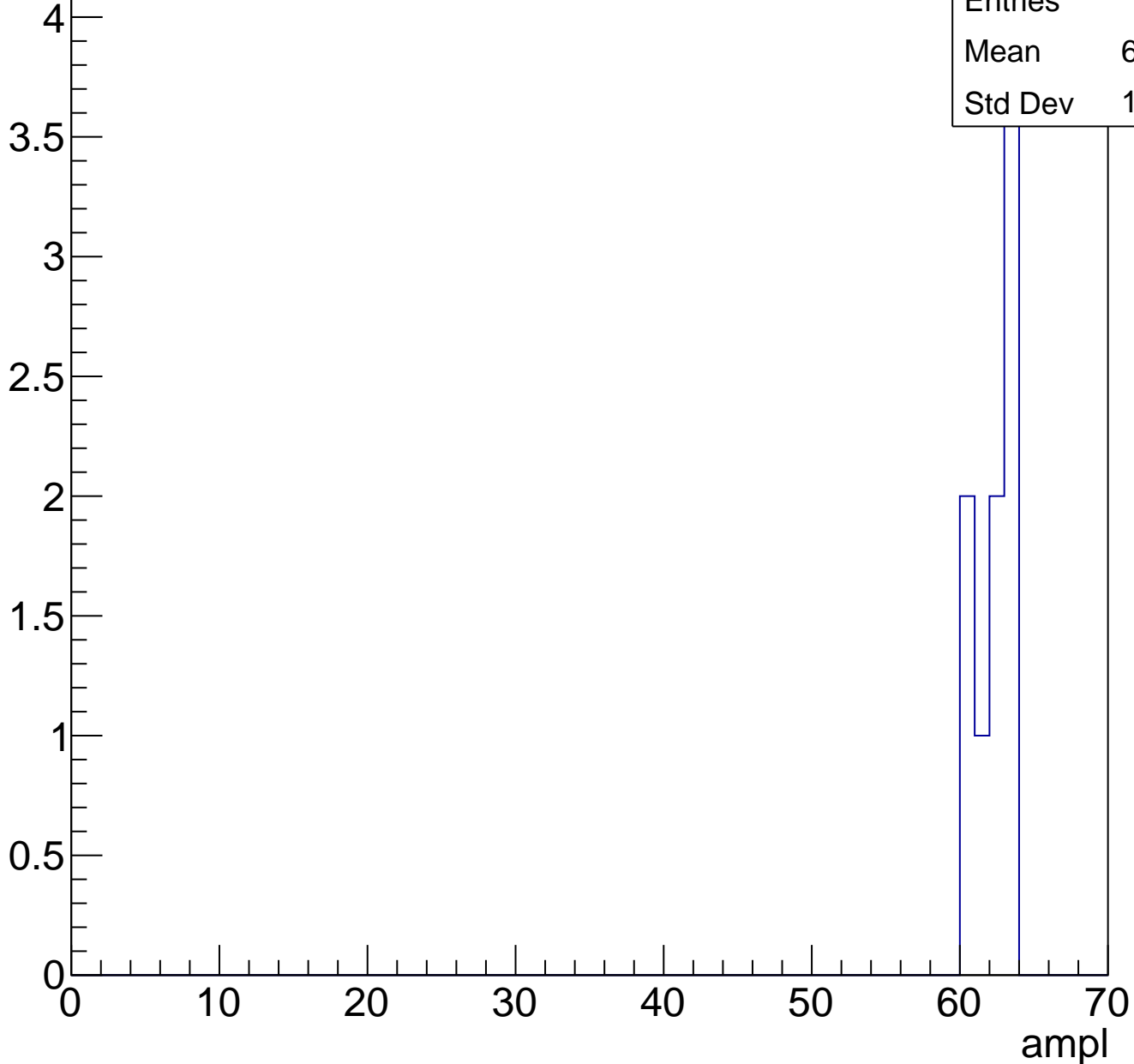
Entries	44
Mean	57.73
Std Dev	9.345



B1L003S, U21-ch107, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch107, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch108, adc0

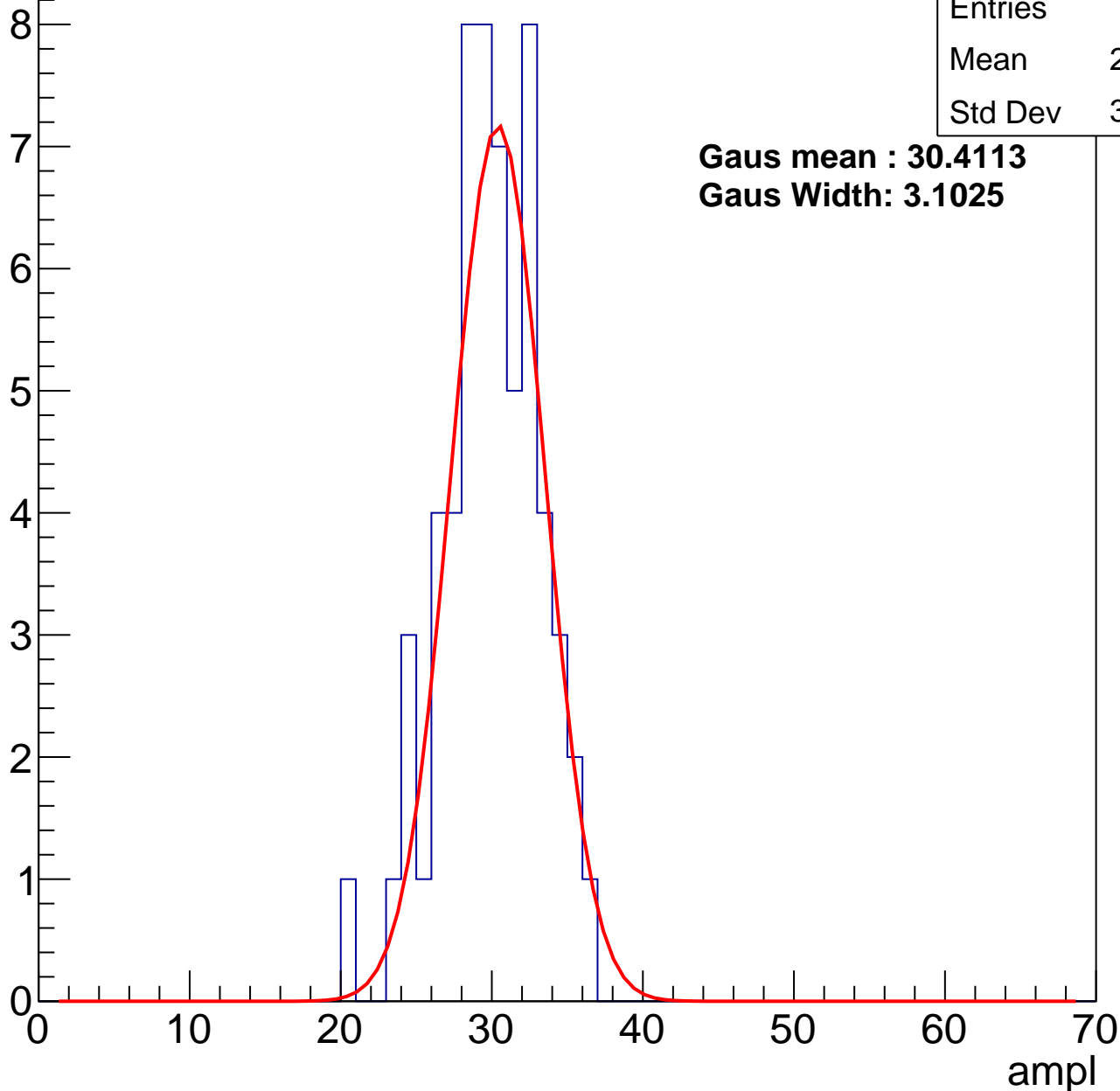
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	29.48
Std Dev	3.196

Gaus mean : 30.4113

Gaus Width: 3.1025



B1L003S, U21-ch108, adc1

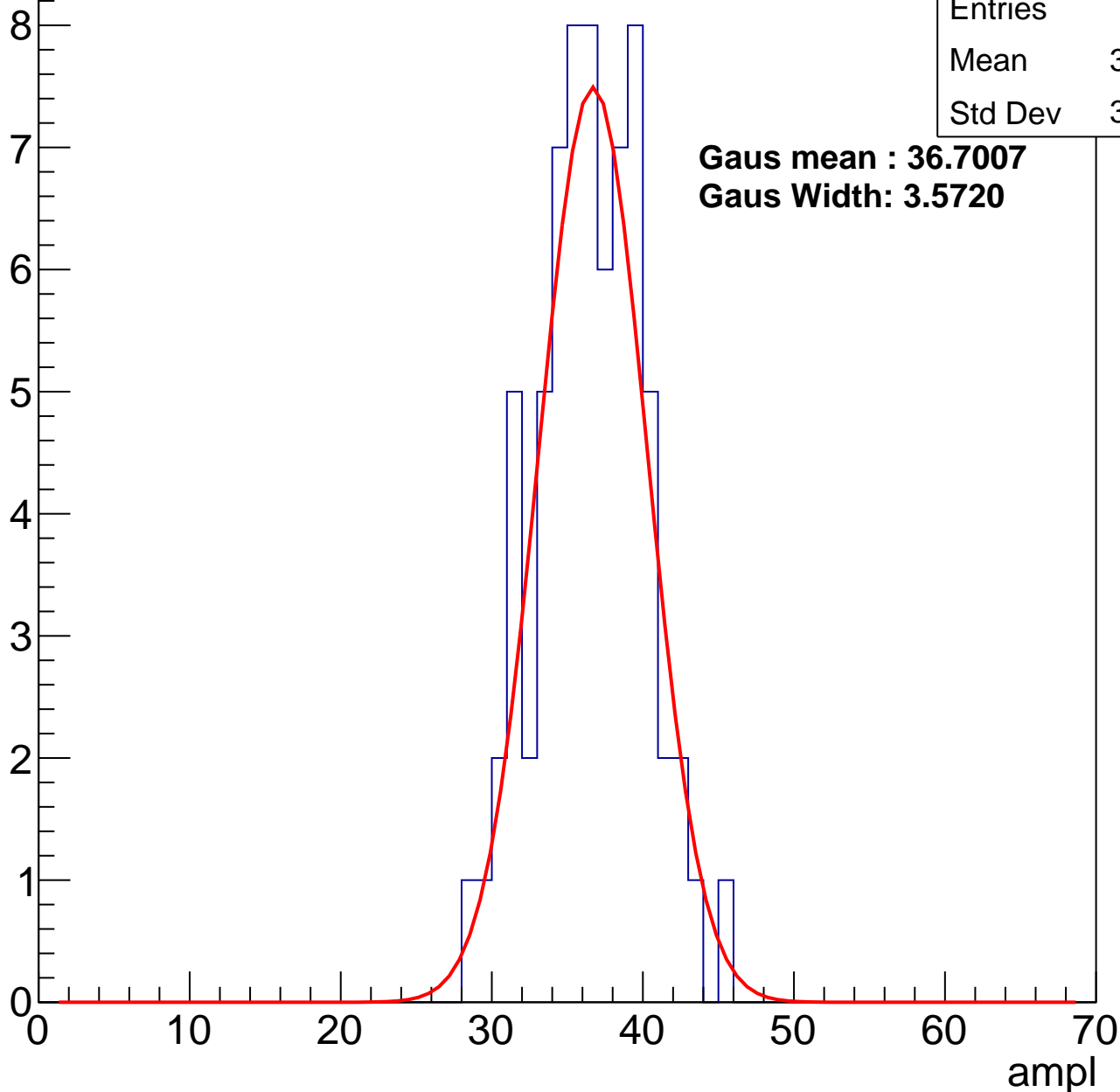
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	36.07
Std Dev	3.482

Gaus mean : 36.7007

Gaus Width: 3.5720



B1L003S, U21-ch108, adc2

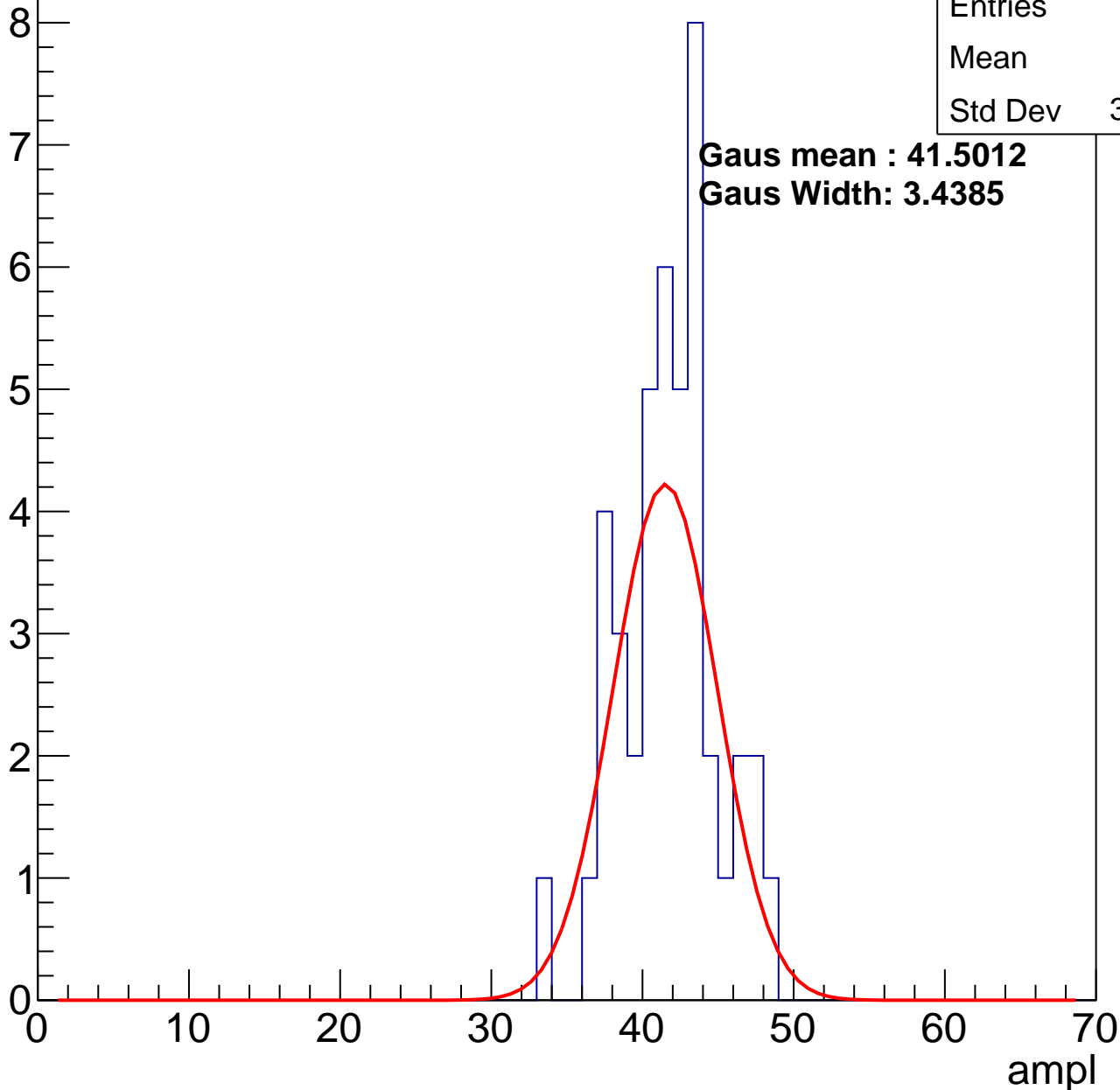
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	43
Mean	41.3
Std Dev	3.166

Gaus mean : 41.5012

Gaus Width: 3.4385

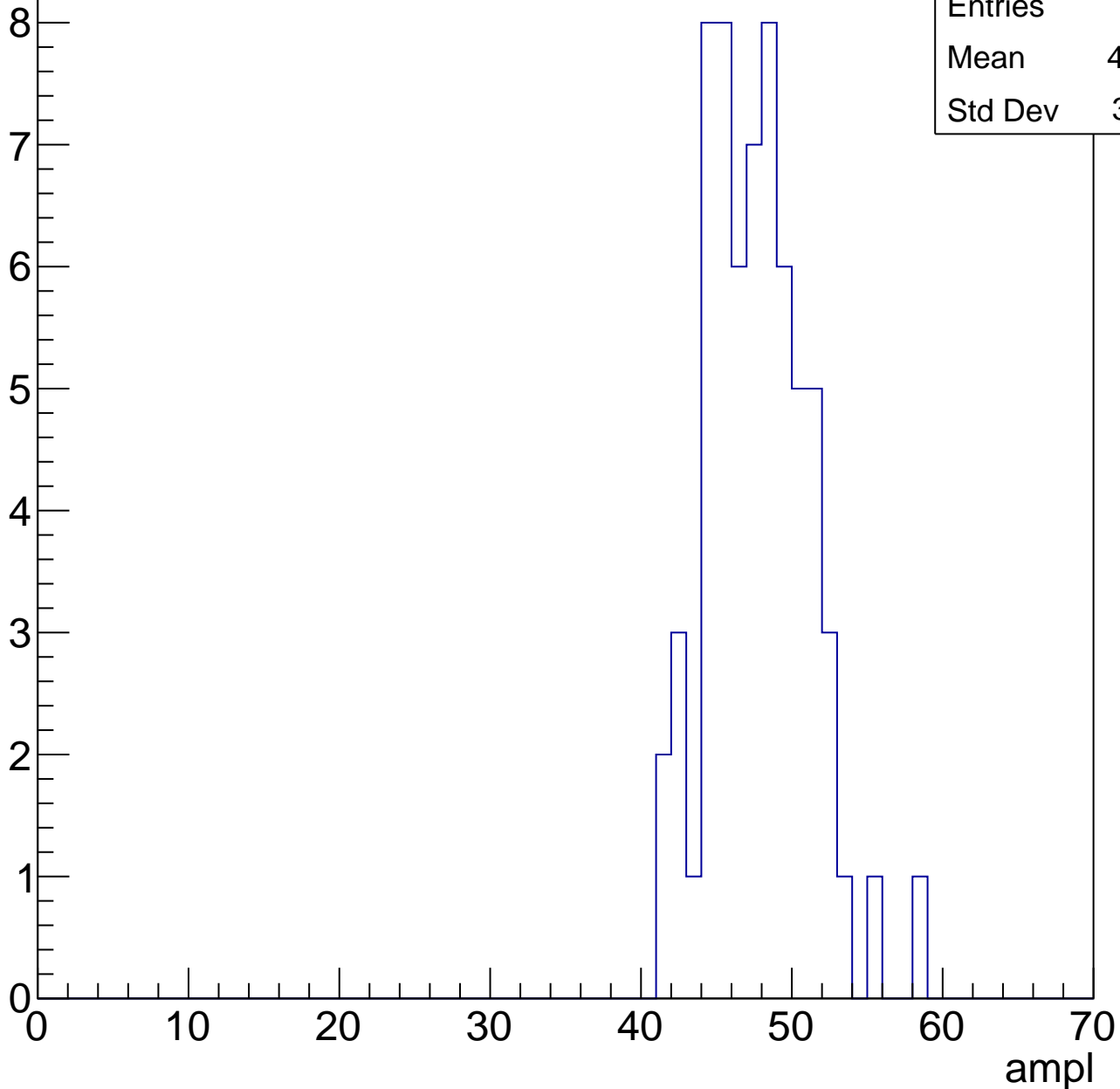


B1L003S, U21-ch108, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	47.28
Std Dev	3.331

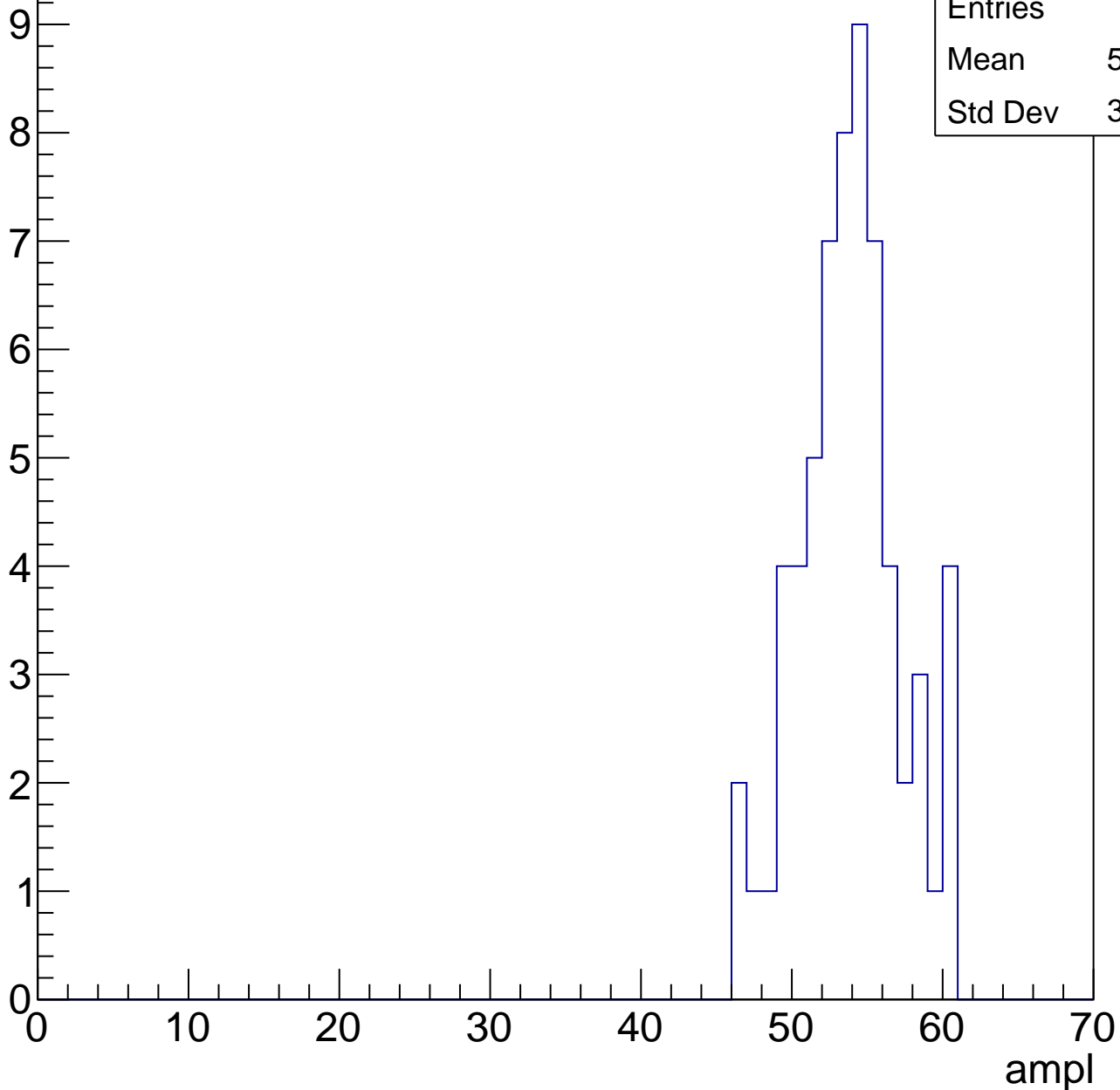


B1L003S, U21-ch108, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	53.35
Std Dev	3.336

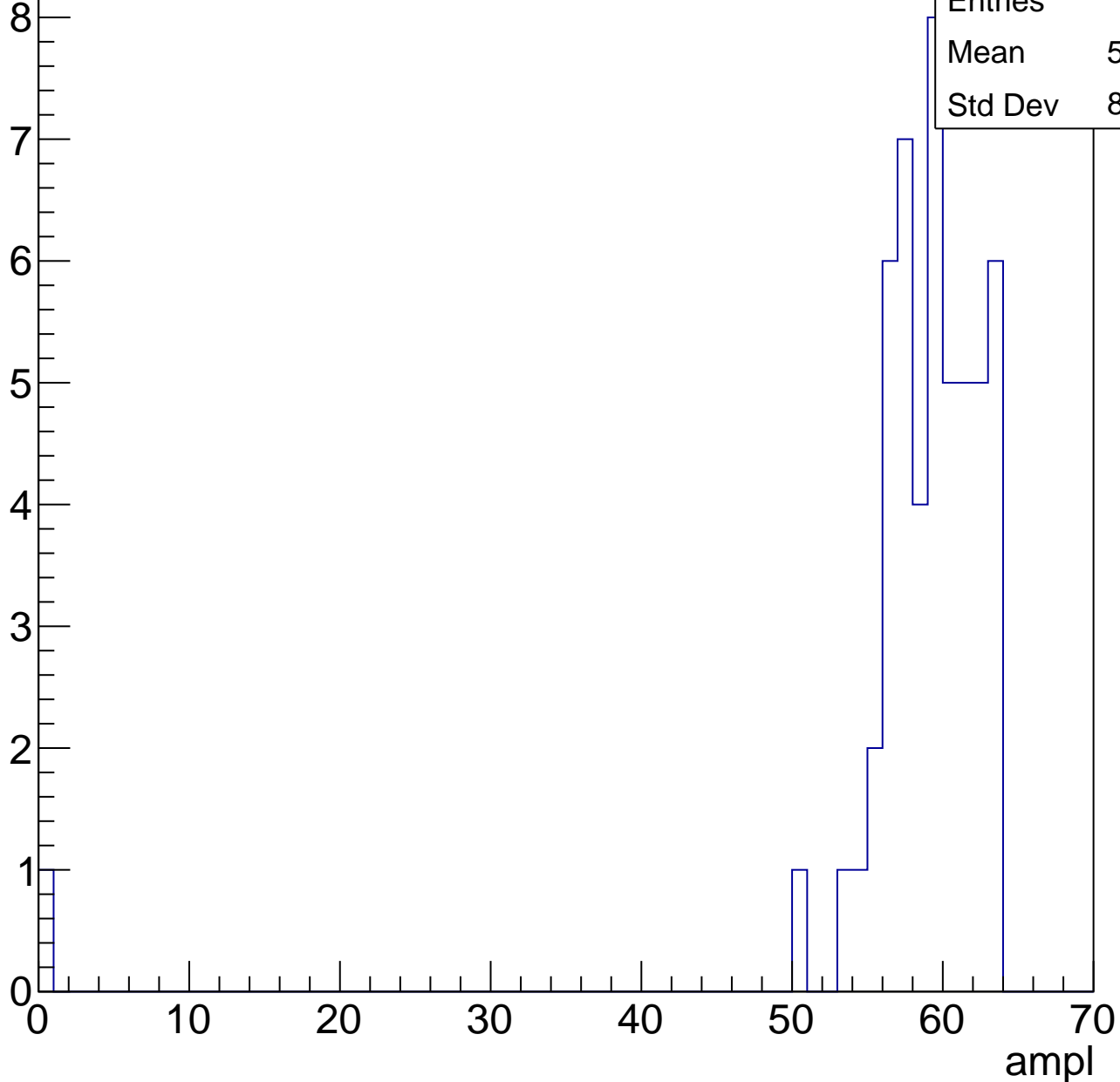


B1L003S, U21-ch108, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	57.67
Std Dev	8.566

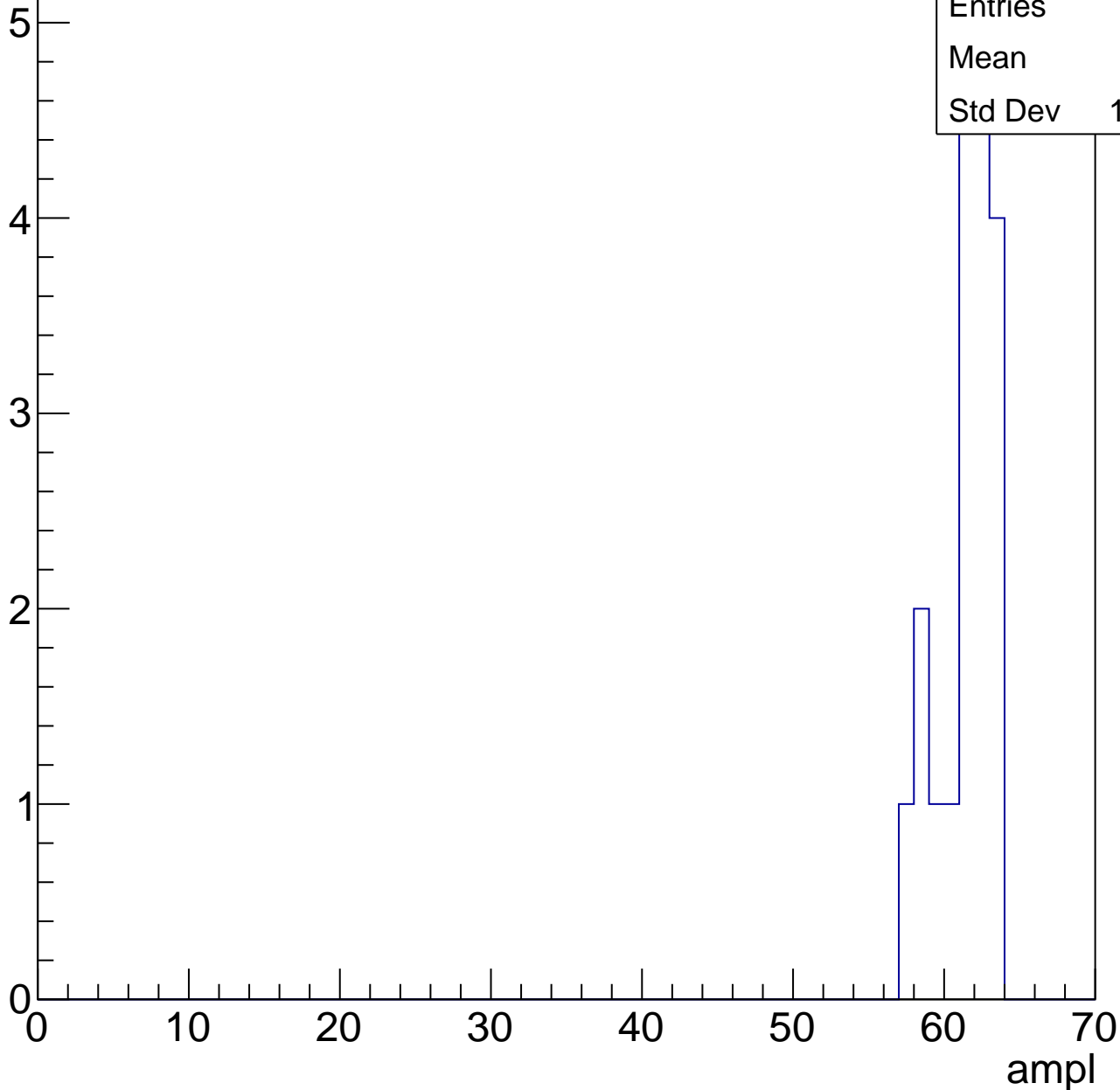


B1L003S, U21-ch108, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	19
Mean	61
Std Dev	1.777



B1L003S, U21-ch108, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch109, adc0

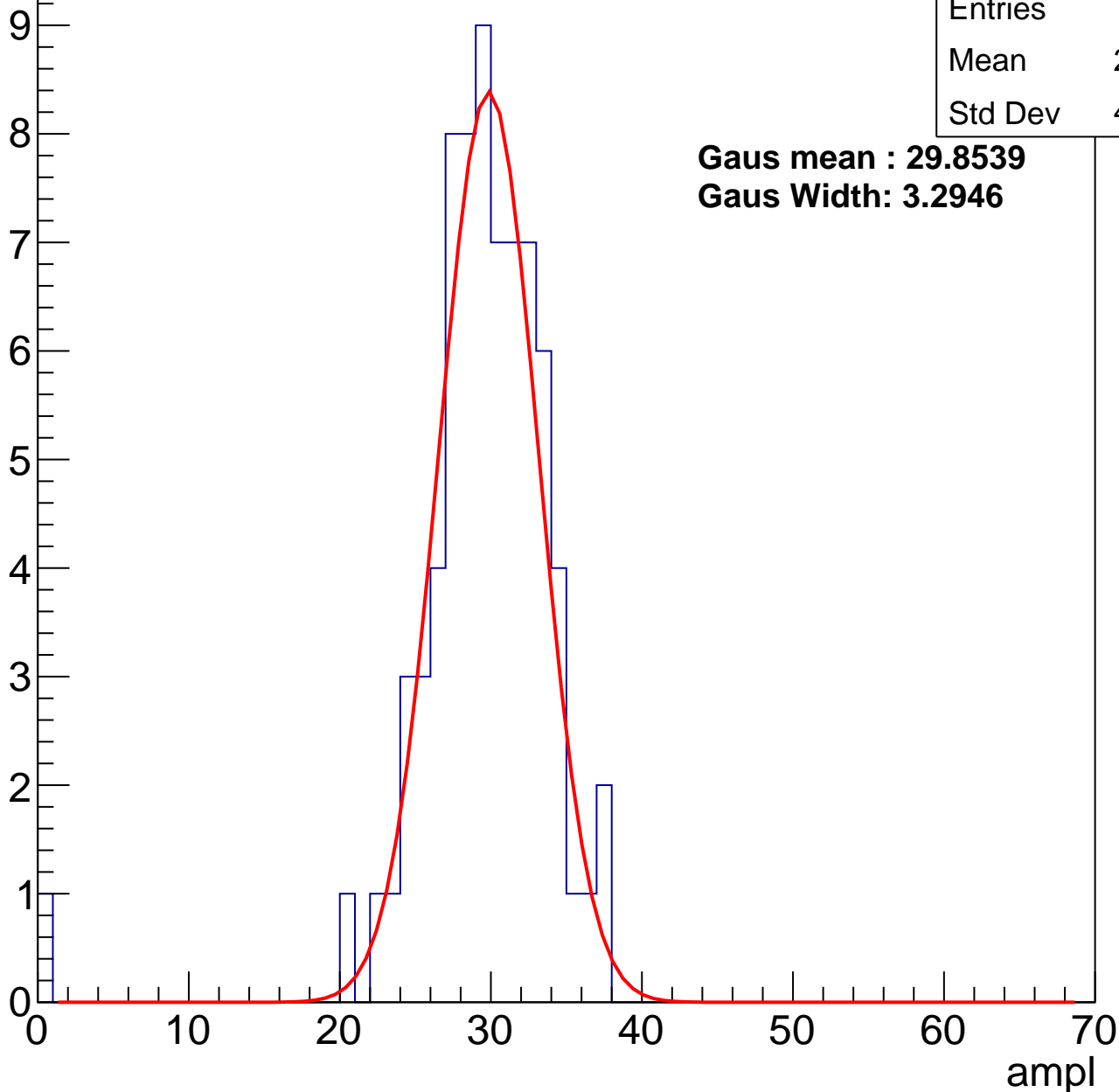
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.01
Std Dev	4.811

Gaus mean : 29.8539

Gaus Width: 3.2946



B1L003S, U21-ch109, adc1

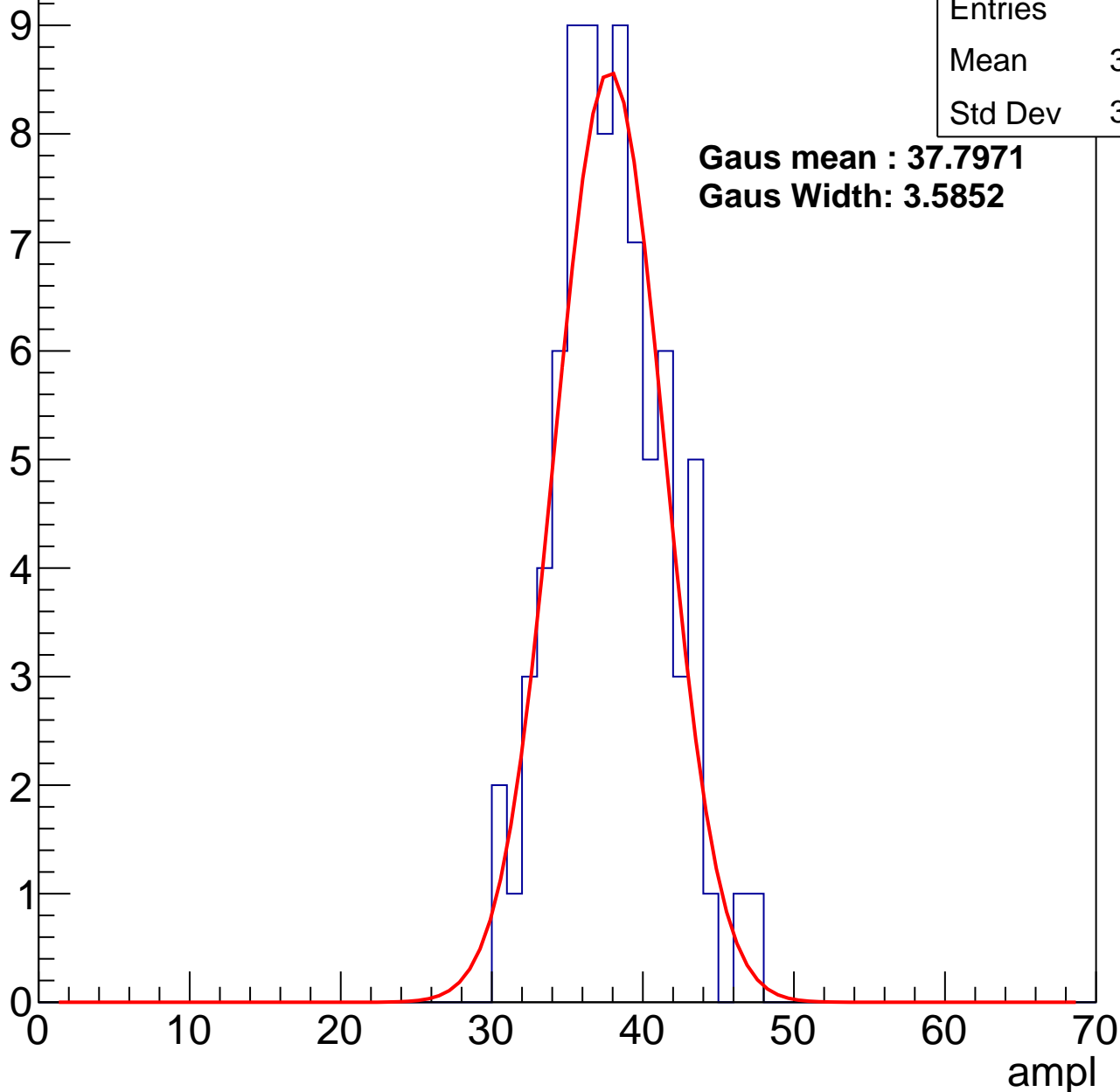
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	80
Mean	37.46
Std Dev	3.567

Gaus mean : 37.7971

Gaus Width: 3.5852



B1L003S, U21-ch109, adc2

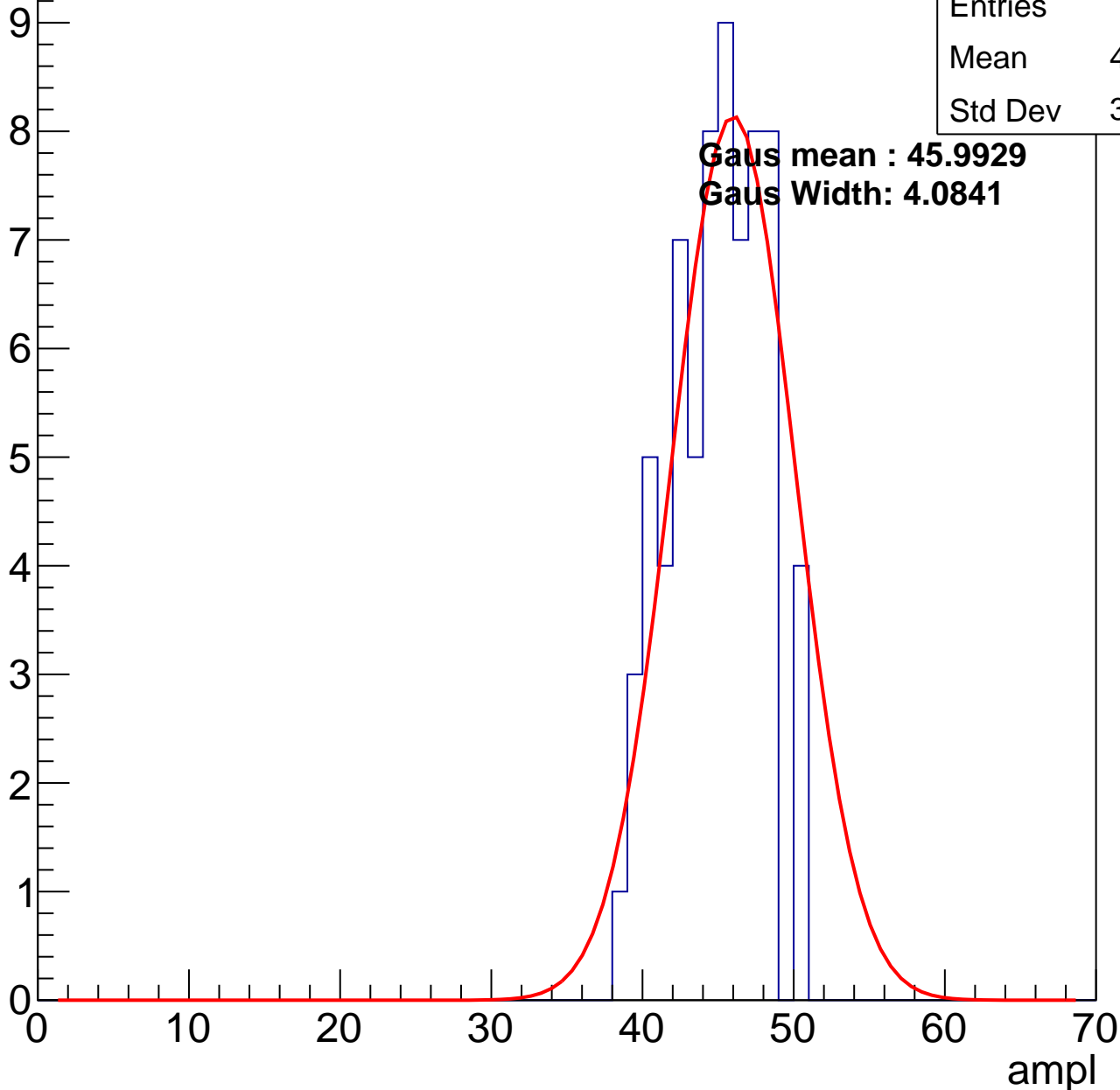
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	44.45
Std Dev	3.015

Gaus mean : 45.9929

Gaus Width: 4.0841

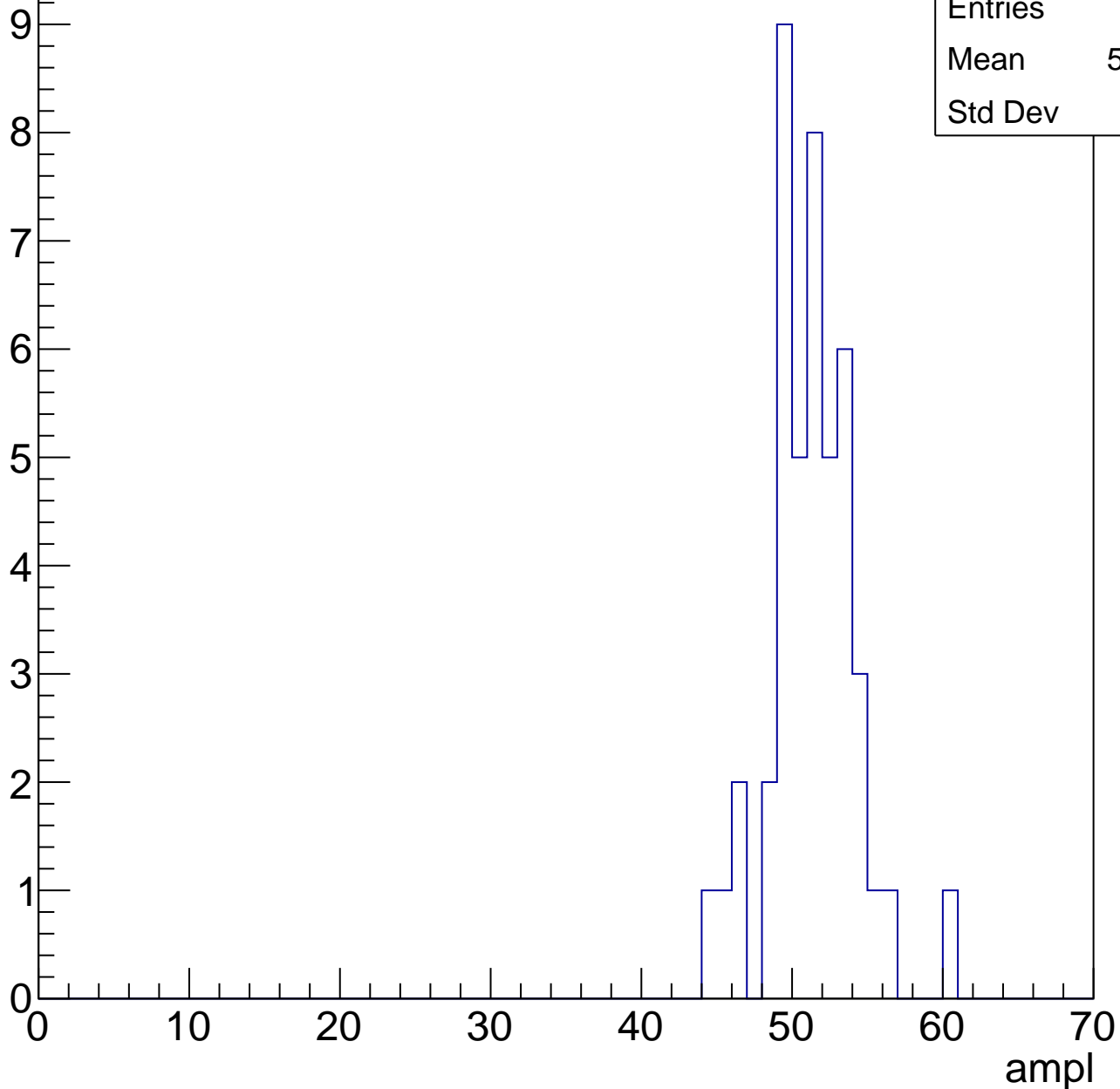


B1L003S, U21-ch109, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	50.82
Std Dev	2.87

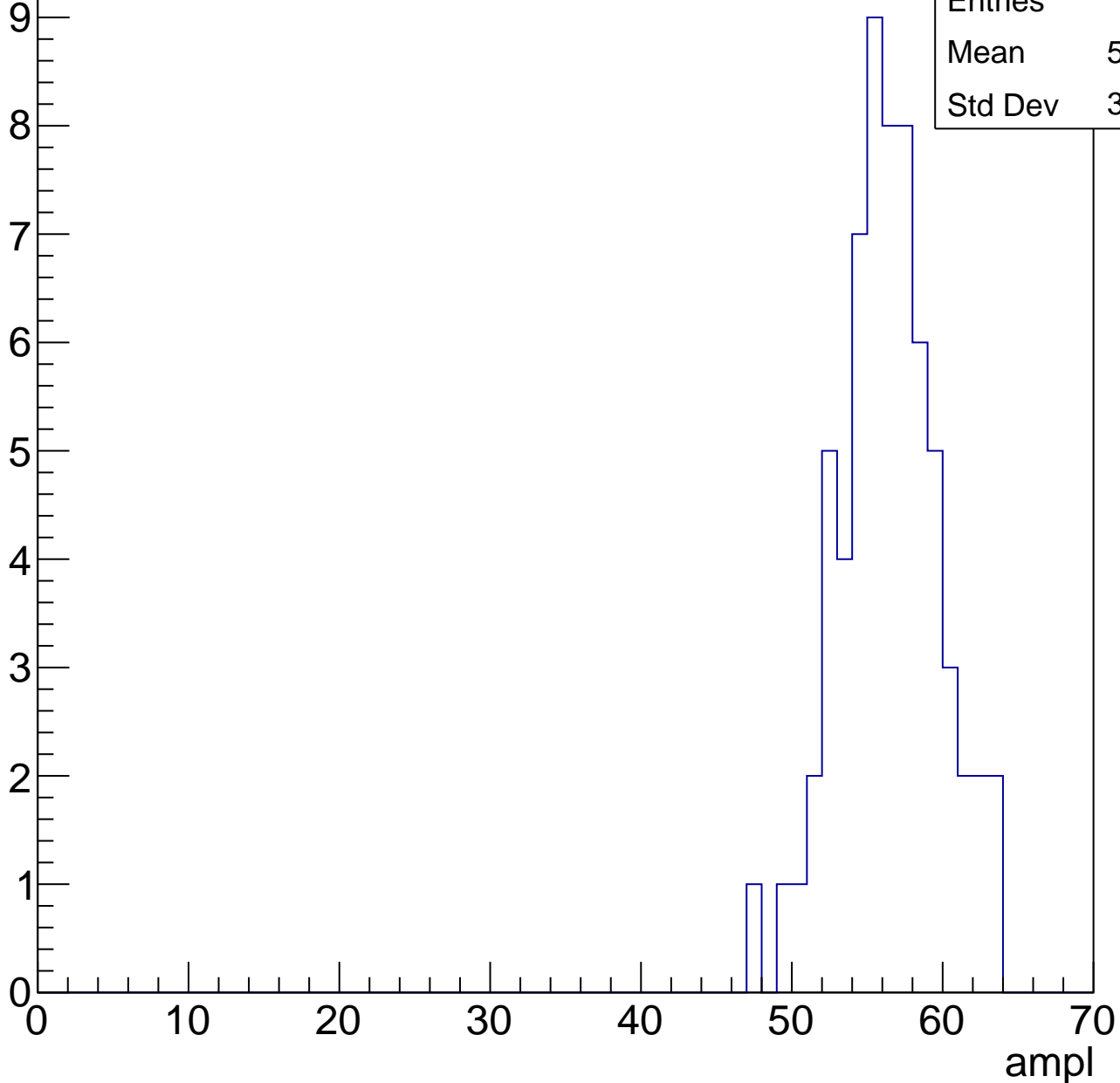


B1L003S, U21-ch109, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	55.94
Std Dev	3.289

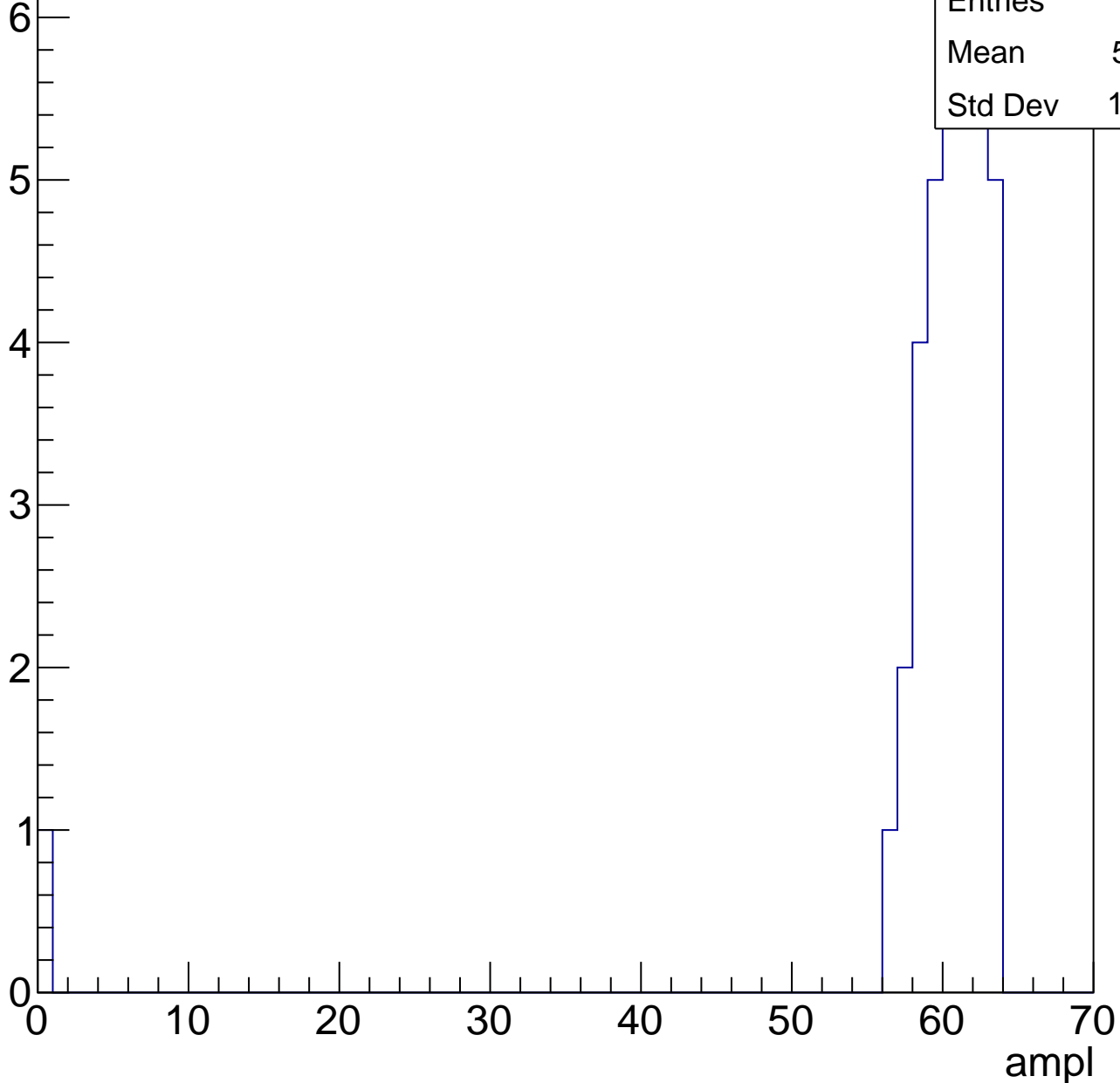


B1L003S, U21-ch109, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	58.61
Std Dev	10.08



B1L003S, U21-ch109, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch109, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch110, adc0

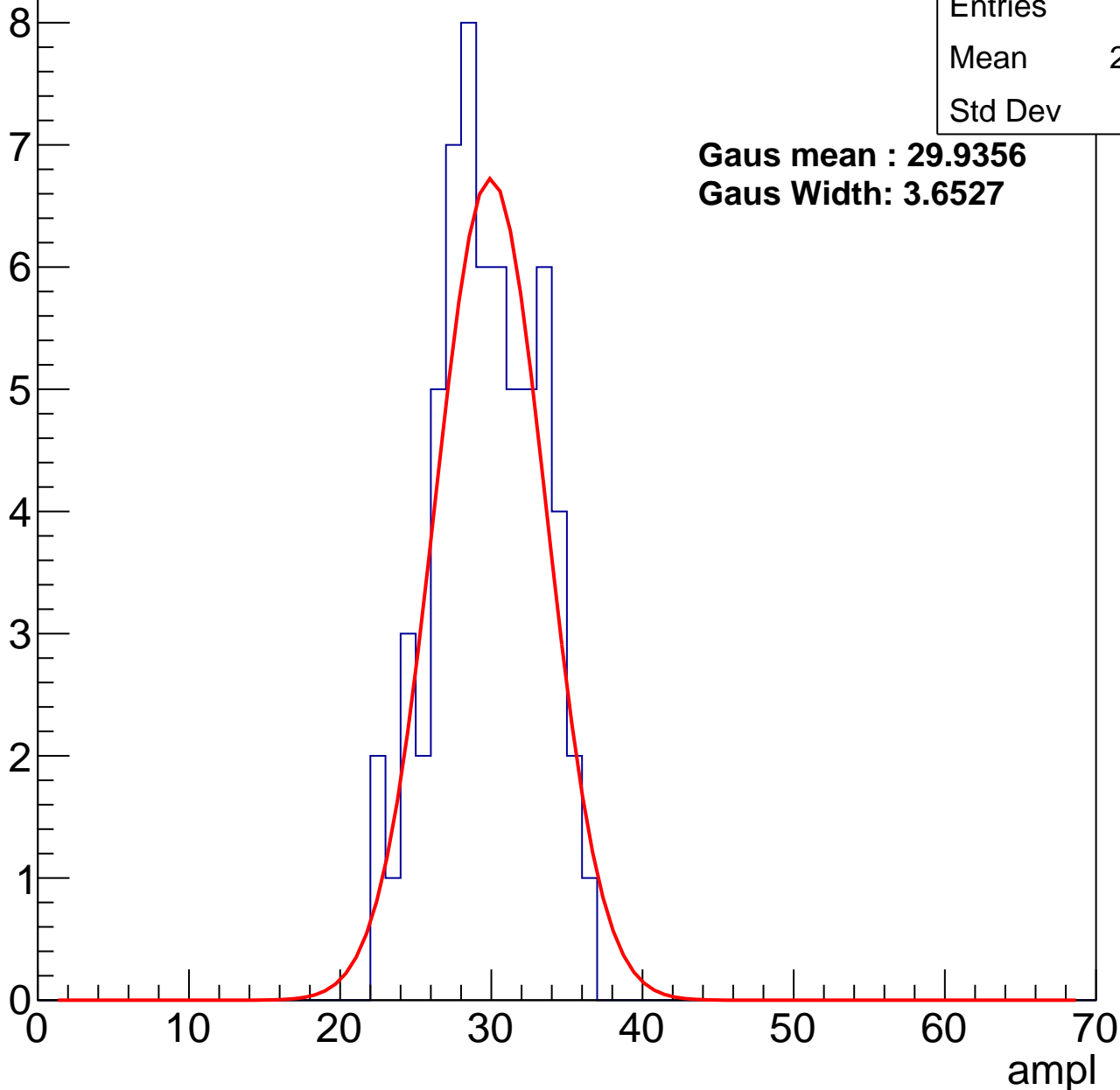
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	29.22
Std Dev	3.35

Gaus mean : 29.9356

Gaus Width: 3.6527



B1L003S, U21-ch110, adc1

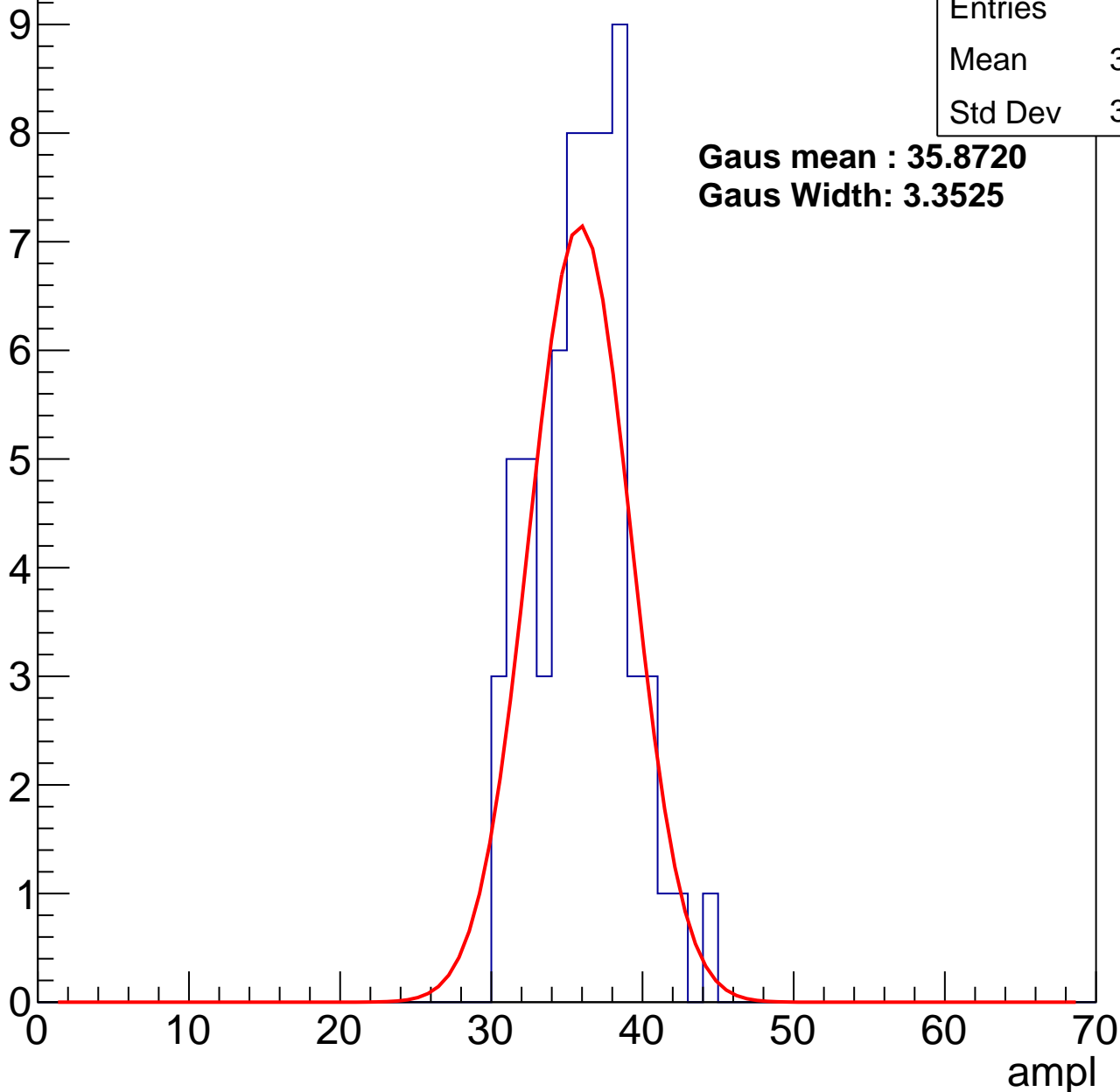
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	35.59
Std Dev	3.076

Gaus mean : 35.8720

Gaus Width: 3.3525



B1L003S, U21-ch110, adc2

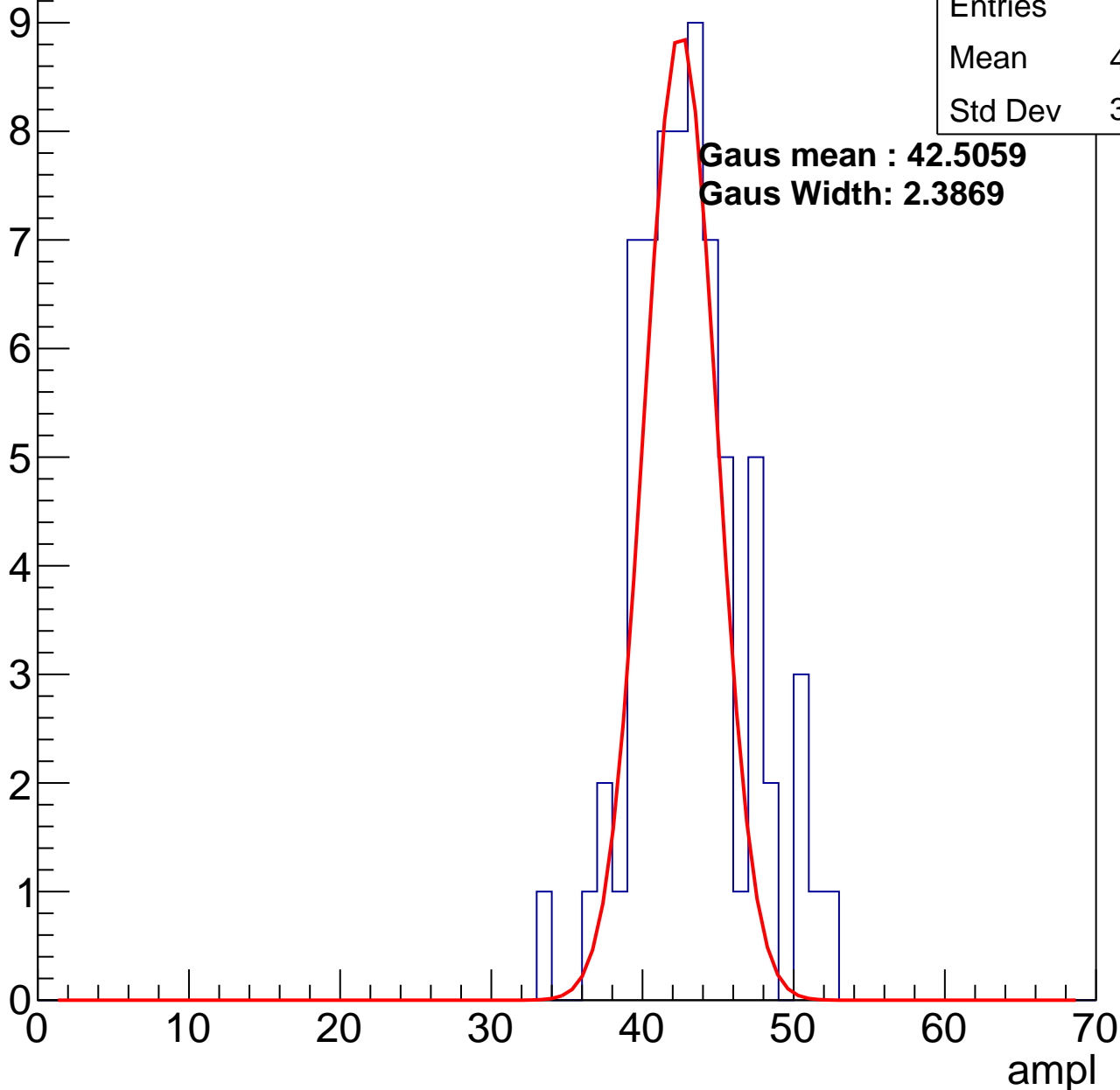
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	42.72
Std Dev	3.639

Gaus mean : 42.5059

Gaus Width: 2.3869

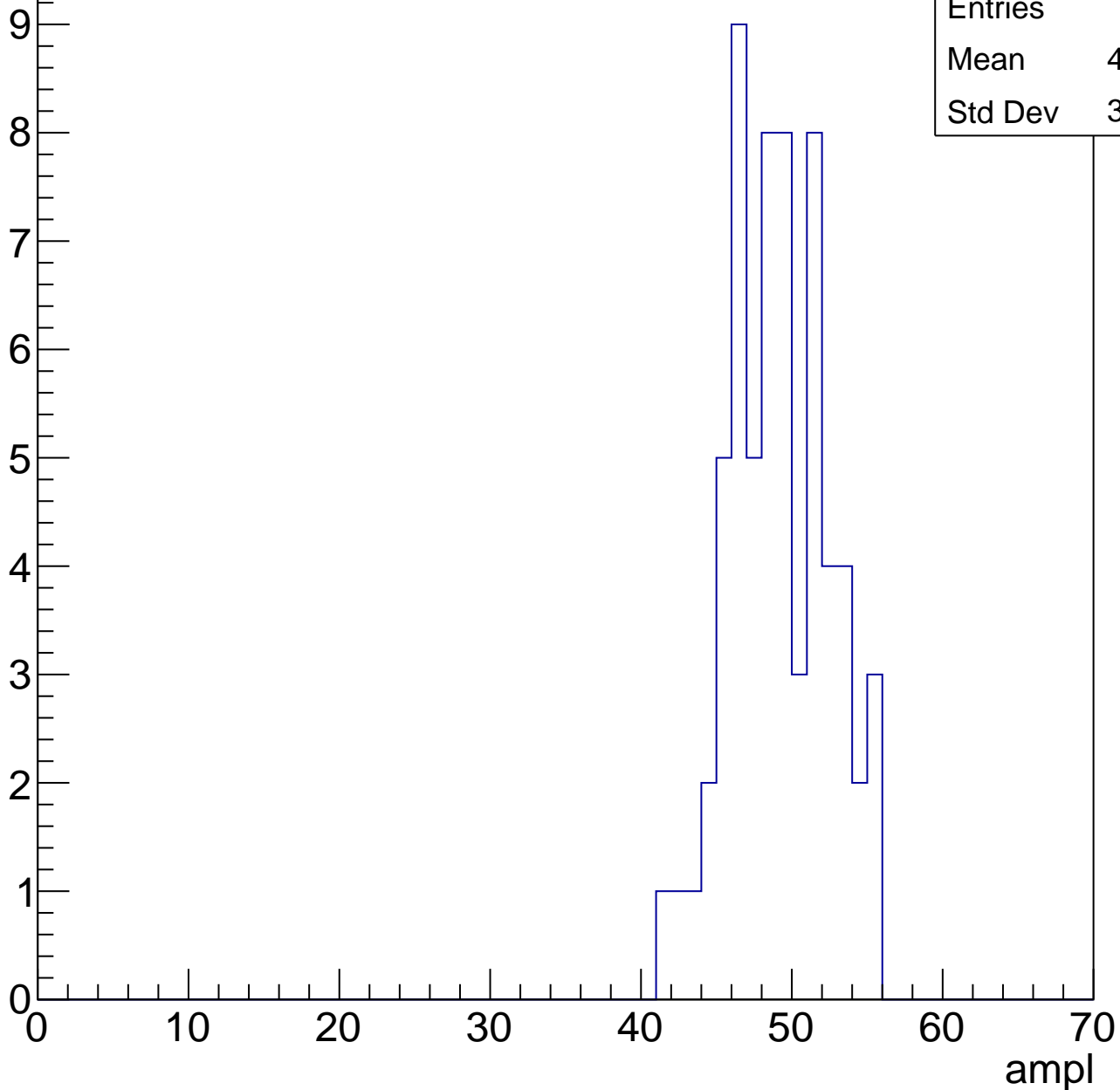


B1L003S, U21-ch110, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.67
Std Dev	3.245

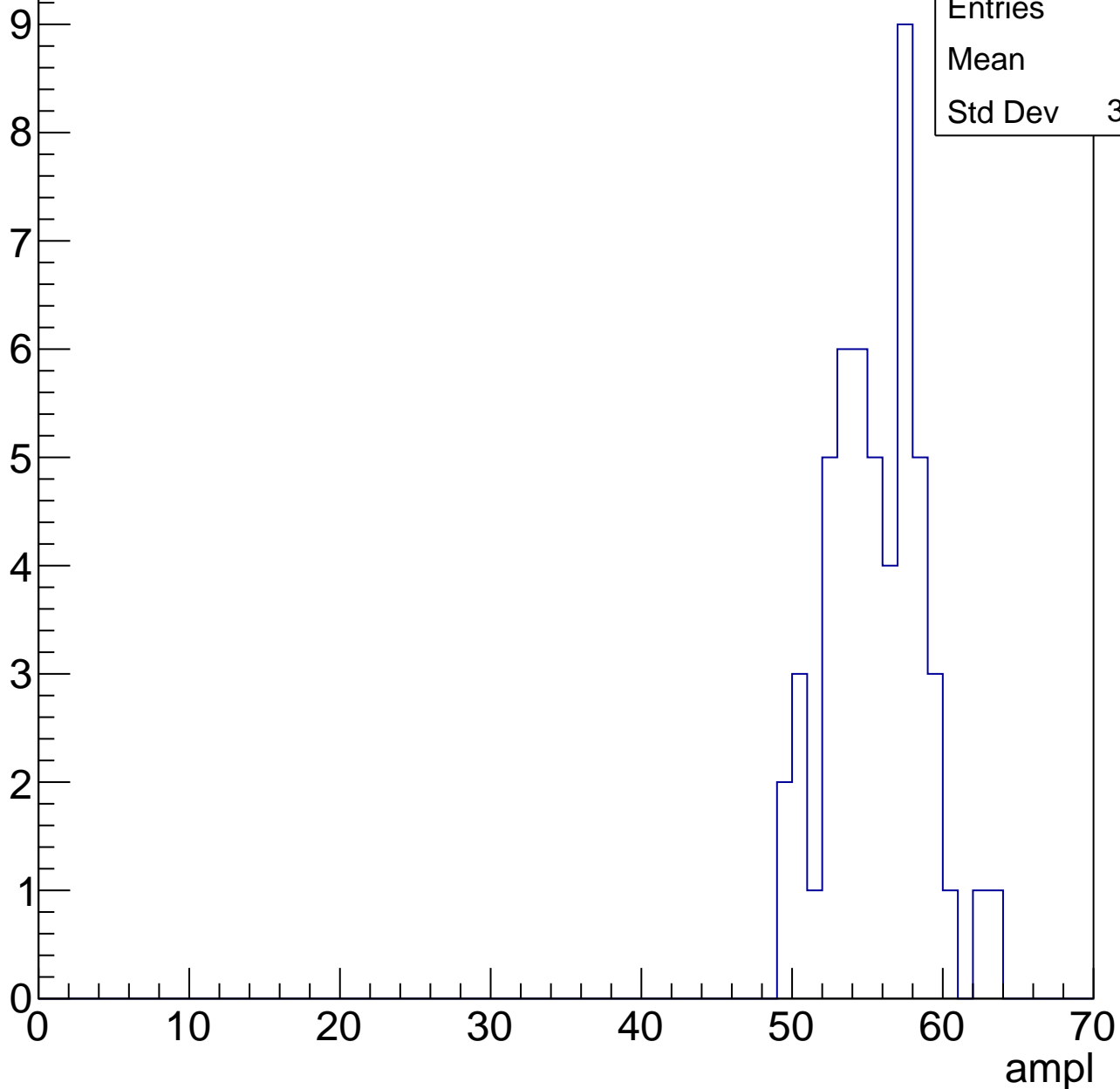


B1L003S, U21-ch110, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.1
Std Dev	3.127

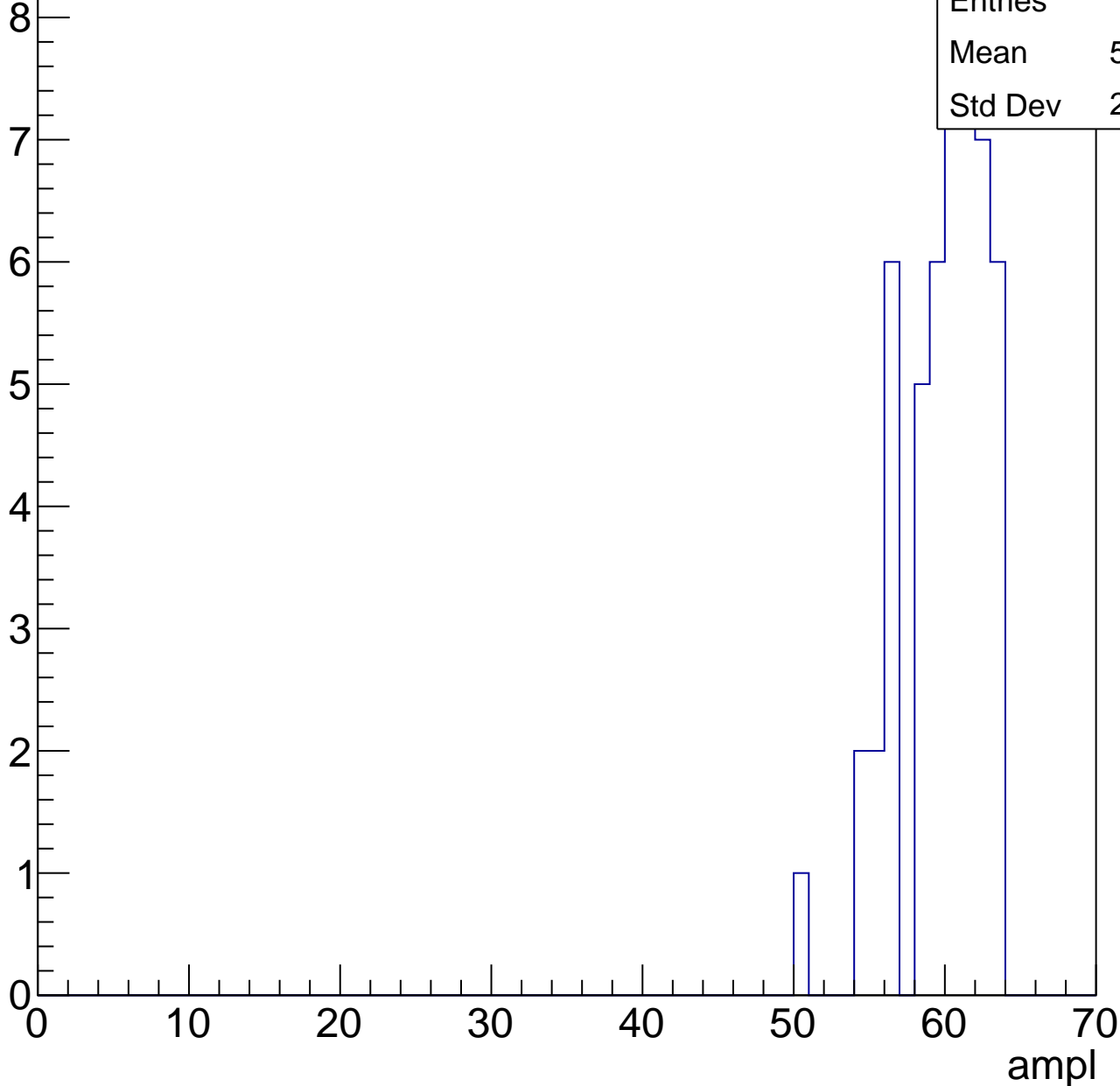


B1L003S, U21-ch110, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

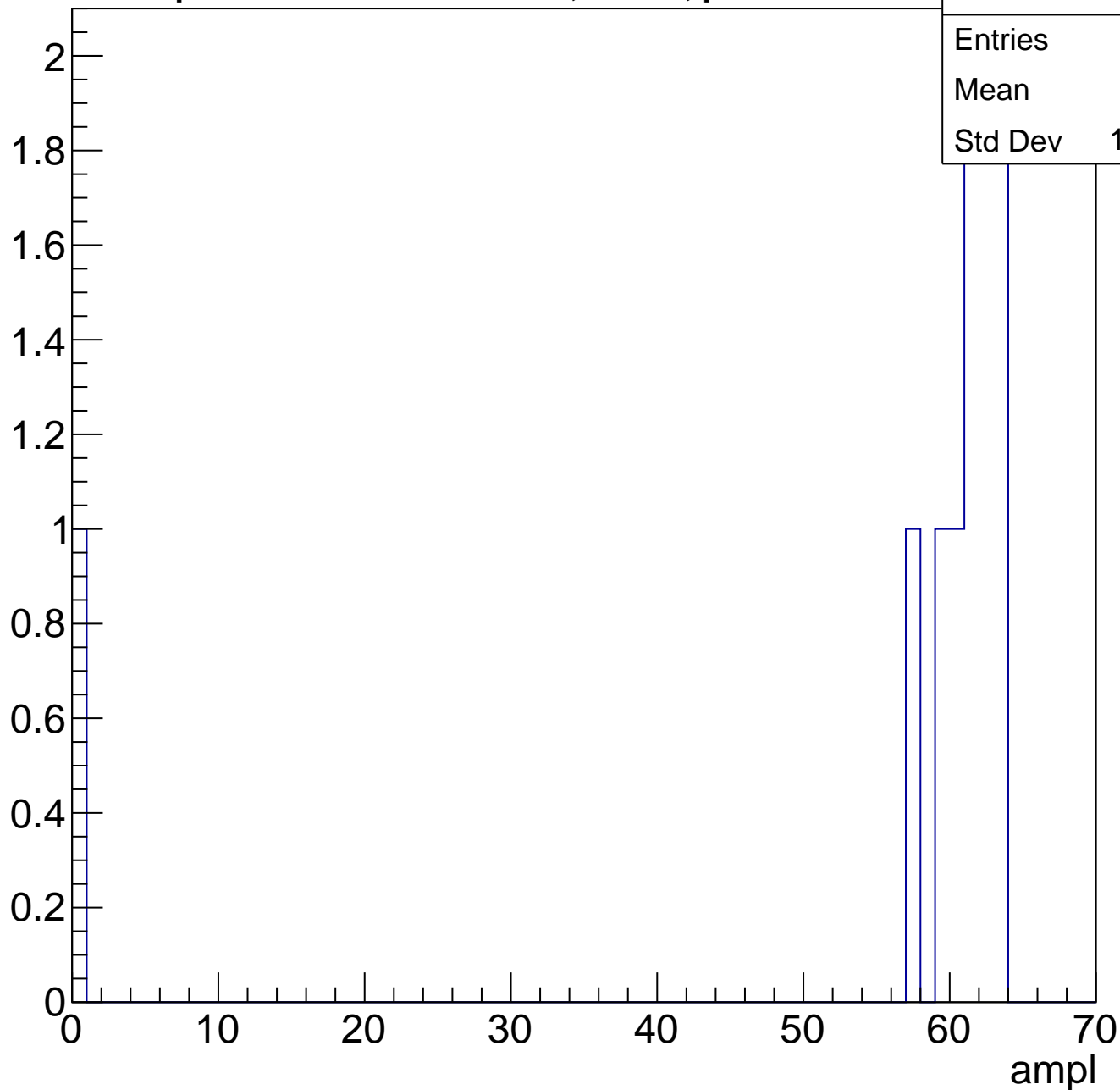
Entries	51
Mean	59.37
Std Dev	2.849



B1L003S, U21-ch110, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch110, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch111, adc0

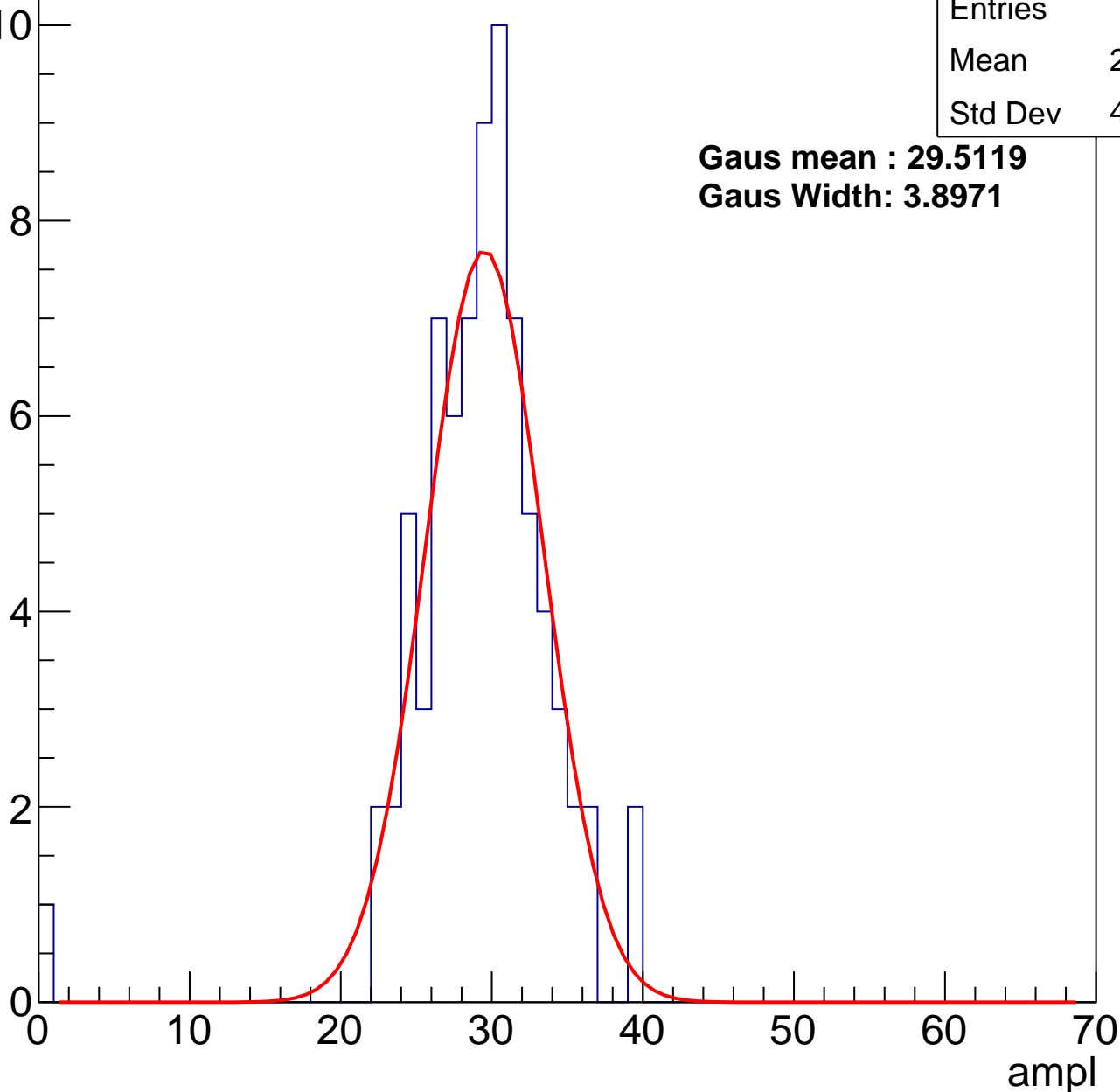
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	28.79
Std Dev	4.924

Gaus mean : 29.5119

Gaus Width: 3.8971



B1L003S, U21-ch111, adc1

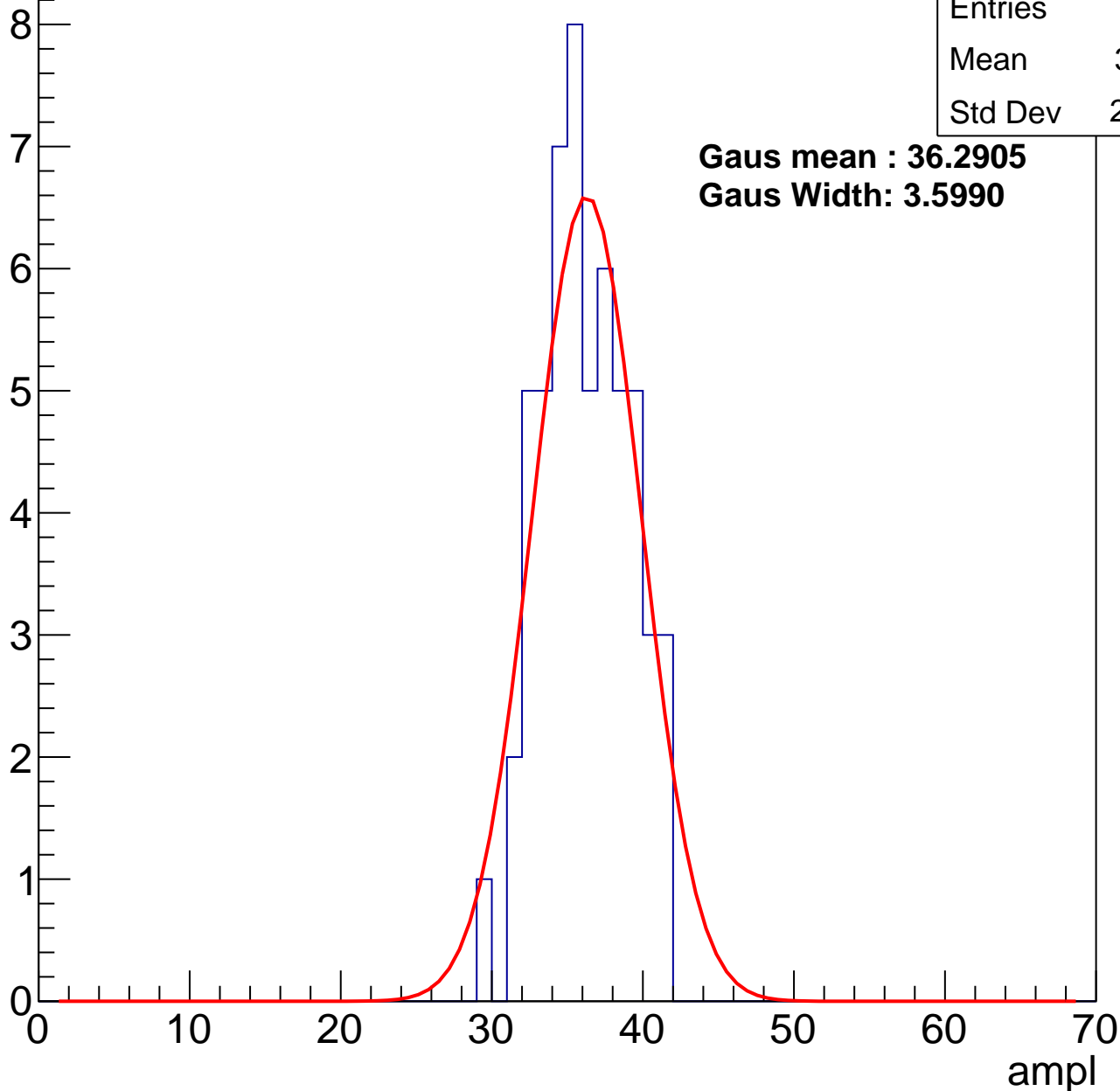
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	35.71
Std Dev	2.858

Gaus mean : 36.2905

Gaus Width: 3.5990



B1L003S, U21-ch111, adc2

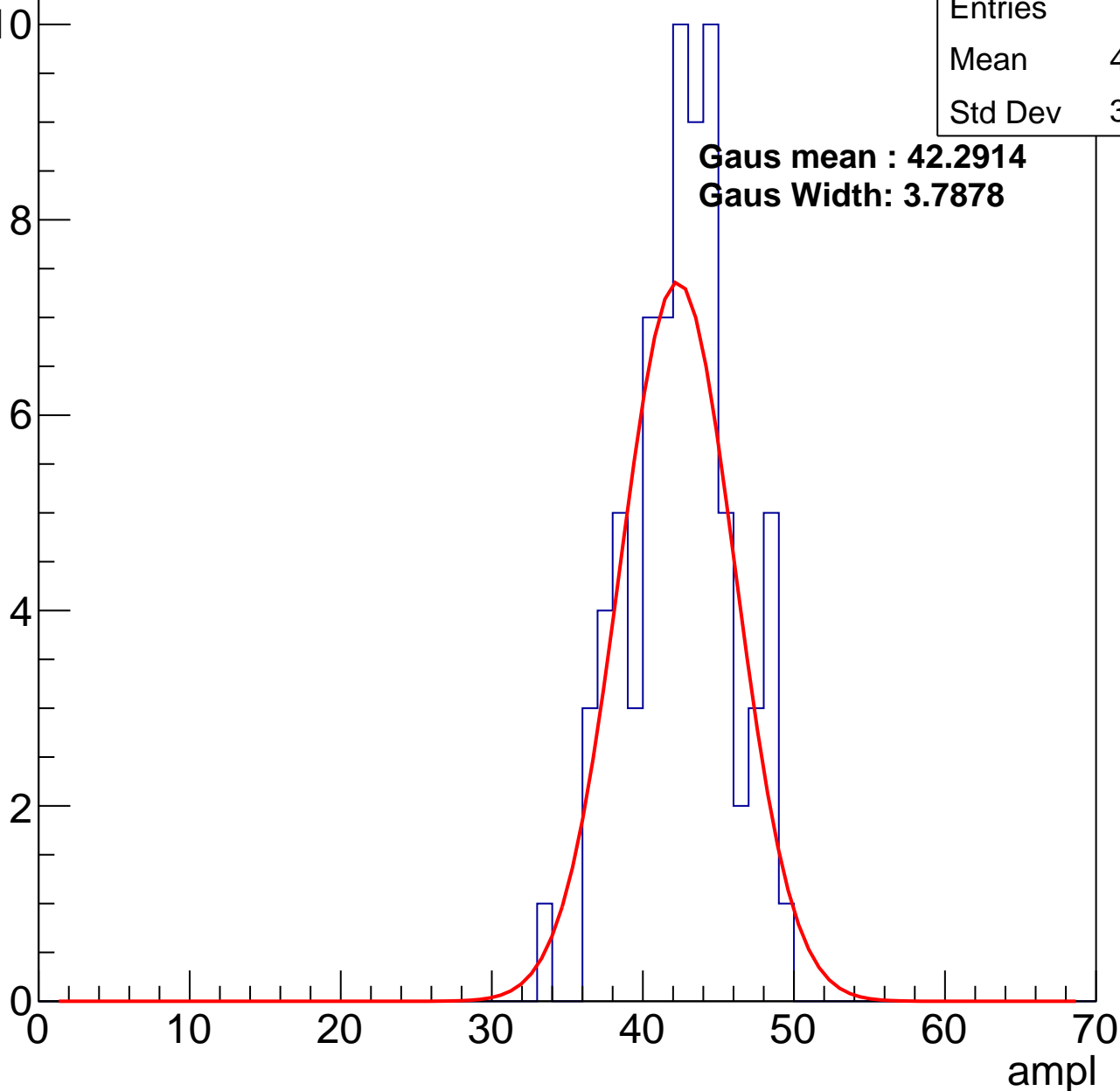
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	75
Mean	42.09
Std Dev	3.387

Gaus mean : 42.2914

Gaus Width: 3.7878

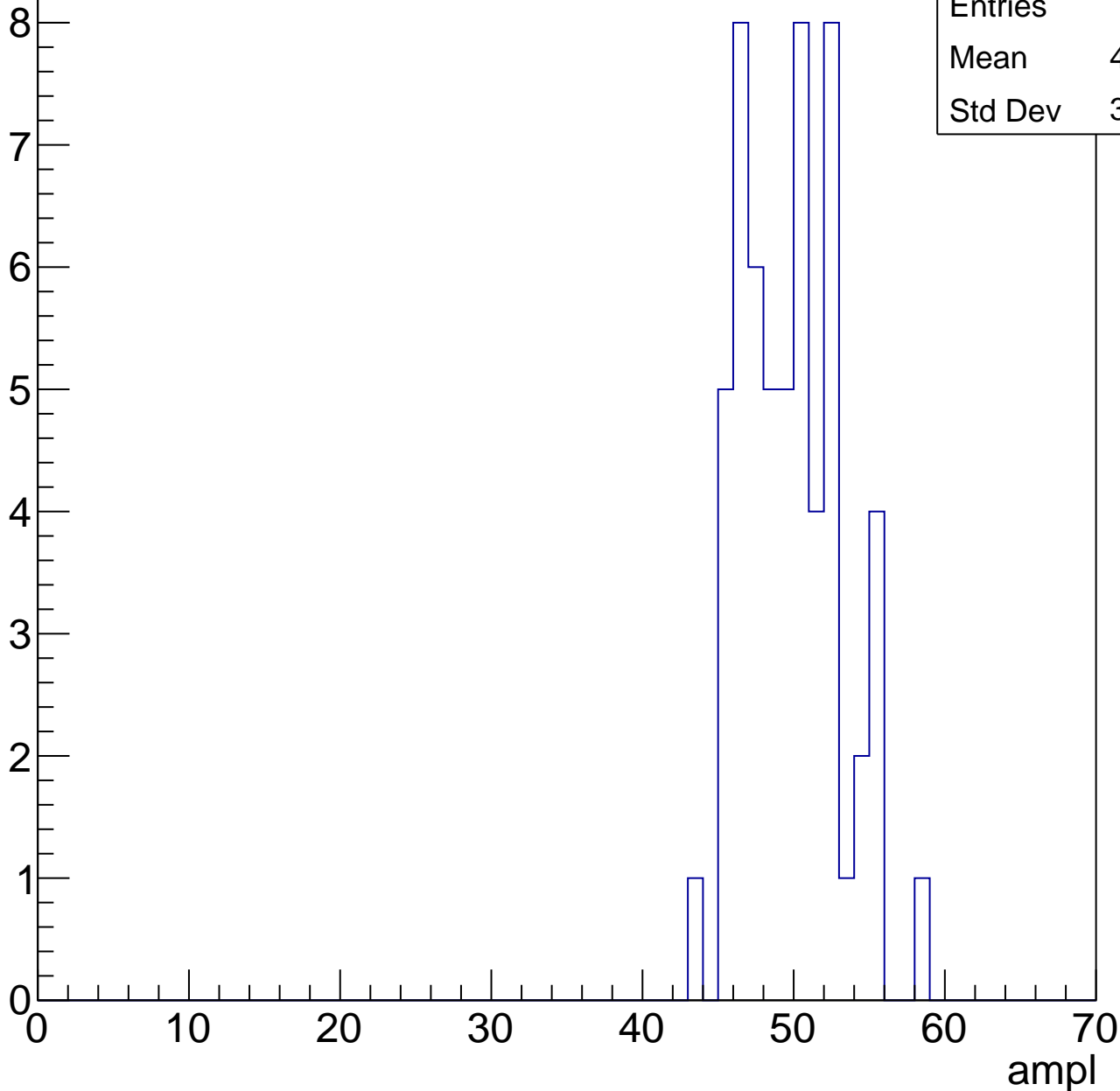


B1L003S, U21-ch111, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	49.34
Std Dev	3.209

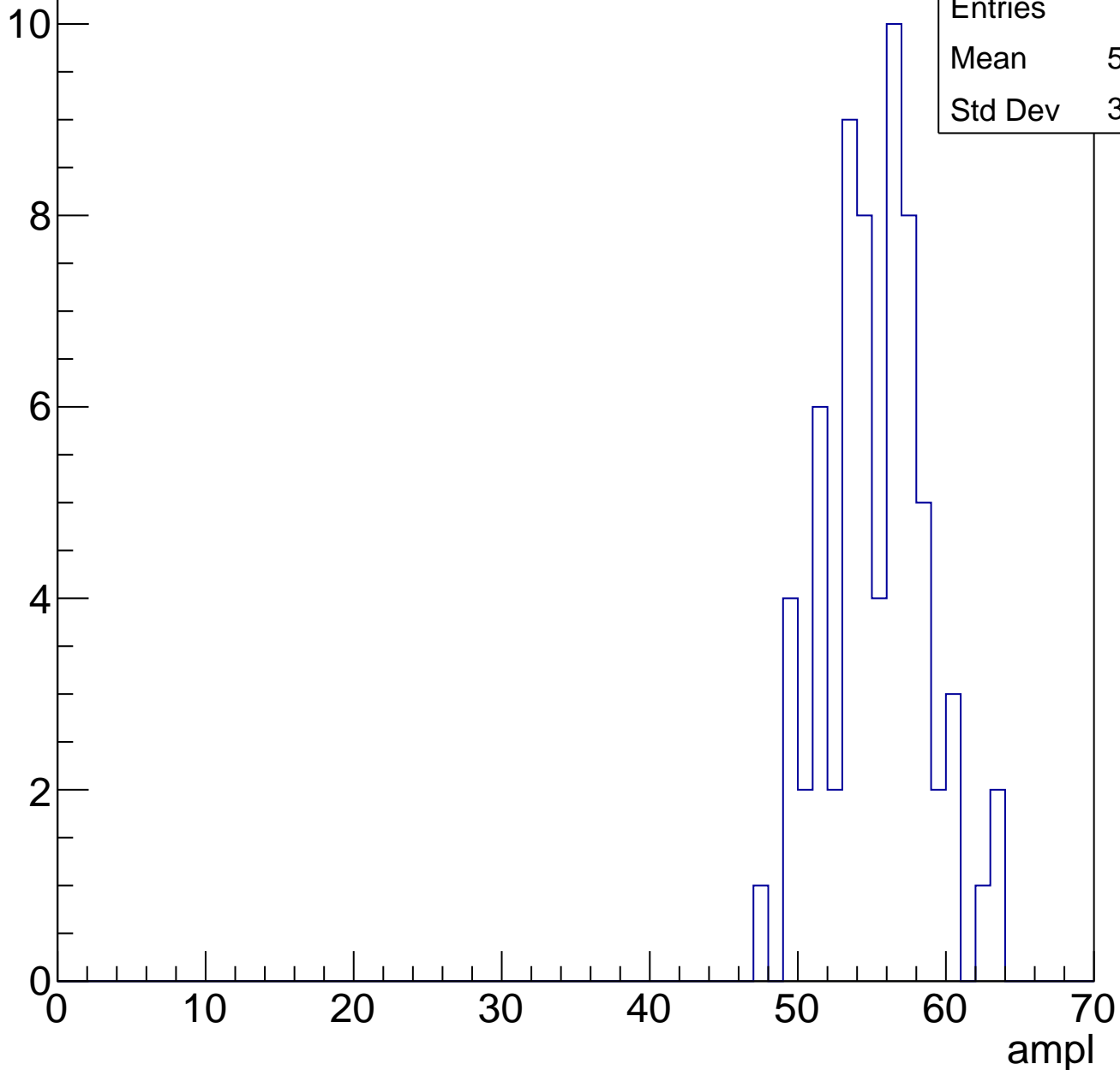


B1L003S, U21-ch111, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	54.84
Std Dev	3.423

Entry

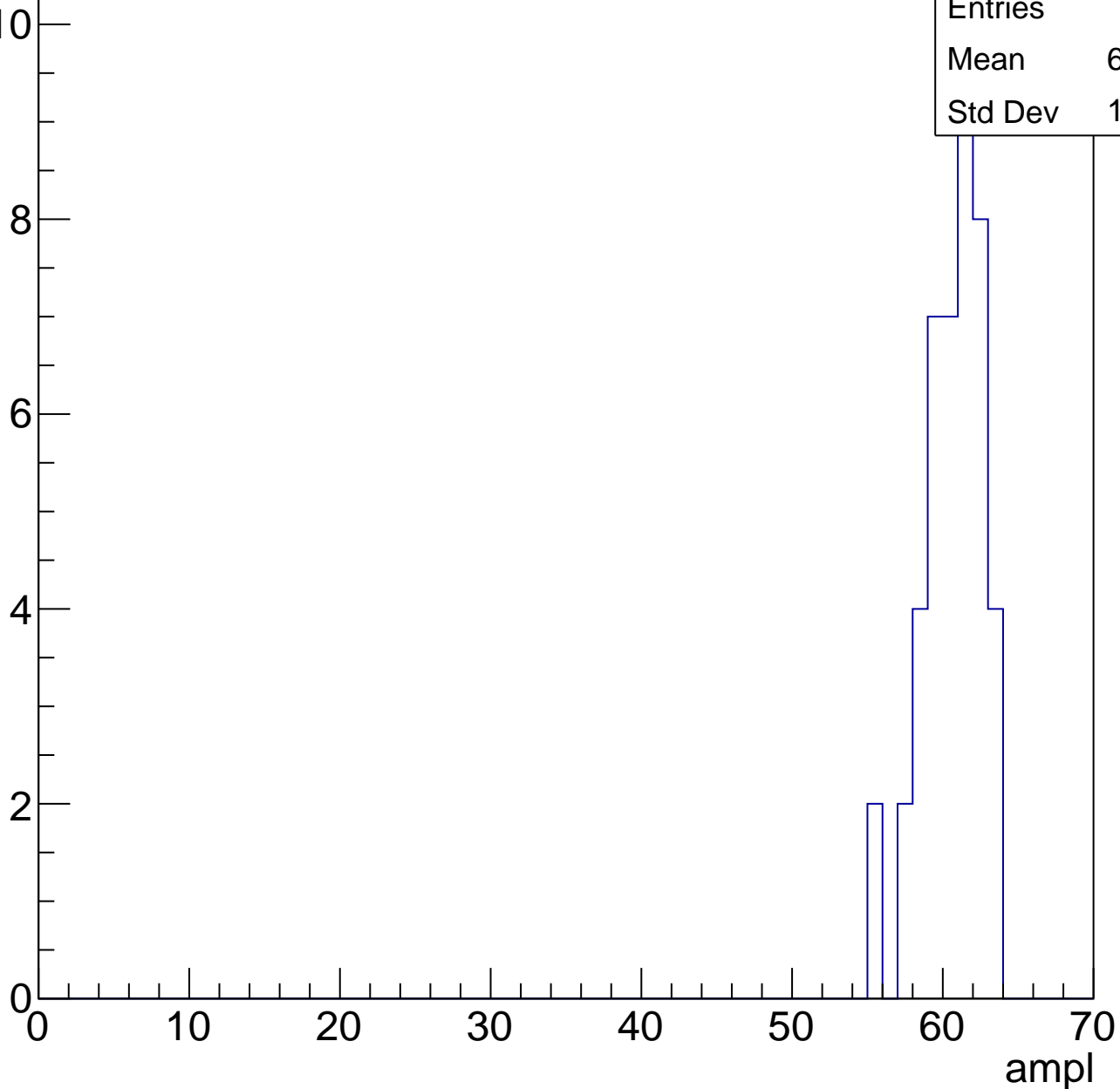


B1L003S, U21-ch111, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

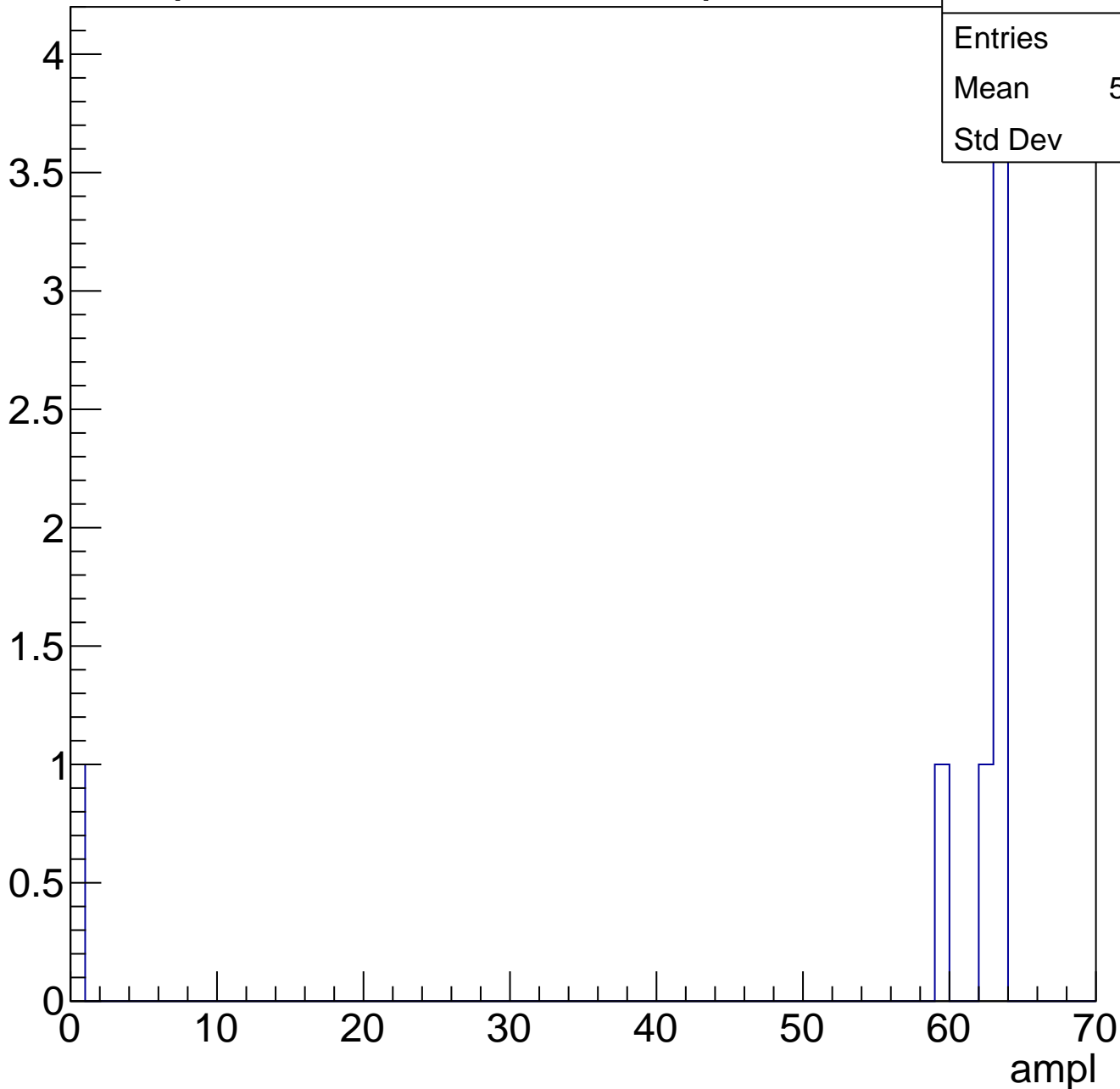
Entries	44
Mean	60.16
Std Dev	1.953



B1L003S, U21-ch111, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

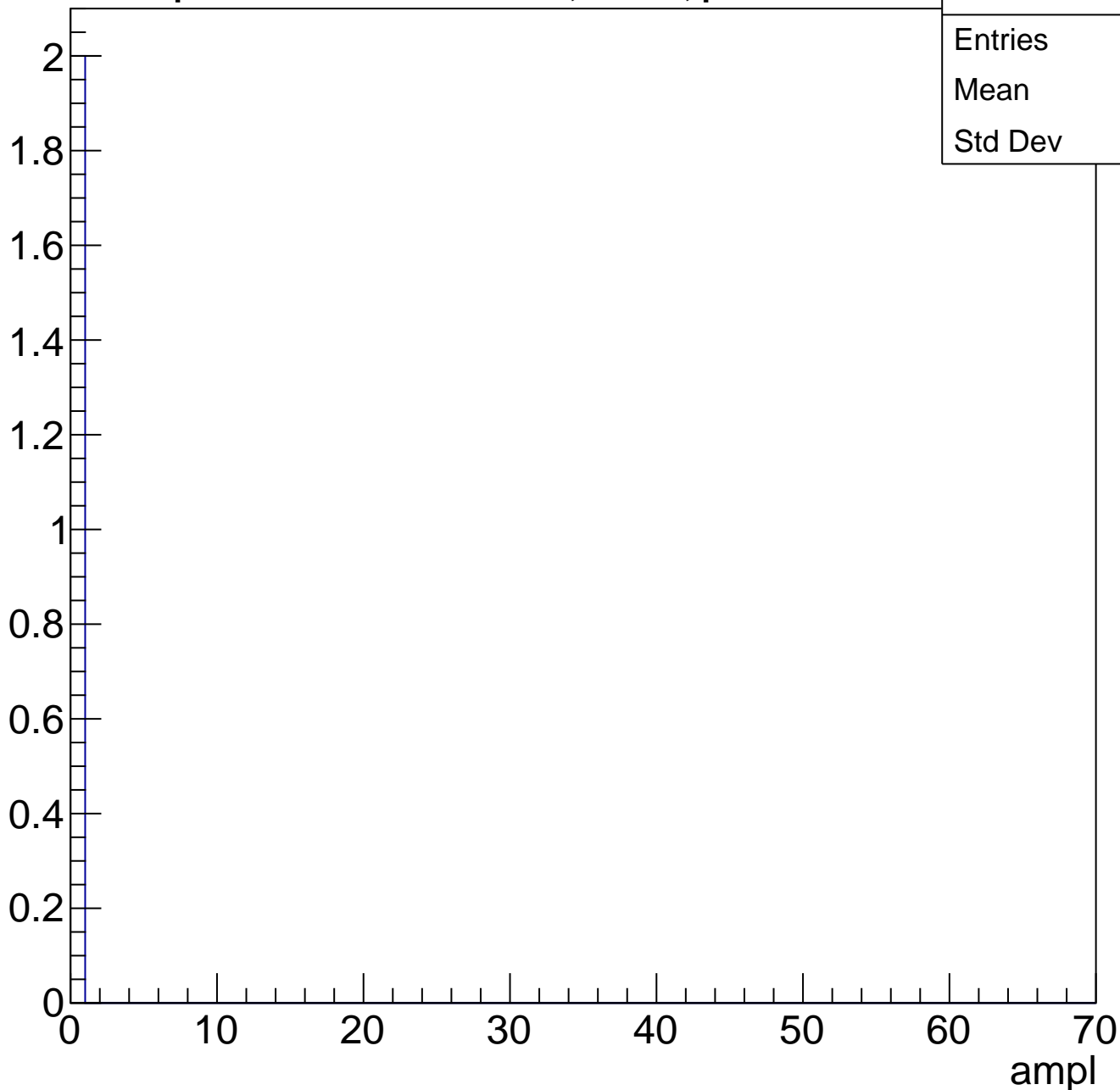


Entries	7
Mean	53.29
Std Dev	21.8

B1L003S, U21-ch111, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch112, adc0

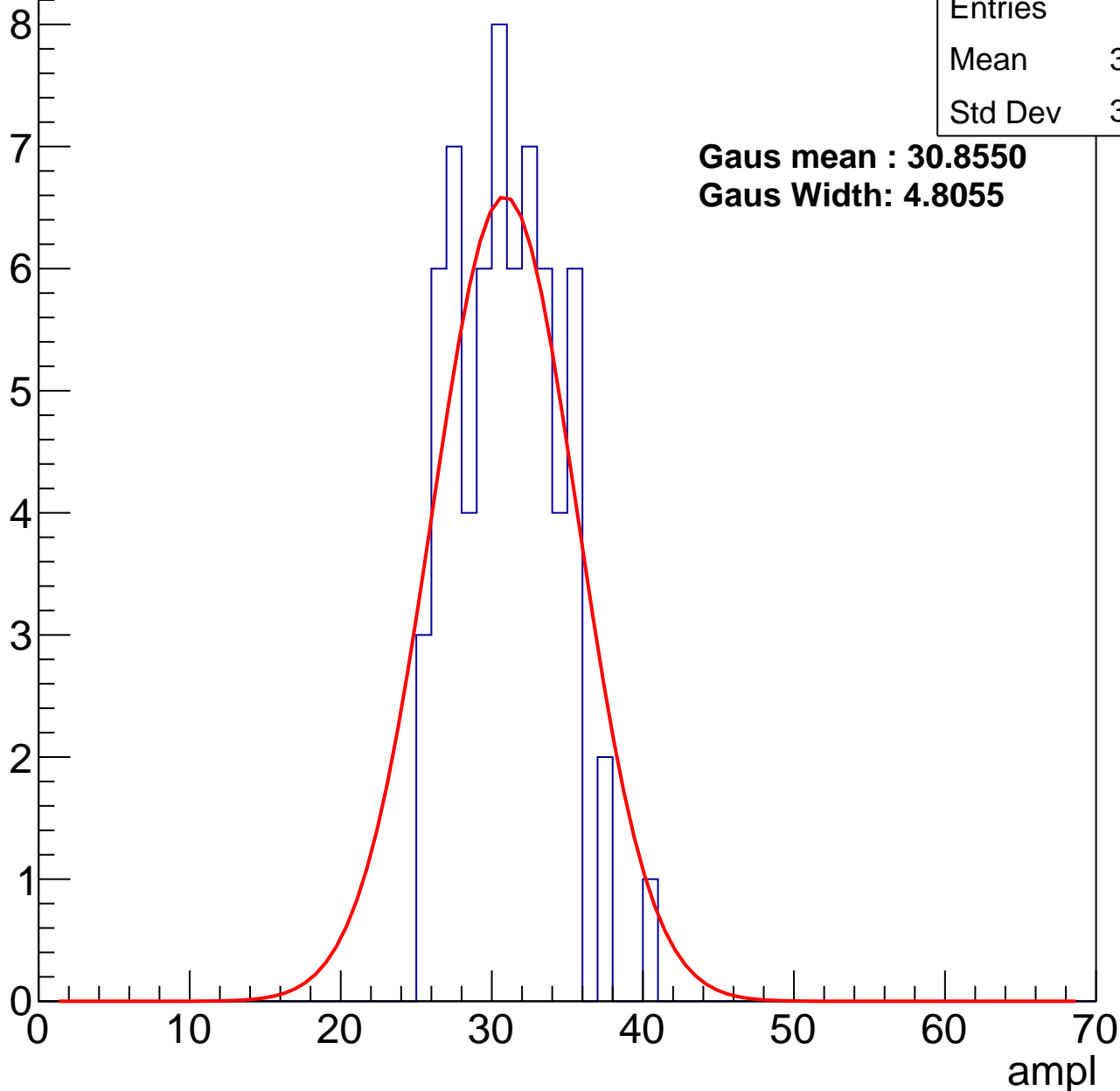
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	30.52
Std Dev	3.345

Gaus mean : 30.8550

Gaus Width: 4.8055



B1L003S, U21-ch112, adc1

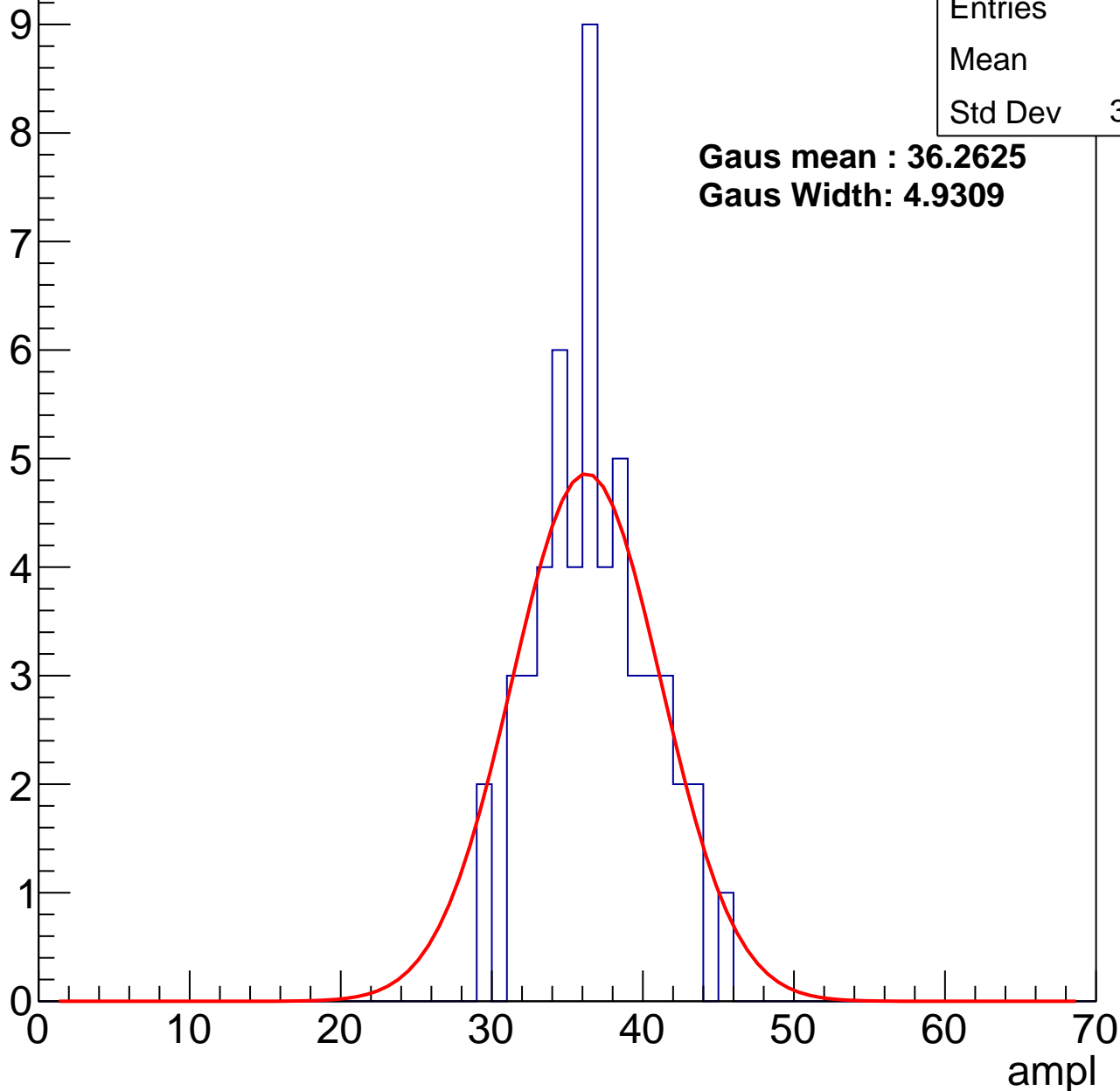
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	36.3
Std Dev	3.614

Gaus mean : 36.2625

Gaus Width: 4.9309



B1L003S, U21-ch112, adc2

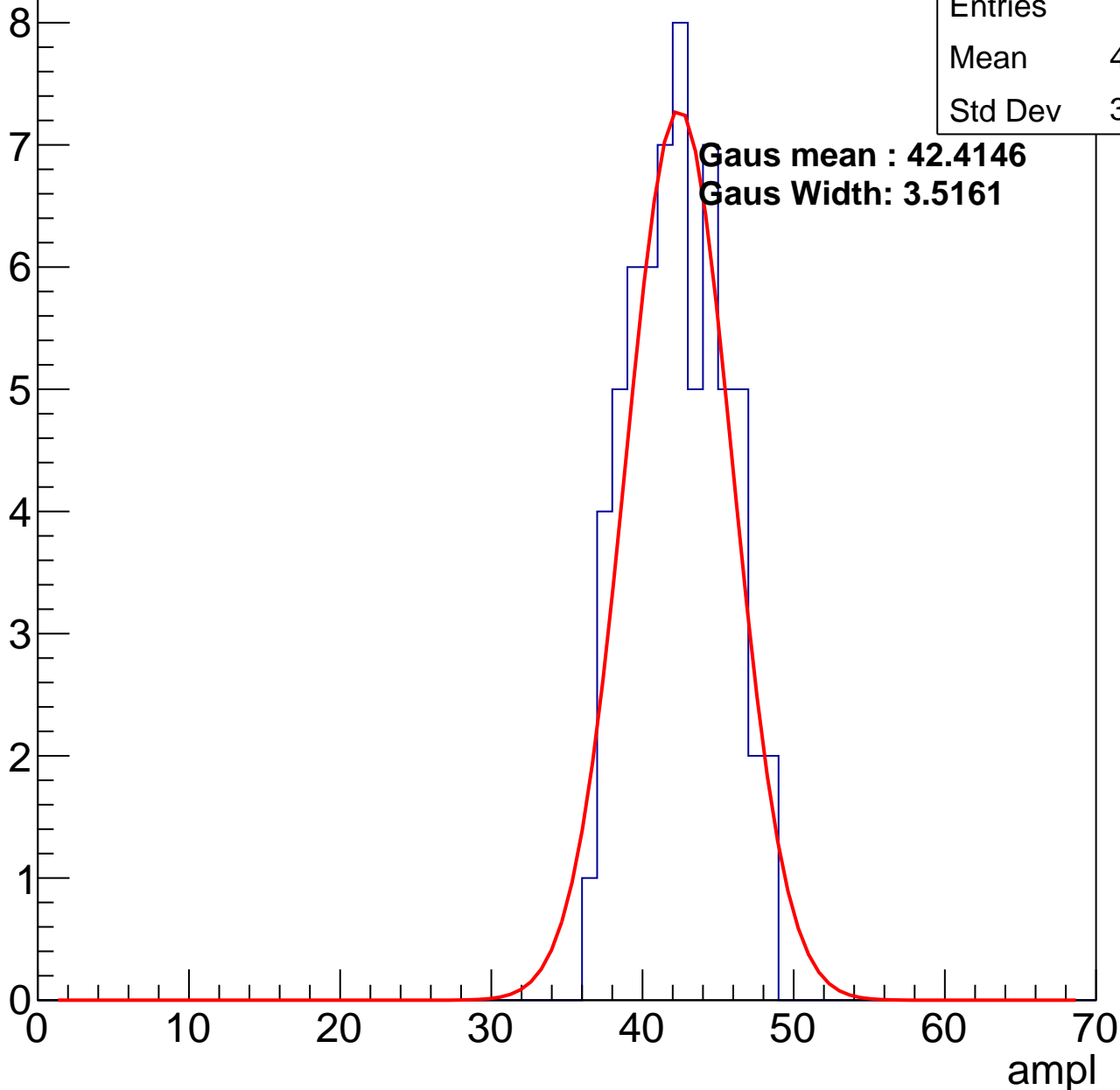
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	41.89
Std Dev	3.035

Gaus mean : 42.4146

Gaus Width: 3.5161

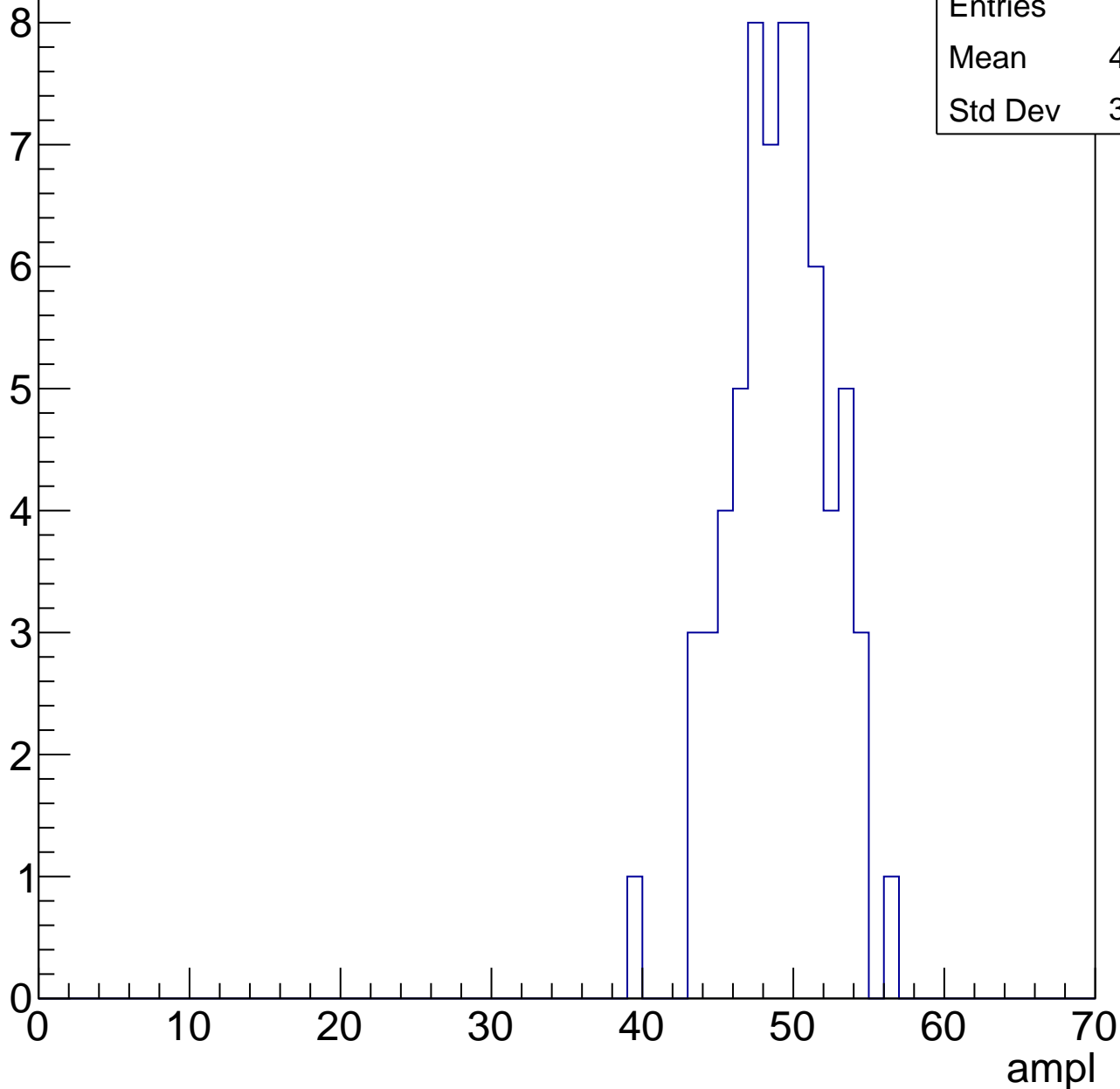


B1L003S, U21-ch112, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	48.65
Std Dev	3.245

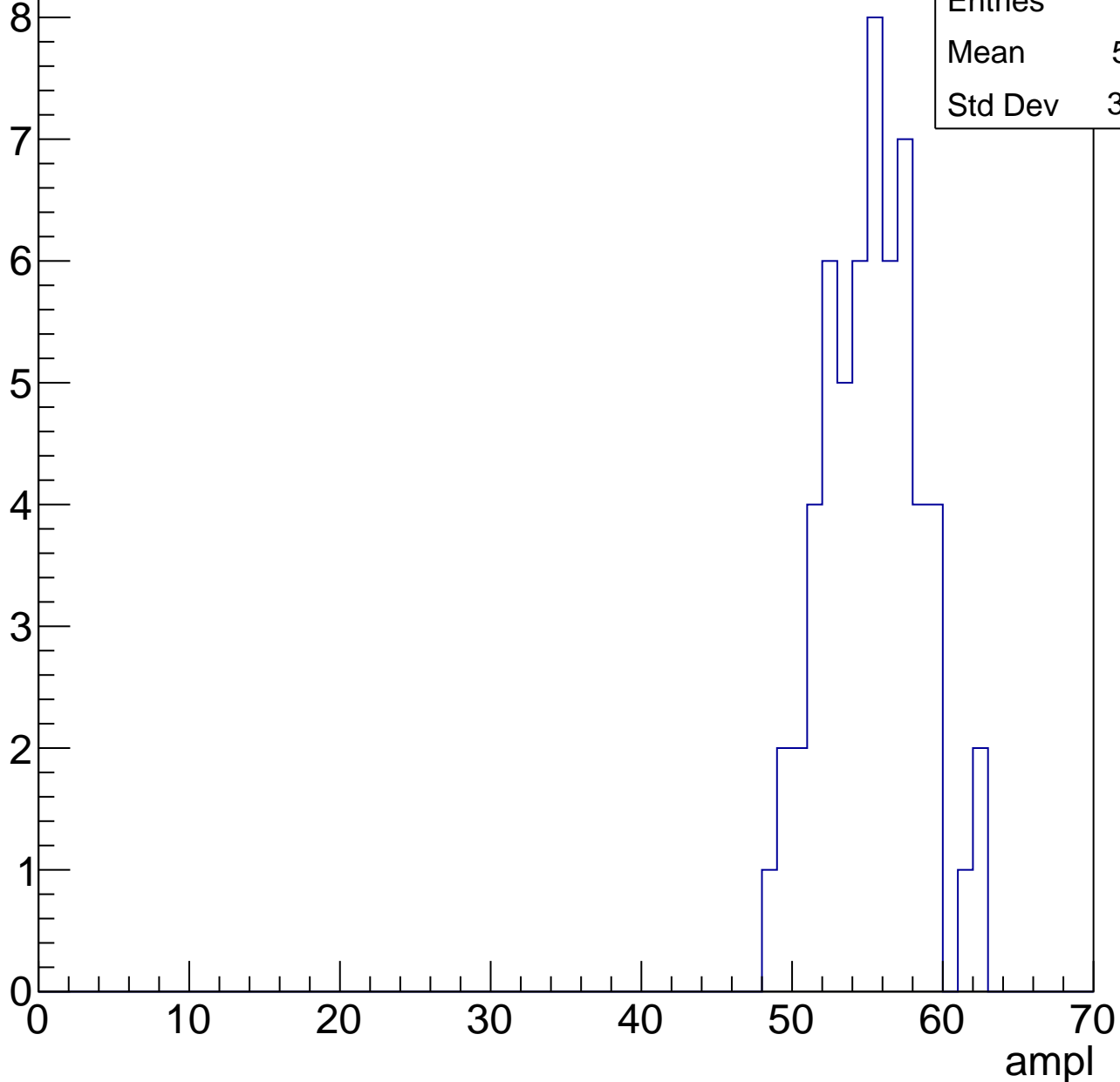


B1L003S, U21-ch112, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	54.81
Std Dev	3.165

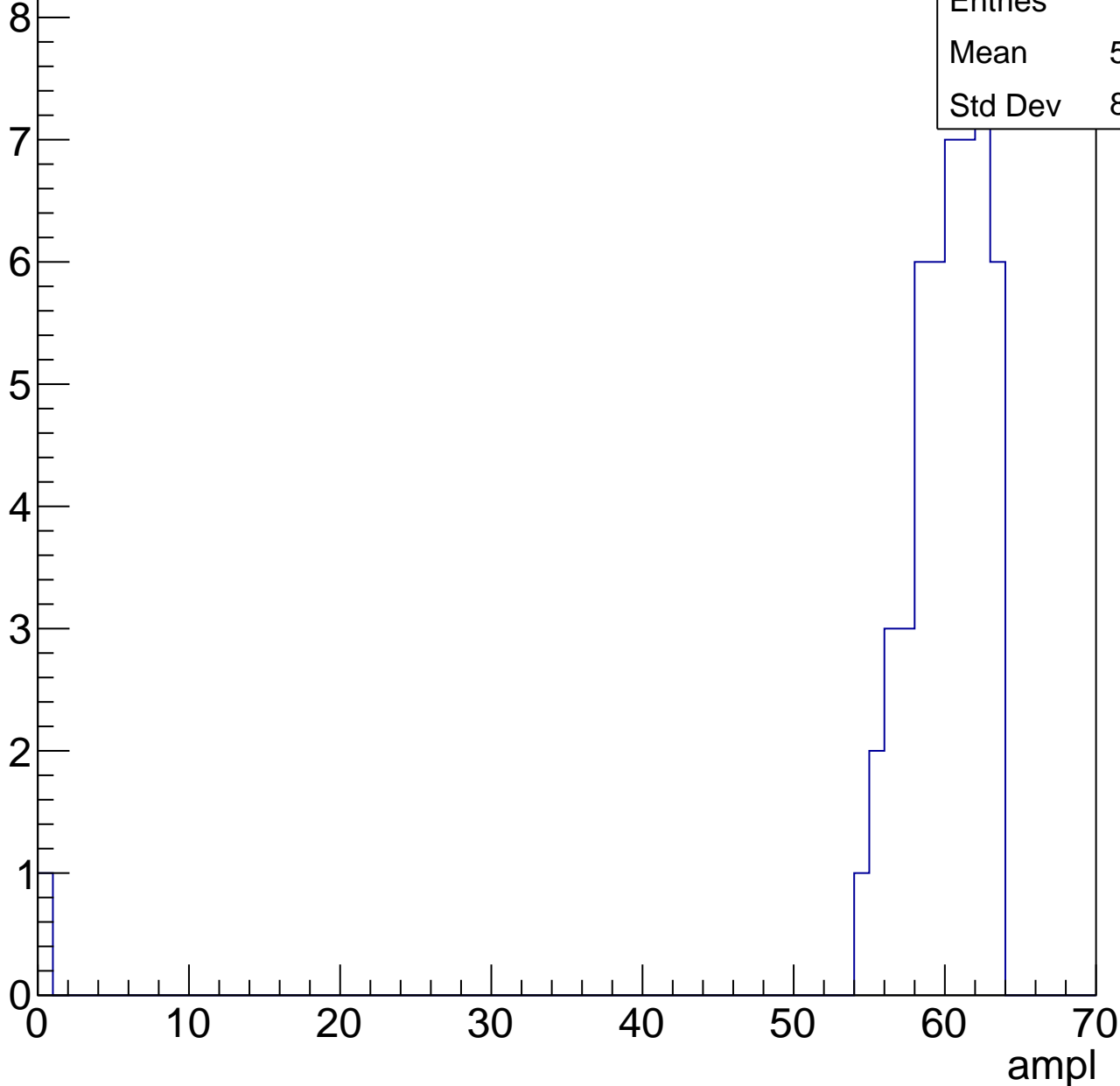


B1L003S, U21-ch112, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

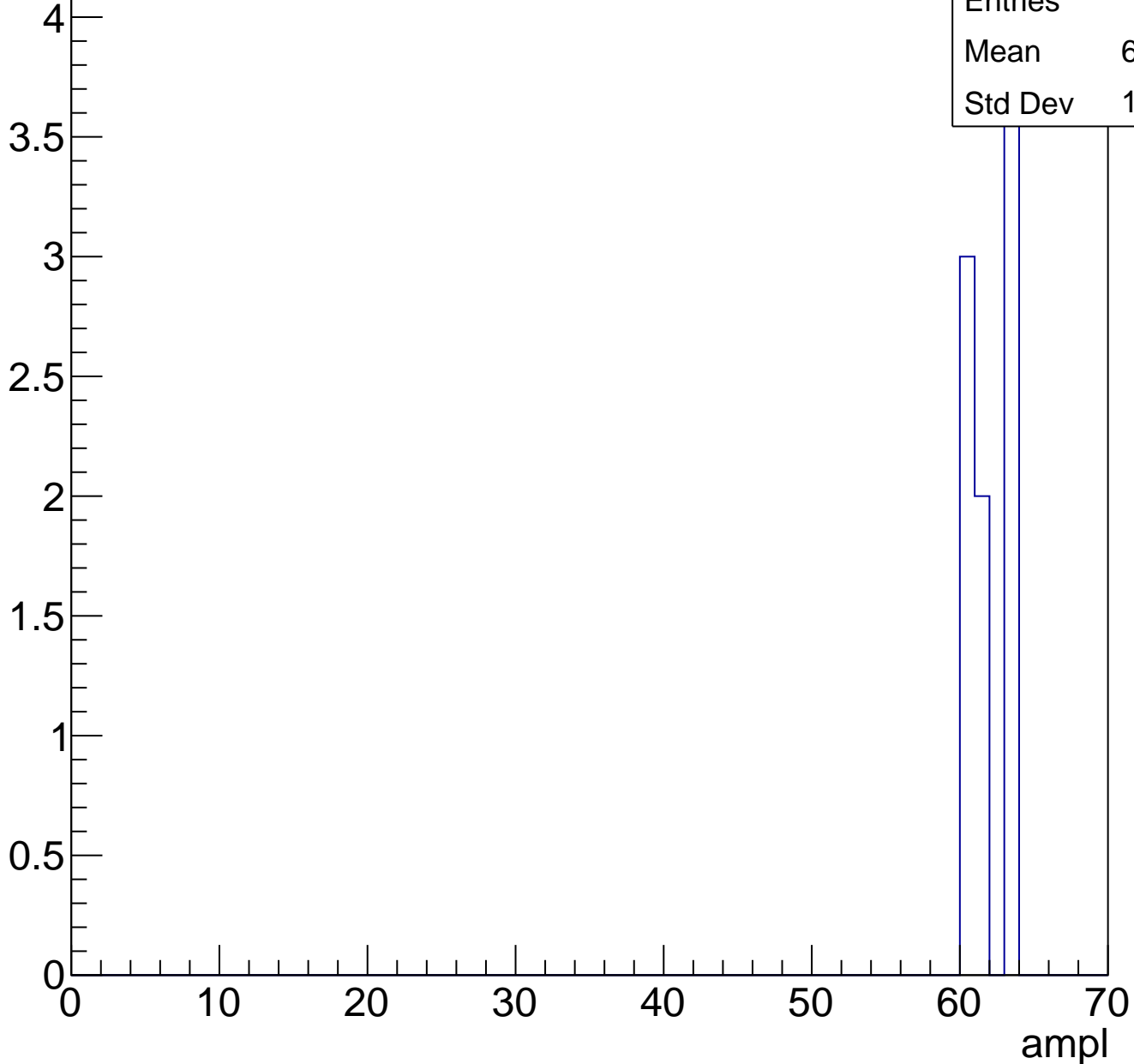
Entries	50
Mean	58.52
Std Dev	8.688



B1L003S, U21-ch112, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	9
Mean	61.56
Std Dev	1.343

B1L003S, U21-ch112, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch113, adc0

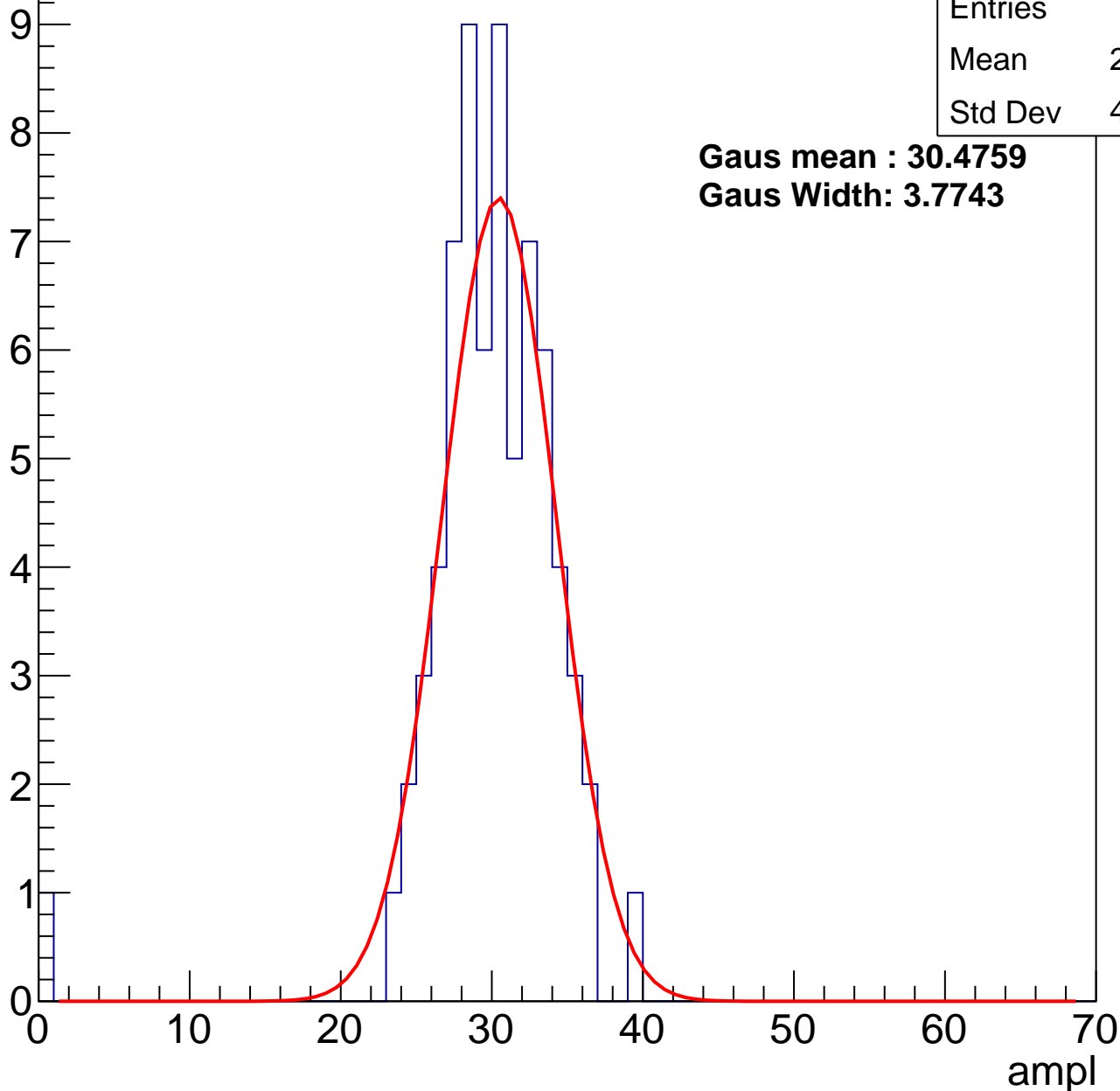
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	70
Mean	29.49
Std Dev	4.819

Gaus mean : 30.4759

Gaus Width: 3.7743



B1L003S, U21-ch113, adc1

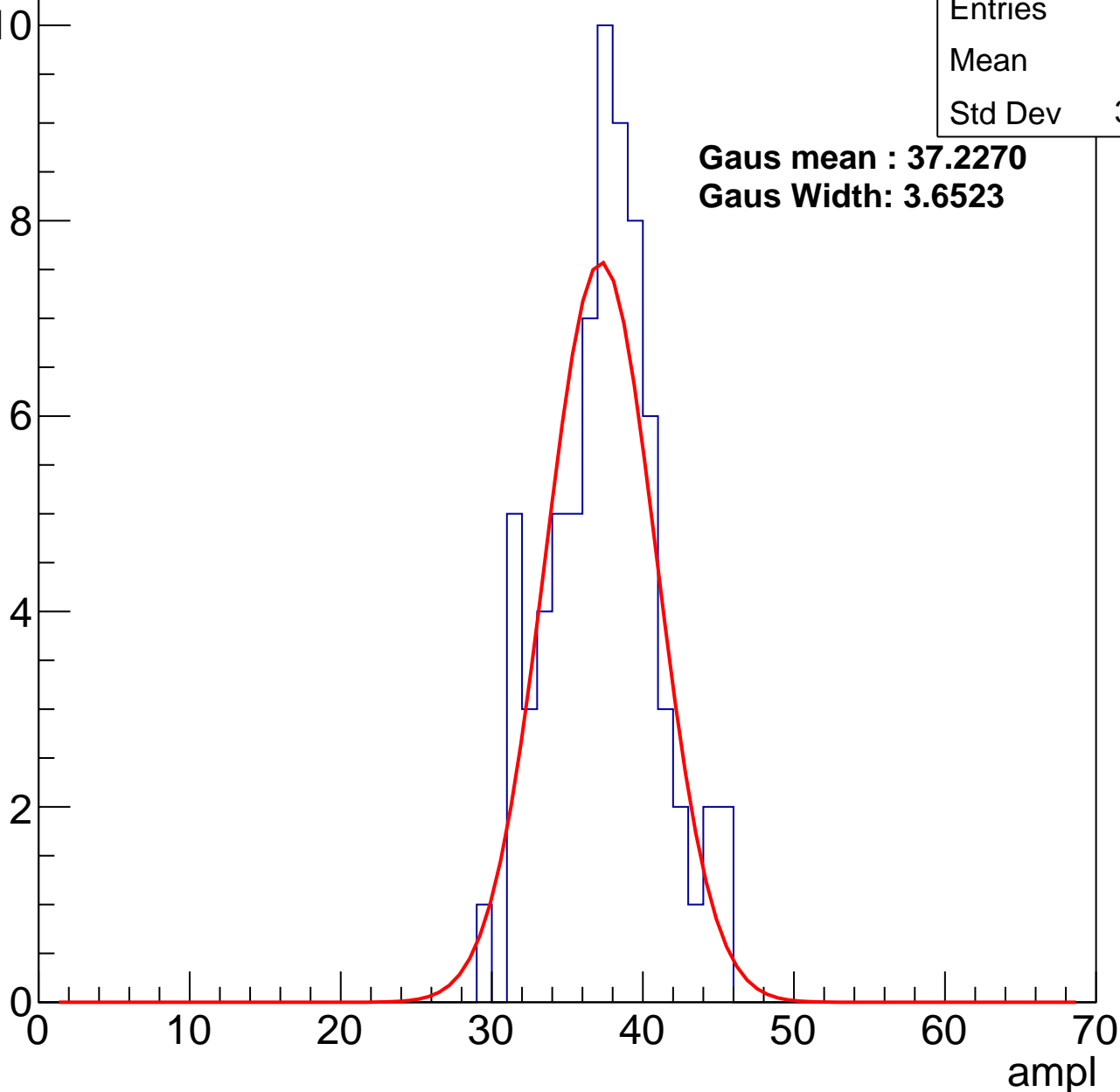
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	37
Std Dev	3.531

Gaus mean : 37.2270

Gaus Width: 3.6523



B1L003S, U21-ch113, adc2

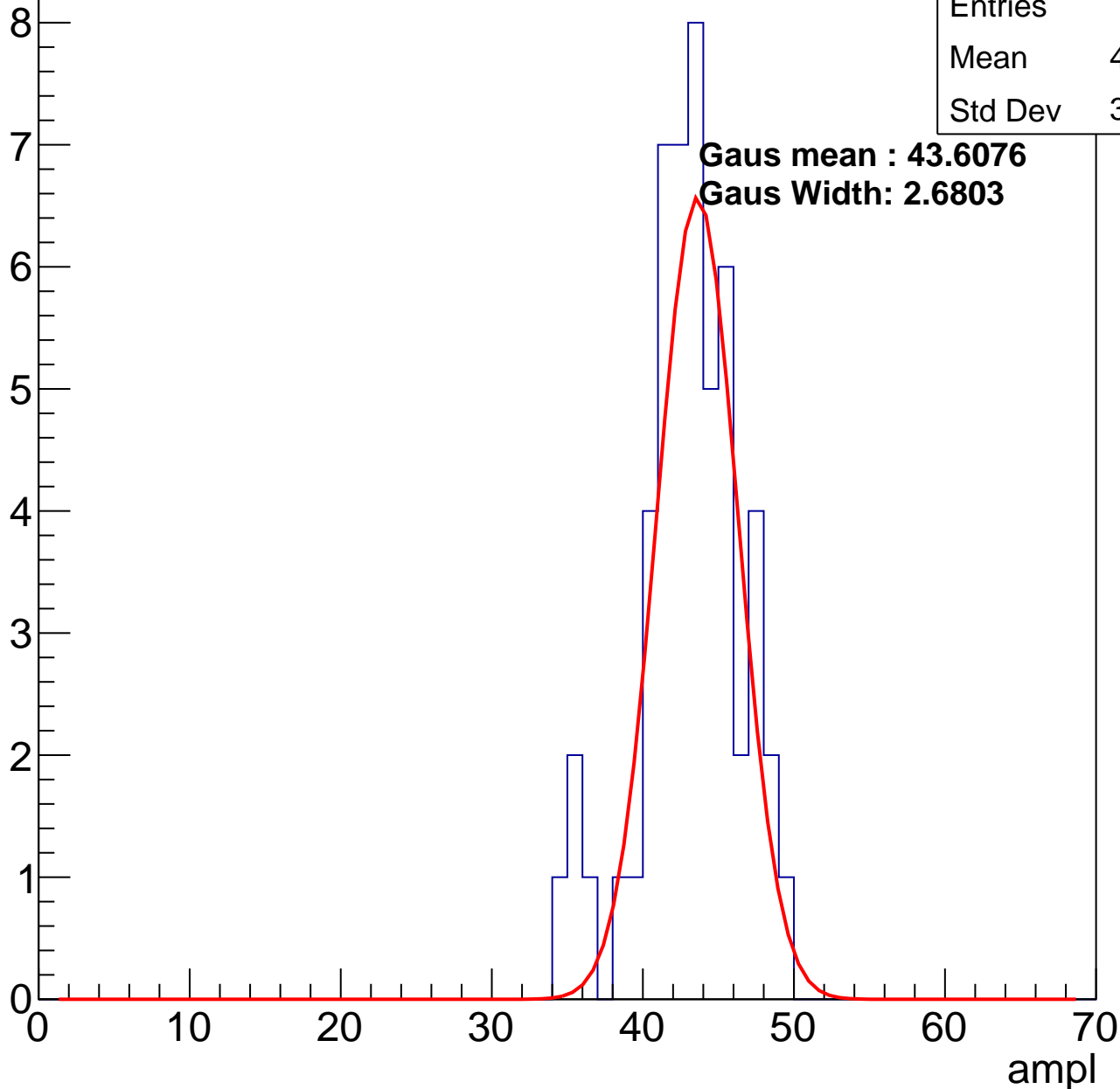
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	42.63
Std Dev	3.282

Gaus mean : 43.6076

Gaus Width: 2.6803

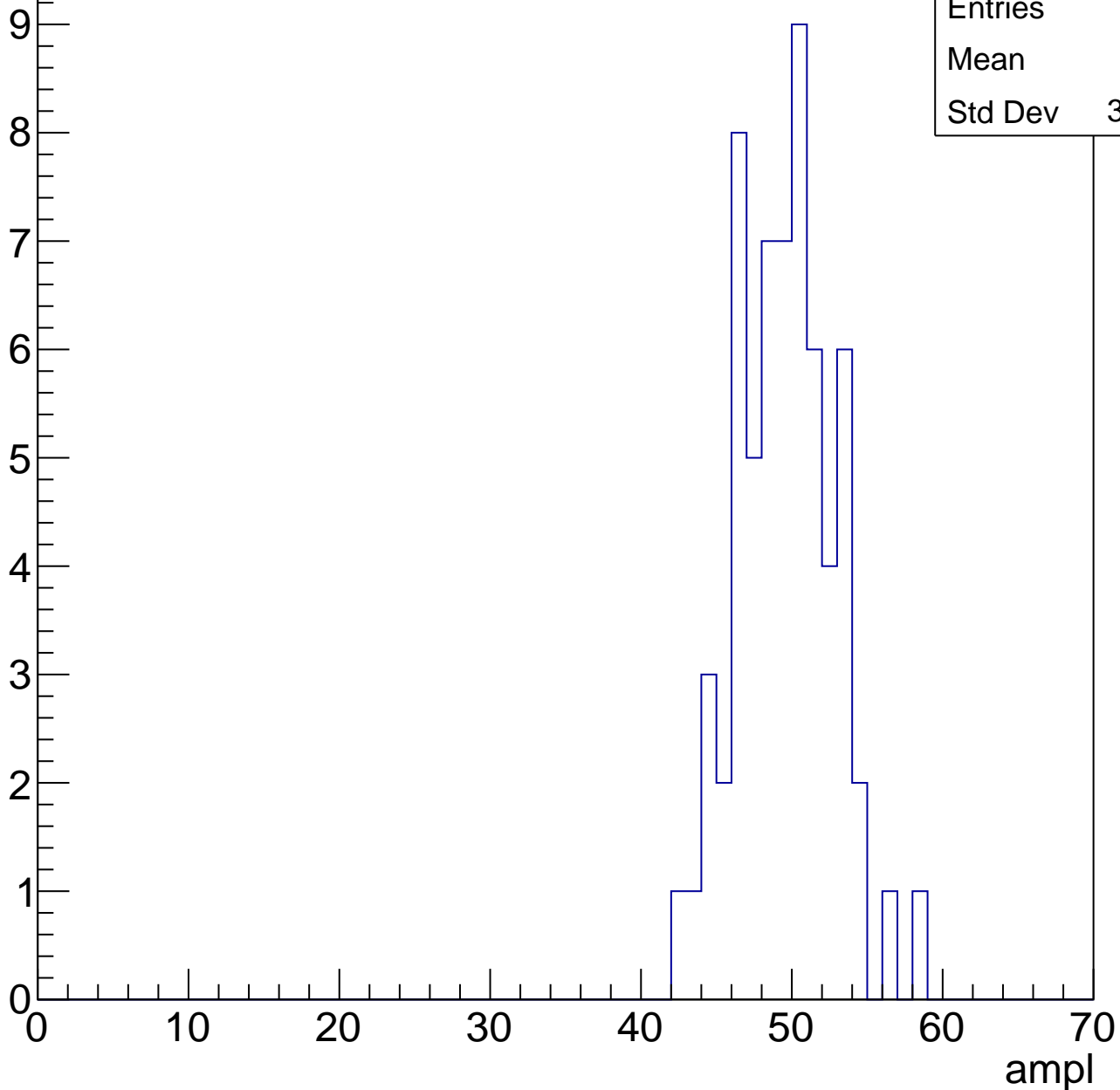


B1L003S, U21-ch113, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	49.1
Std Dev	3.176

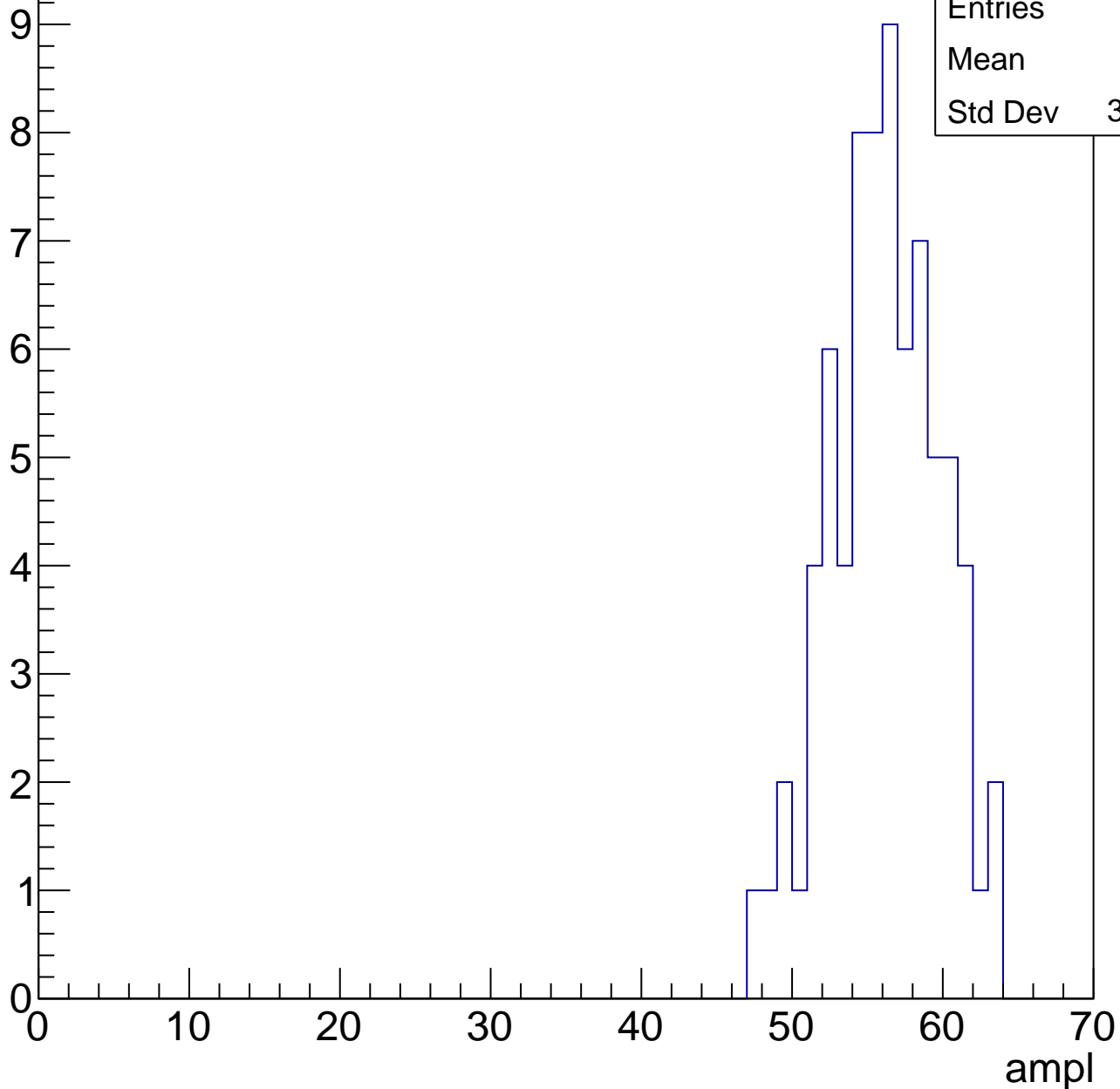


B1L003S, U21-ch113, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	55.7
Std Dev	3.559

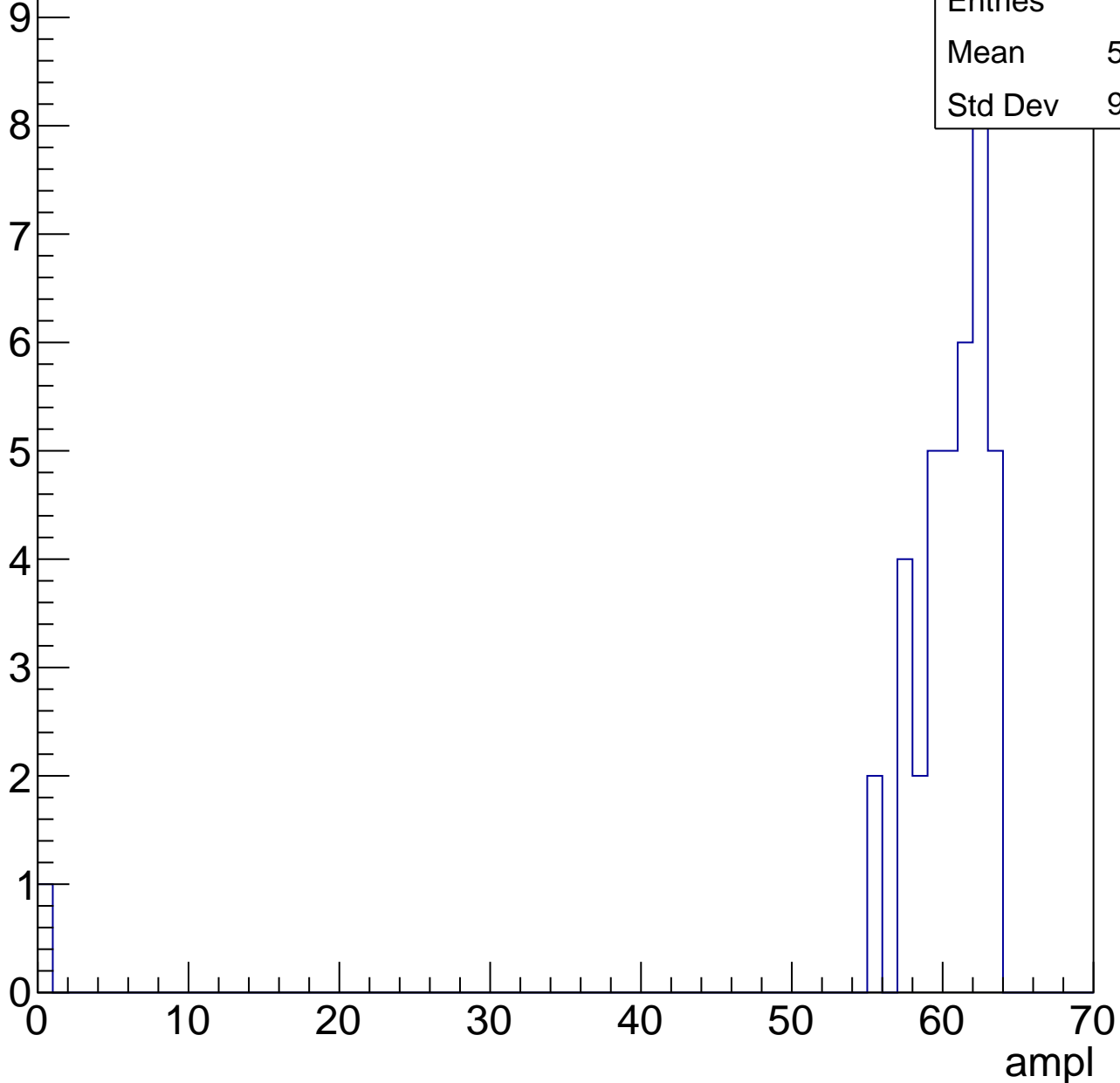


B1L003S, U21-ch113, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	39
Mean	58.67
Std Dev	9.762



B1L003S, U21-ch113, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch113, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch114, adc0

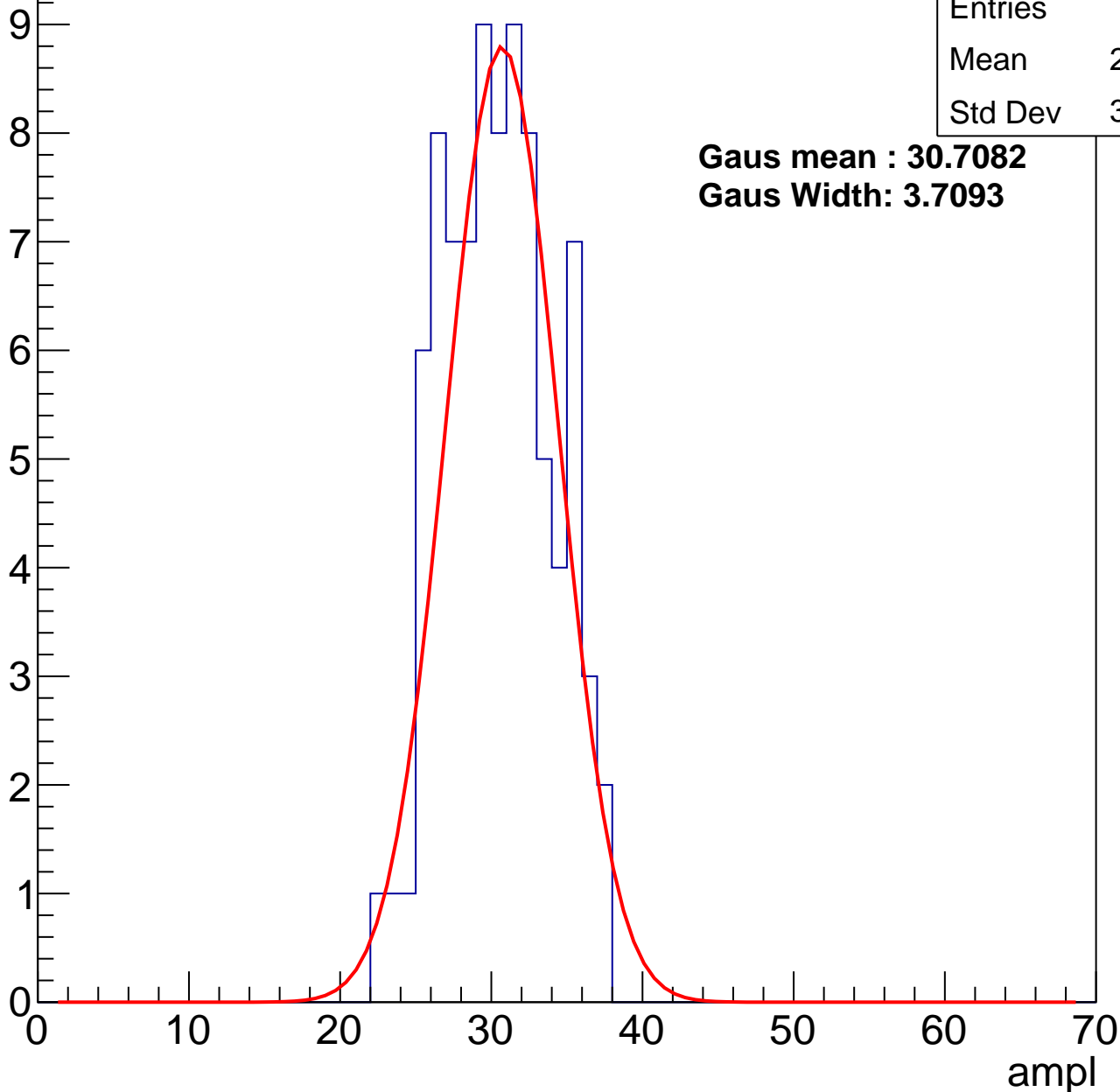
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	86
Mean	29.95
Std Dev	3.507

Gaus mean : 30.7082

Gaus Width: 3.7093



B1L003S, U21-ch114, adc1

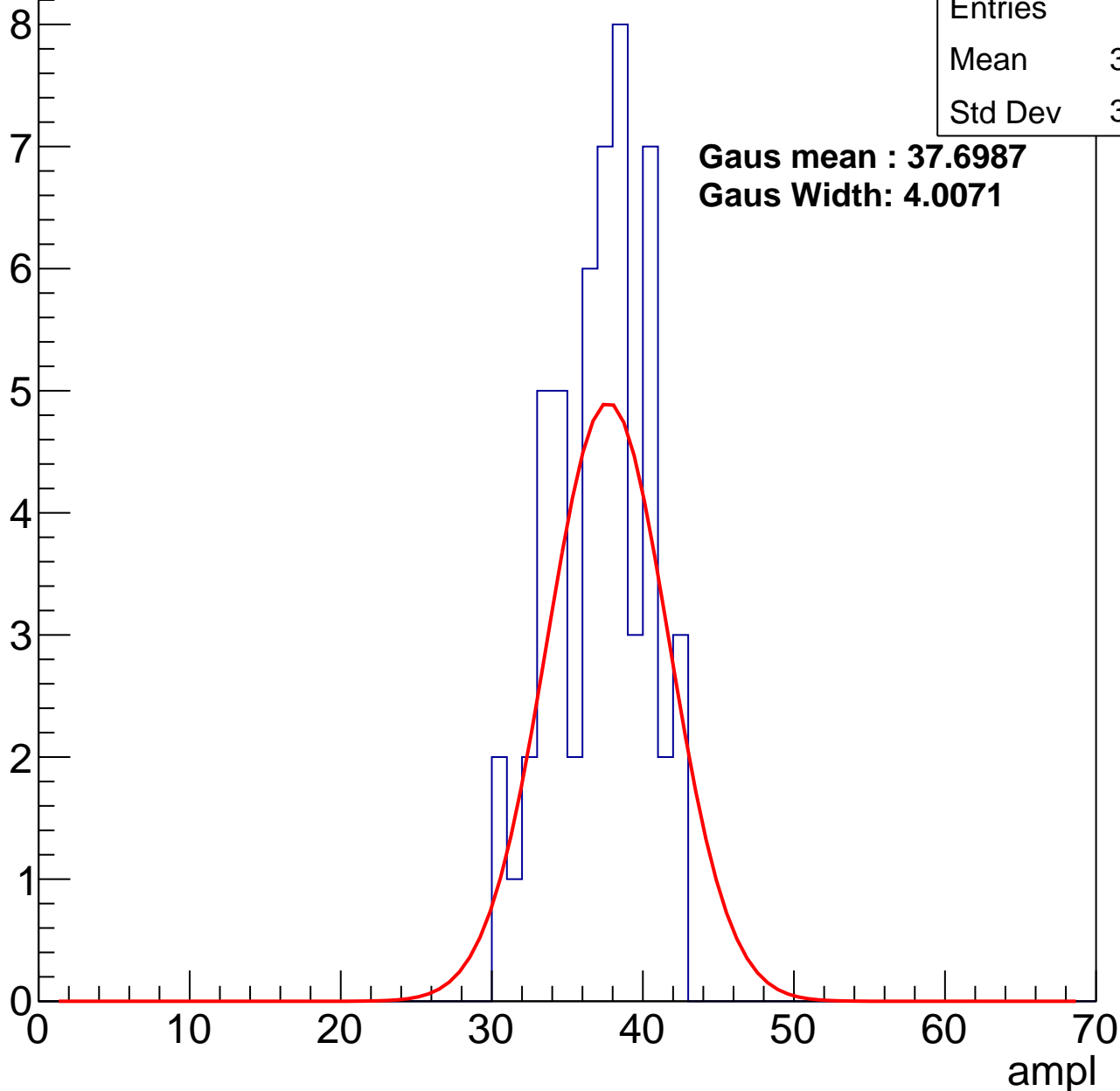
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	36.68
Std Dev	3.095

Gaus mean : 37.6987

Gaus Width: 4.0071



B1L003S, U21-ch114, adc2

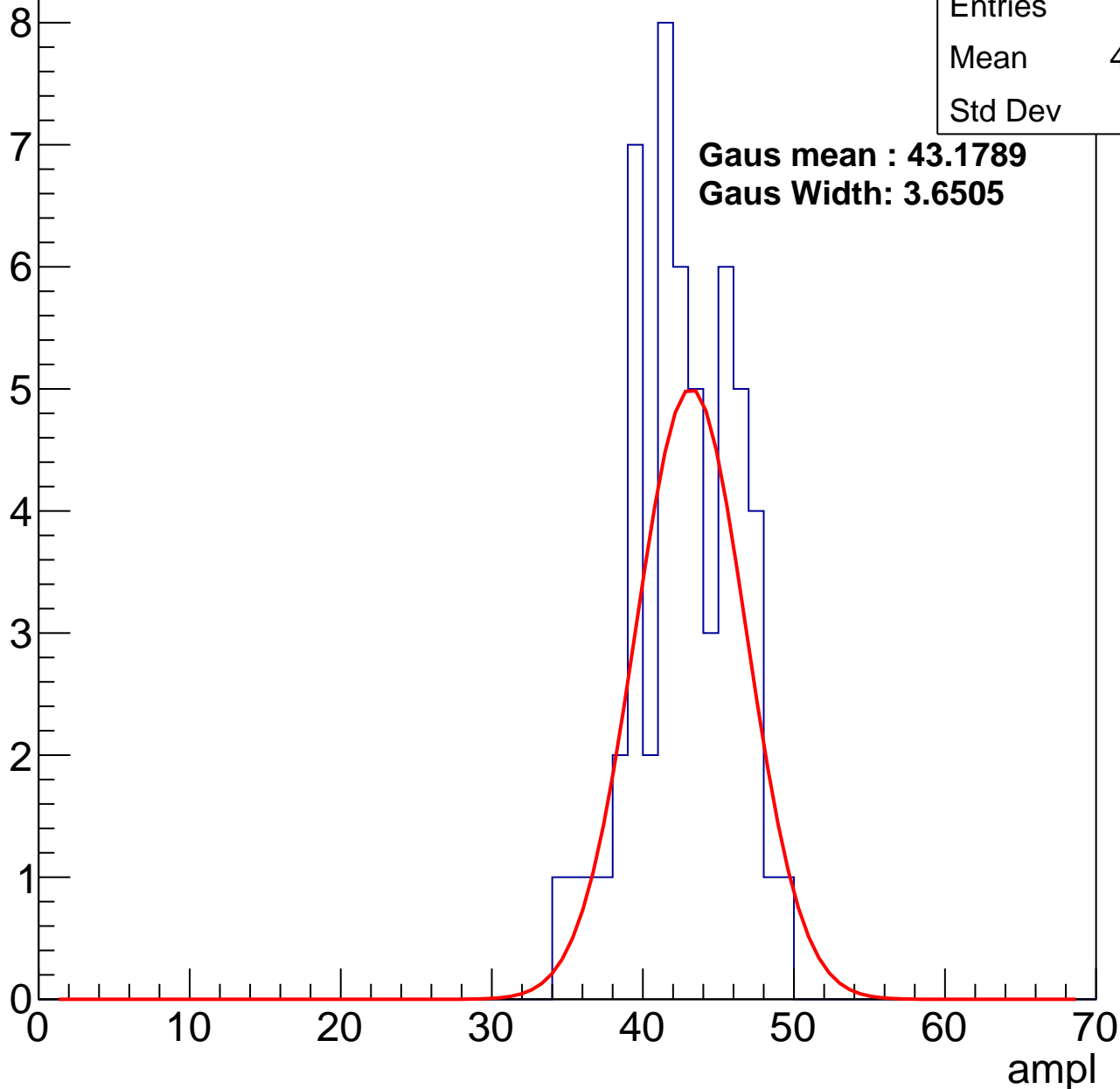
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	42.28
Std Dev	3.38

Gaus mean : 43.1789

Gaus Width: 3.6505

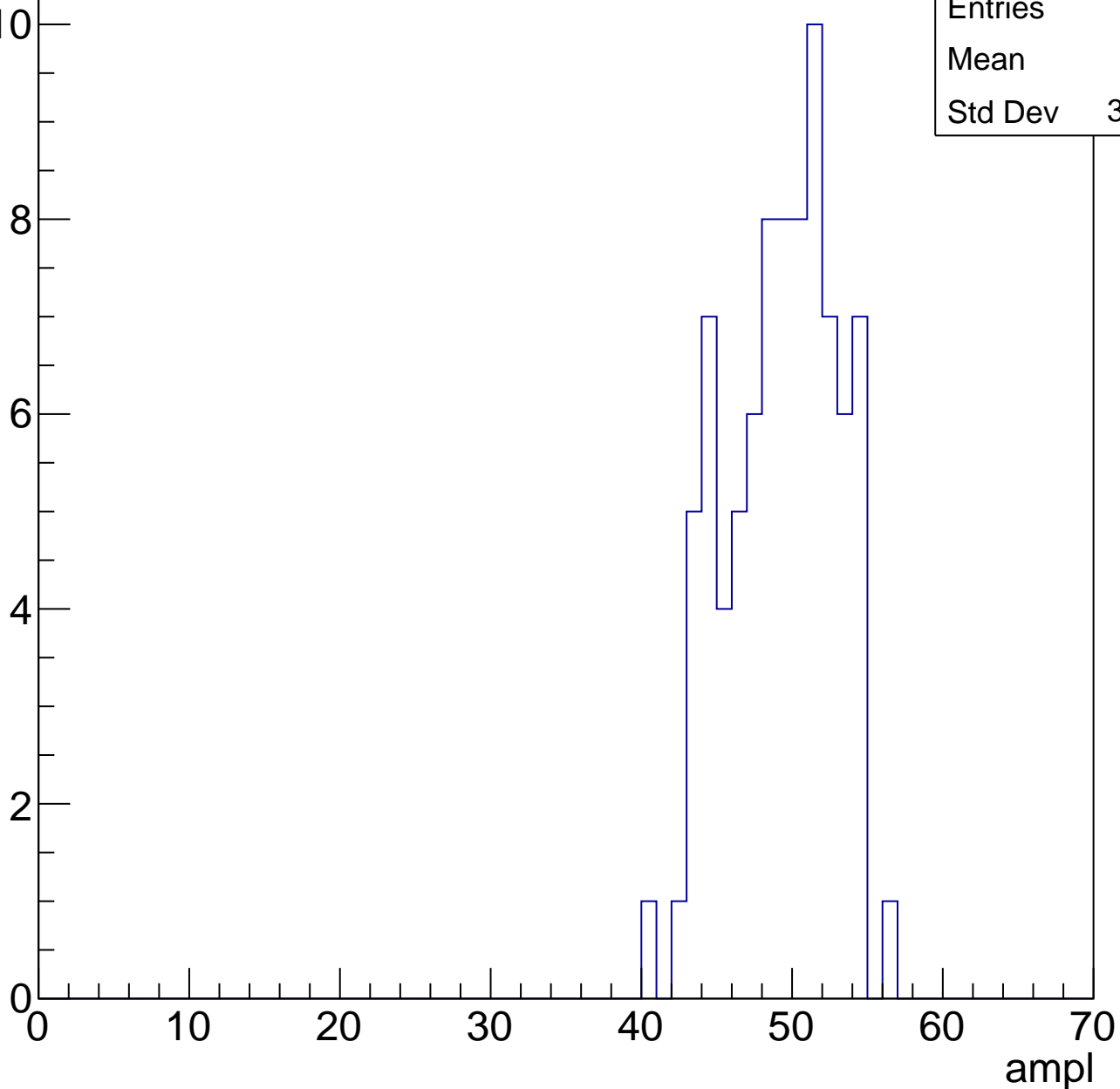


B1L003S, U21-ch114, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	84
Mean	48.8
Std Dev	3.542

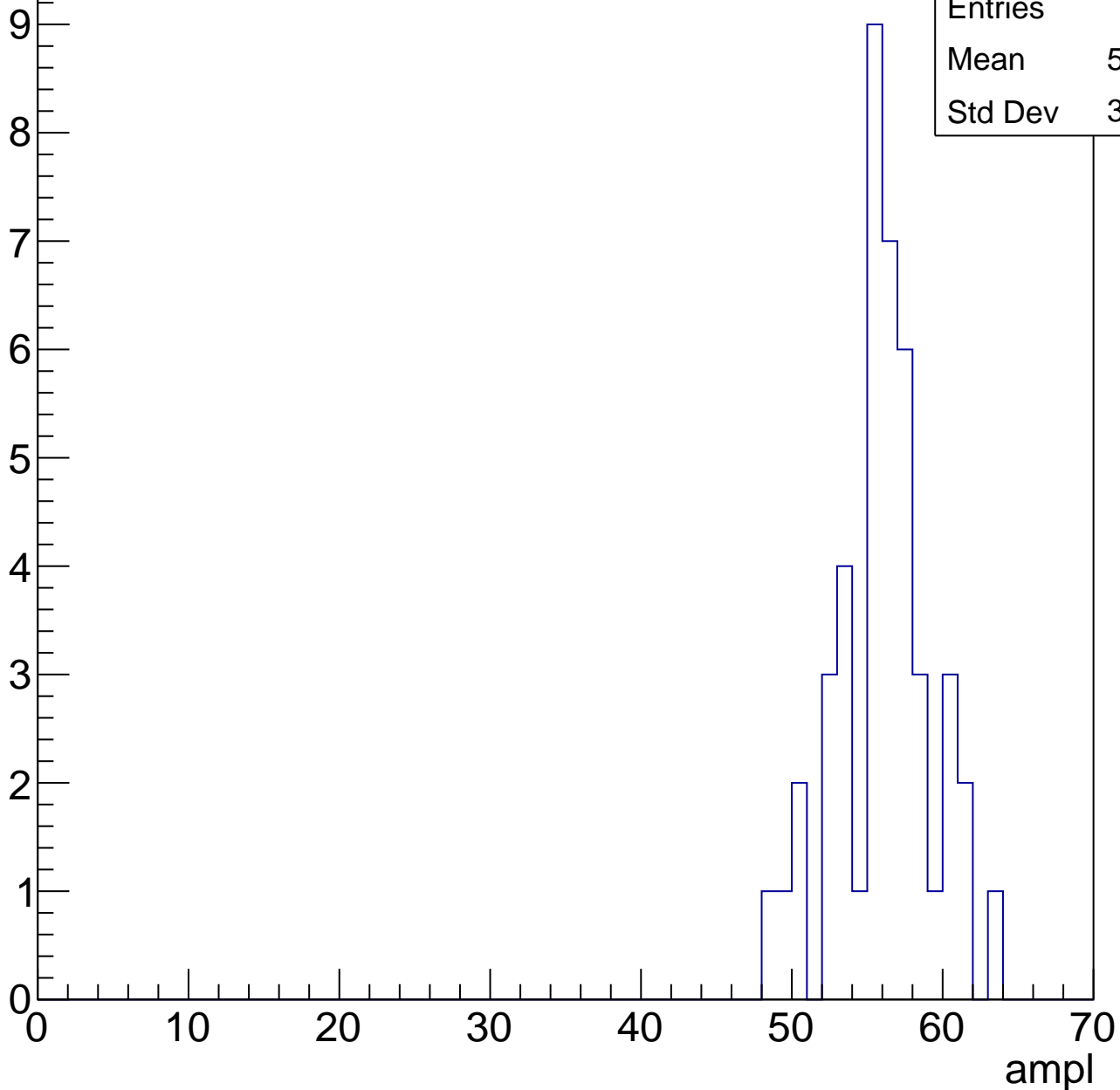


B1L003S, U21-ch114, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

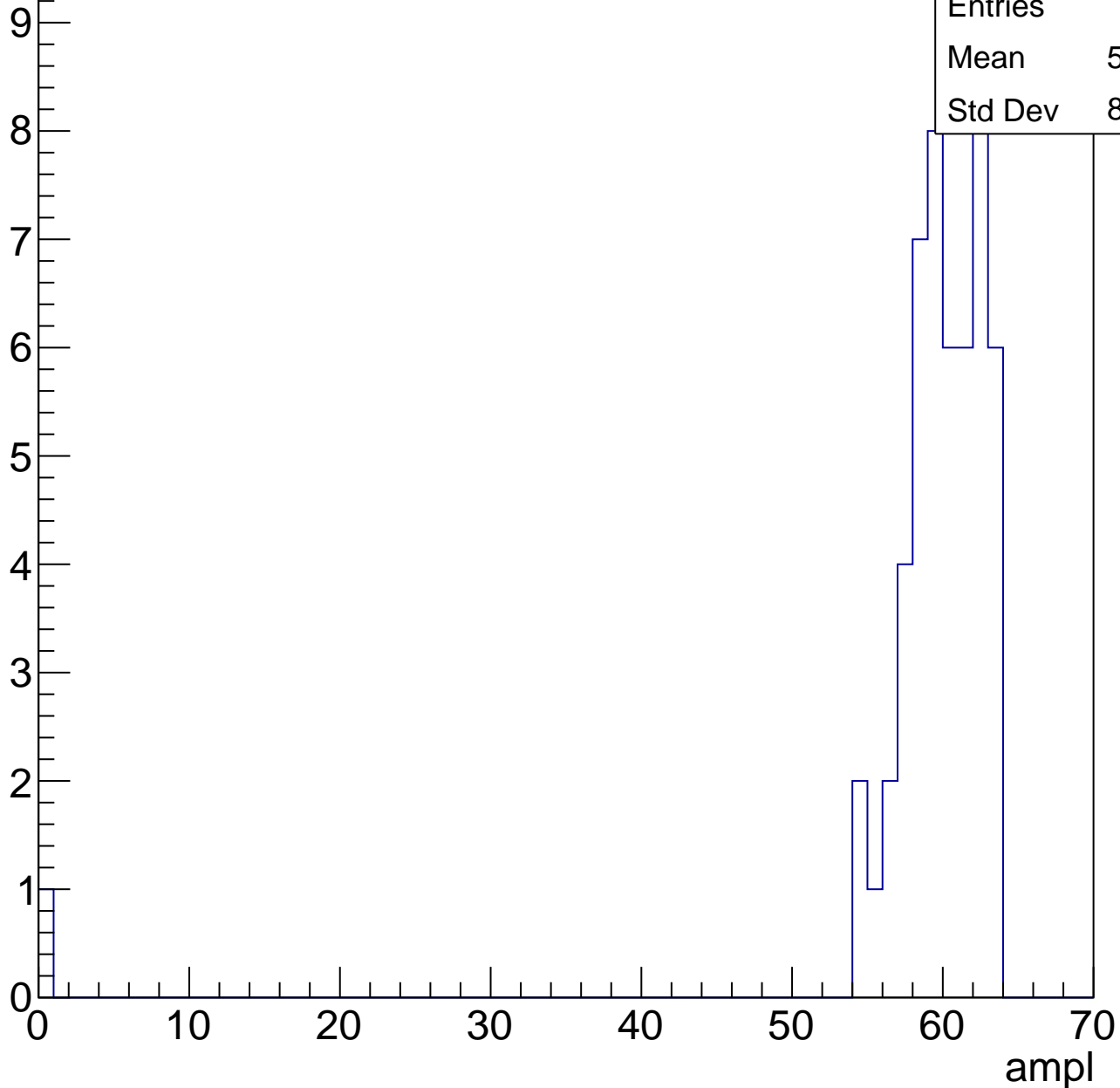
Entries	44
Mean	55.59
Std Dev	3.193



B1L003S, U21-ch114, adc5

calib_packv5_042523_0143.root, FC#13, port D2

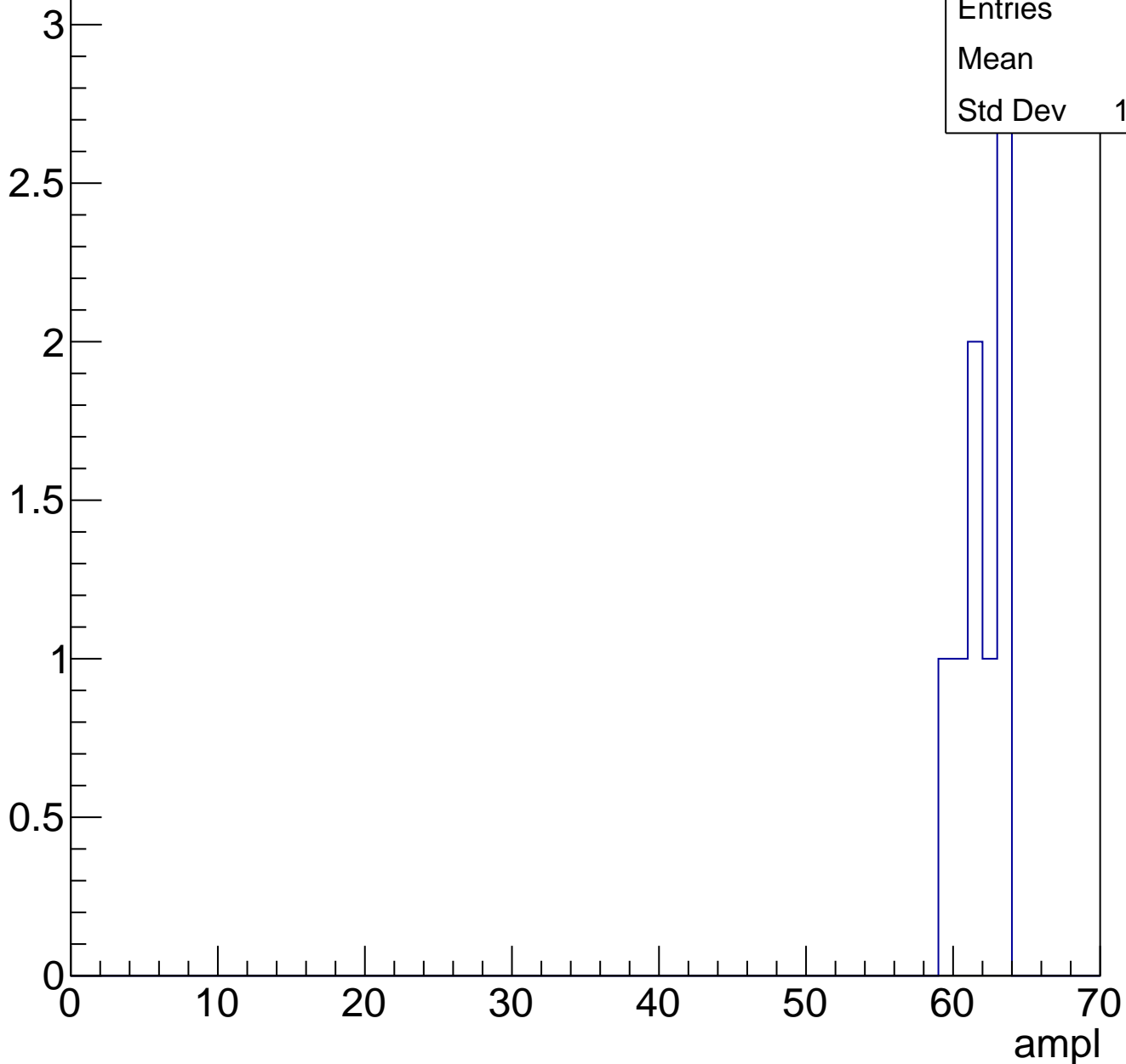
Entry



B1L003S, U21-ch114, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch114, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch115, adc0

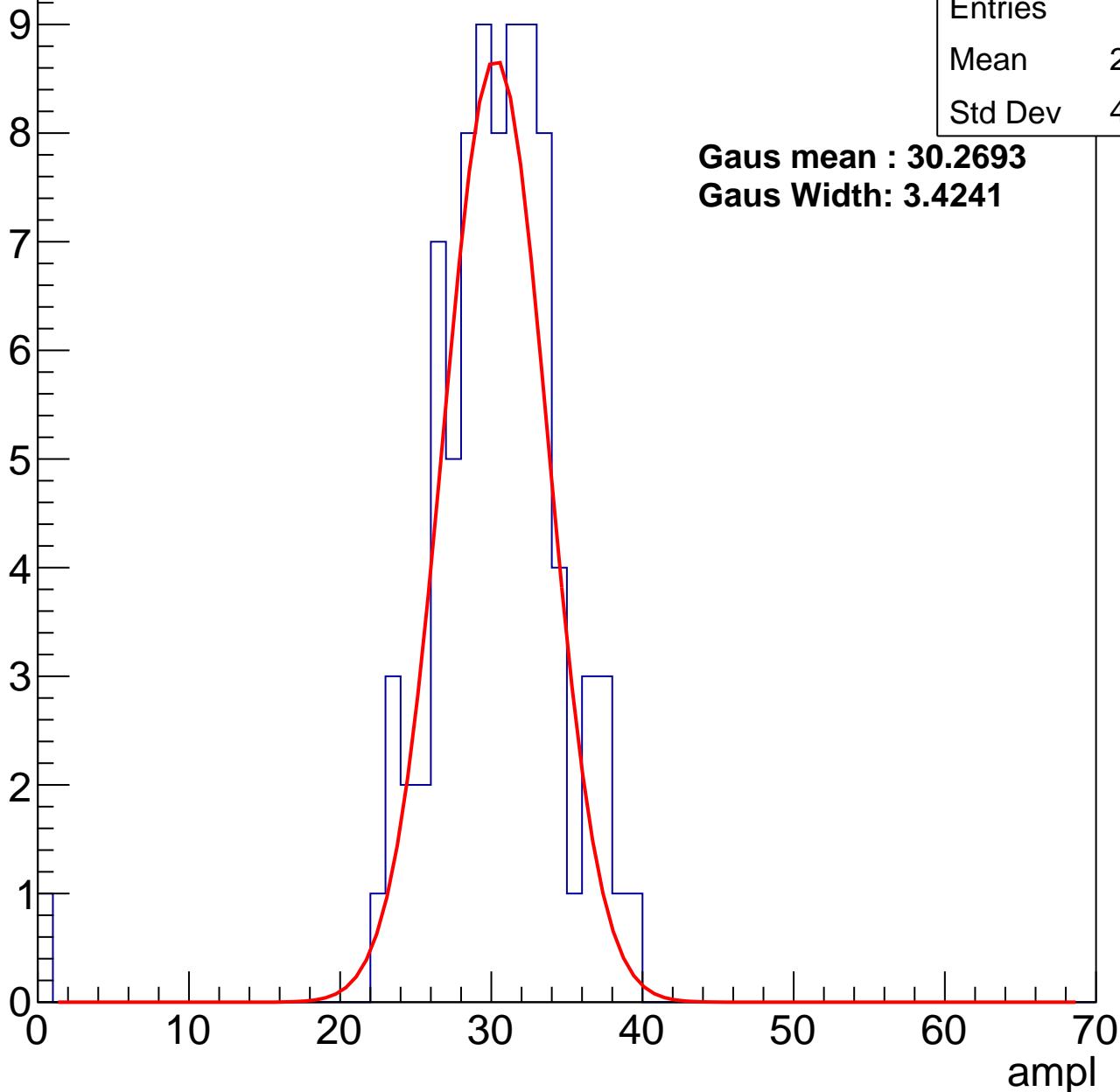
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	29.75
Std Dev	4.889

Gaus mean : 30.2693

Gaus Width: 3.4241



B1L003S, U21-ch115, adc1

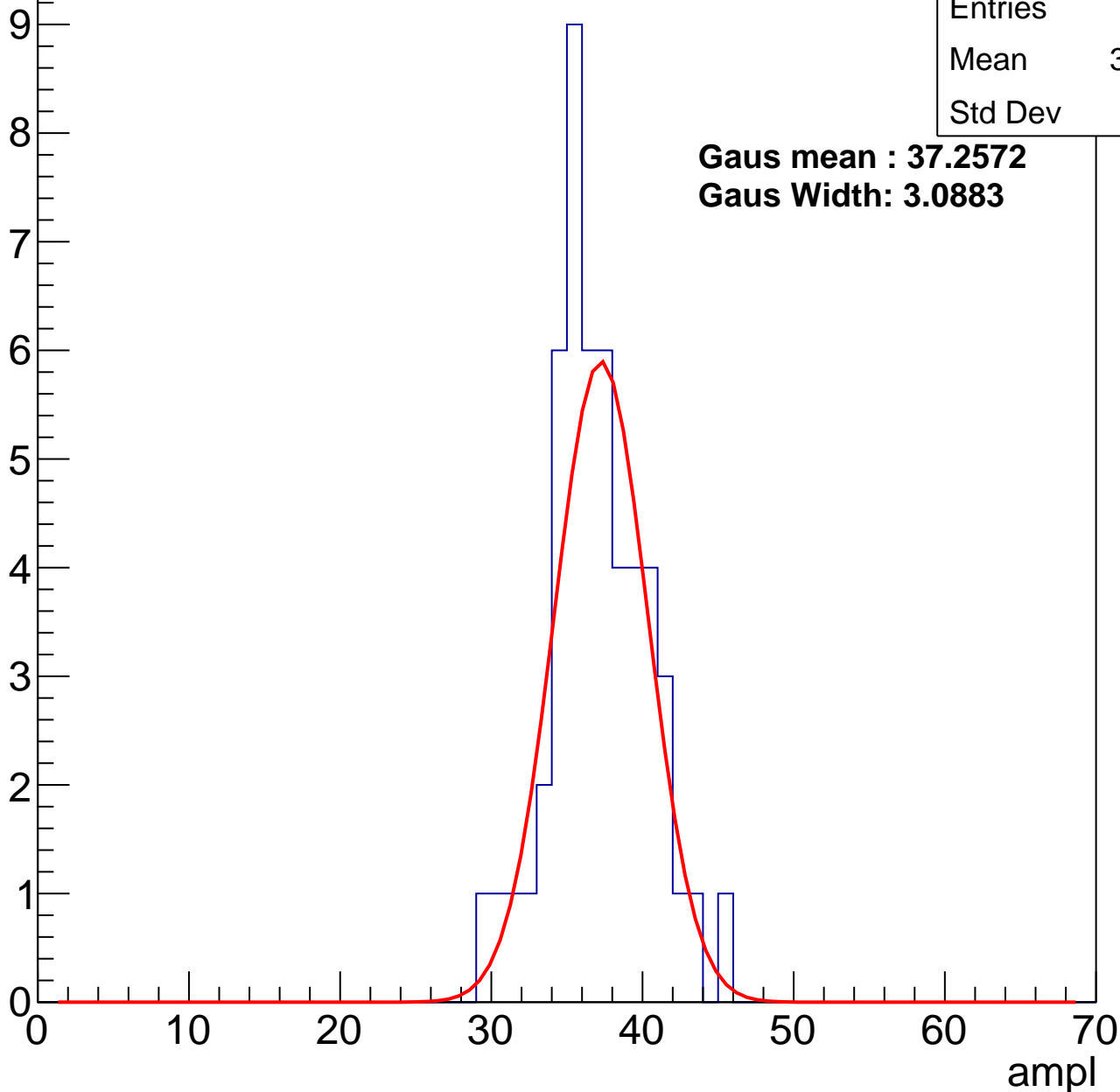
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	36.59
Std Dev	3.2

Gaus mean : 37.2572

Gaus Width: 3.0883



B1L003S, U21-ch115, adc2

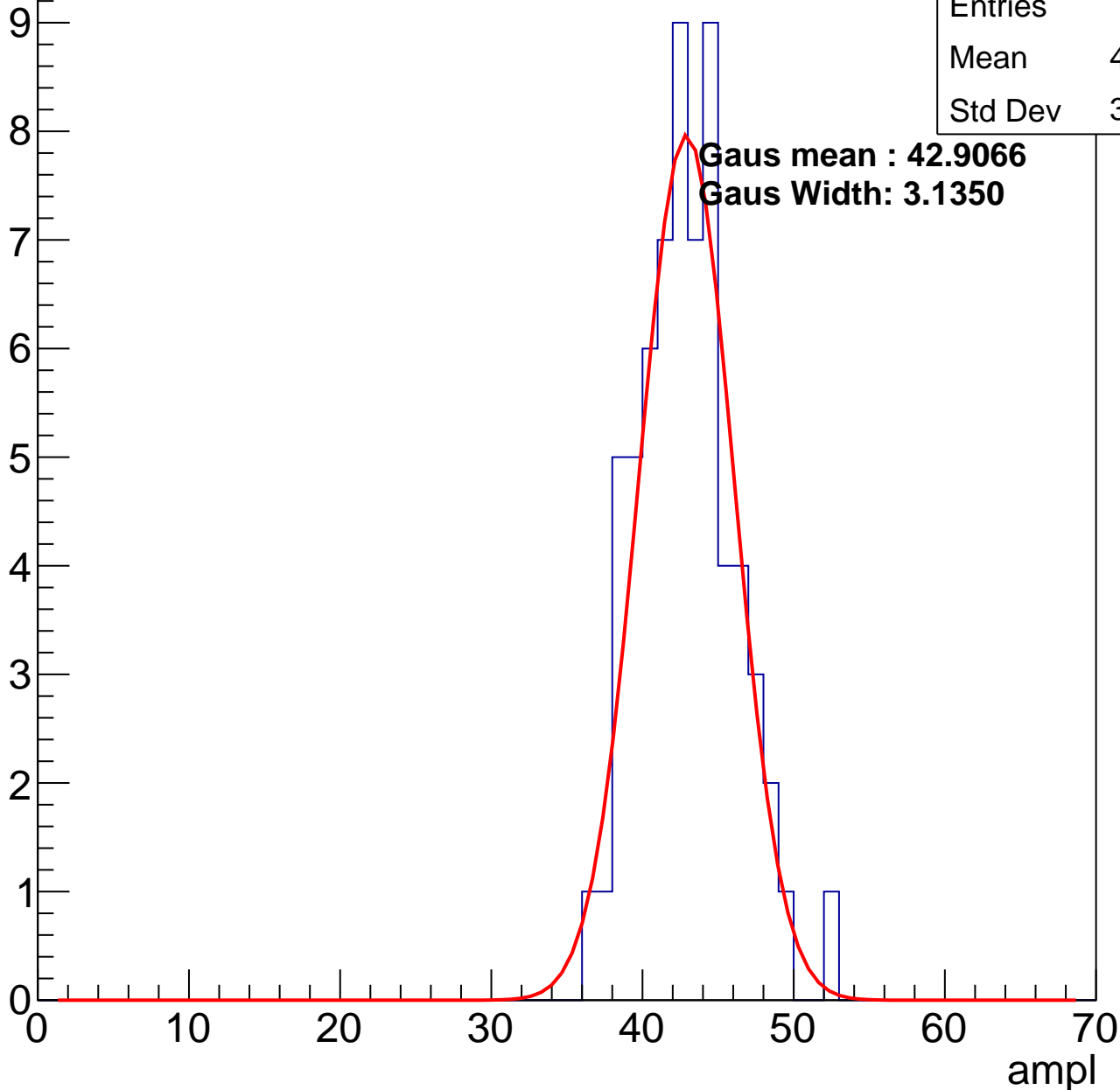
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.49
Std Dev	3.138

Gaus mean : 42.9066

Gaus Width: 3.1350

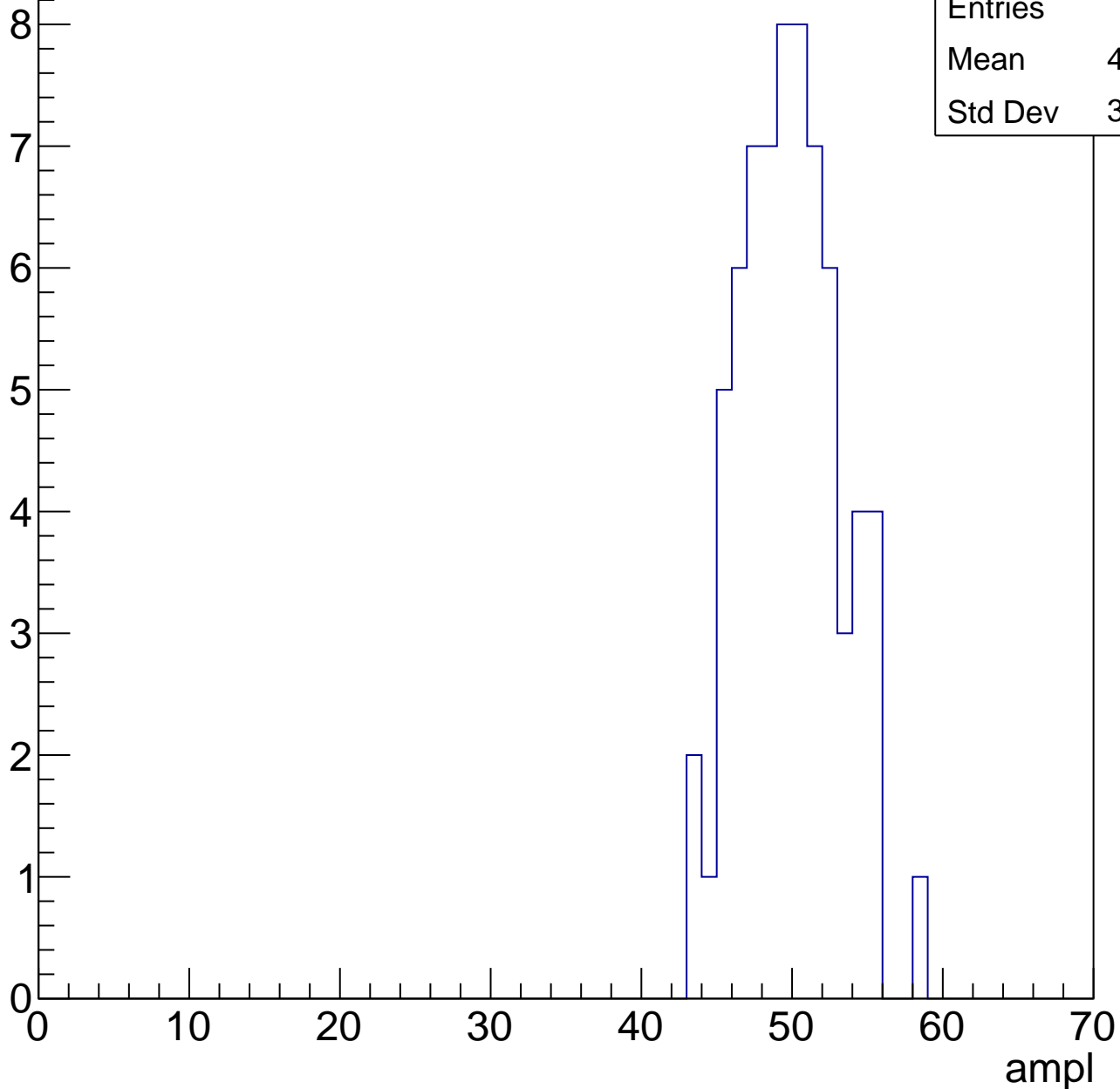


B1L003S, U21-ch115, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.42
Std Dev	3.223

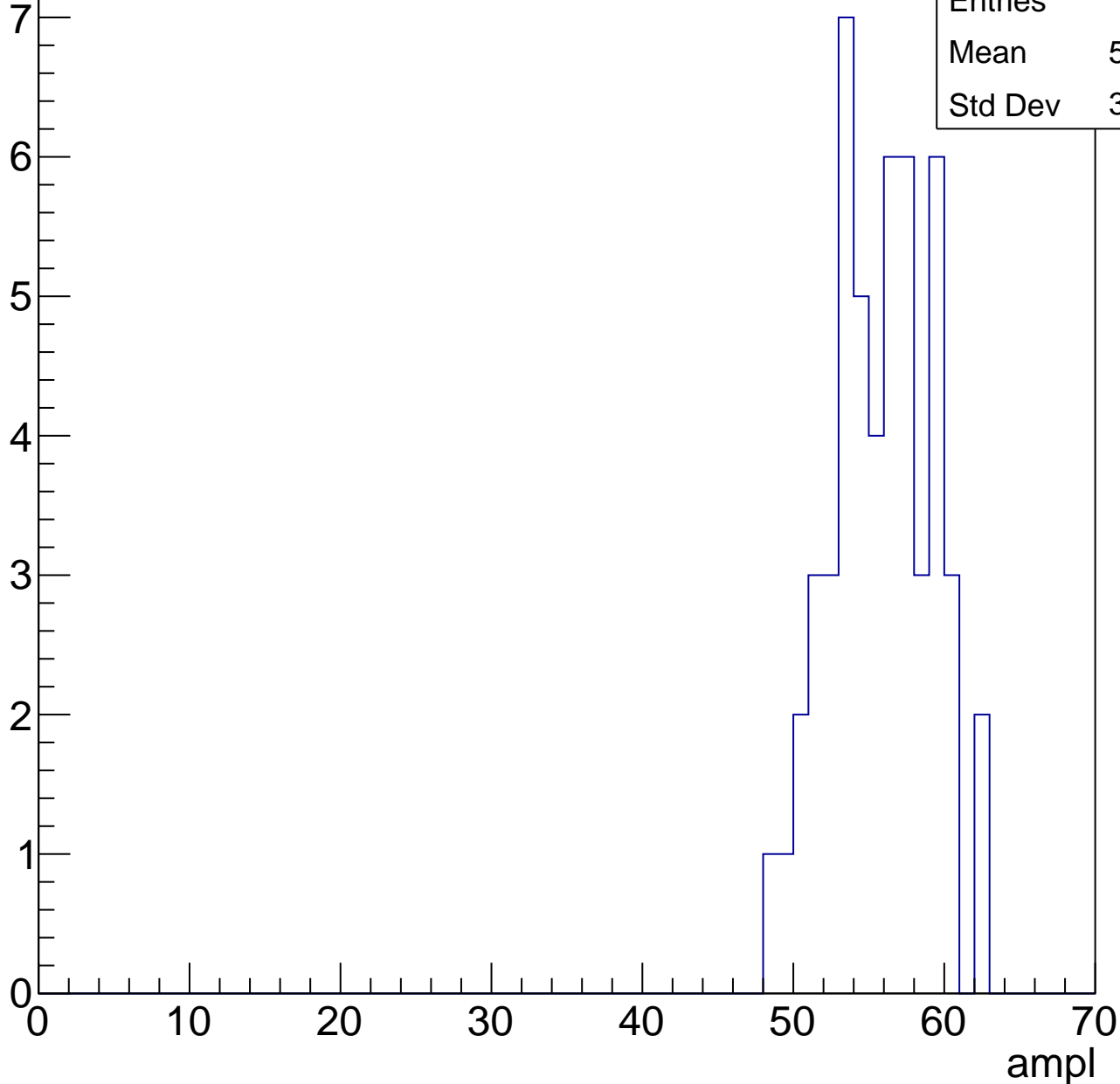


B1L003S, U21-ch115, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	55.33
Std Dev	3.292

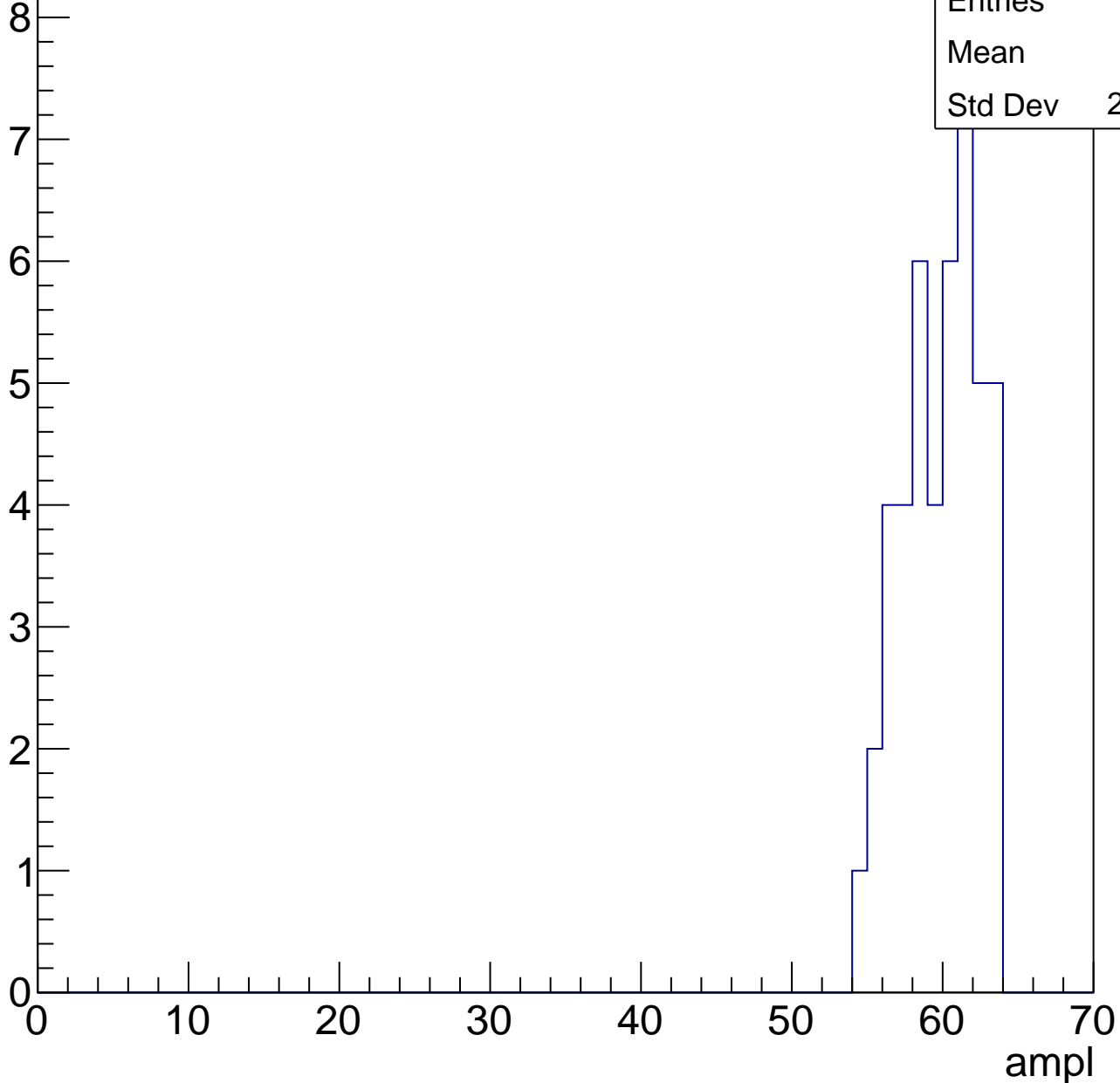


B1L003S, U21-ch115, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	59.4
Std Dev	2.453

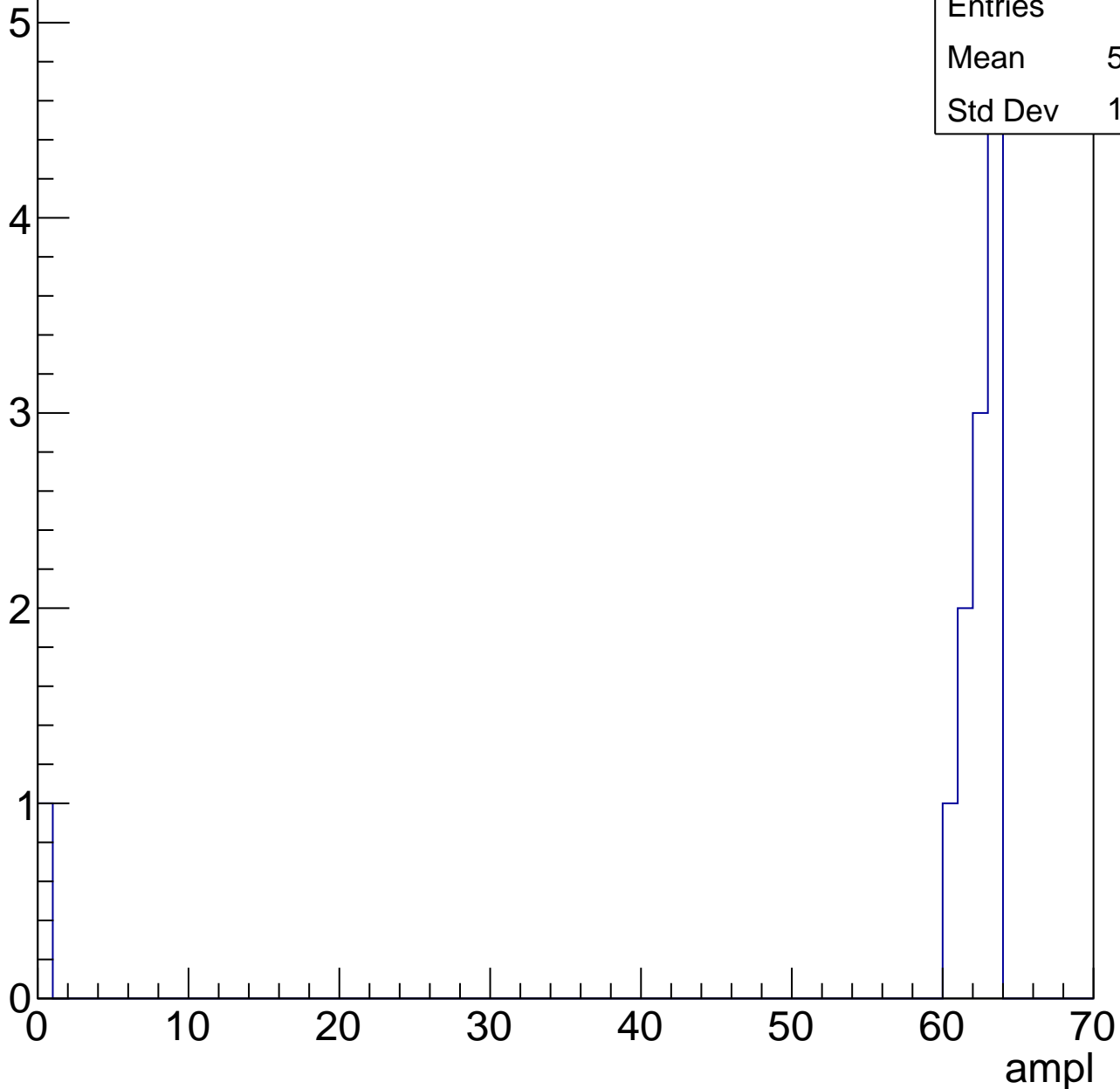


B1L003S, U21-ch115, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

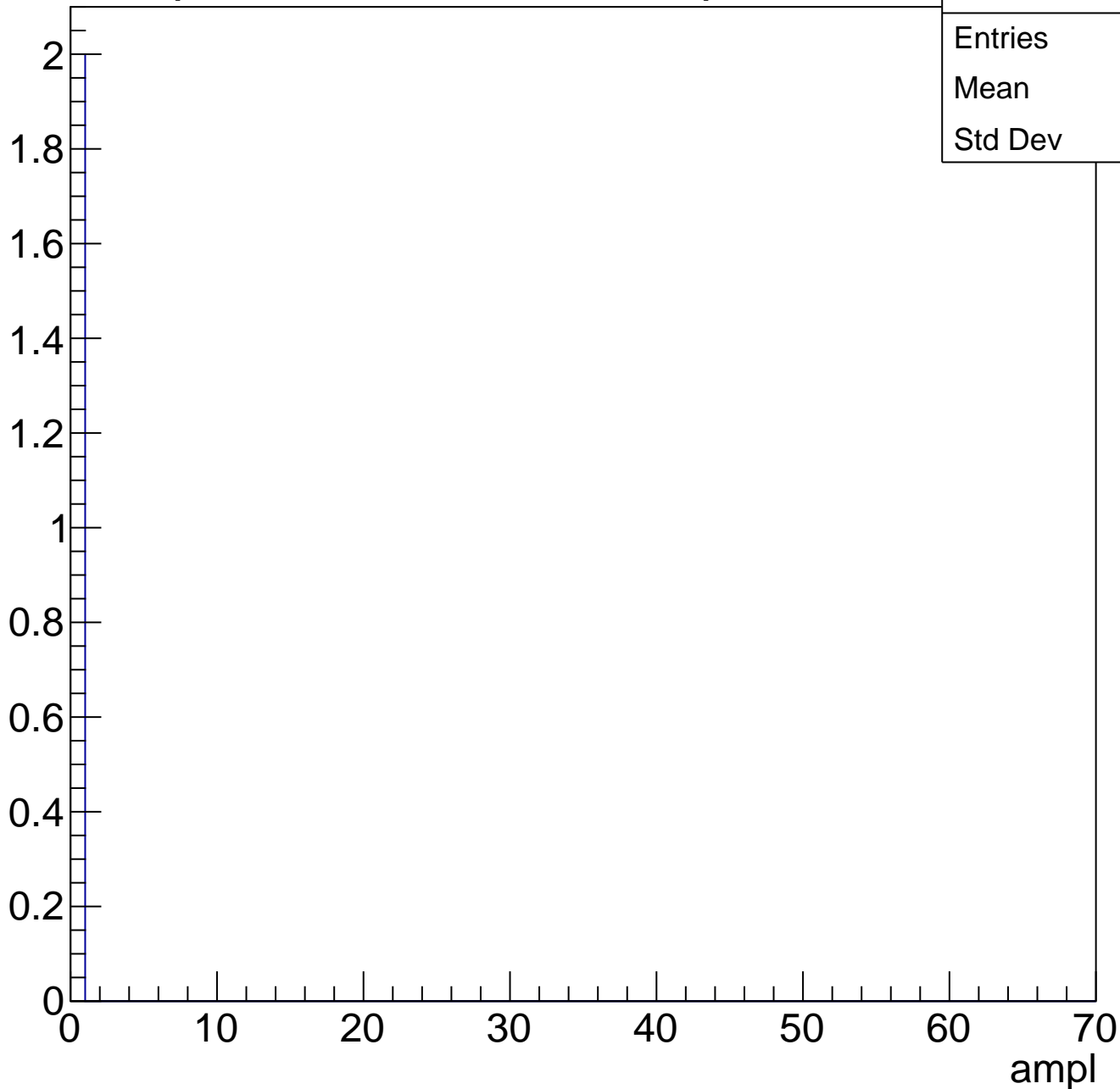
Entries	12
Mean	56.92
Std Dev	17.19



B1L003S, U21-ch115, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch116, adc0

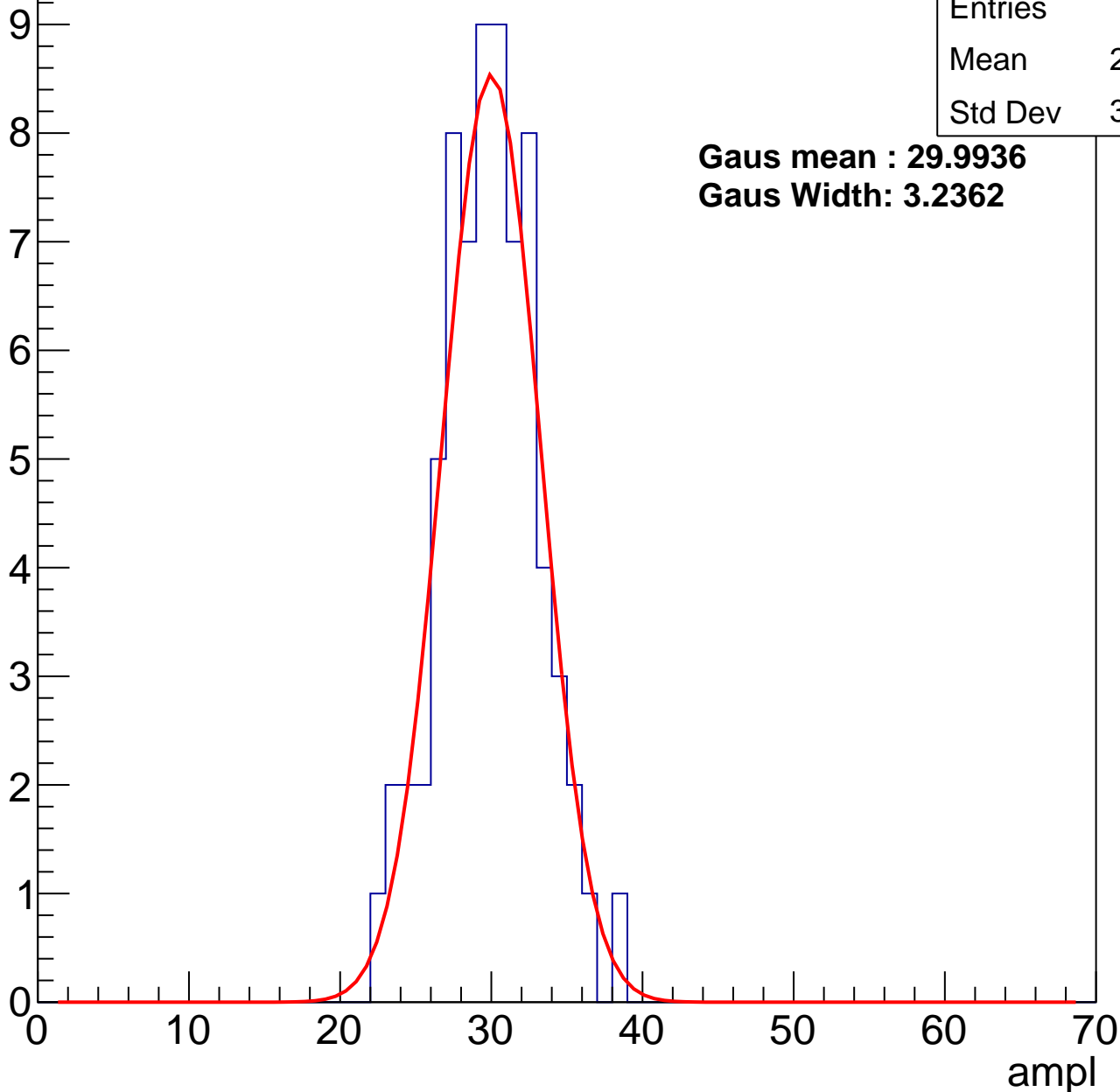
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	29.44
Std Dev	3.192

Gaus mean : 29.9936

Gaus Width: 3.2362



B1L003S, U21-ch116, adc1

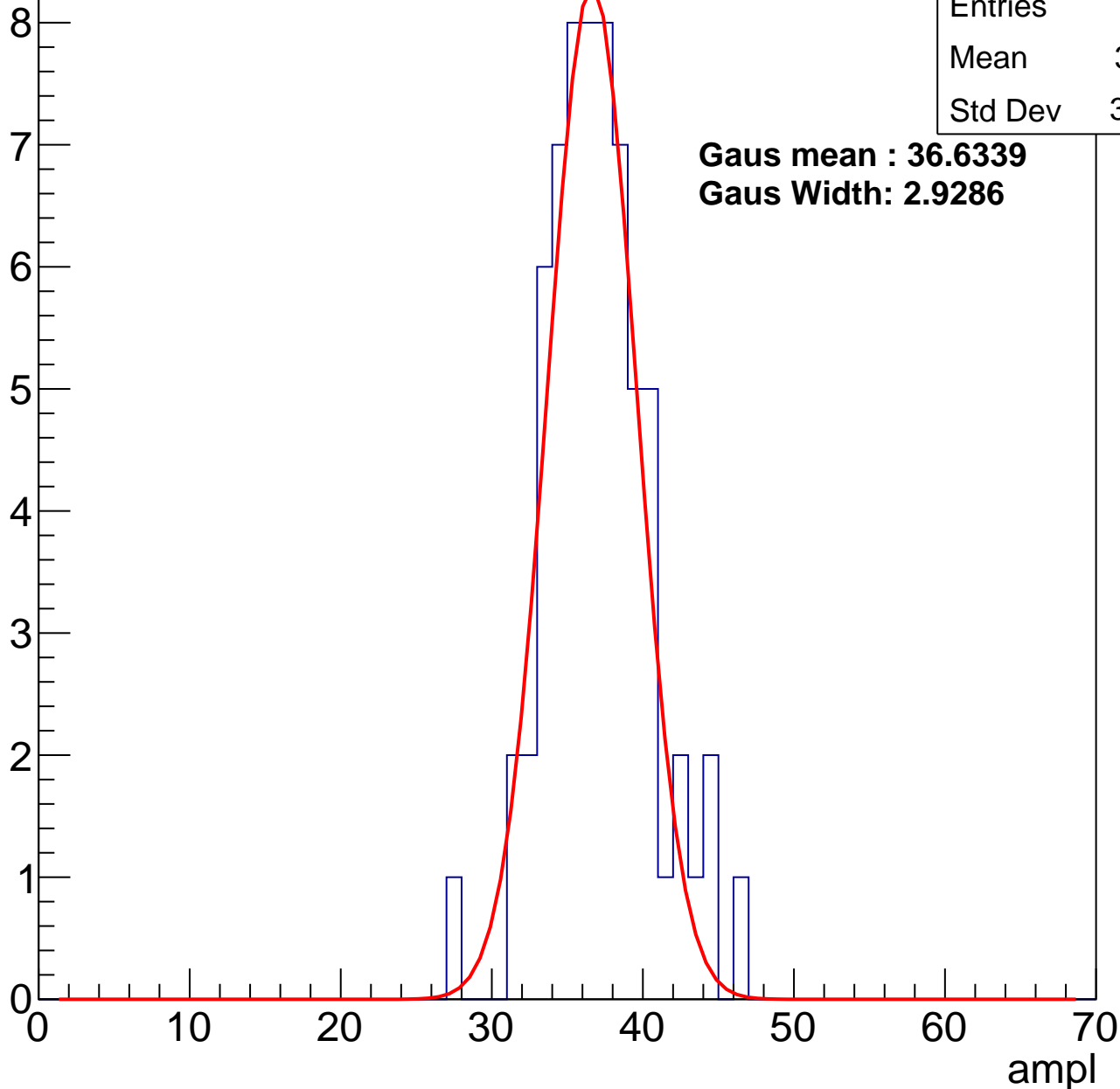
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	66
Mean	36.61
Std Dev	3.402

Gaus mean : 36.6339

Gaus Width: 2.9286



B1L003S, U21-ch116, adc2

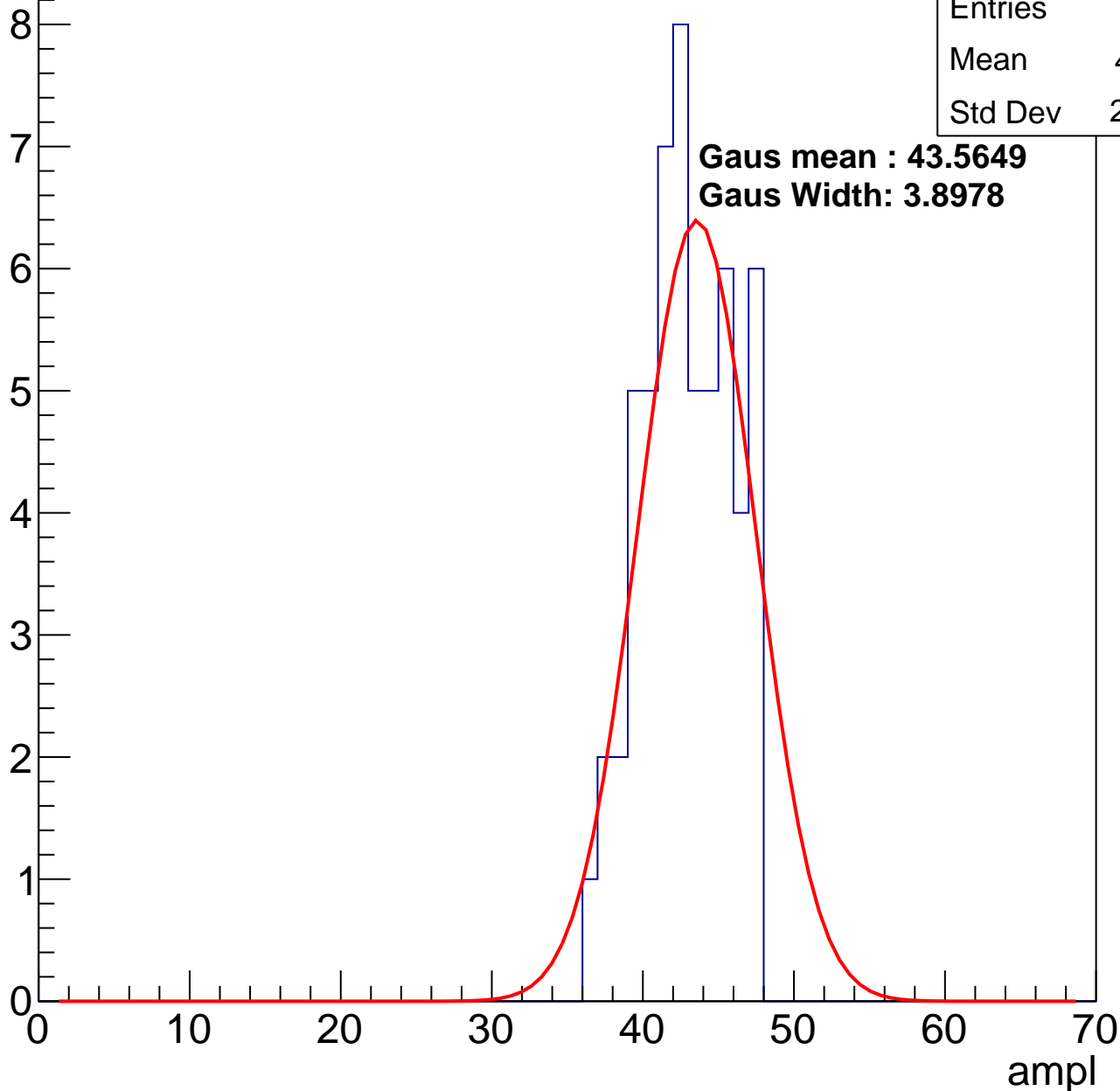
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	56
Mean	42.41
Std Dev	2.908

Gaus mean : 43.5649

Gaus Width: 3.8978

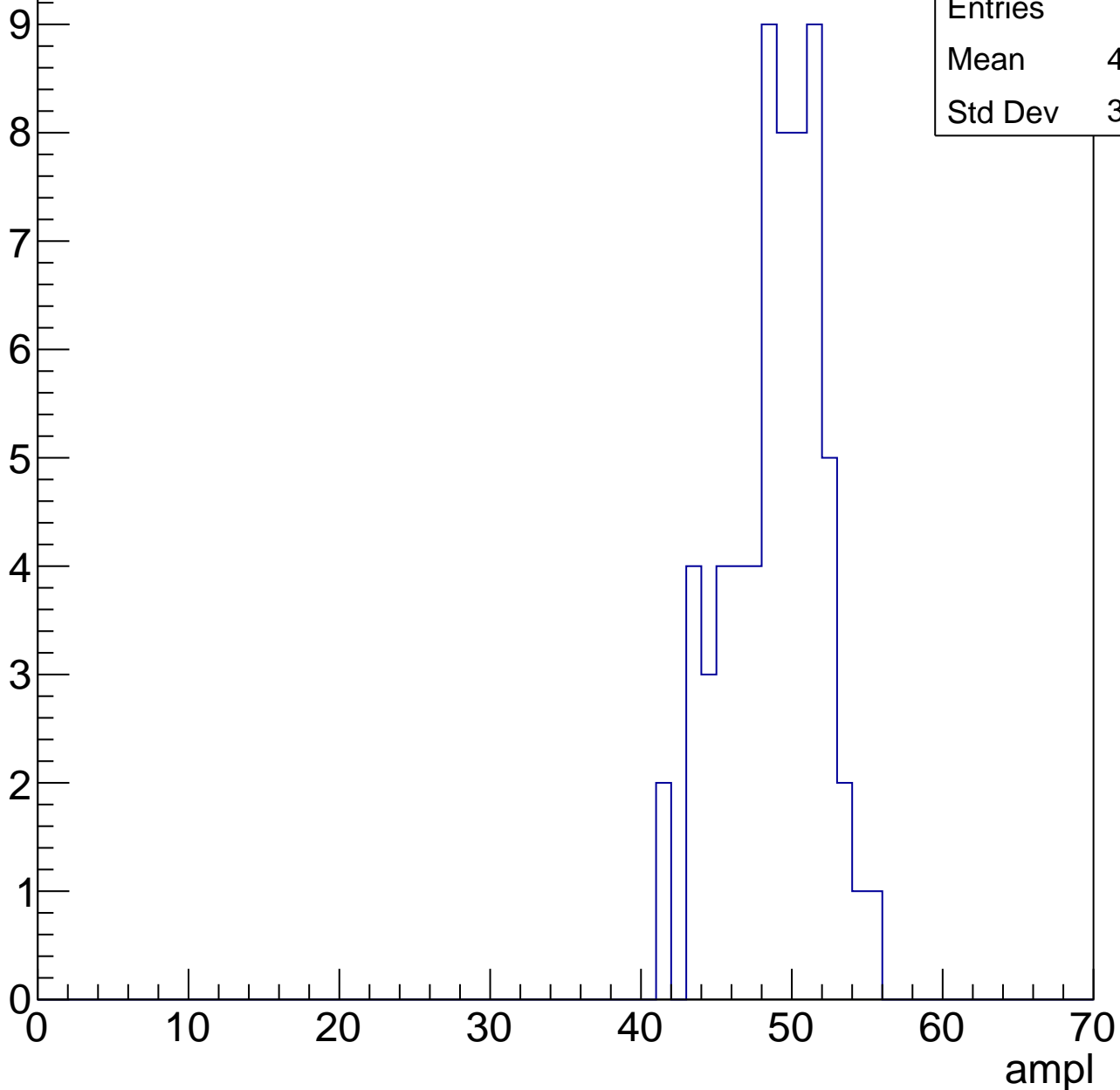


B1L003S, U21-ch116, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.38
Std Dev	3.135

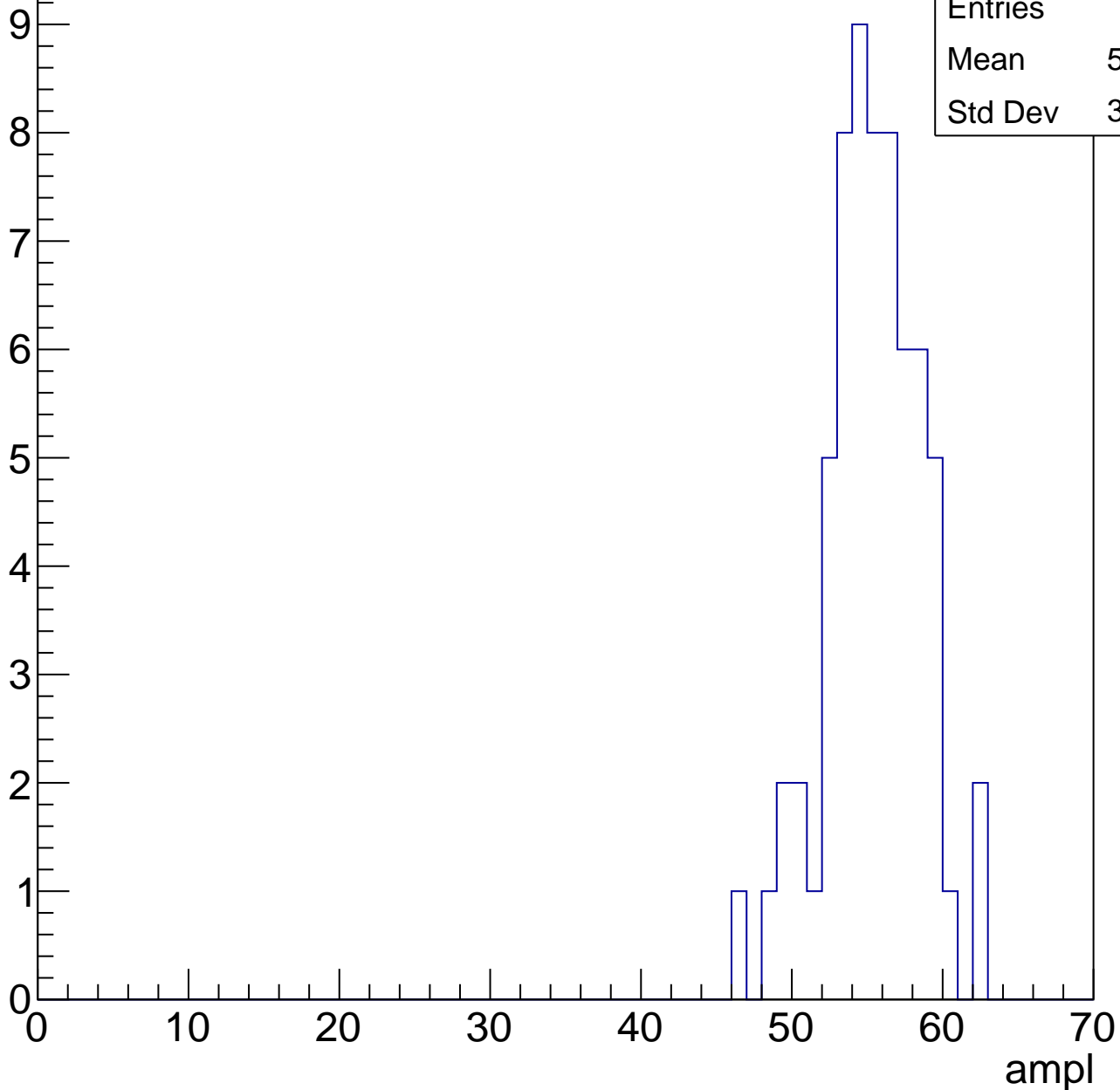


B1L003S, U21-ch116, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	54.92
Std Dev	3.144

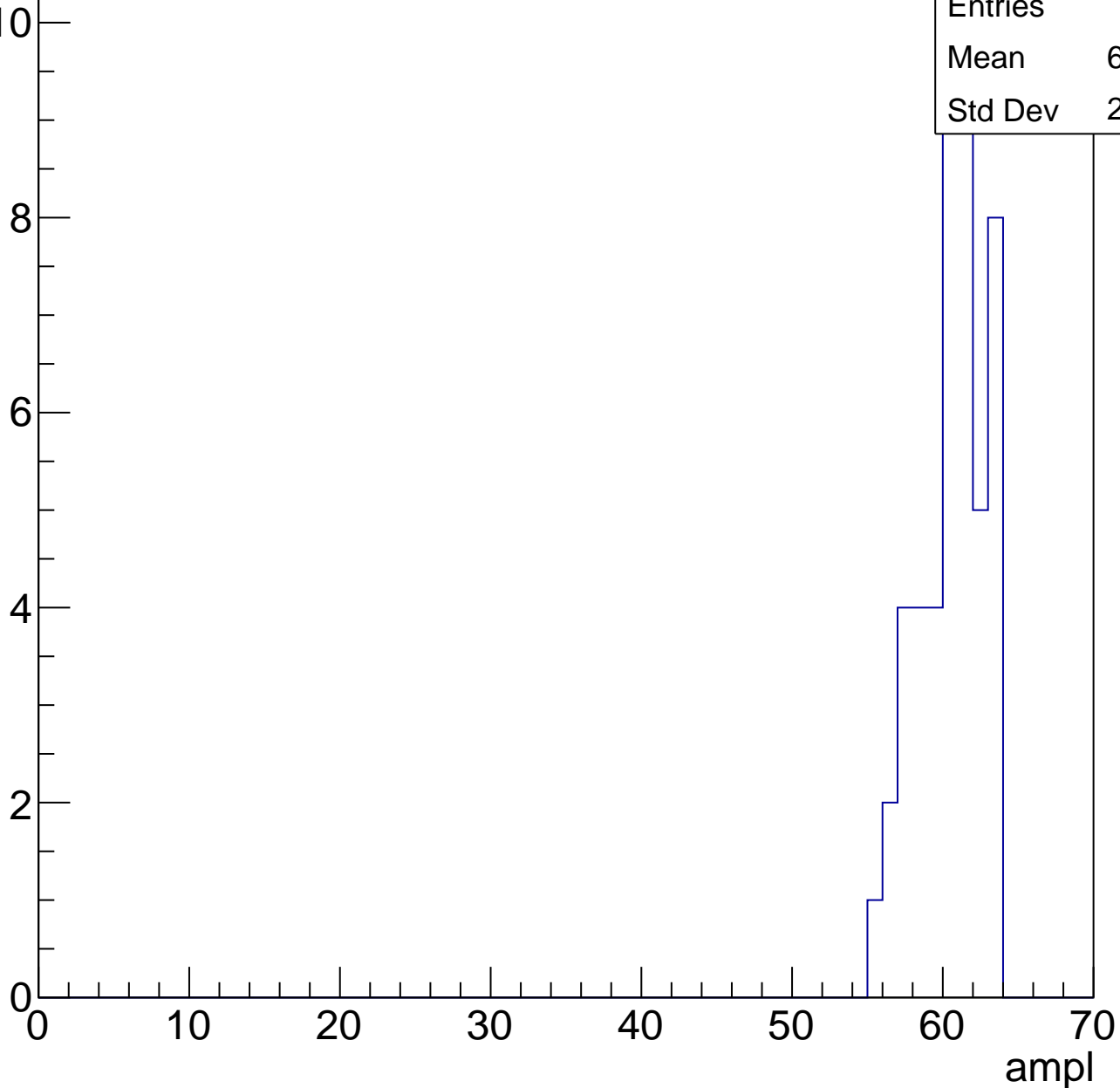


B1L003S, U21-ch116, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

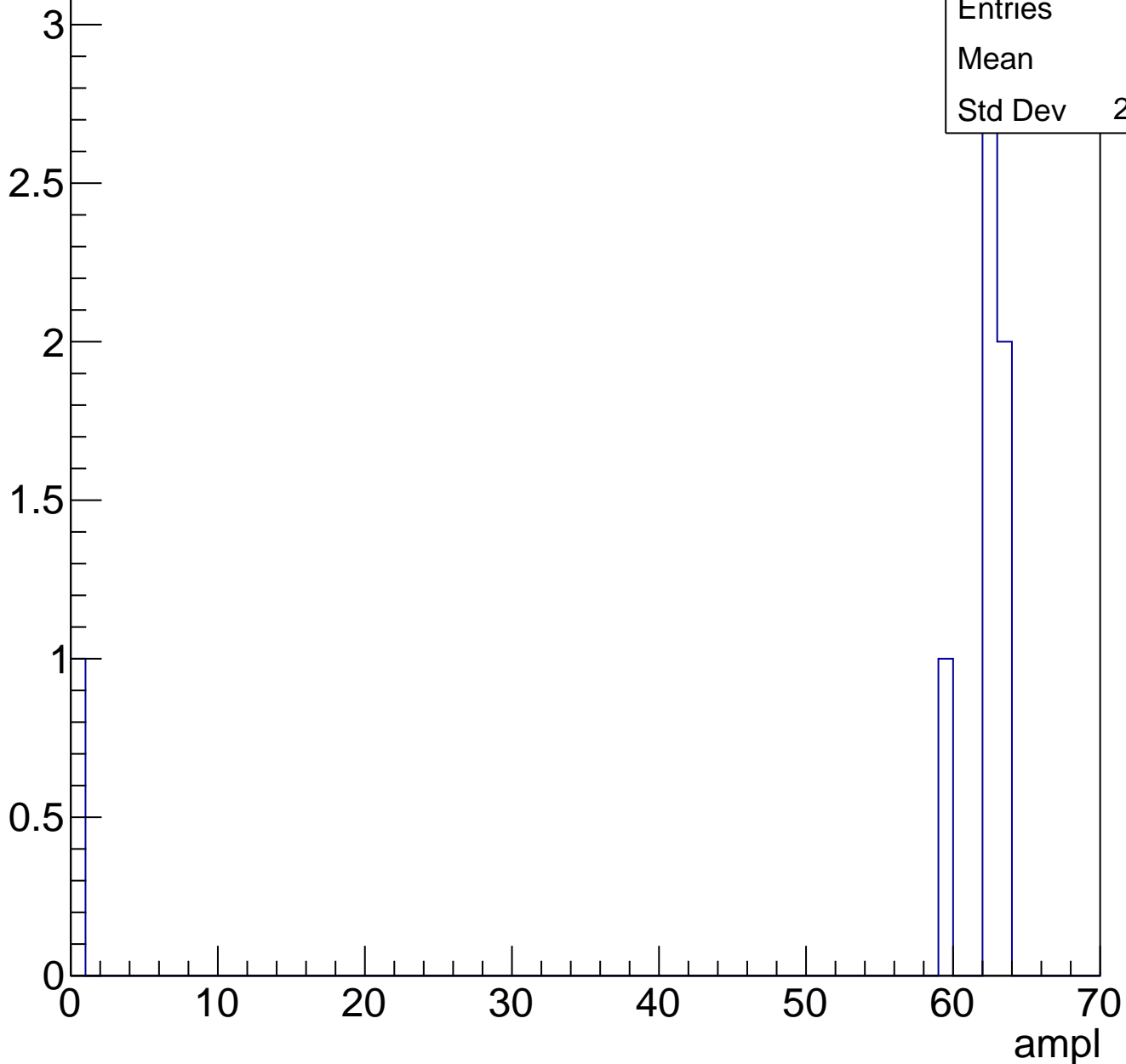
Entries	47
Mean	60.15
Std Dev	2.134



B1L003S, U21-ch116, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	7
Mean	53
Std Dev	21.67

B1L003S, U21-ch116, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch117, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	69
Mean	28.86
Std Dev	3.316

Gaus mean : 29.4977

Gaus Width: 3.7615

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

B1L003S, U21-ch117, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	60
Mean	35.13
Std Dev	3.144

Gaus mean : 35.8110

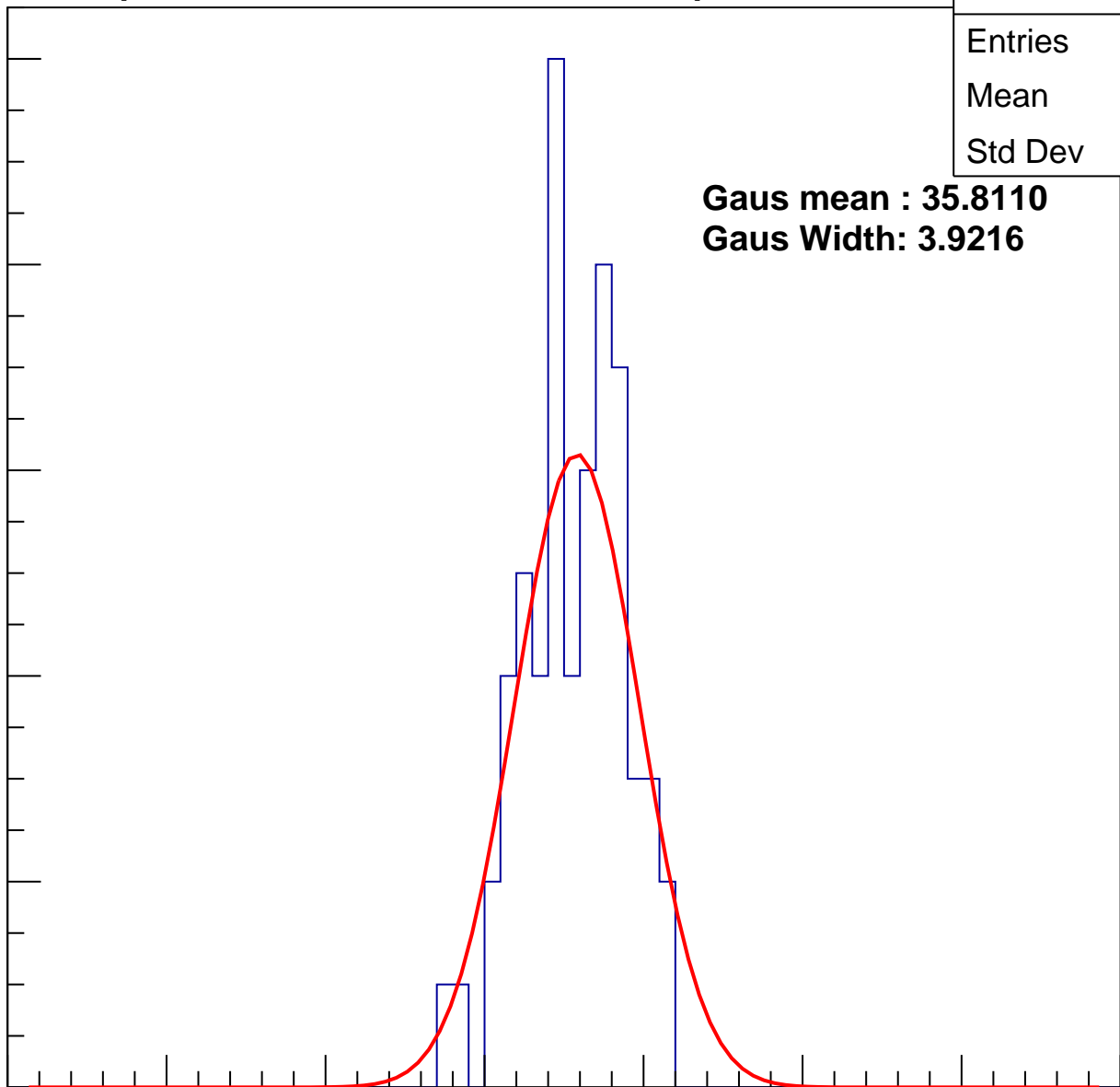
Gaus Width: 3.9216

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch117, adc2

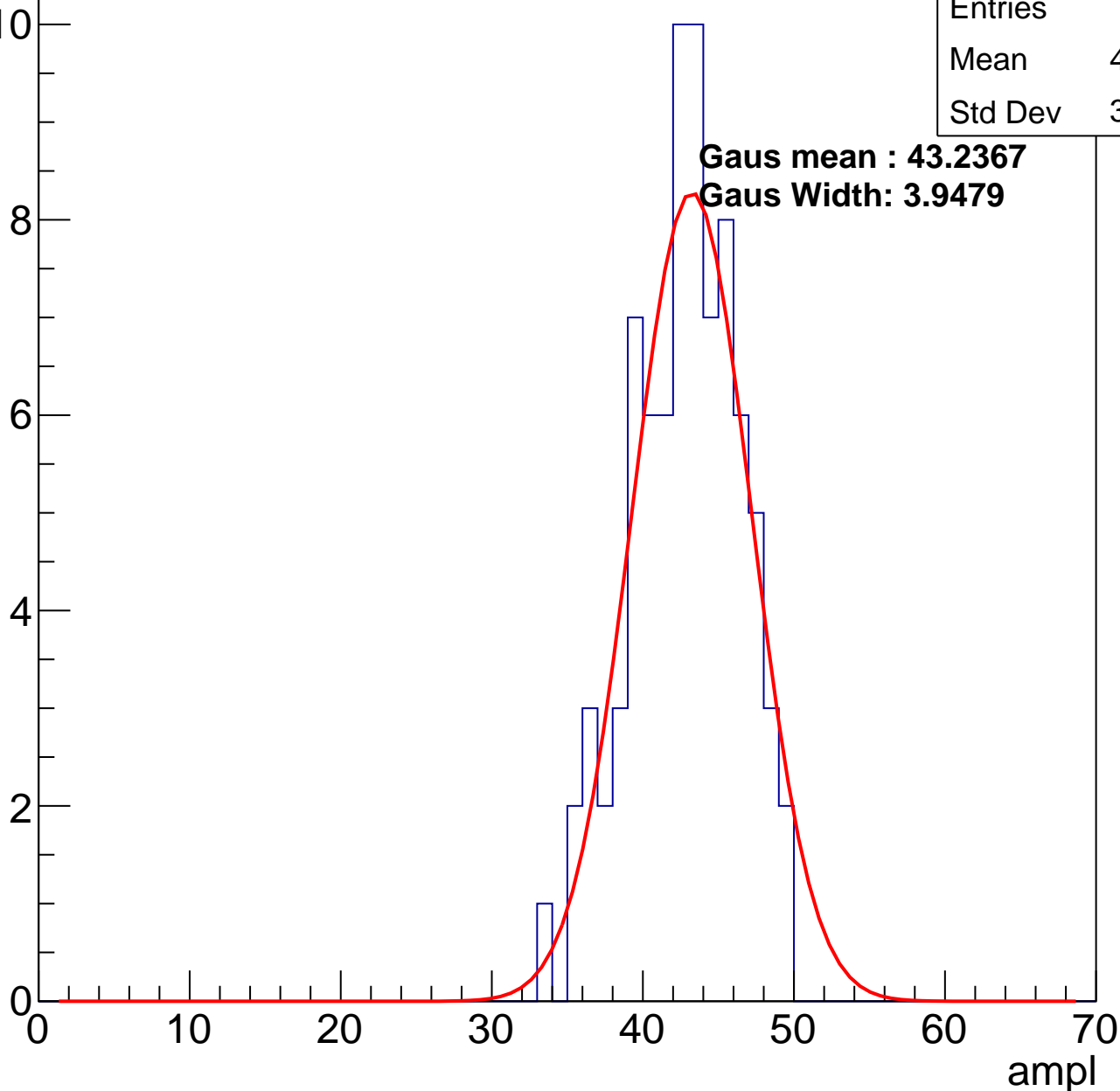
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	81
Mean	42.33
Std Dev	3.524

Gaus mean : 43.2367

Gaus Width: 3.9479

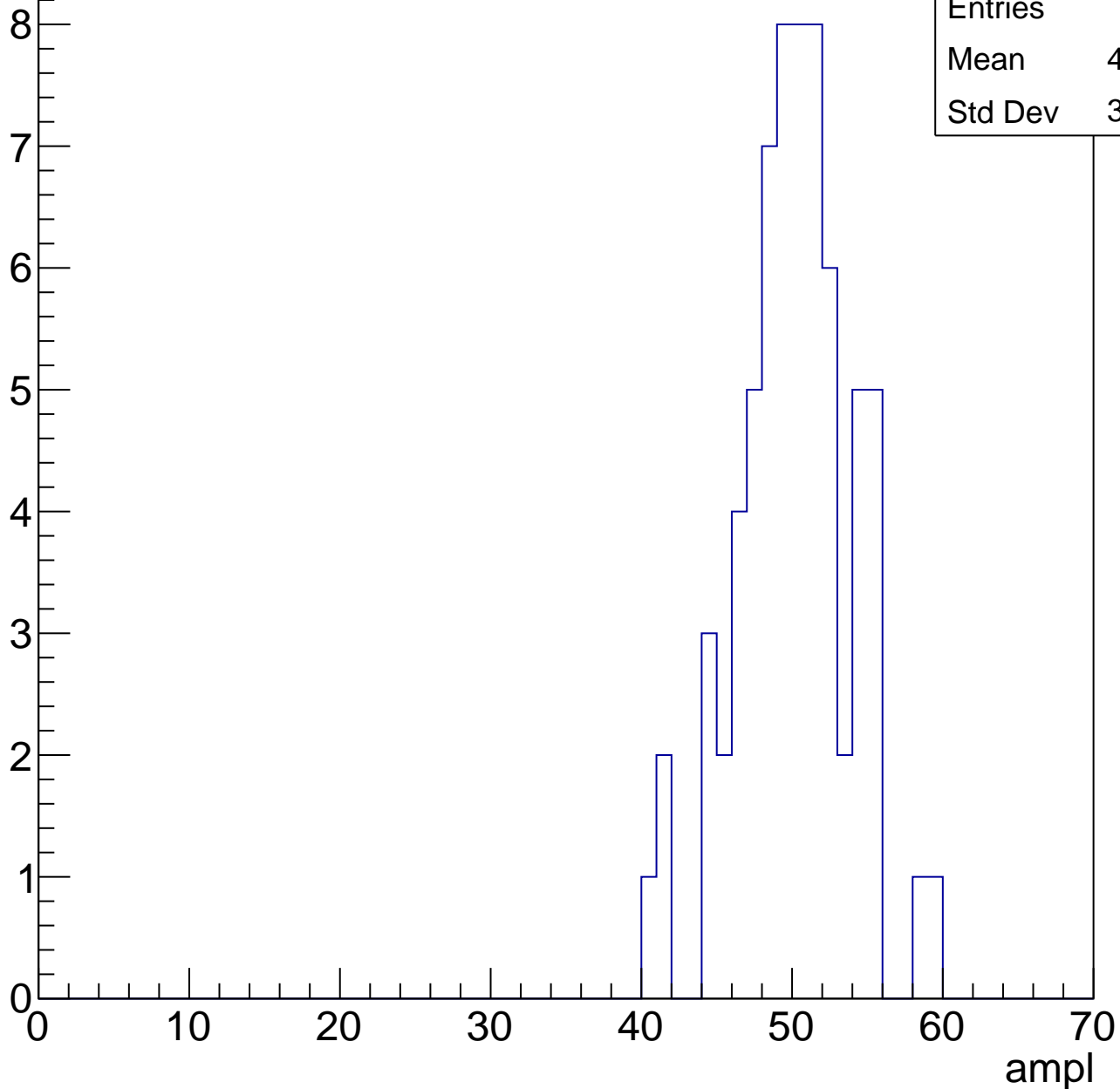


B1L003S, U21-ch117, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	49.69
Std Dev	3.754

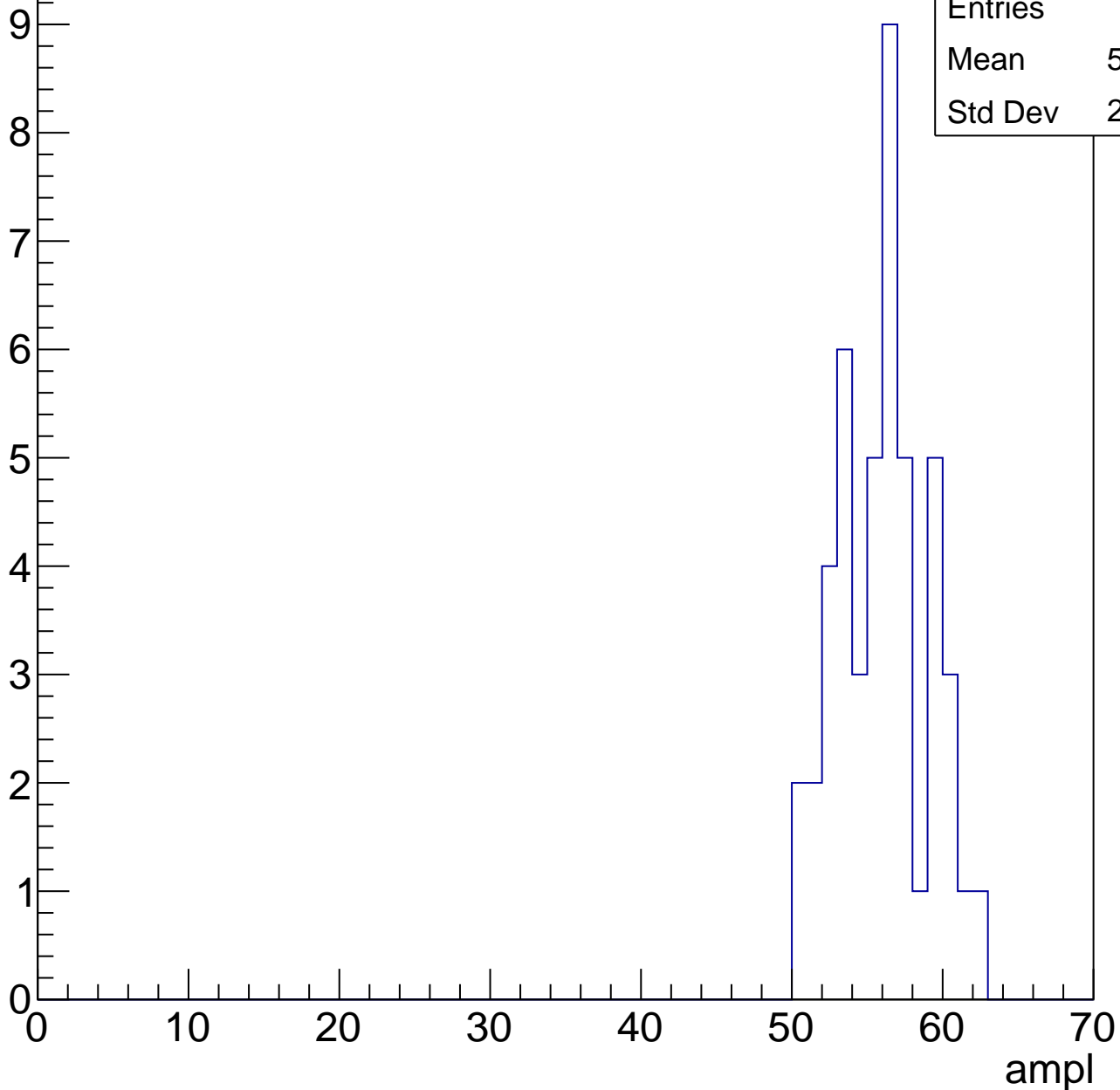


B1L003S, U21-ch117, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	47
Mean	55.53
Std Dev	2.952

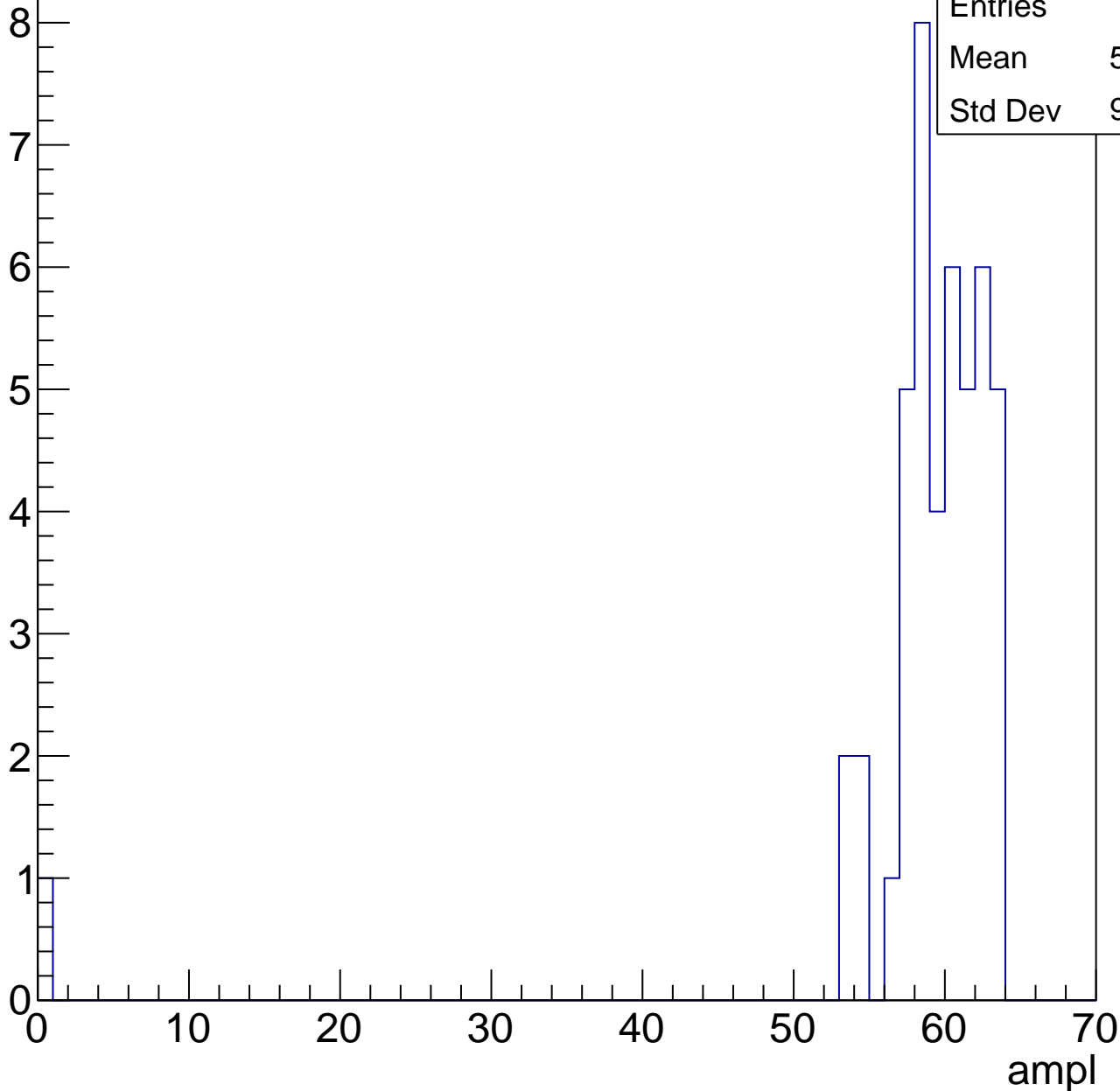


B1L003S, U21-ch117, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	57.93
Std Dev	9.127

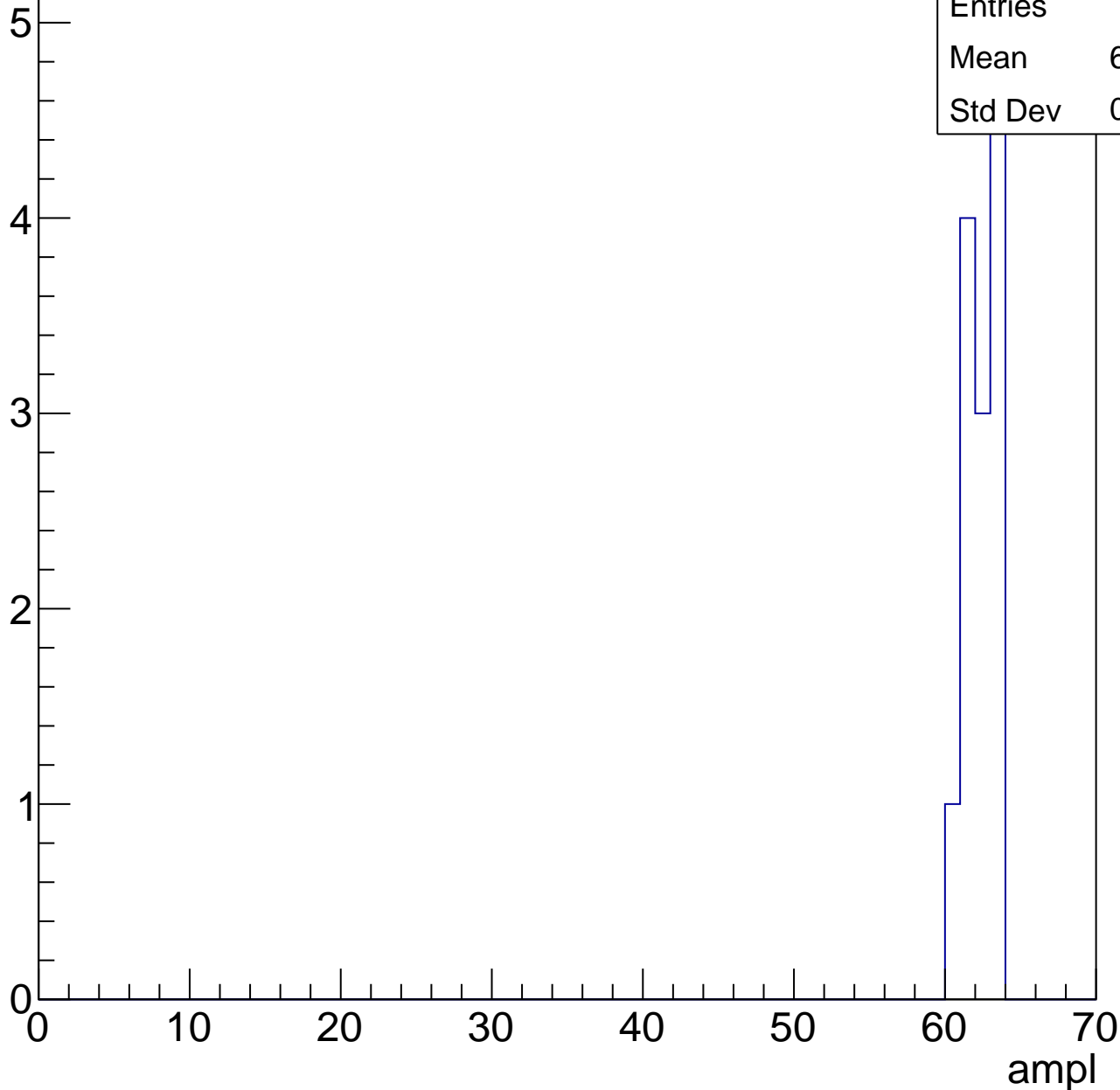


B1L003S, U21-ch117, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	13
Mean	61.92
Std Dev	0.997



B1L003S, U21-ch117, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch118, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	56
Mean	29.95
Std Dev	4.937

Gaus mean : 30.3396

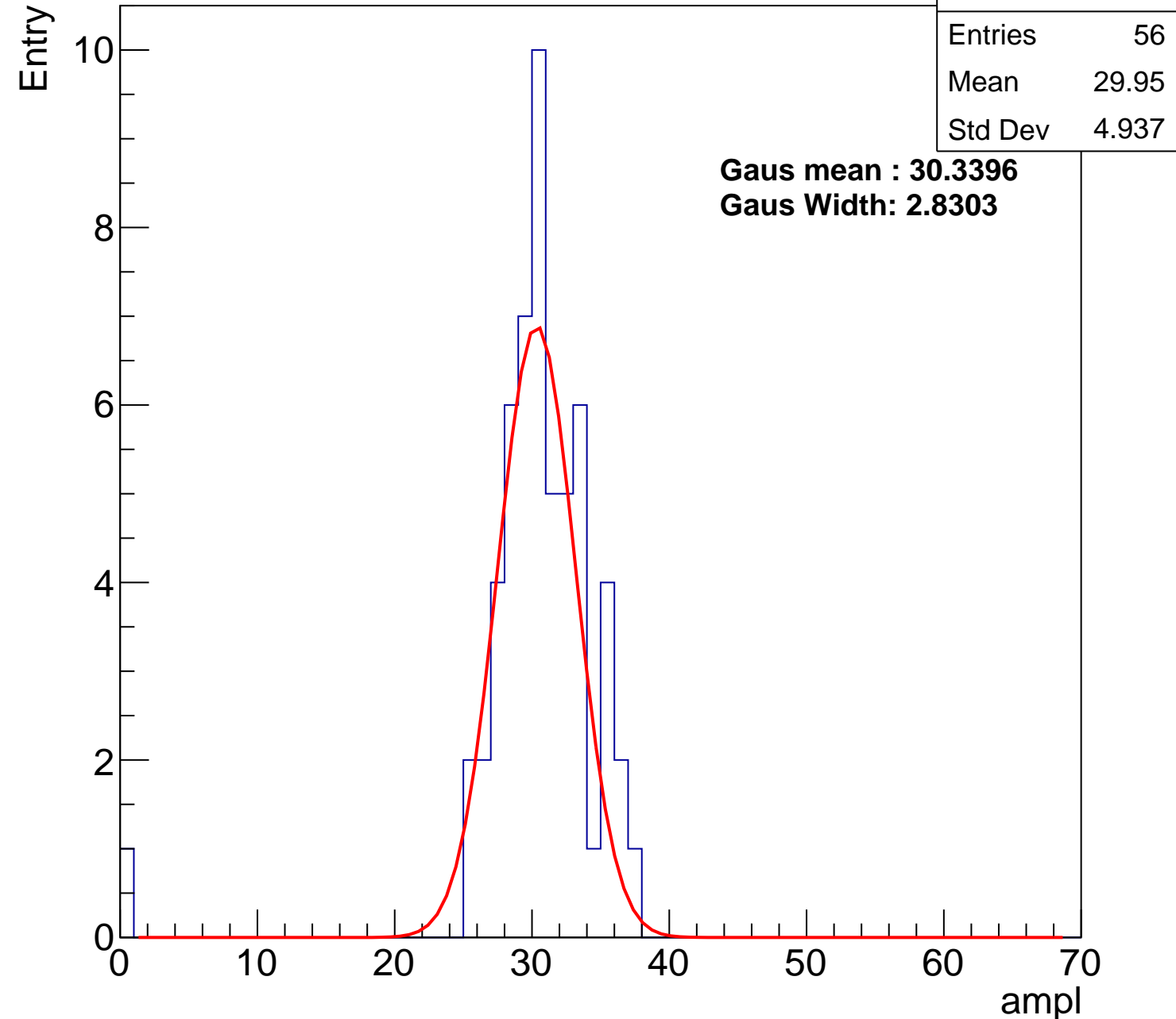
Gaus Width: 2.8303

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch118, adc1

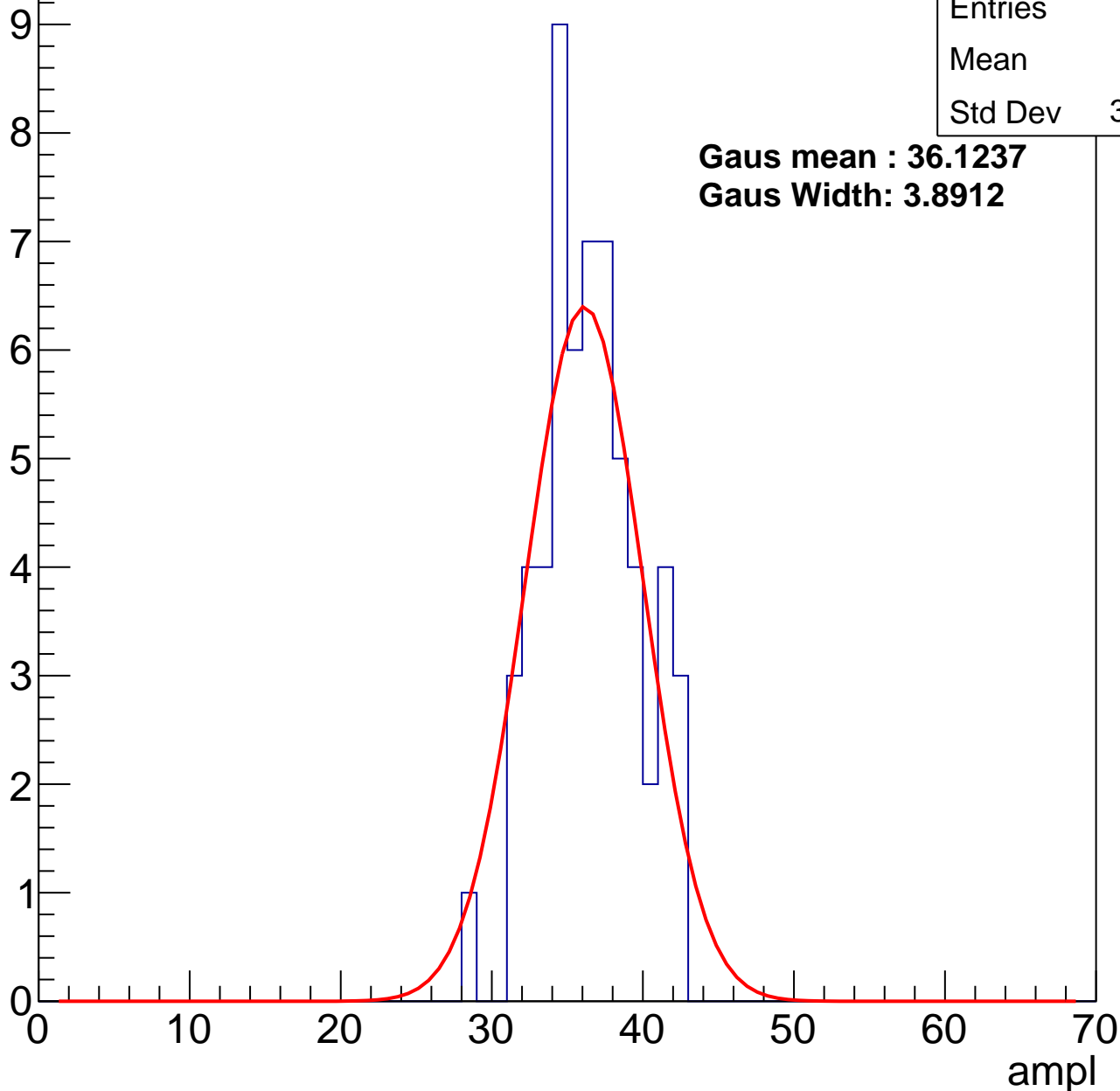
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36
Std Dev	3.146

Gaus mean : 36.1237

Gaus Width: 3.8912



B1L003S, U21-ch118, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	42.66
Std Dev	3.08

Gaus mean : 43.1137

Gaus Width: 3.4103

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

0

2

4

6

8

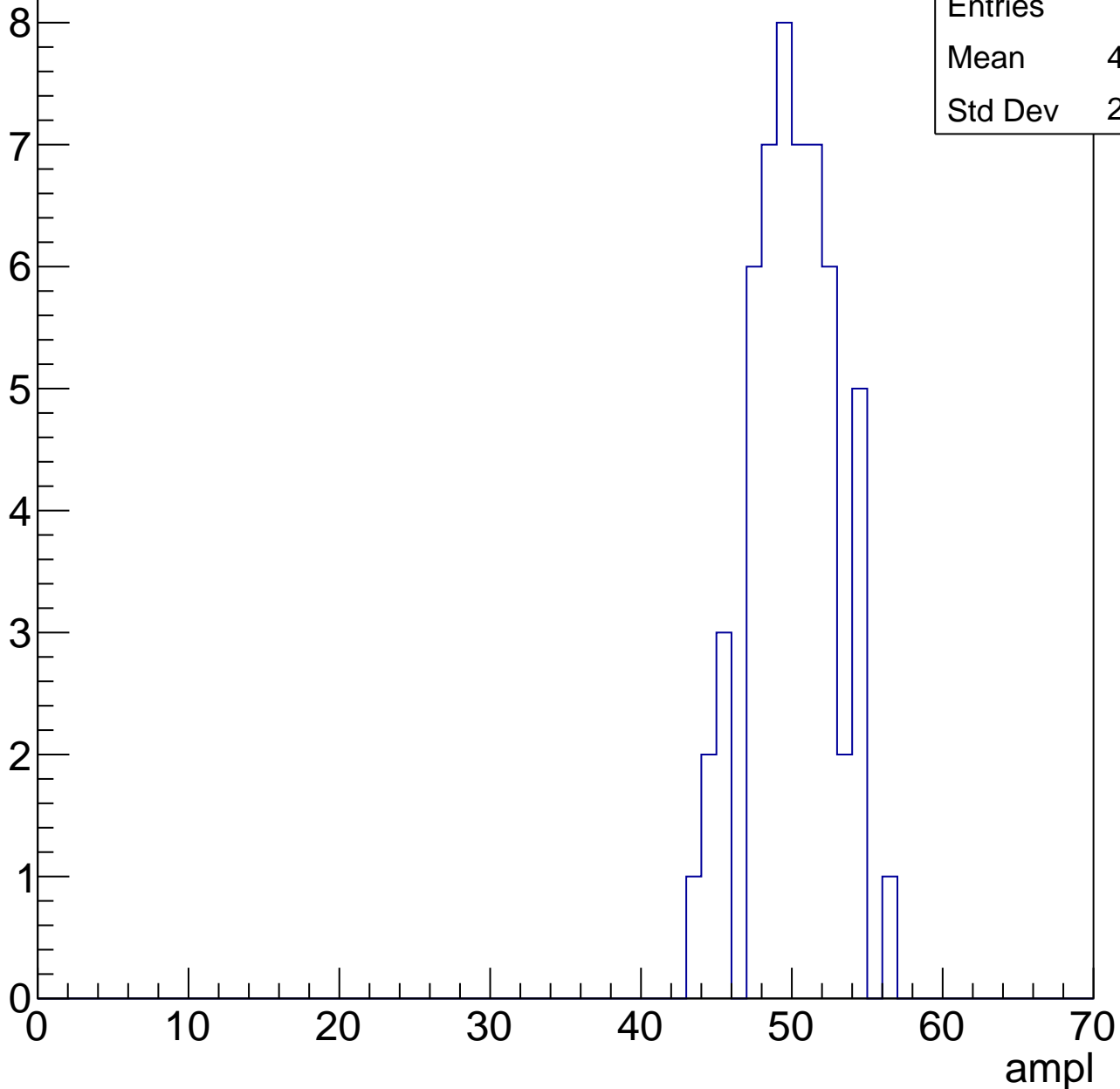
10

B1L003S, U21-ch118, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	49.58
Std Dev	2.833

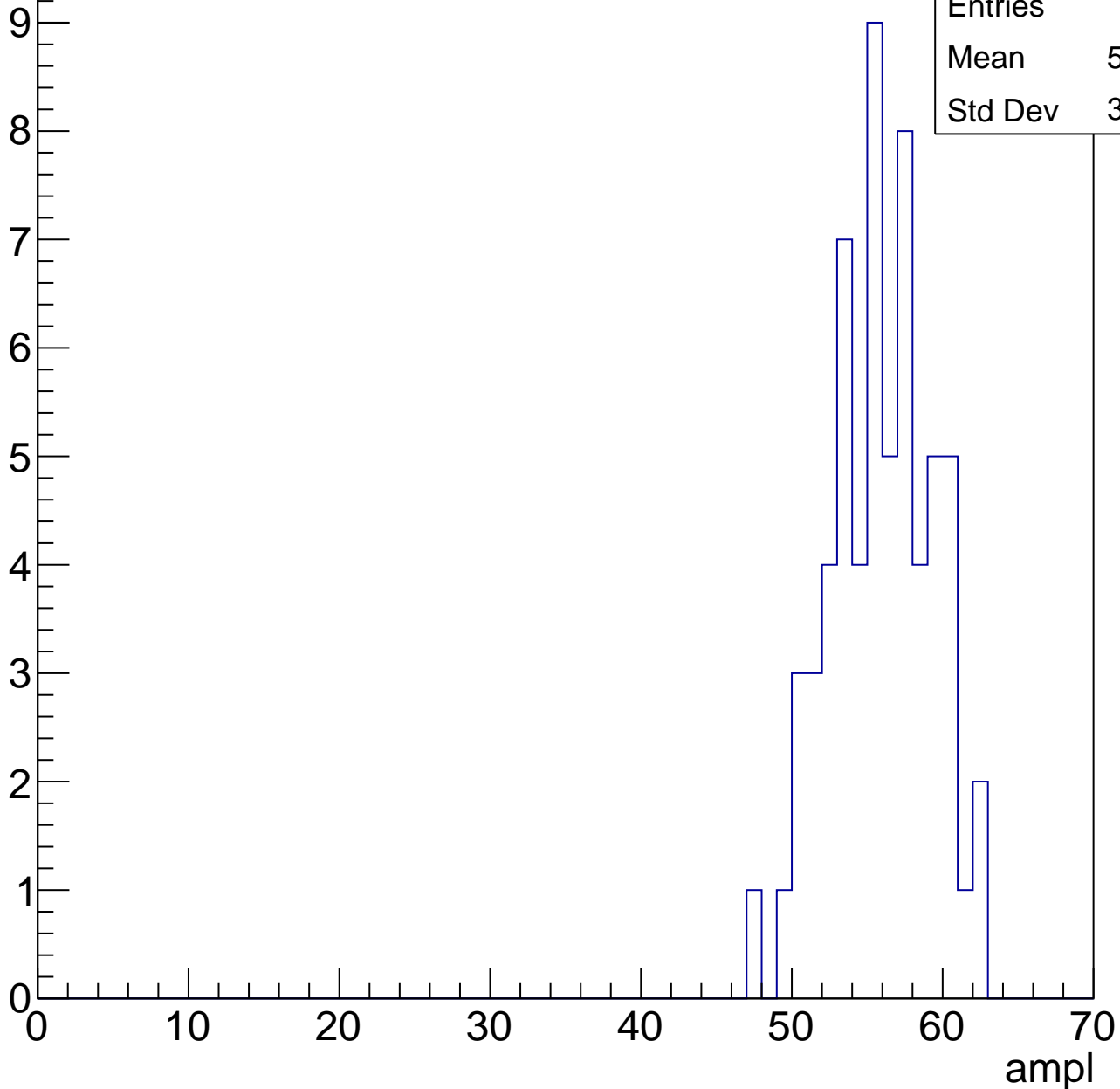


B1L003S, U21-ch118, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	55.44
Std Dev	3.339

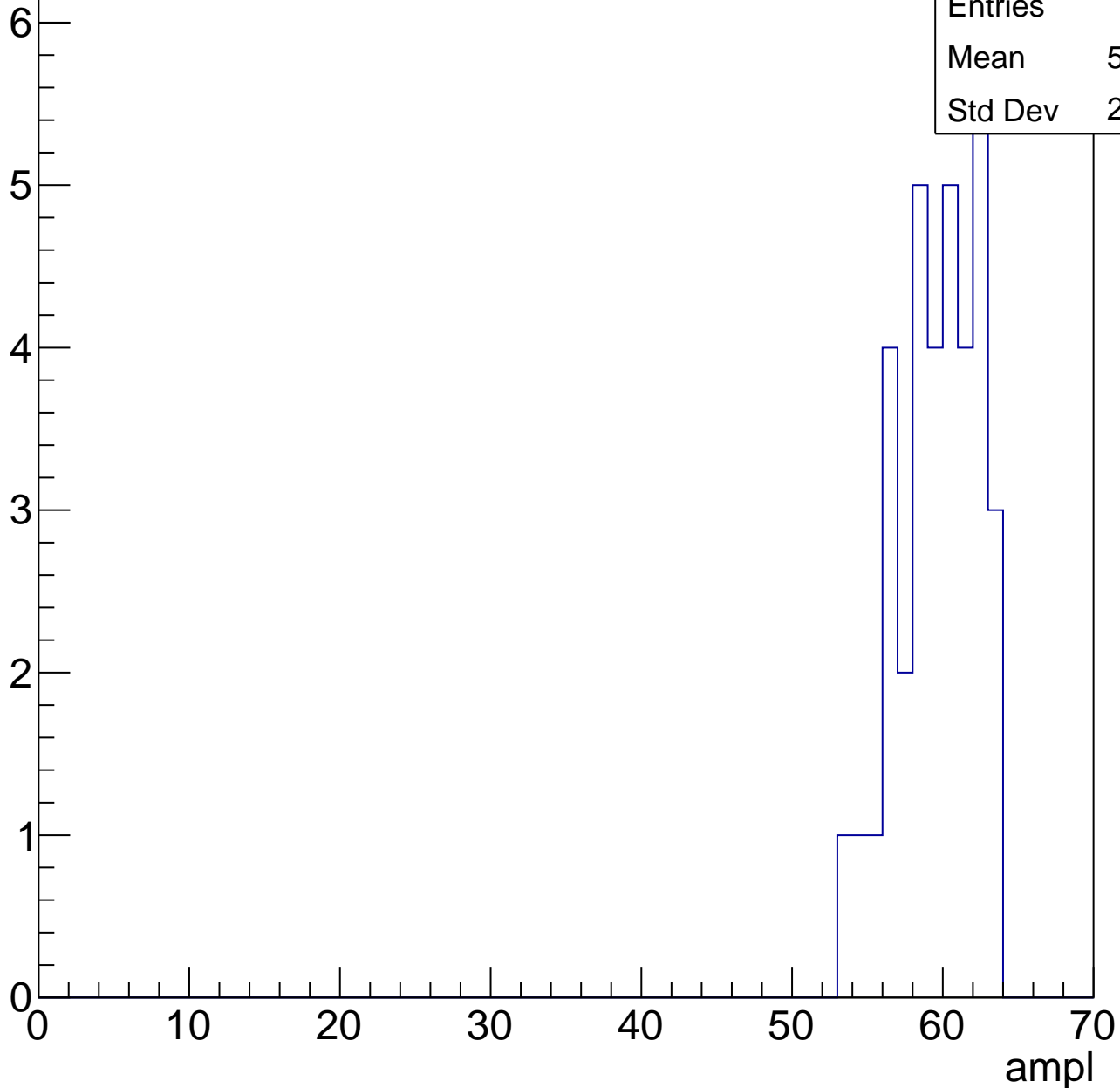


B1L003S, U21-ch118, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	36
Mean	59.19
Std Dev	2.623

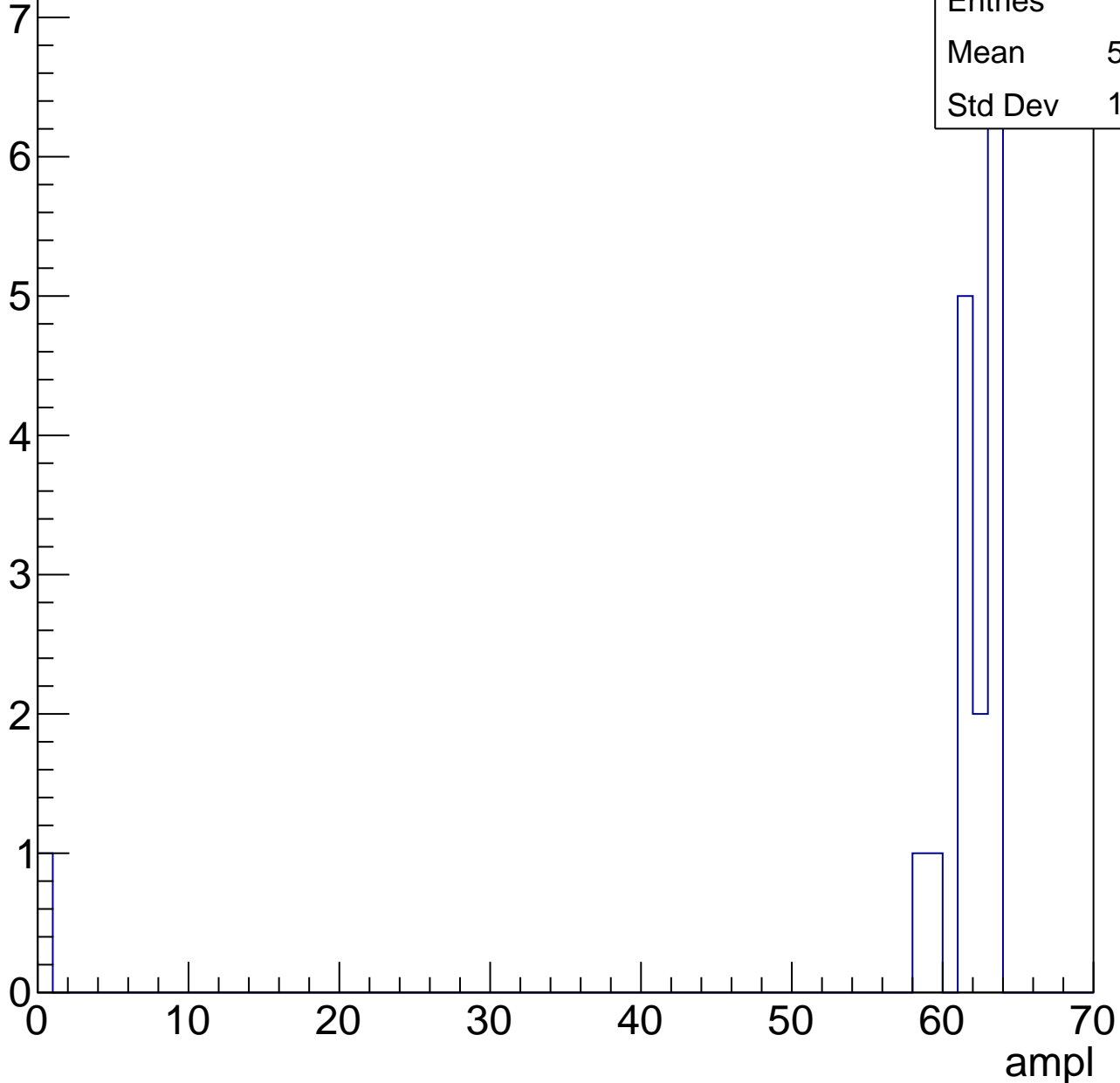


B1L003S, U21-ch118, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	17
Mean	58.06
Std Dev	14.59



B1L003S, U21-ch118, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	2
Mean	0
Std Dev	0

B1L003S, U21-ch119, adc0

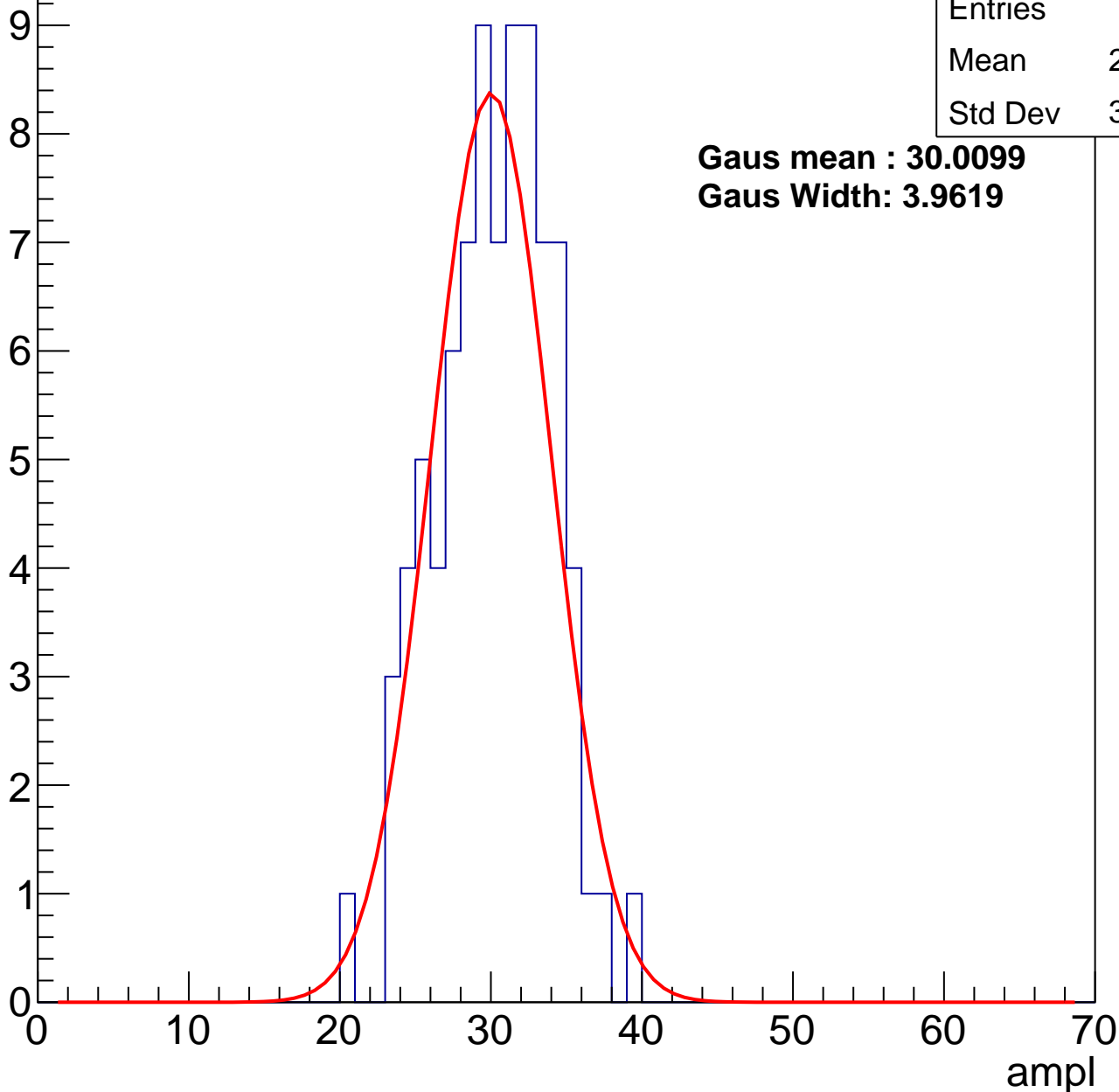
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	85
Mean	29.78
Std Dev	3.683

Gaus mean : 30.0099

Gaus Width: 3.9619



B1L003S, U21-ch119, adc1

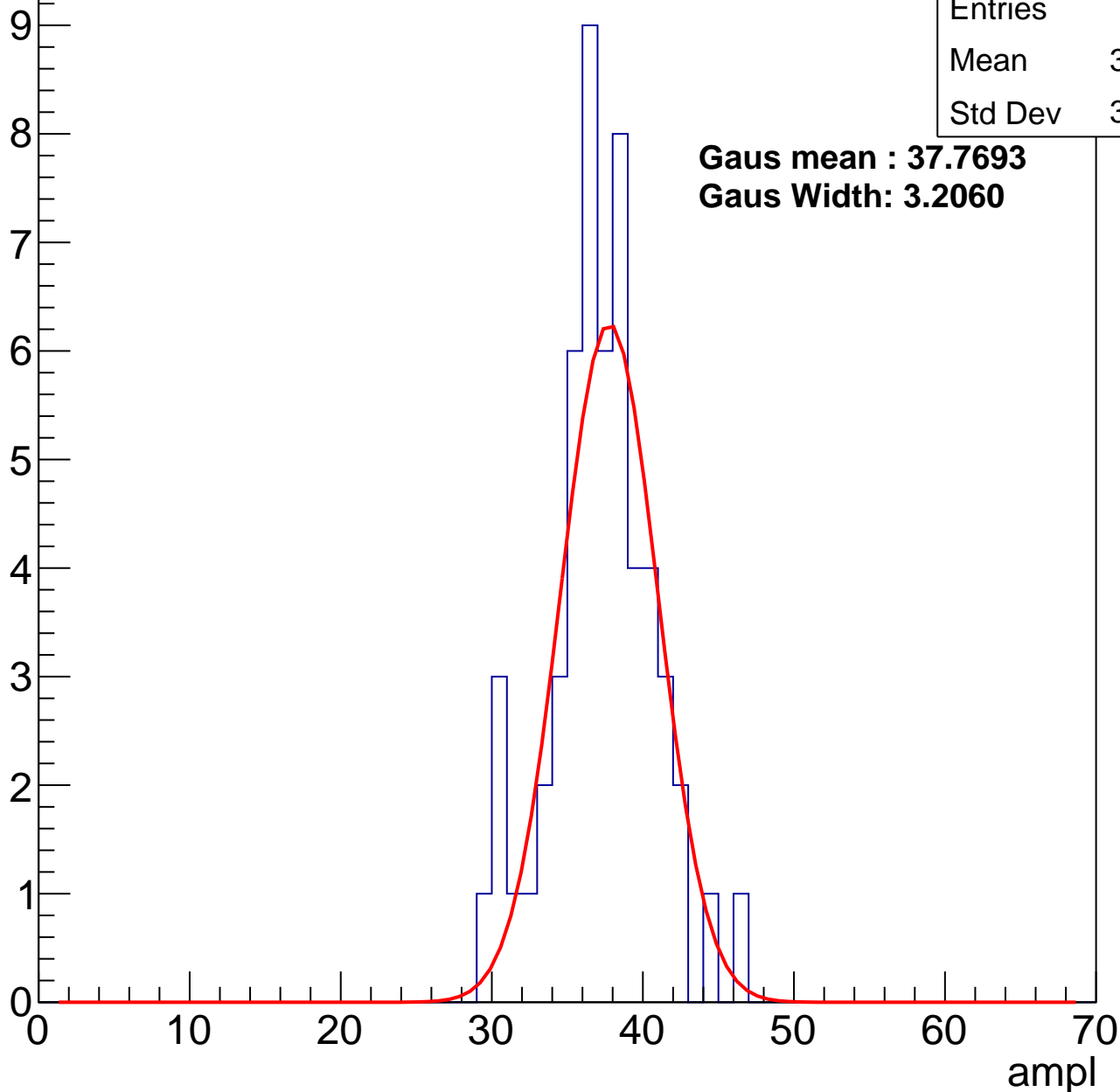
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	55
Mean	36.78
Std Dev	3.436

Gaus mean : 37.7693

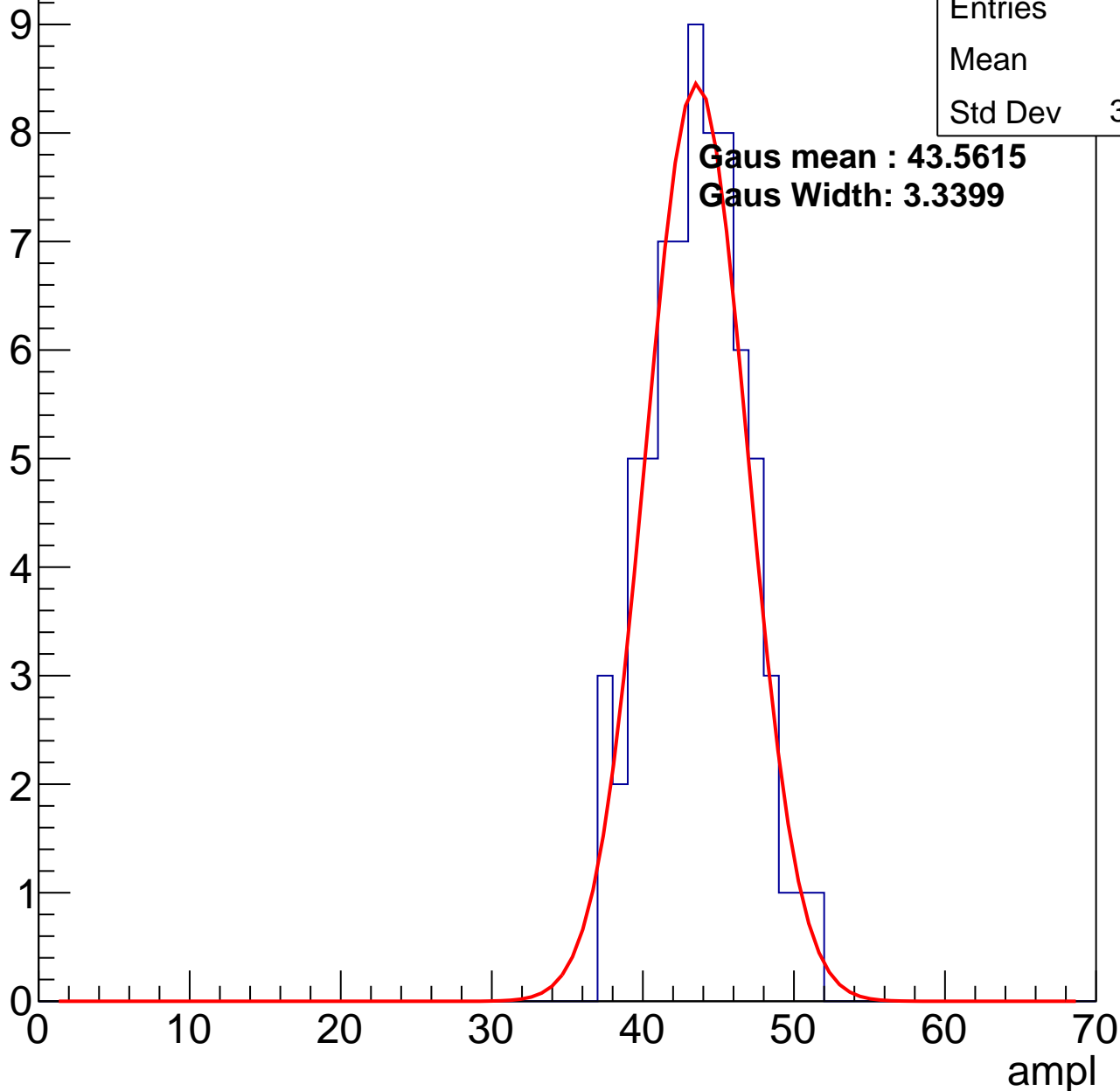
Gaus Width: 3.2060



B1L003S, U21-ch119, adc2

calib_packv5_042523_0143.root, FC#13, port D2

Entry

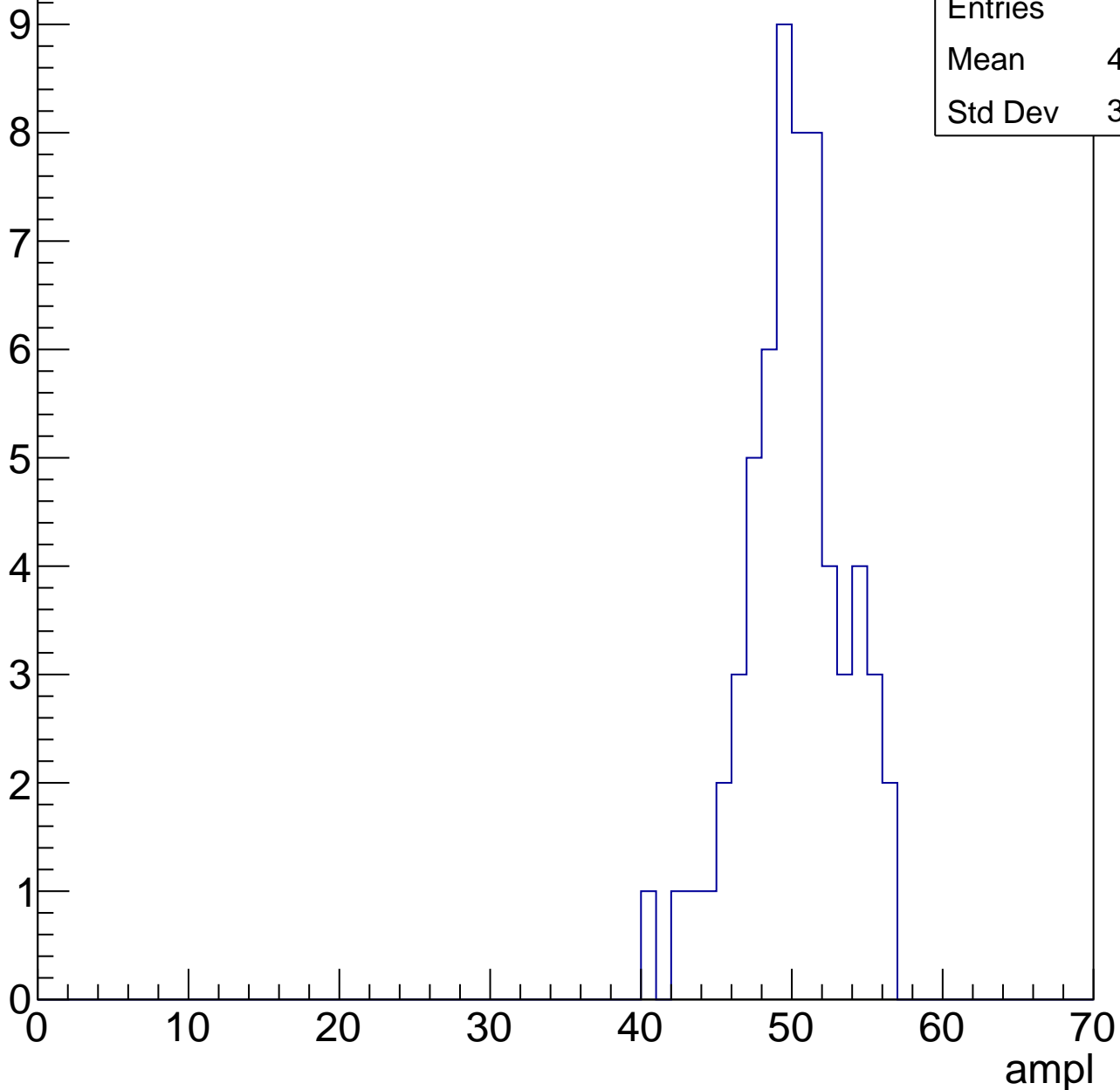


B1L003S, U21-ch119, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	49.66
Std Dev	3.338

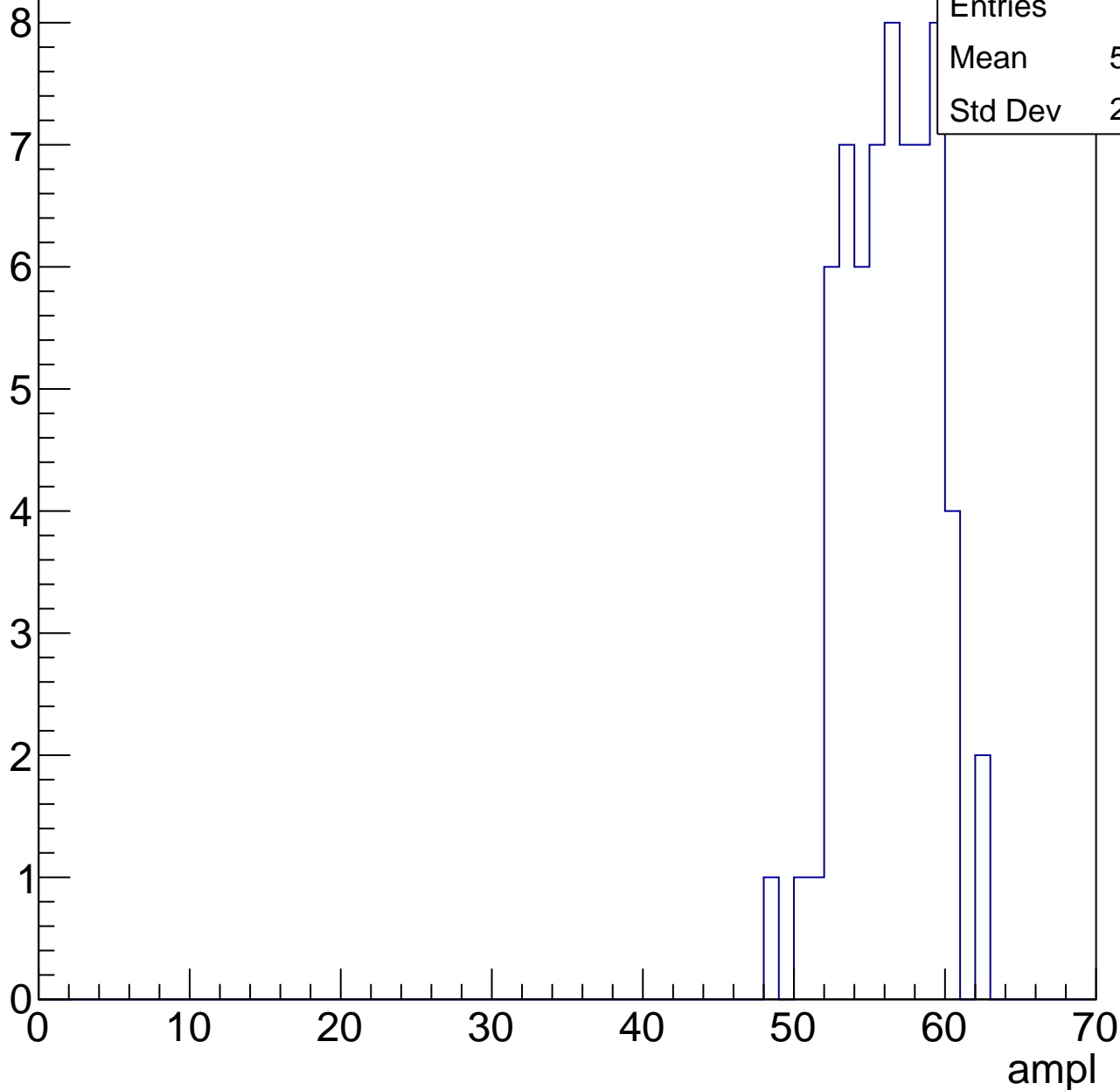


B1L003S, U21-ch119, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	55.85
Std Dev	2.926

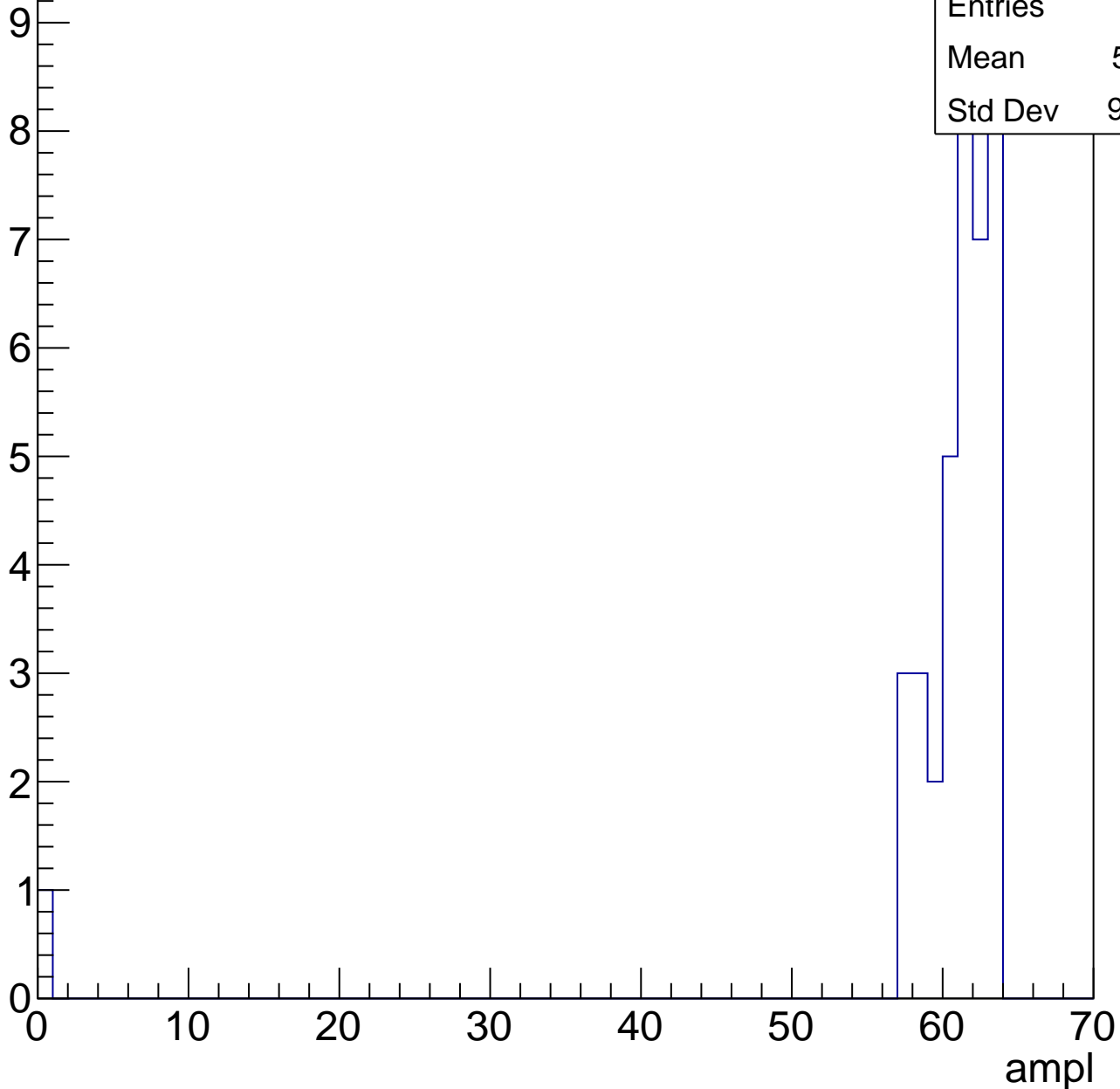


B1L003S, U21-ch119, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	38
Mean	59.21
Std Dev	9.903



B1L003S, U21-ch119, adc6

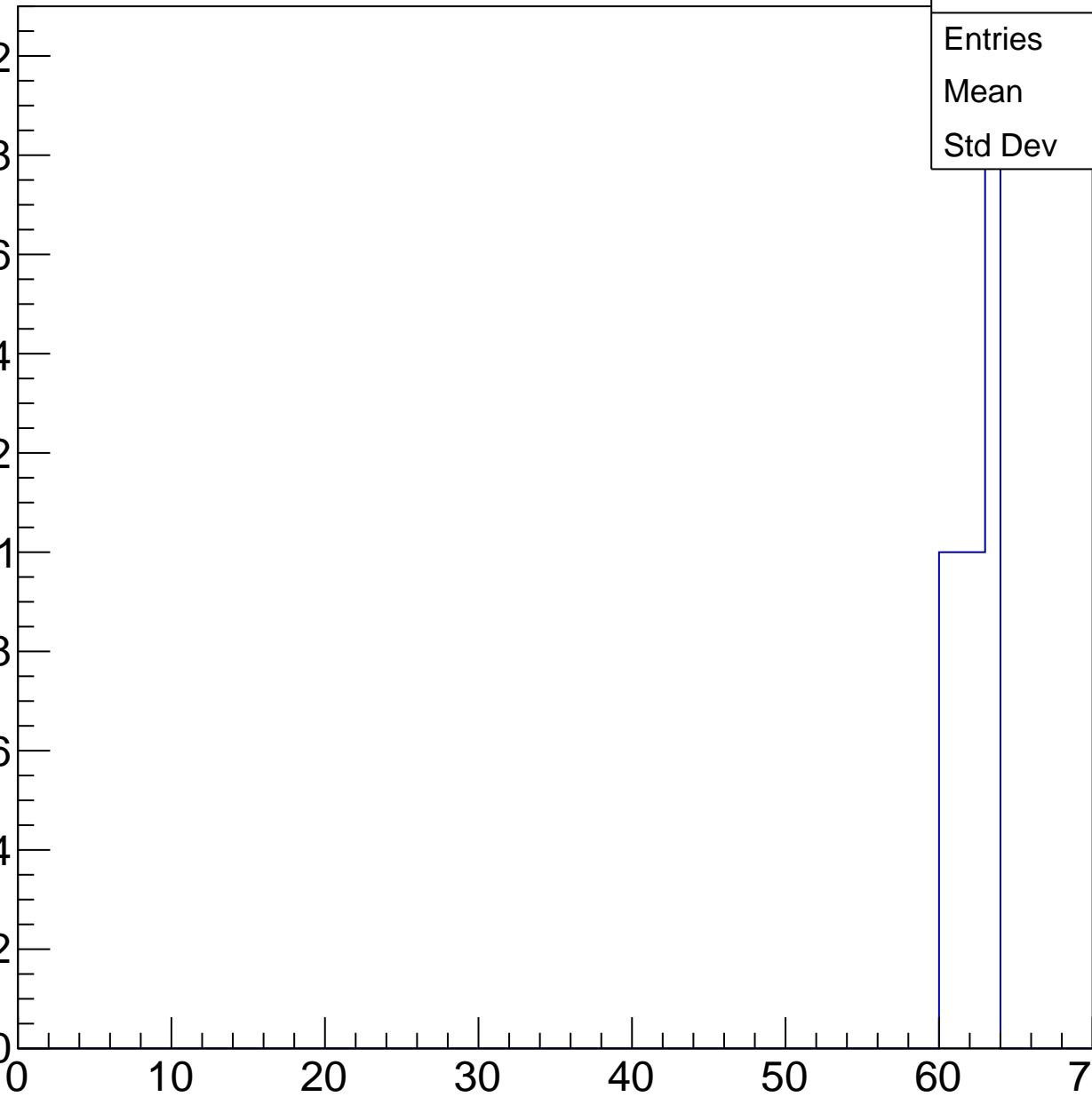
calib_packv5_042523_0143.root, FC#13, port D2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl



B1L003S, U21-ch119, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch120, adc0

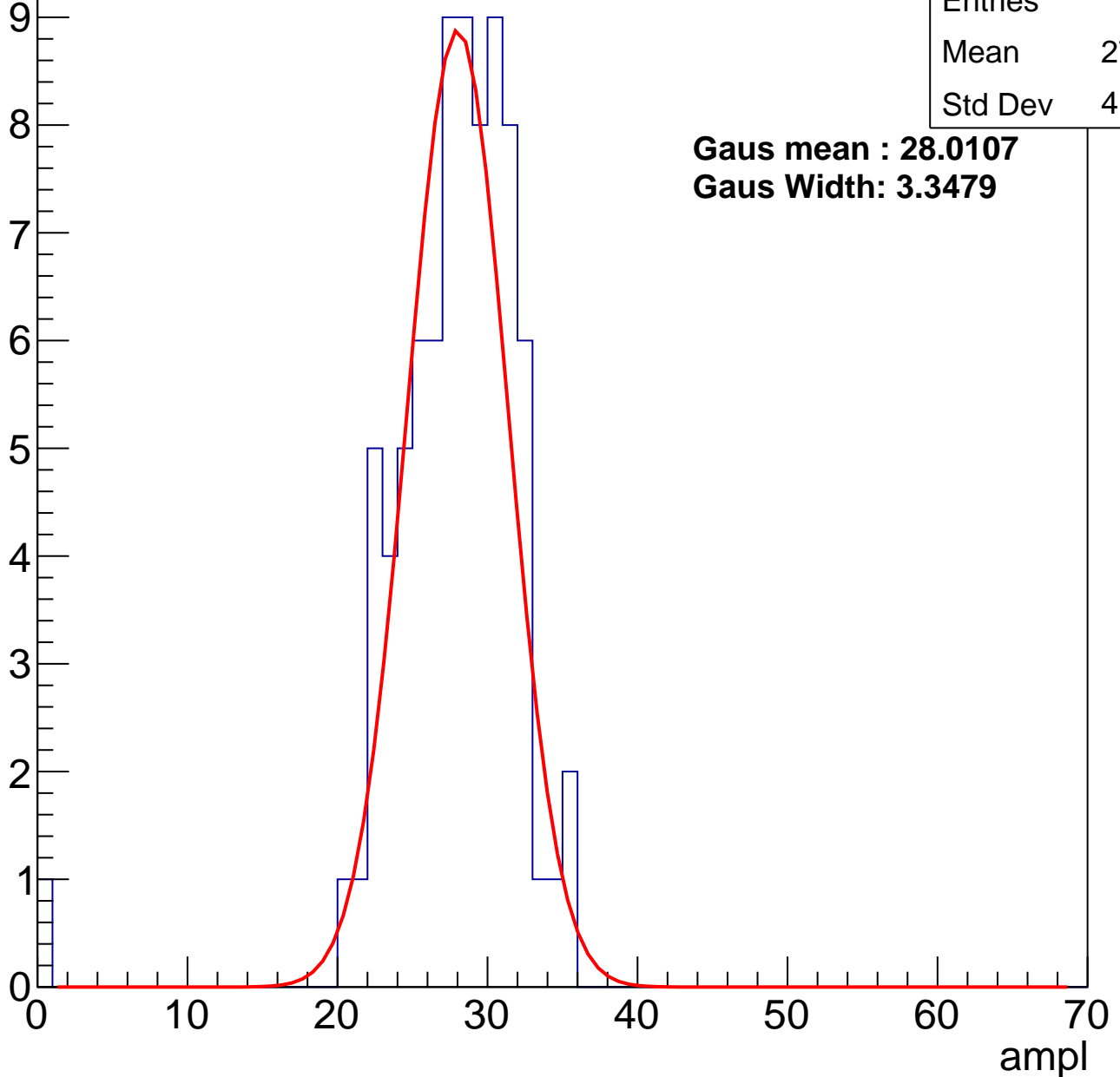
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	82
Mean	27.35
Std Dev	4.527

Gaus mean : 28.0107

Gaus Width: 3.3479

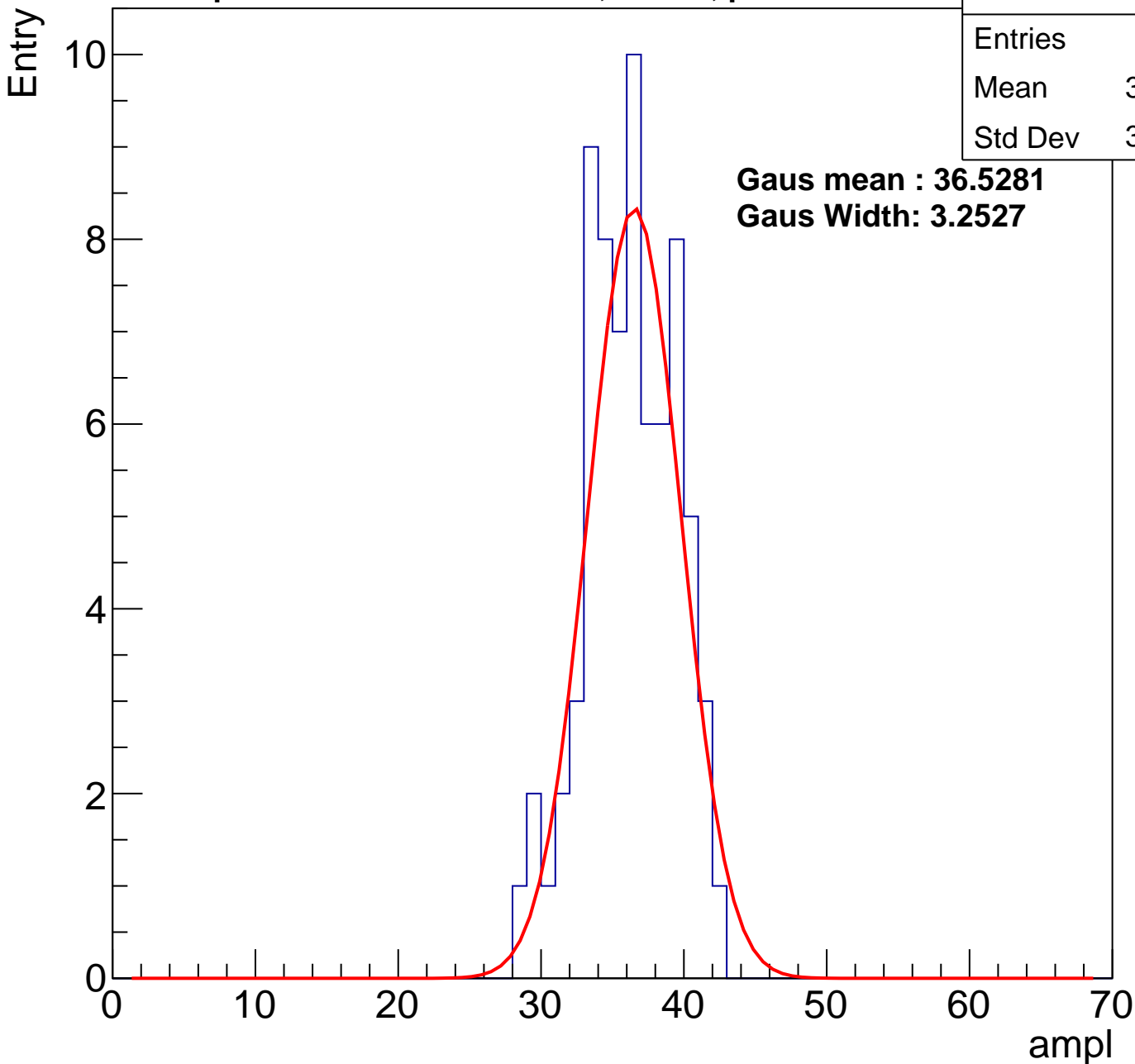


B1L003S, U21-ch120, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	72
Mean	35.76
Std Dev	3.129

Gaus mean : 36.5281
Gaus Width: 3.2527



B1L003S, U21-ch120, adc2

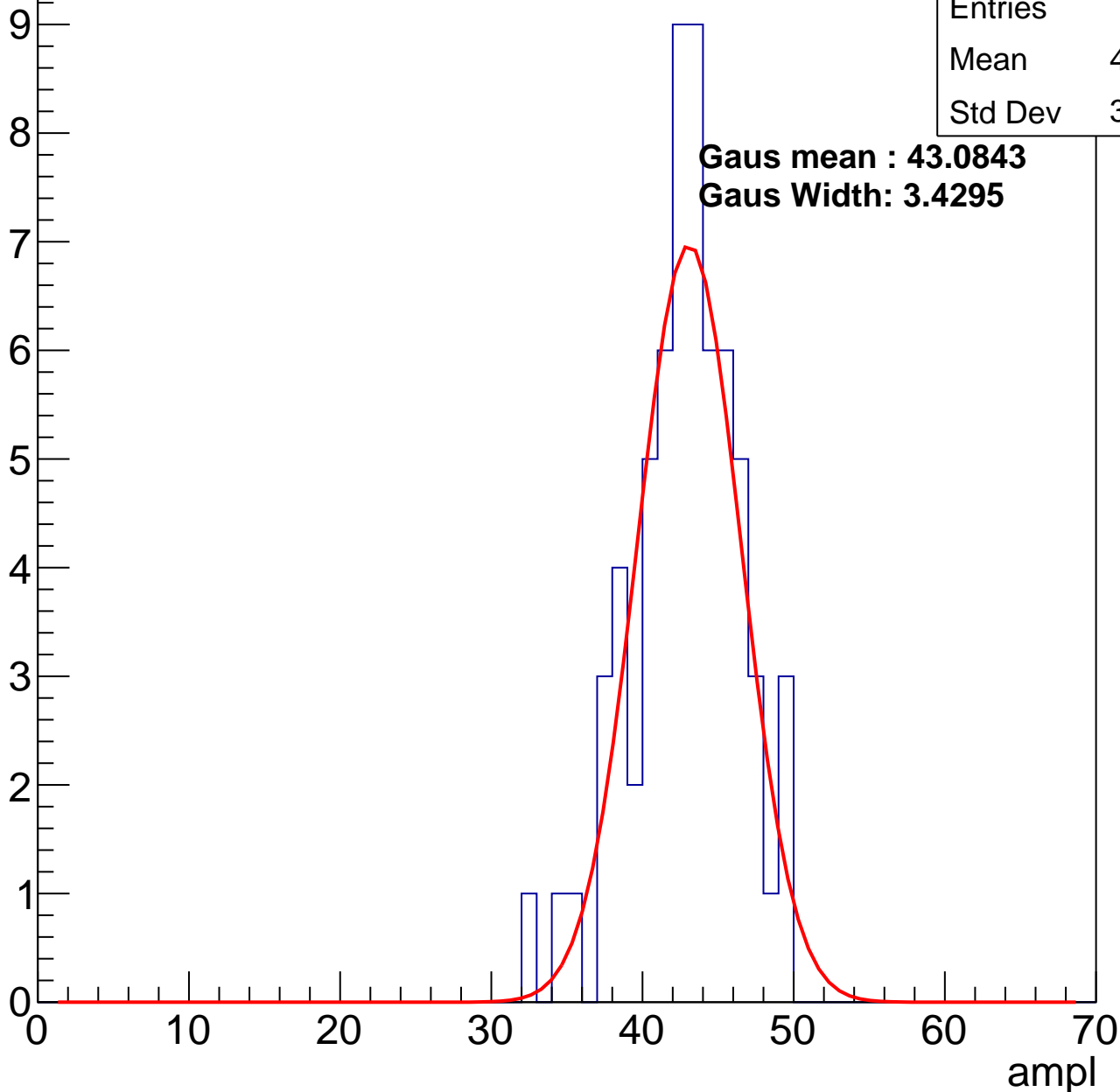
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	42.35
Std Dev	3.536

Gaus mean : 43.0843

Gaus Width: 3.4295

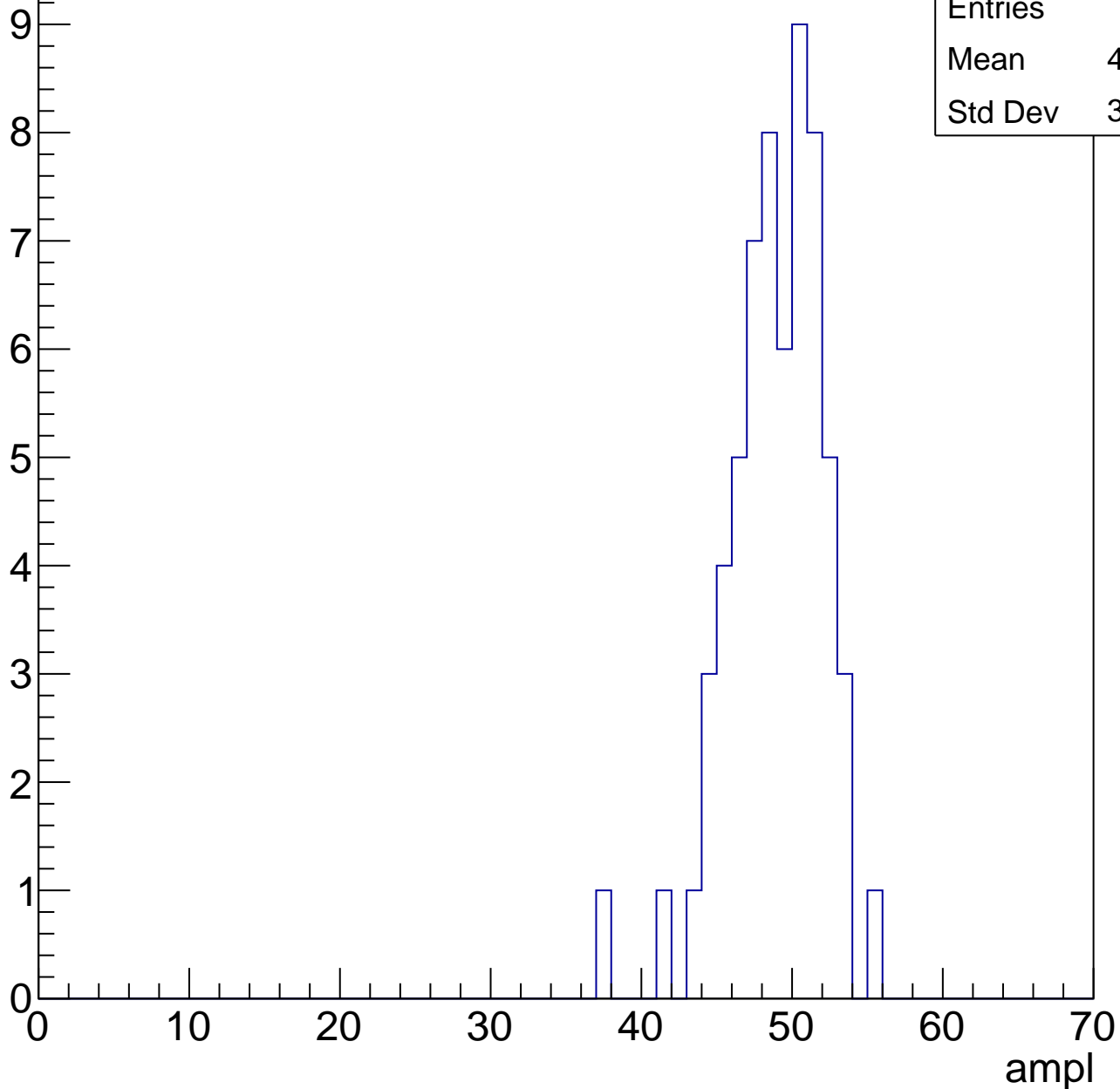


B1L003S, U21-ch120, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	62
Mean	48.42
Std Dev	3.145

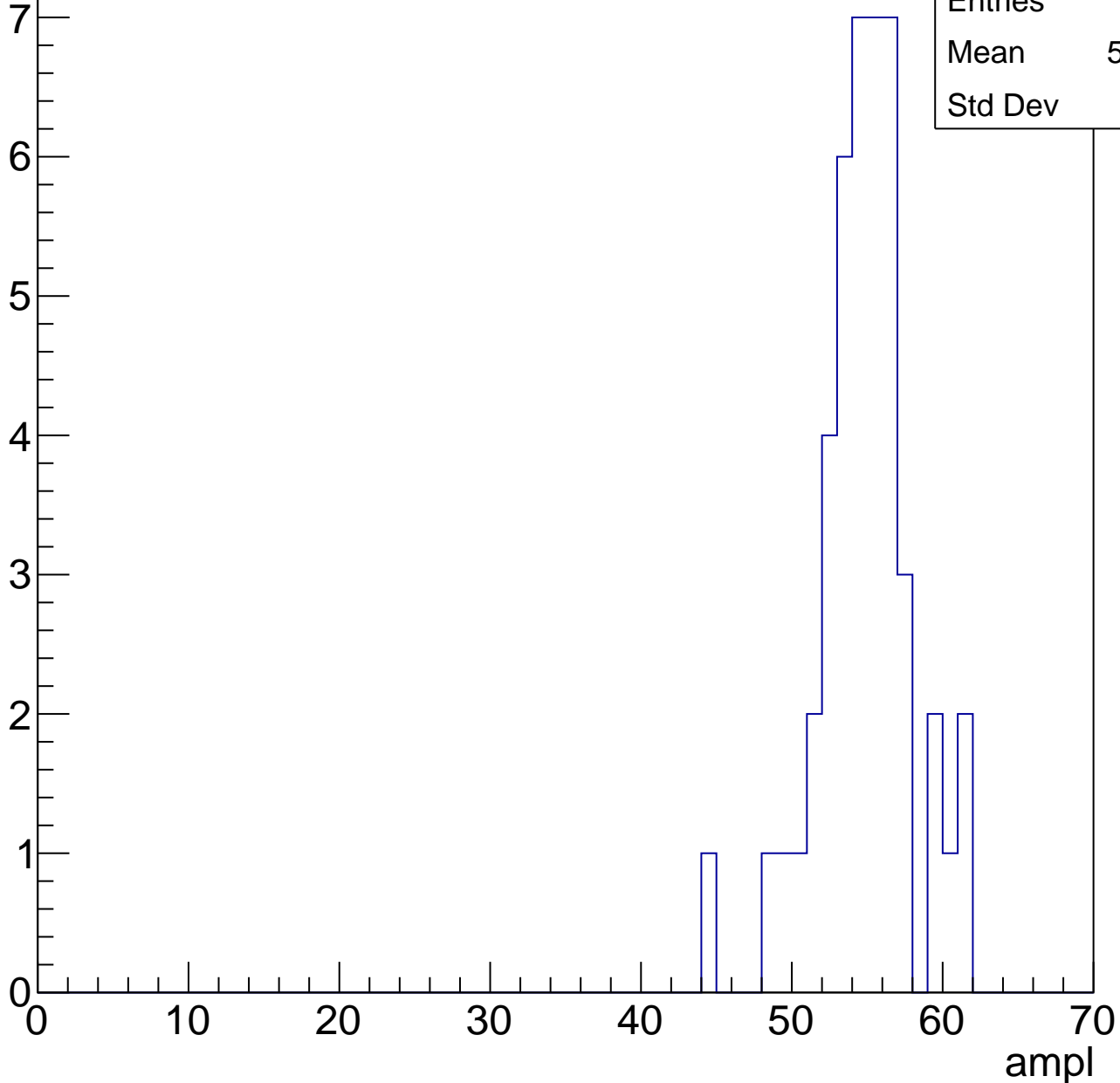


B1L003S, U21-ch120, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	45
Mean	54.33
Std Dev	3.19



B1L003S, U21-ch120, adc5

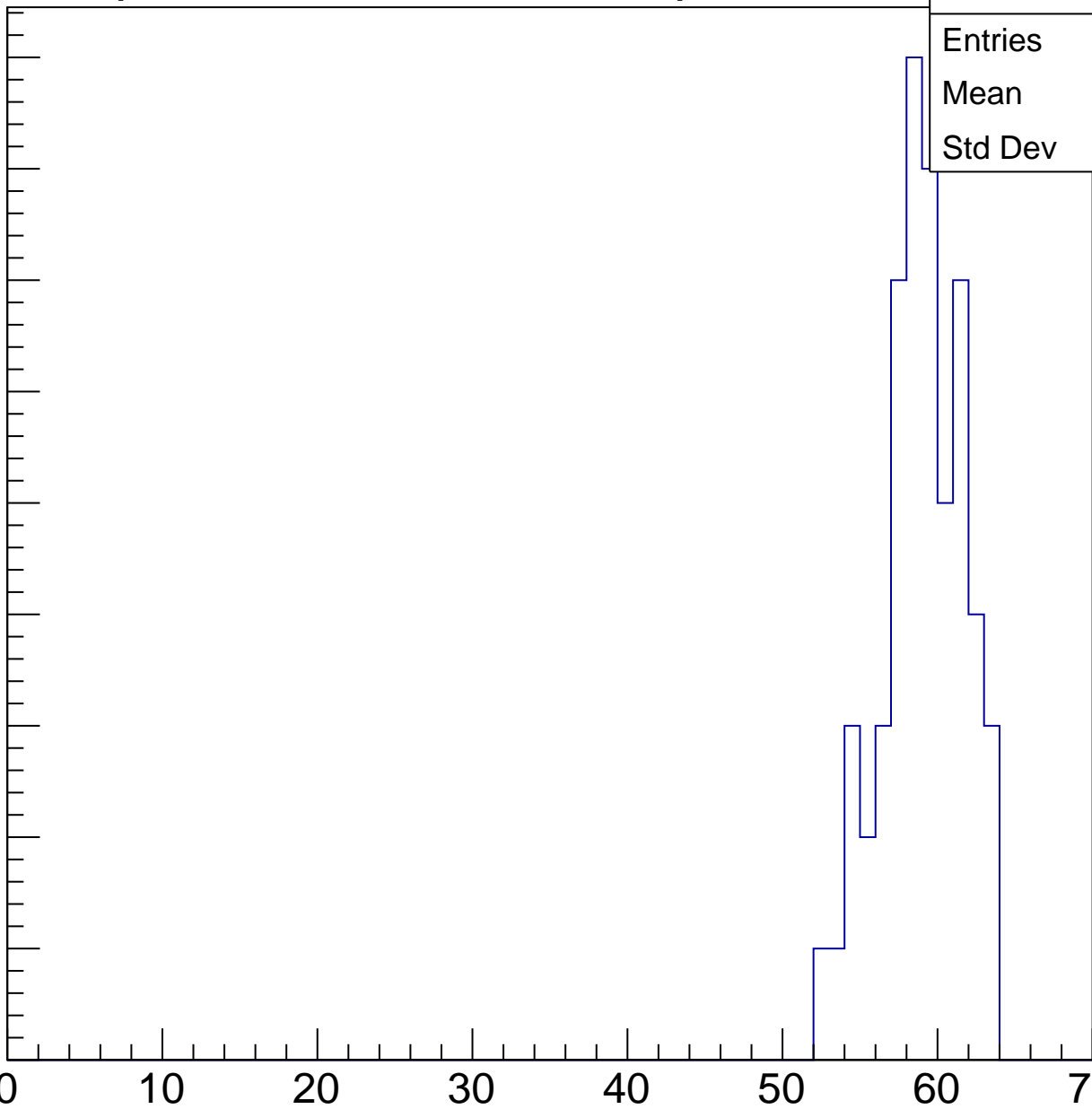
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	58.53
Std Dev	2.611

ampl

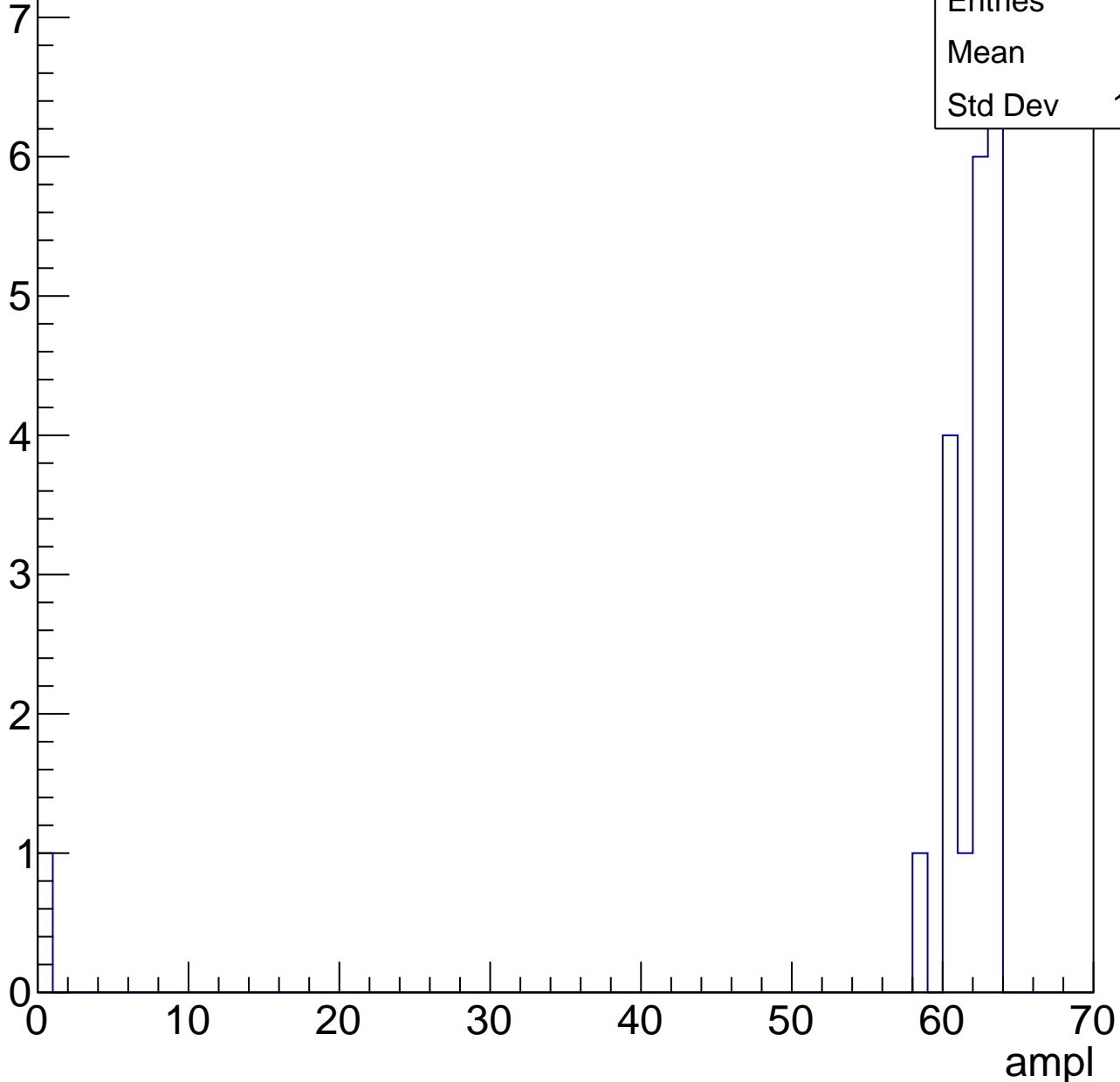


B1L003S, U21-ch120, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

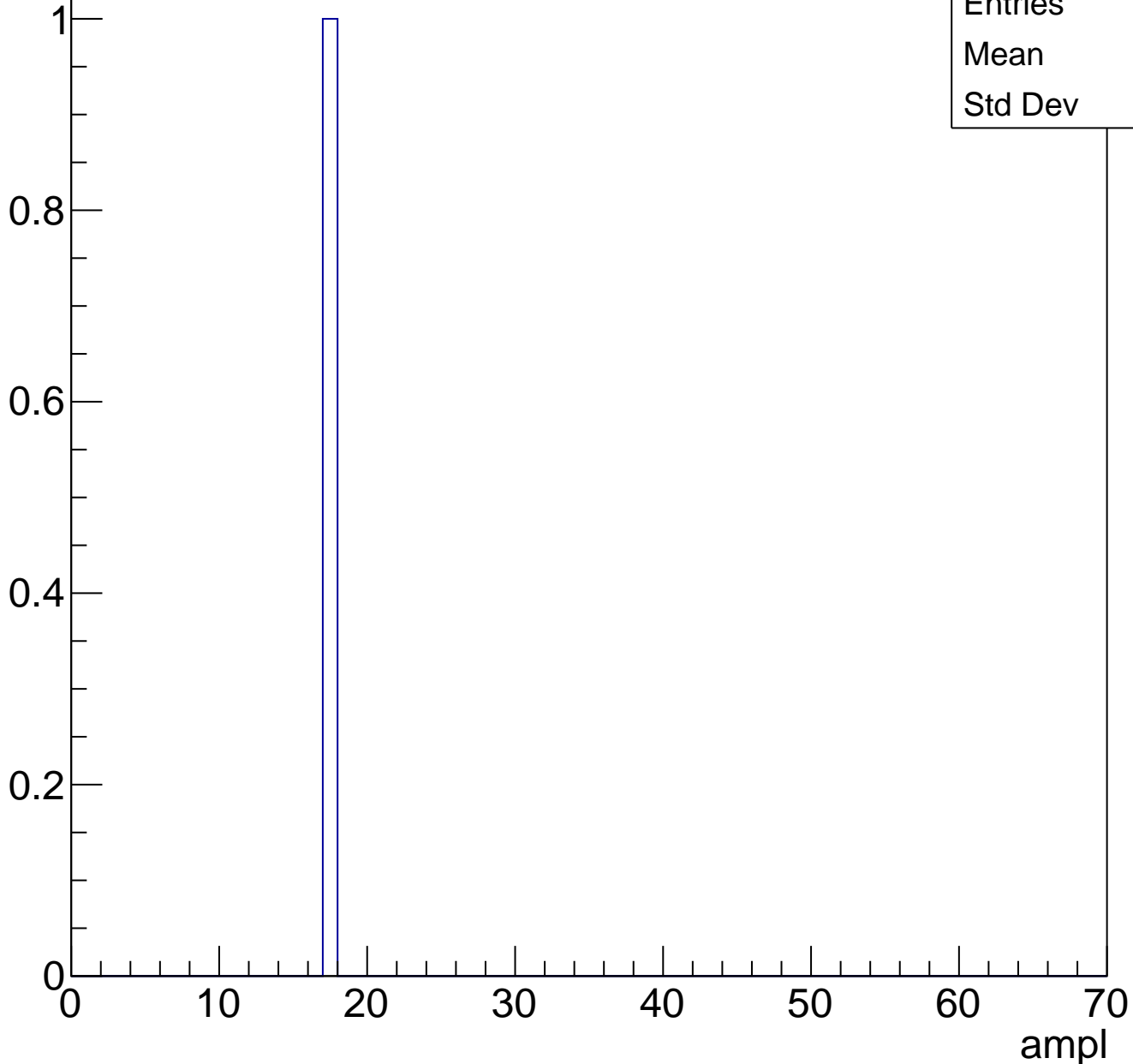
Entries	20
Mean	58.6
Std Dev	13.51



B1L003S, U21-ch120, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	17
Std Dev	0

B1L003S, U21-ch121, adc0

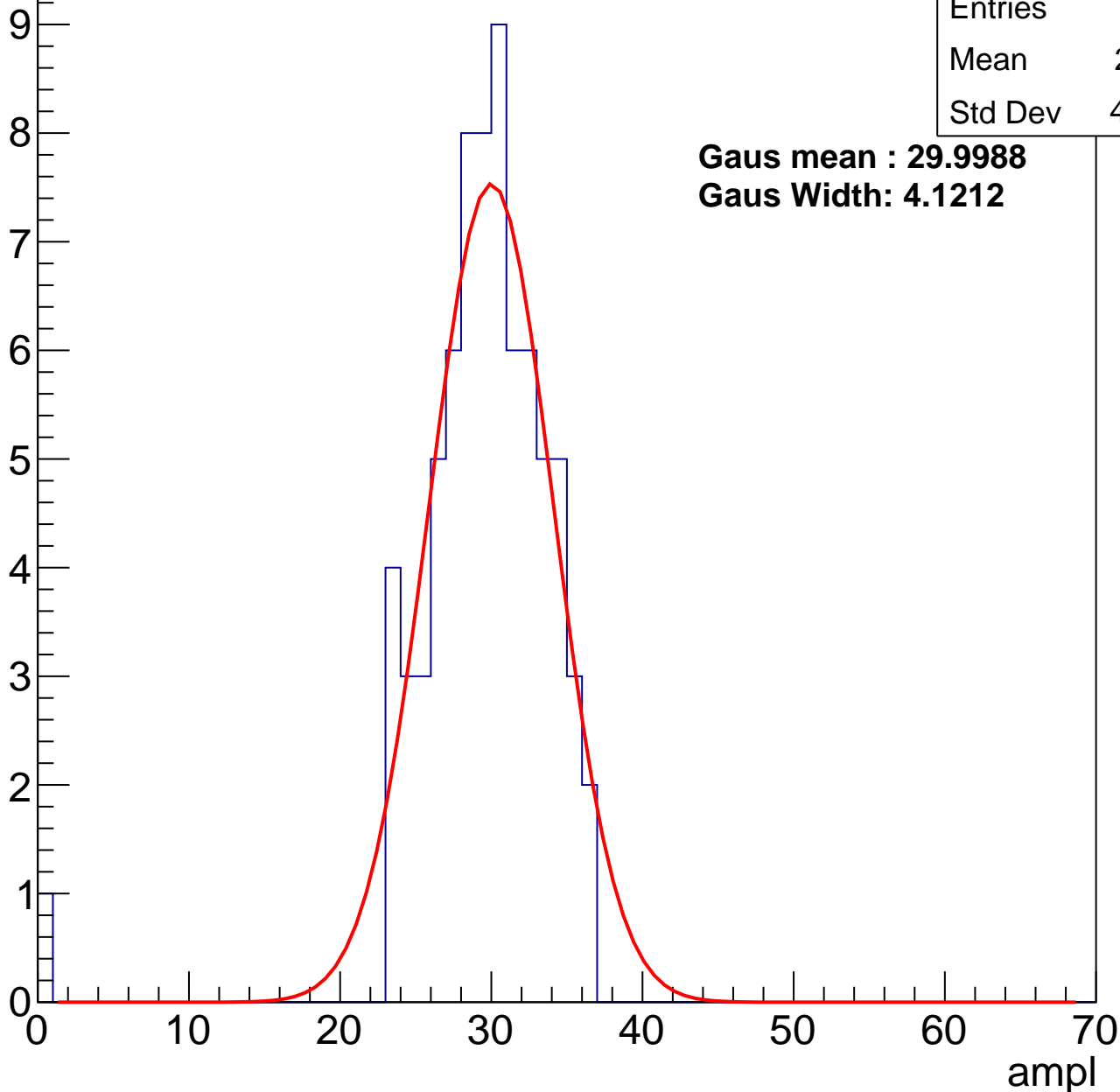
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	29.01
Std Dev	4.769

Gaus mean : 29.9988

Gaus Width: 4.1212



B1L003S, U21-ch121, adc1

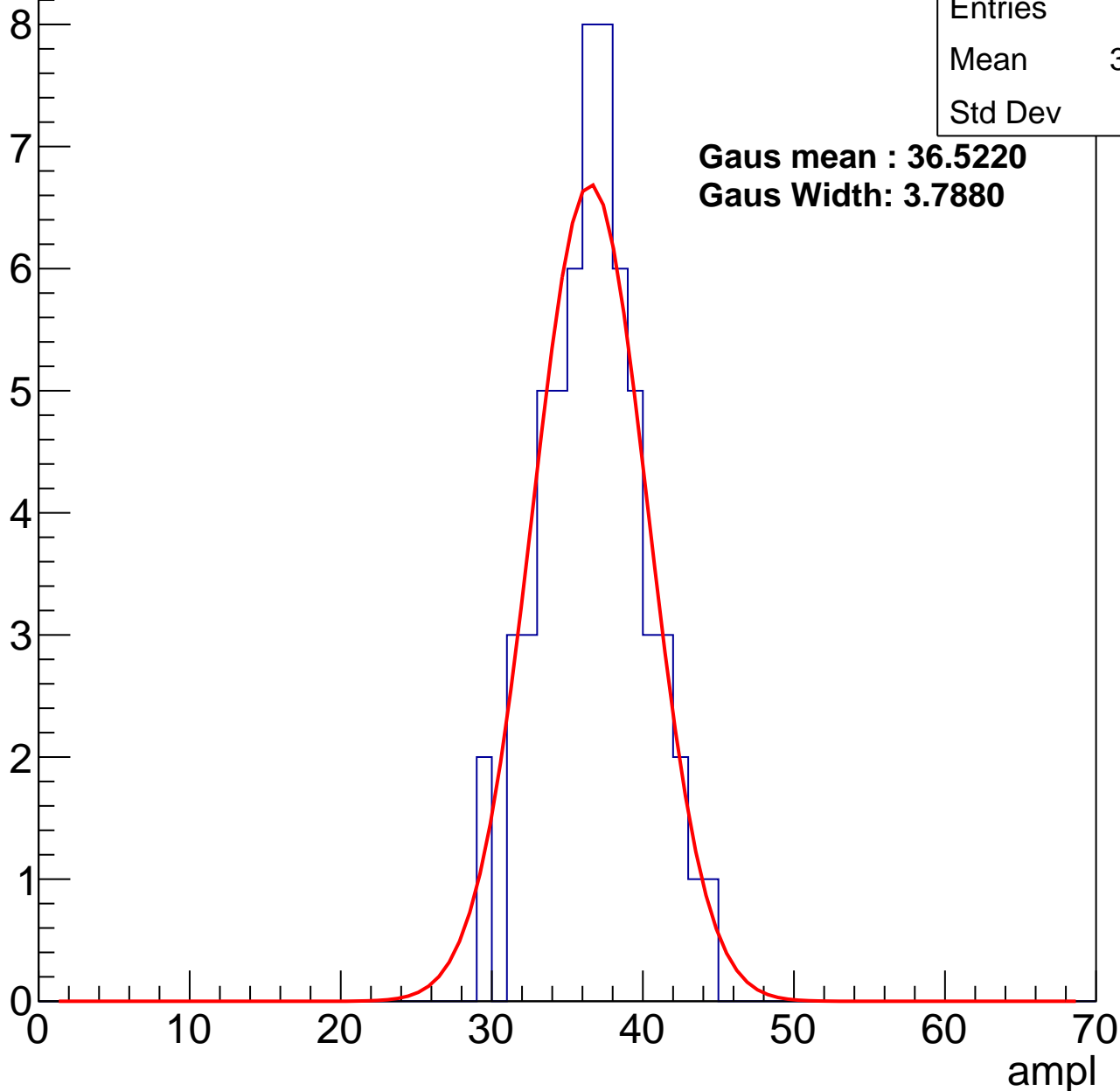
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	36.28
Std Dev	3.32

Gaus mean : 36.5220

Gaus Width: 3.7880



B1L003S, U21-ch121, adc2

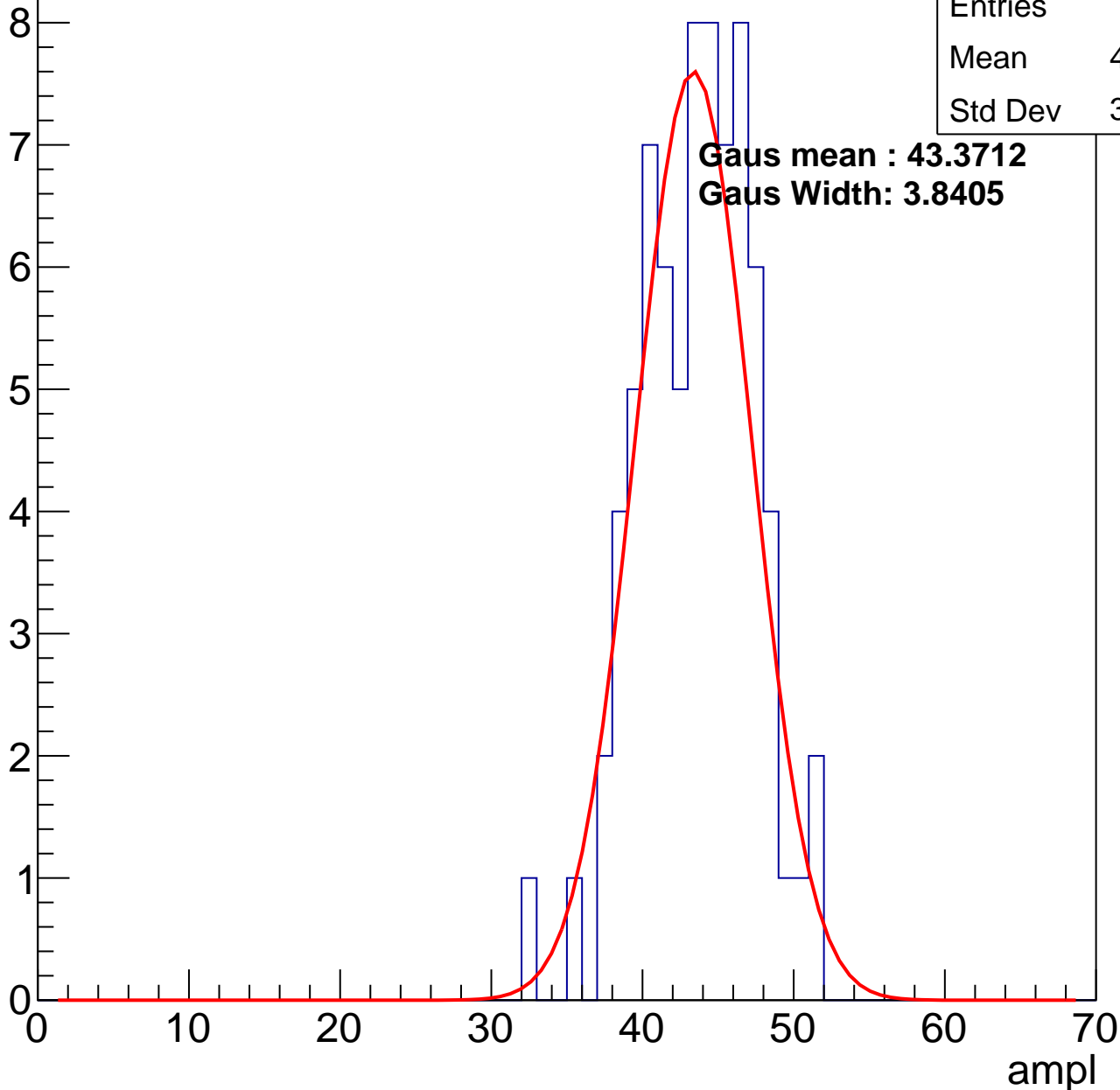
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	76
Mean	43.13
Std Dev	3.708

Gaus mean : 43.3712

Gaus Width: 3.8405

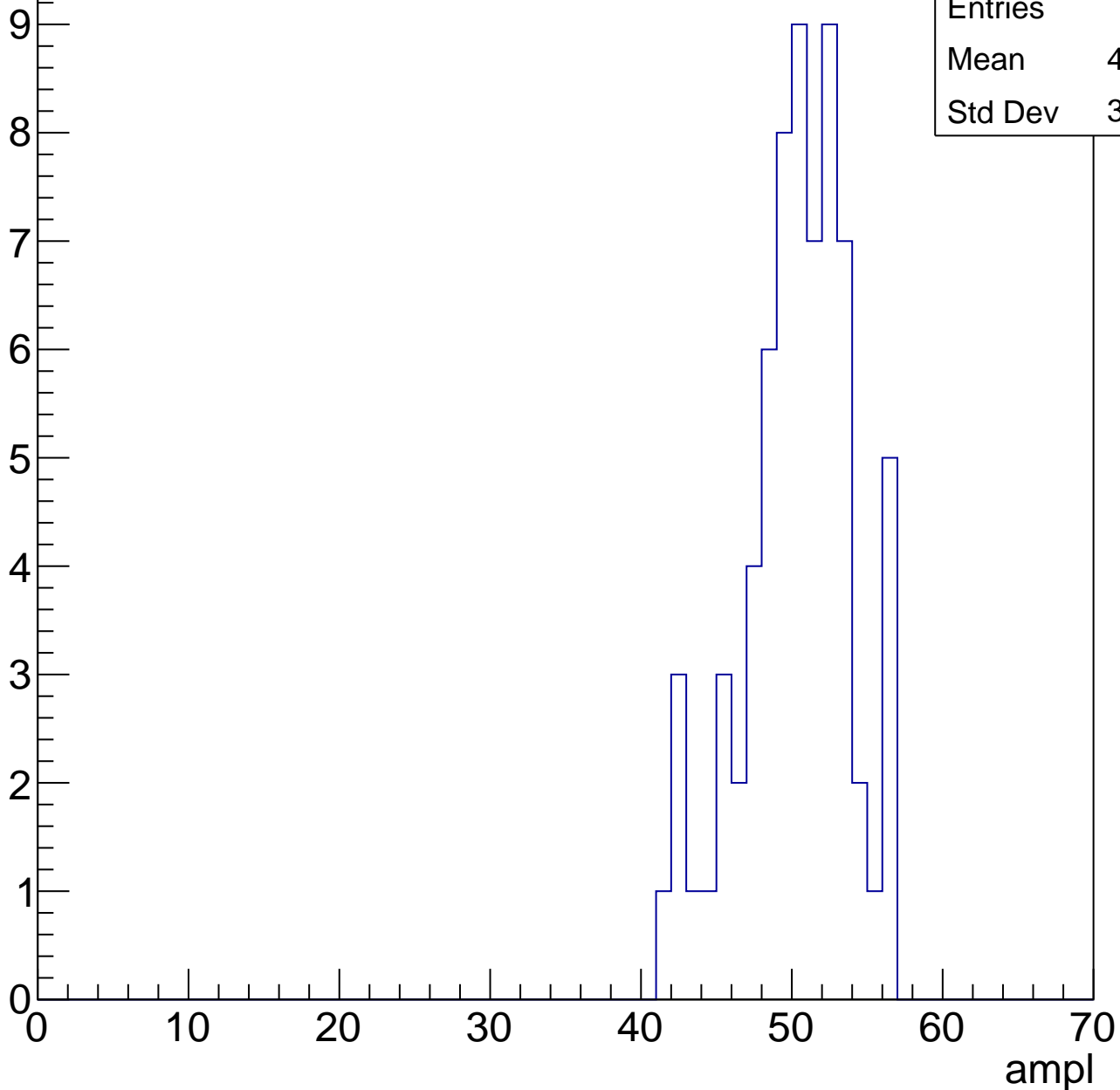


B1L003S, U21-ch121, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	69
Mean	49.83
Std Dev	3.559

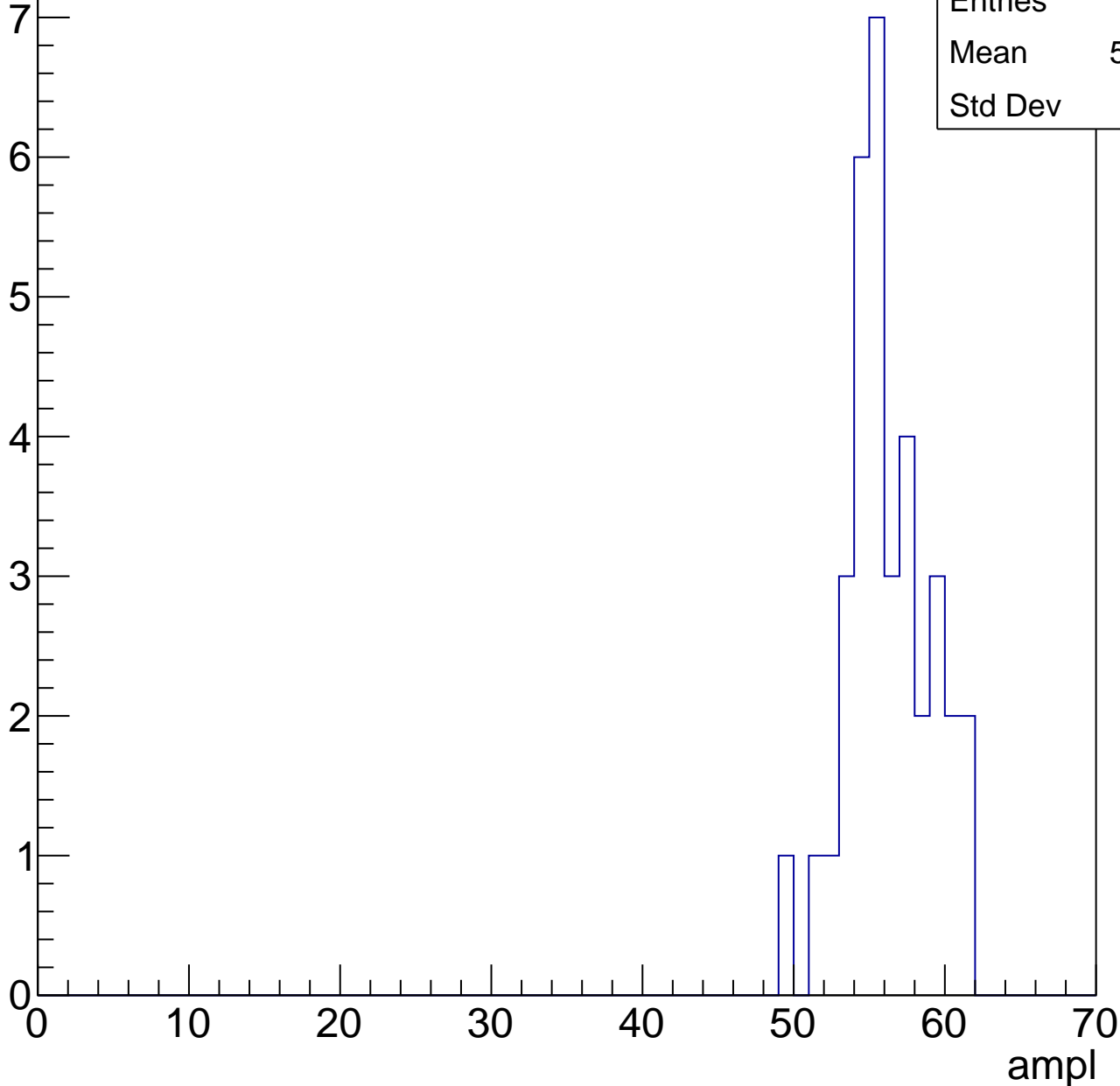


B1L003S, U21-ch121, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	35
Mean	55.74
Std Dev	2.76

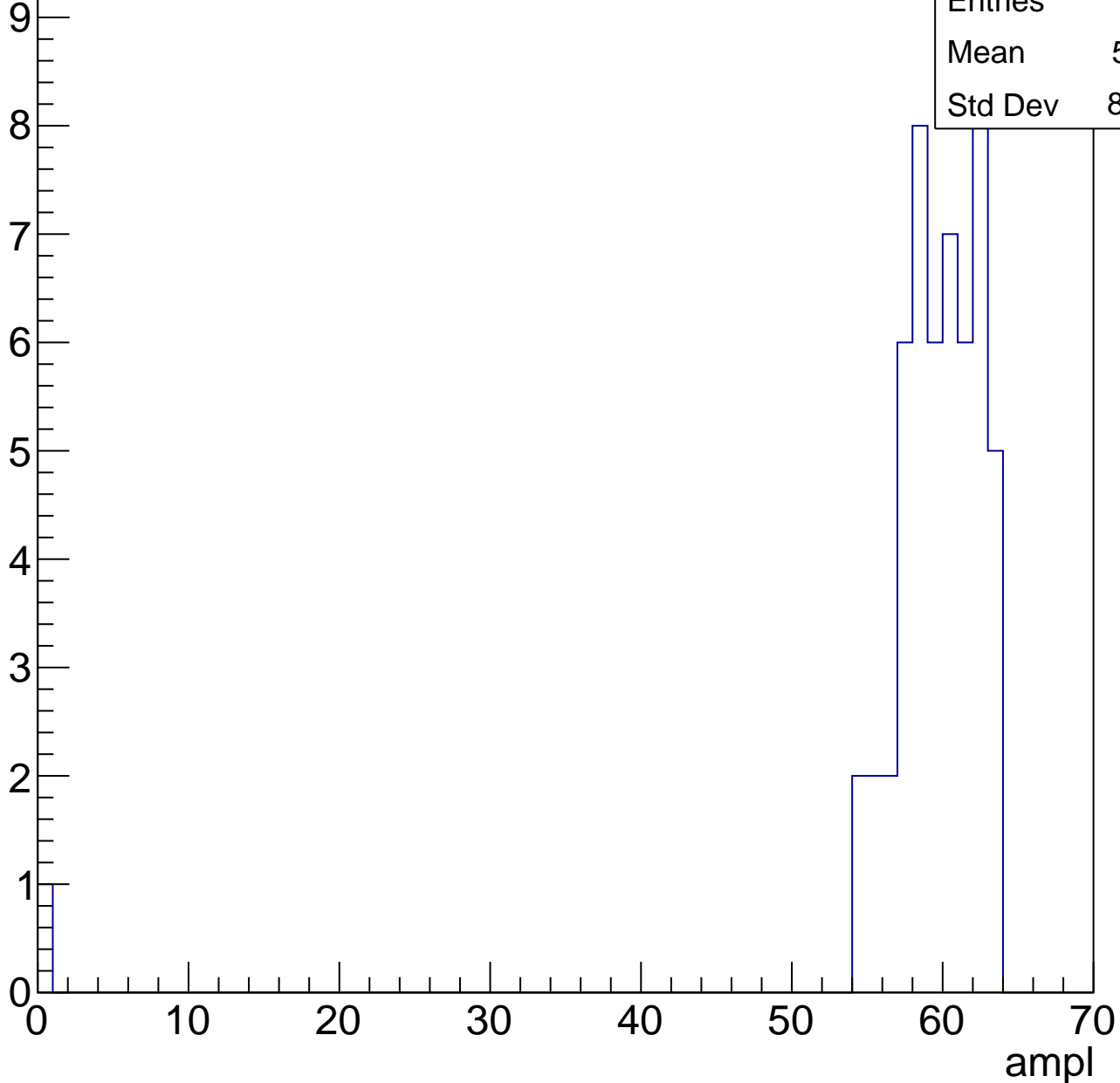


B1L003S, U21-ch121, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	54
Mean	58.31
Std Dev	8.366

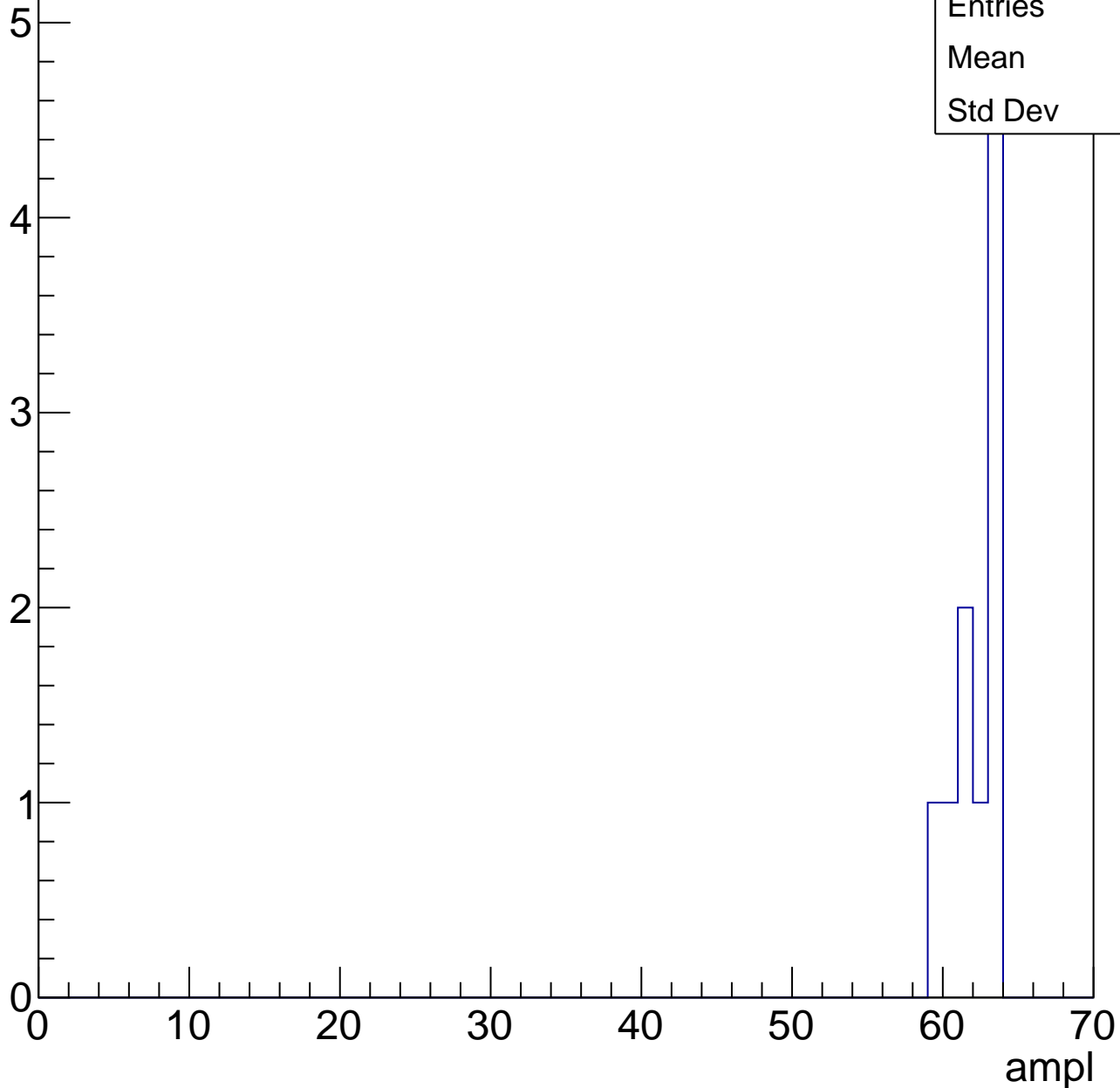


B1L003S, U21-ch121, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	10
Mean	61.8
Std Dev	1.4



B1L003S, U21-ch121, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch122, adc0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	79
Mean	29.39
Std Dev	5.775

Gaus mean : 31.0656

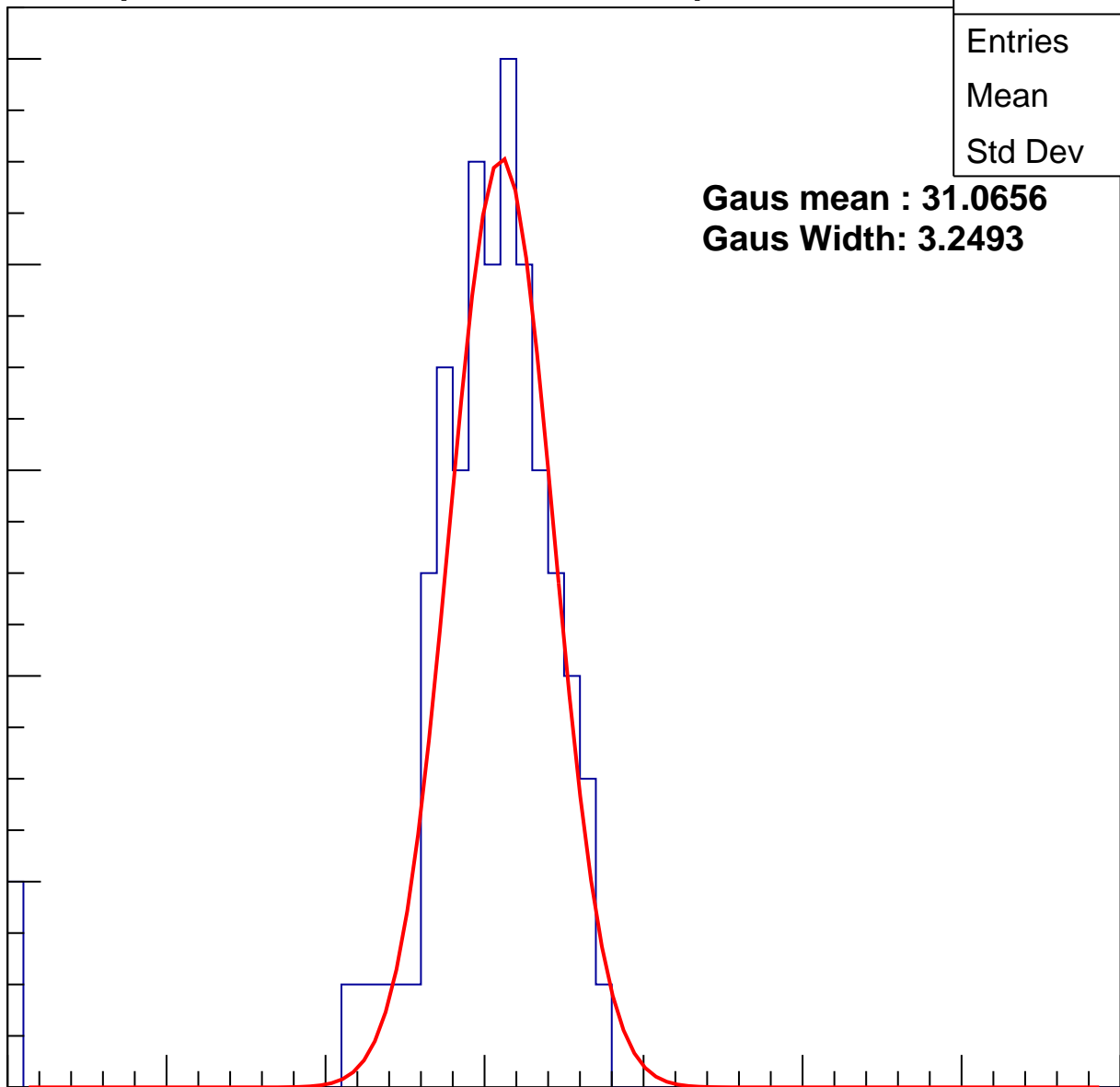
Gaus Width: 3.2493

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch122, adc1

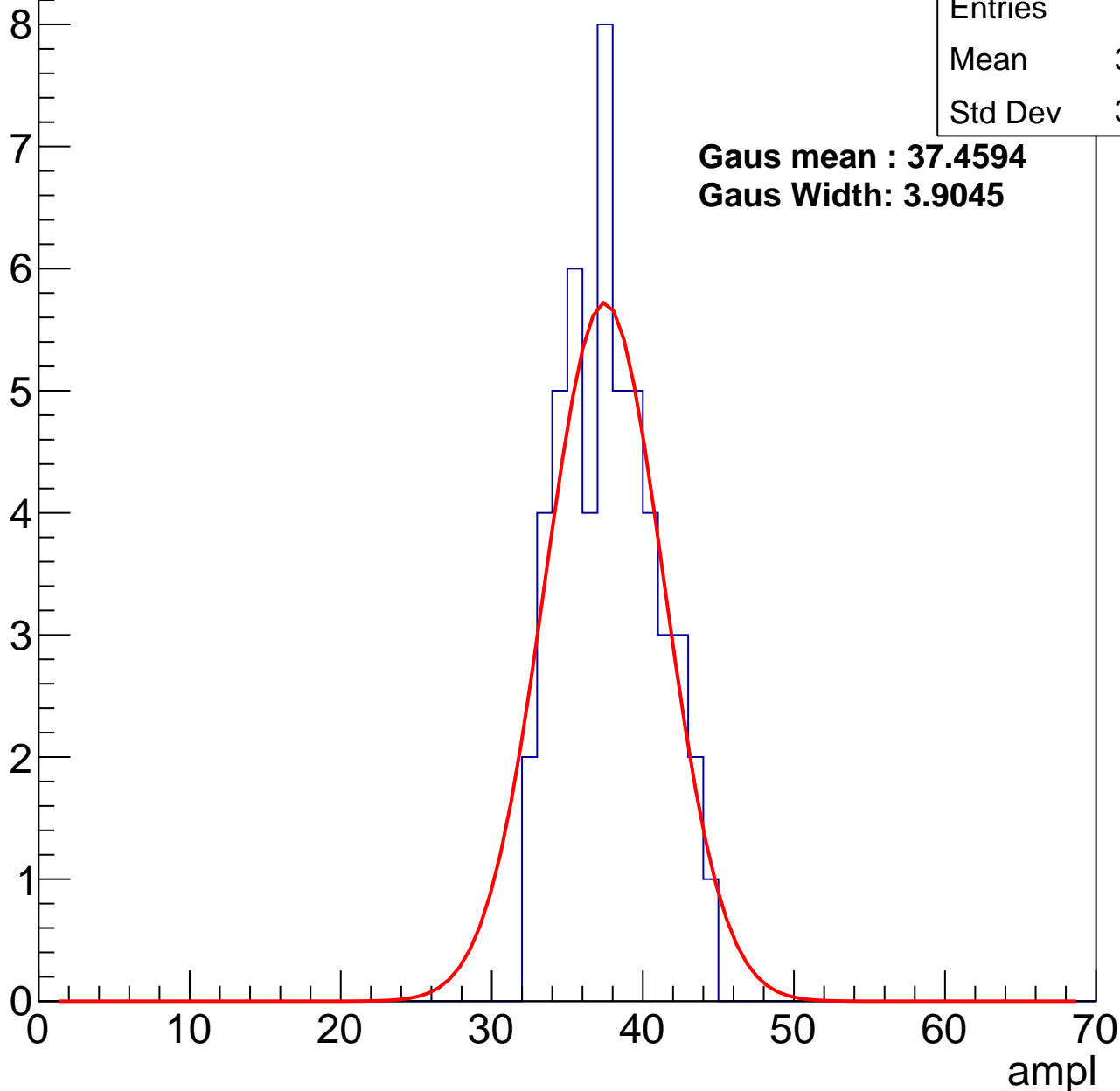
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	52
Mean	37.31
Std Dev	3.061

Gaus mean : 37.4594

Gaus Width: 3.9045



B1L003S, U21-ch122, adc2

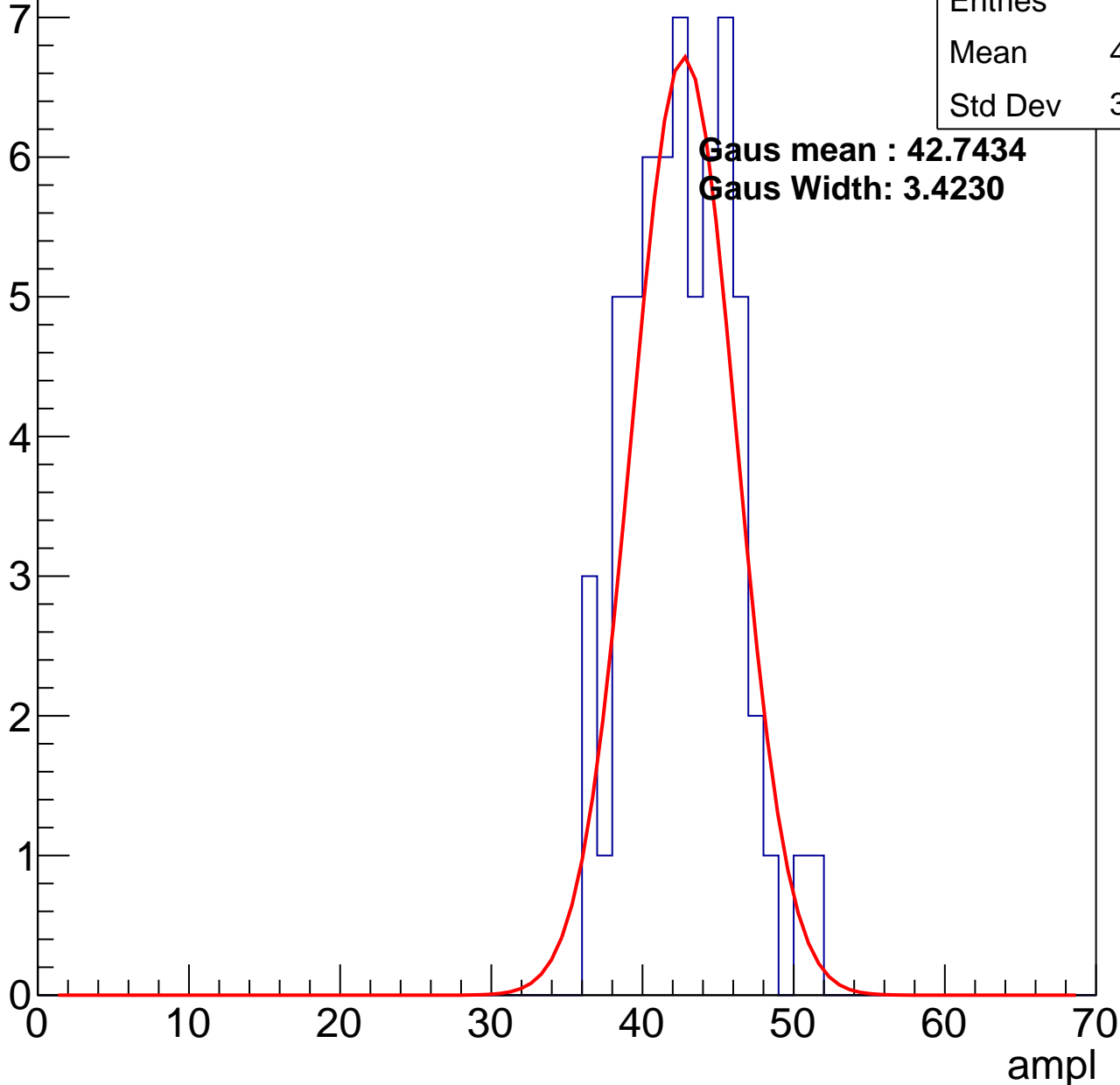
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	61
Mean	42.25
Std Dev	3.357

Gaus mean : 42.7434

Gaus Width: 3.4230

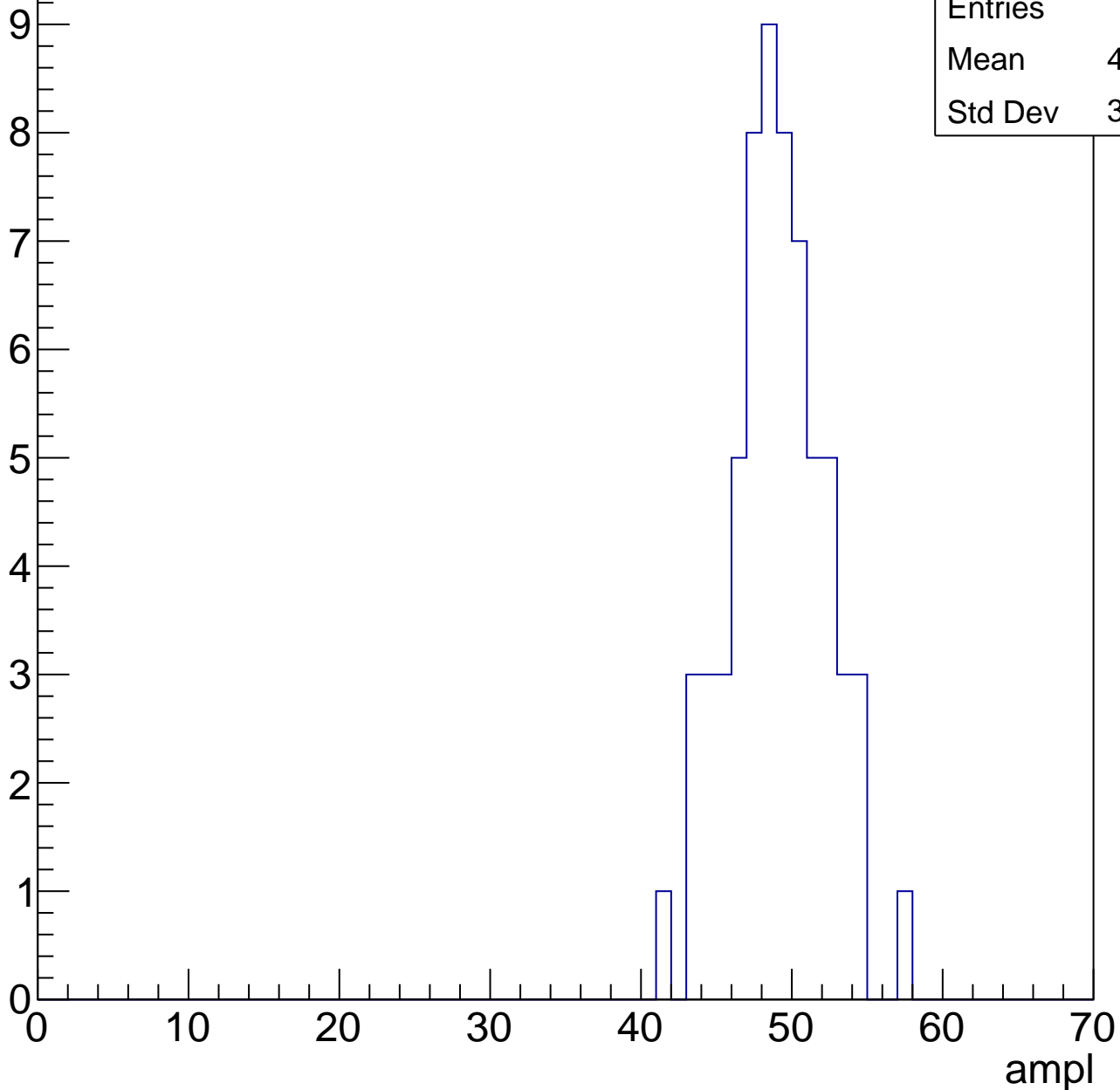


B1L003S, U21-ch122, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	64
Mean	48.59
Std Dev	3.136

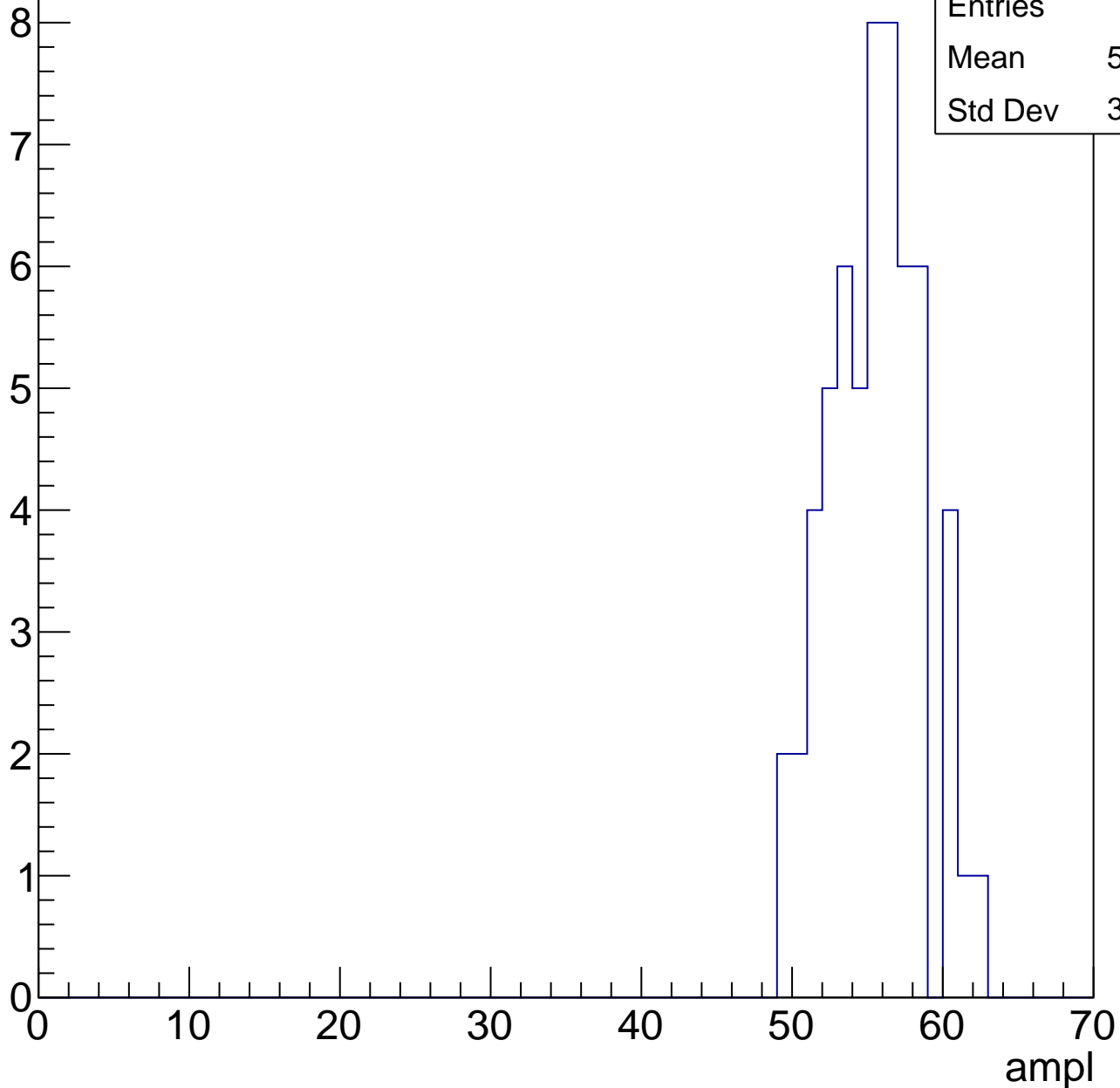


B1L003S, U21-ch122, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	55.02
Std Dev	3.026

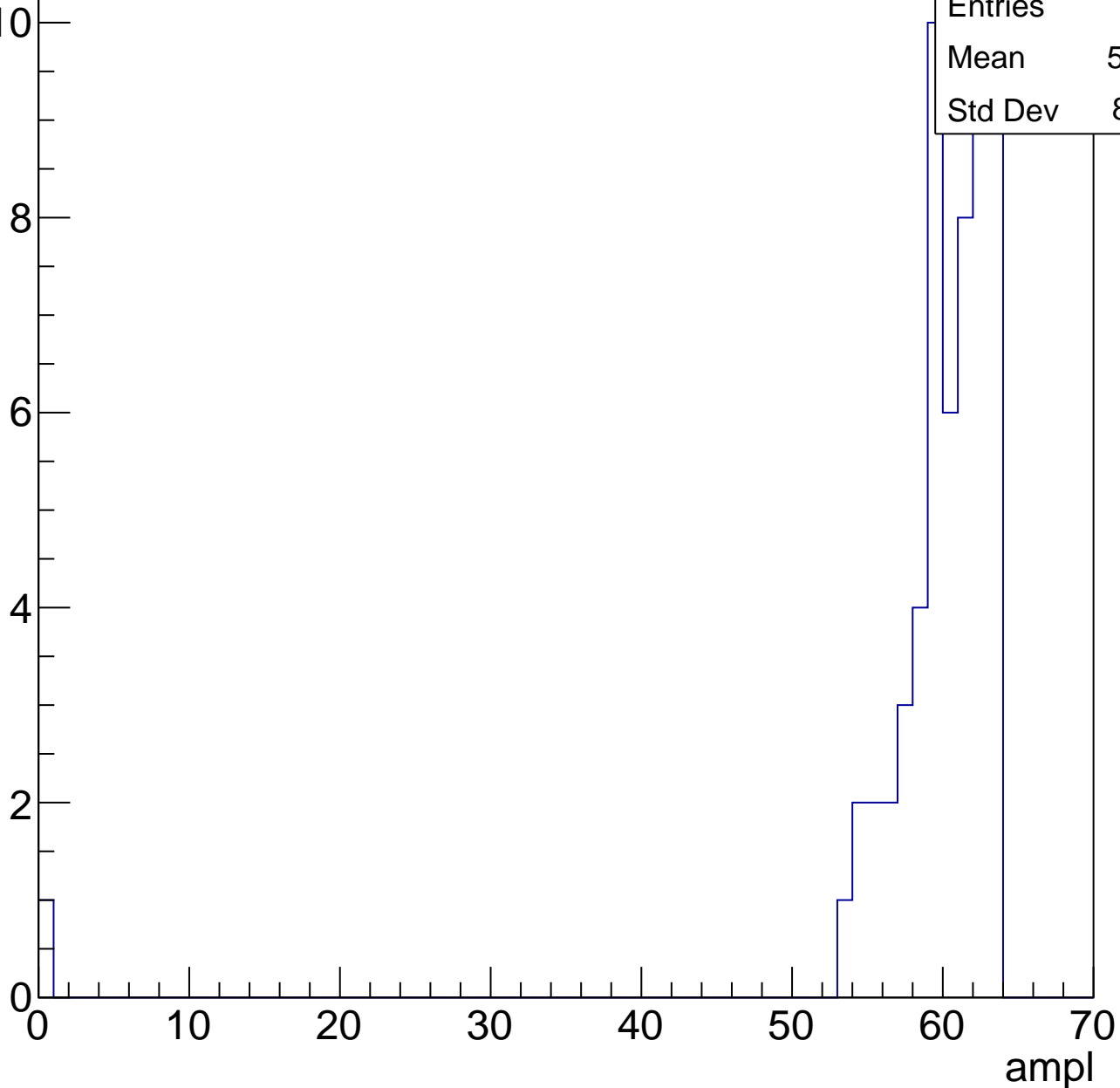


B1L003S, U21-ch122, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	58.83
Std Dev	8.211



B1L003S, U21-ch122, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	61
Std Dev	0

B1L003S, U21-ch122, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch123, adc0

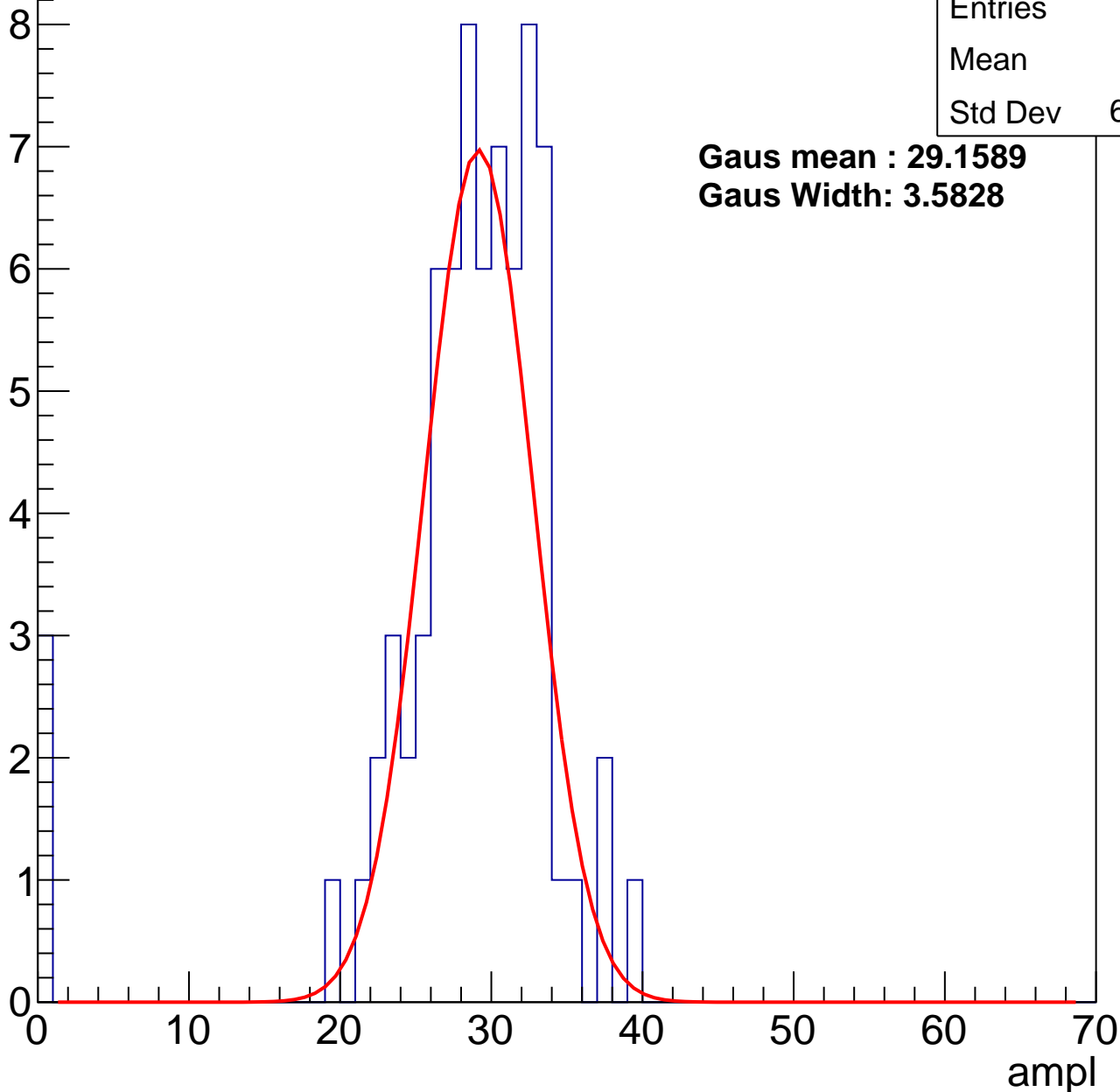
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	74
Mean	27.8
Std Dev	6.852

Gaus mean : 29.1589

Gaus Width: 3.5828



B1L003S, U21-ch123, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	67
Mean	35.49
Std Dev	3.444

Gaus mean : 36.4870

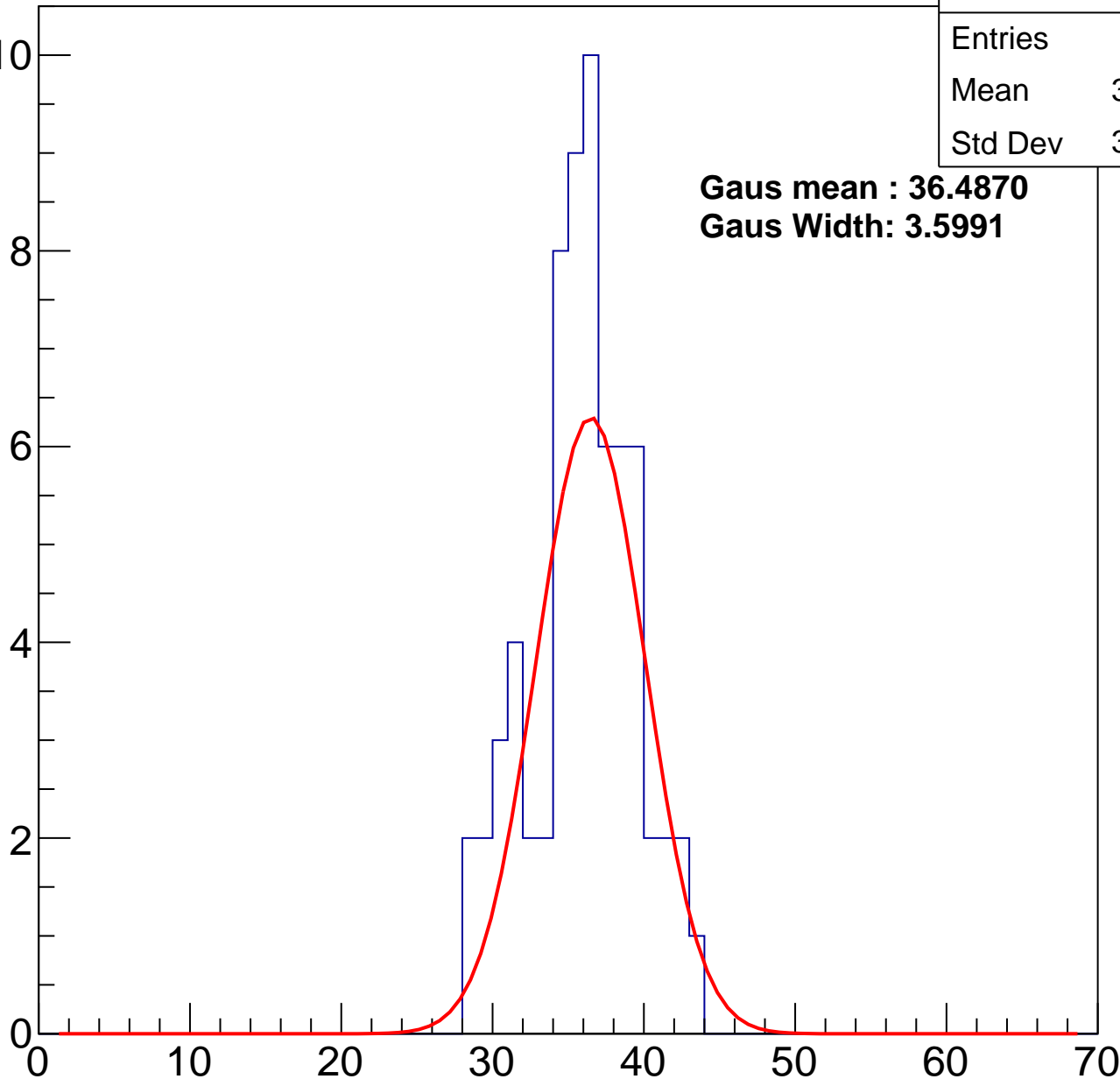
Gaus Width: 3.5991

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch123, adc2

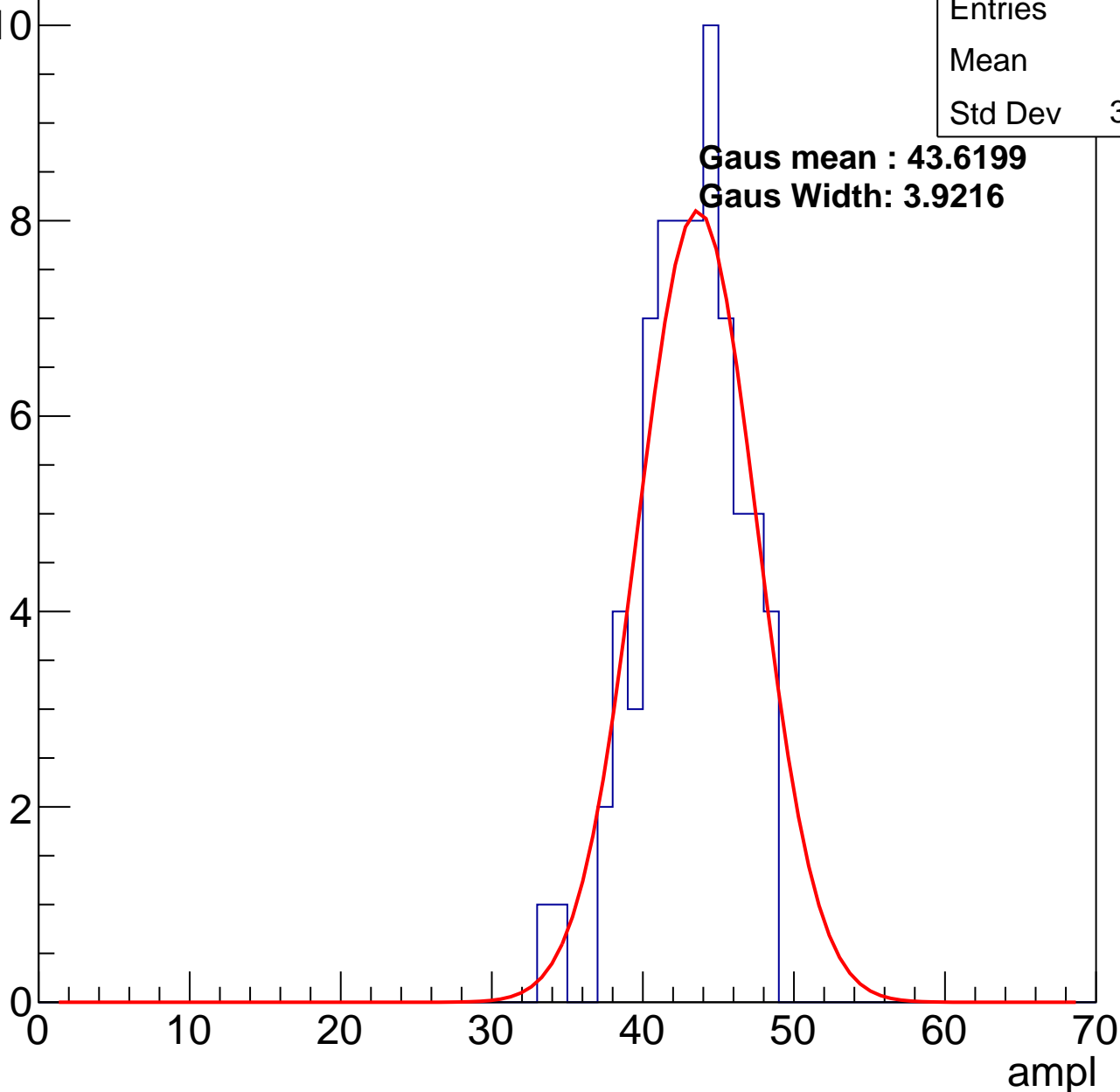
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	73
Mean	42.6
Std Dev	3.217

Gaus mean : 43.6199

Gaus Width: 3.9216

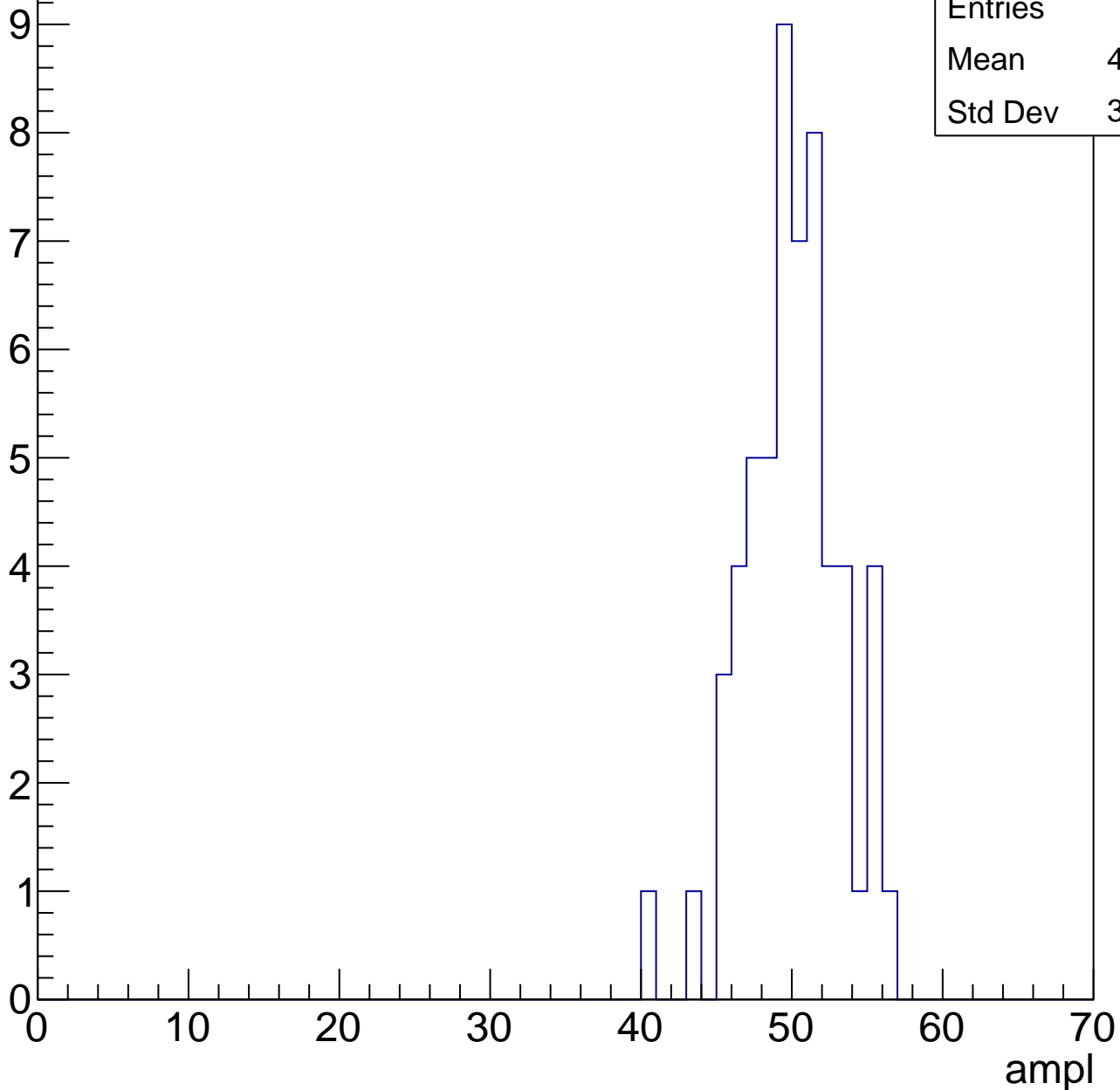


B1L003S, U21-ch123, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	49.58
Std Dev	3.145

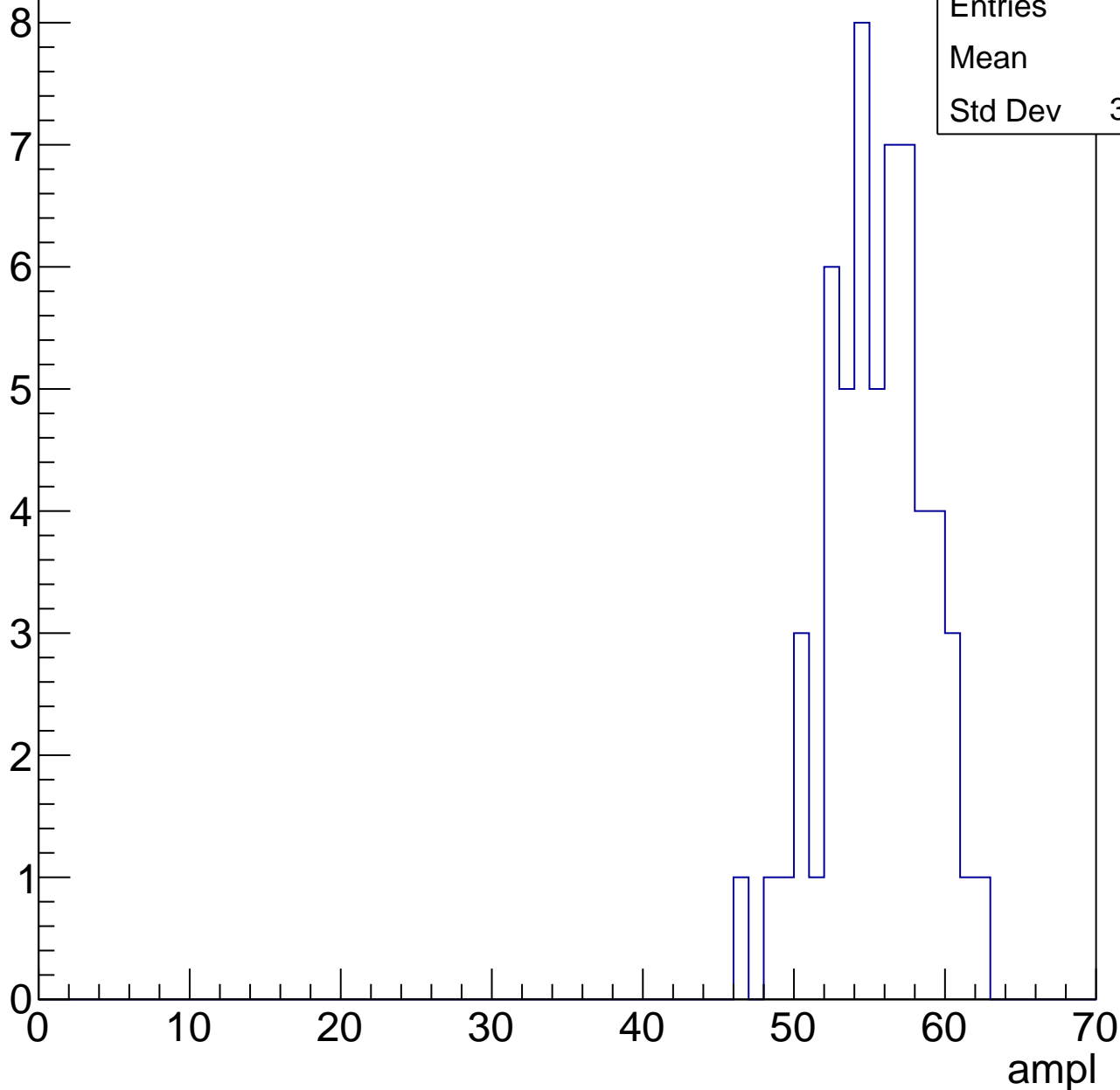


B1L003S, U21-ch123, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

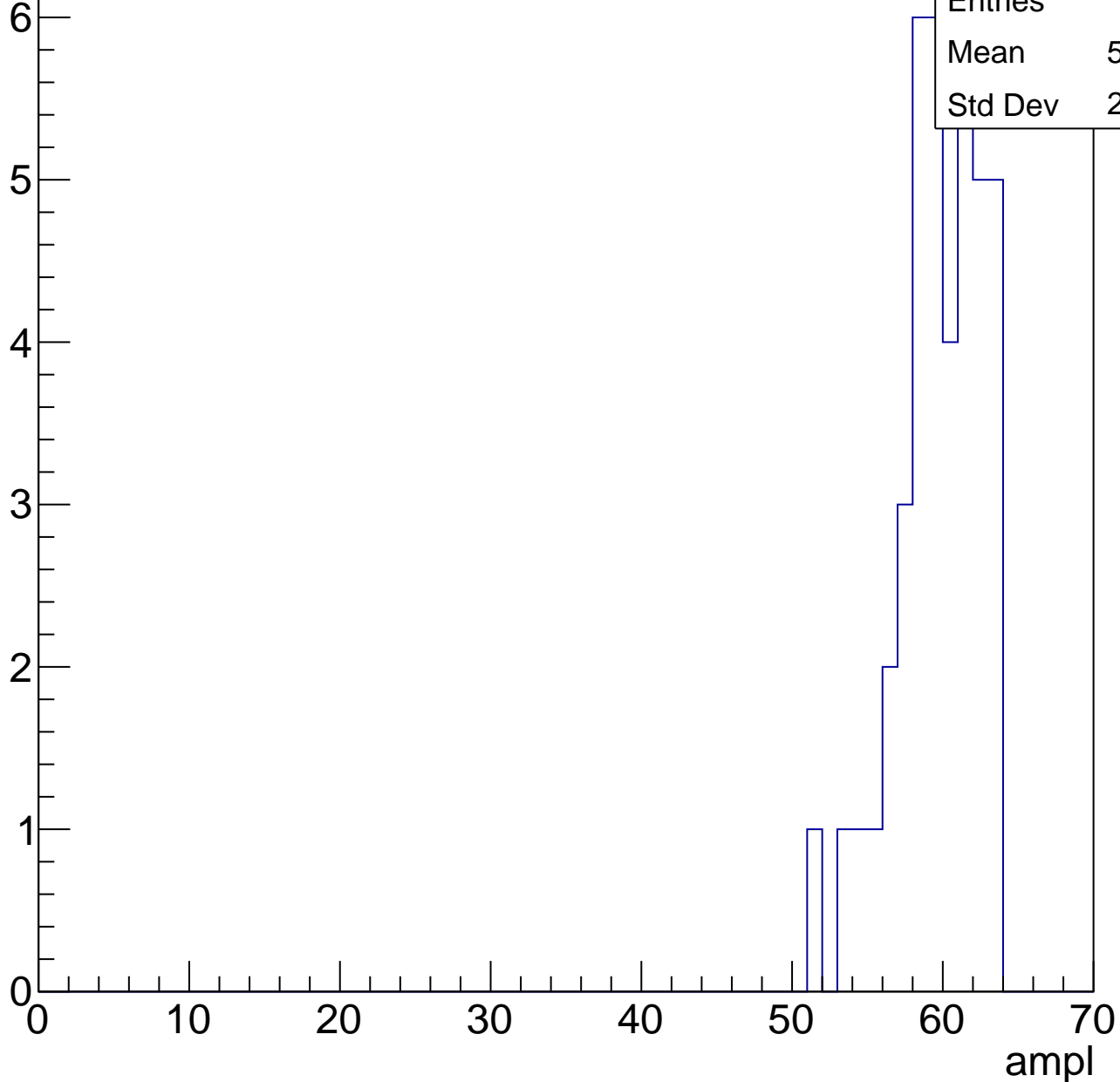
Entries	58
Mean	55
Std Dev	3.306



B1L003S, U21-ch123, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

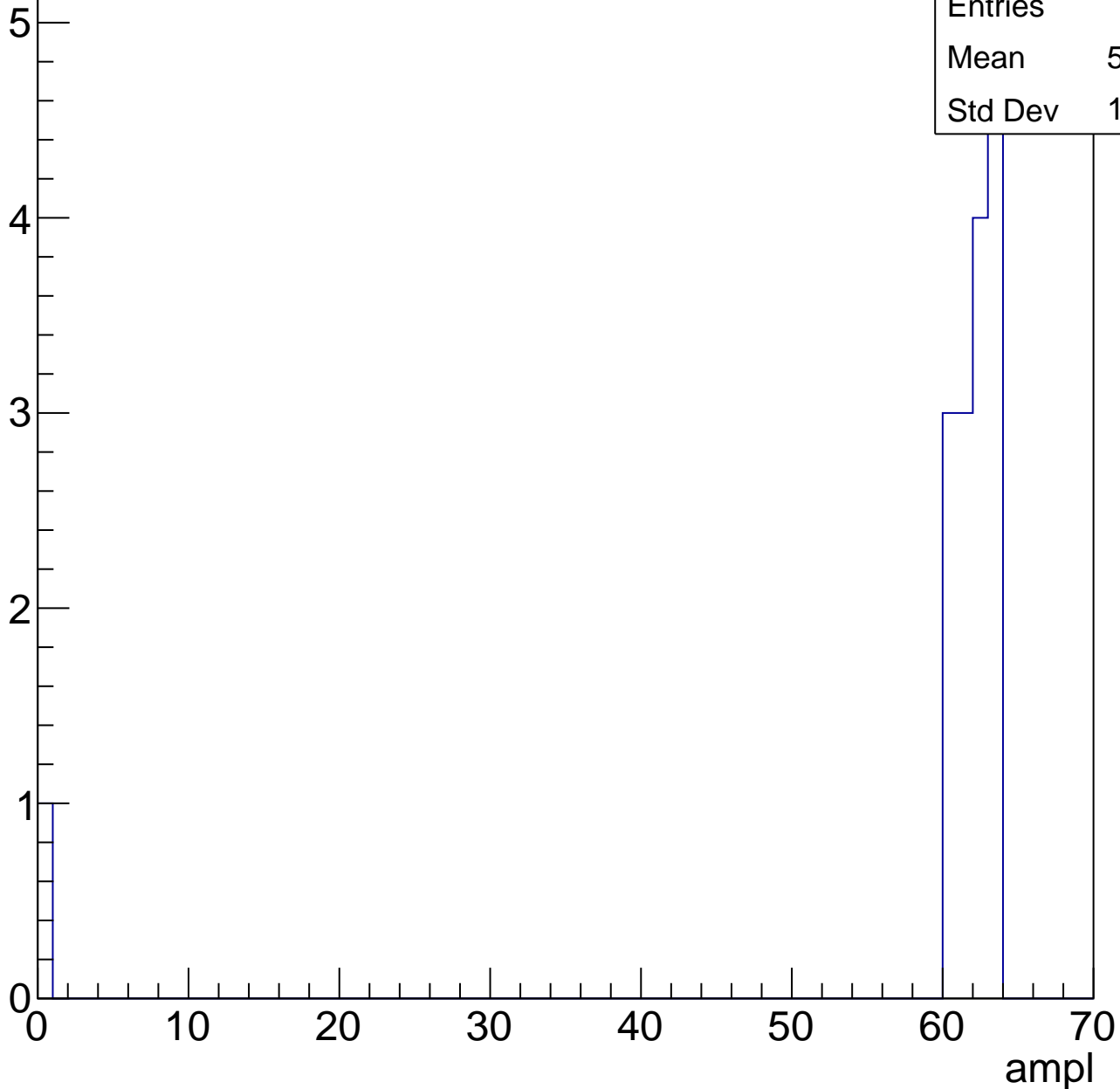


B1L003S, U21-ch123, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	16
Mean	57.88
Std Dev	14.98



B1L003S, U21-ch123, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	0
Std Dev	0

B1L003S, U21-ch124, adc0

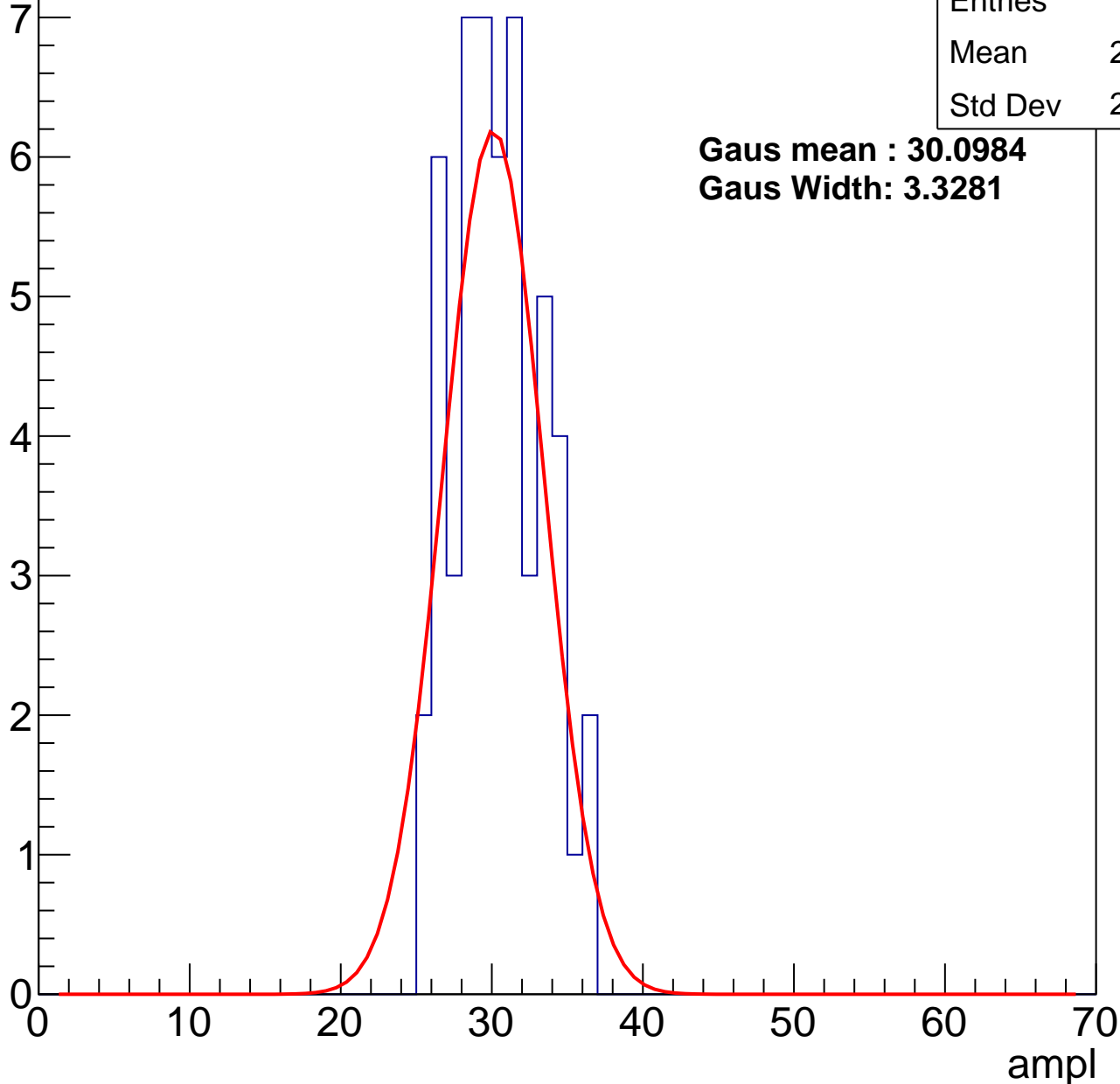
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	53
Mean	29.94
Std Dev	2.858

Gaus mean : 30.0984

Gaus Width: 3.3281



B1L003S, U21-ch124, adc1

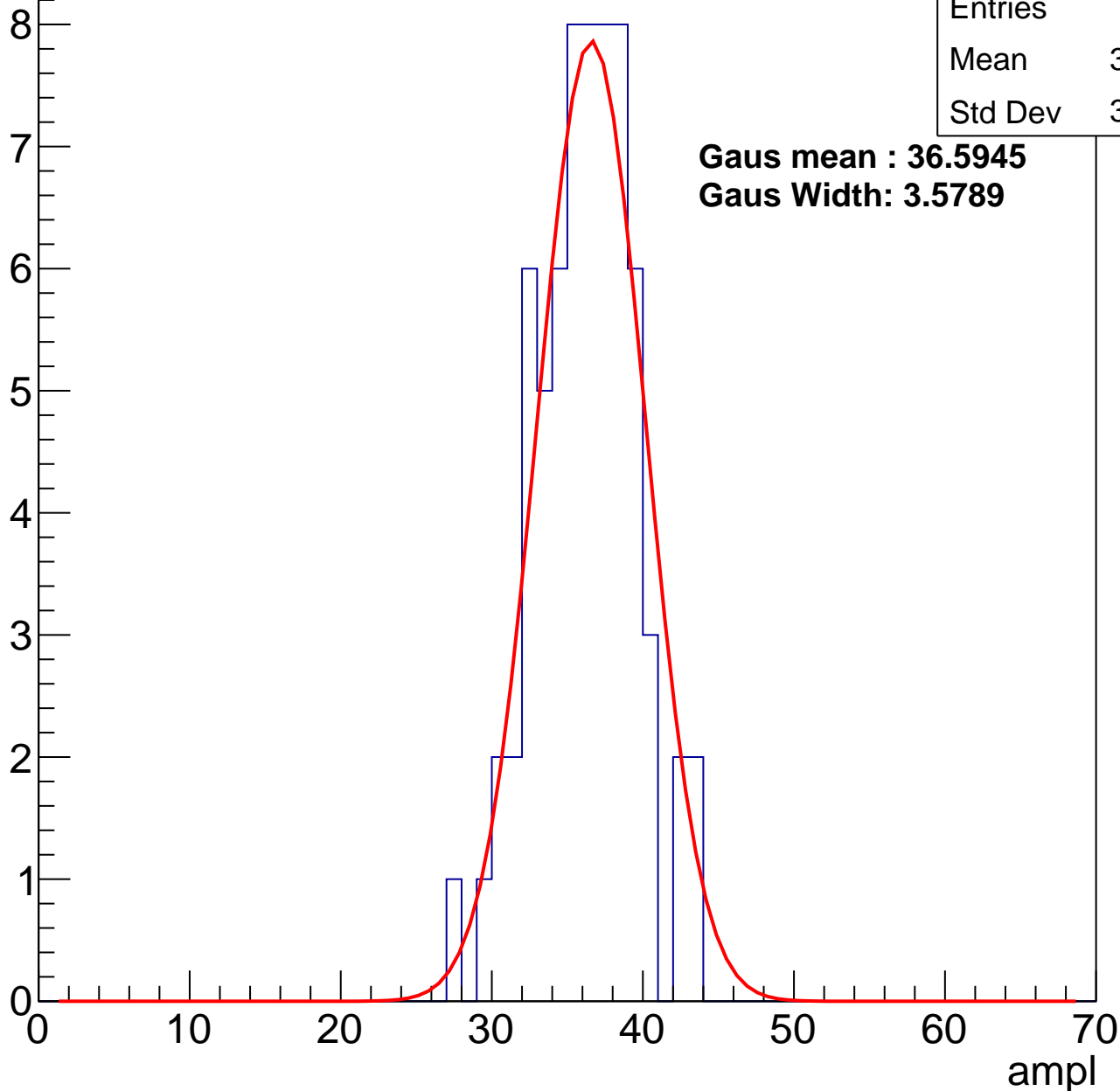
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	35.75
Std Dev	3.283

Gaus mean : 36.5945

Gaus Width: 3.5789



B1L003S, U21-ch124, adc2

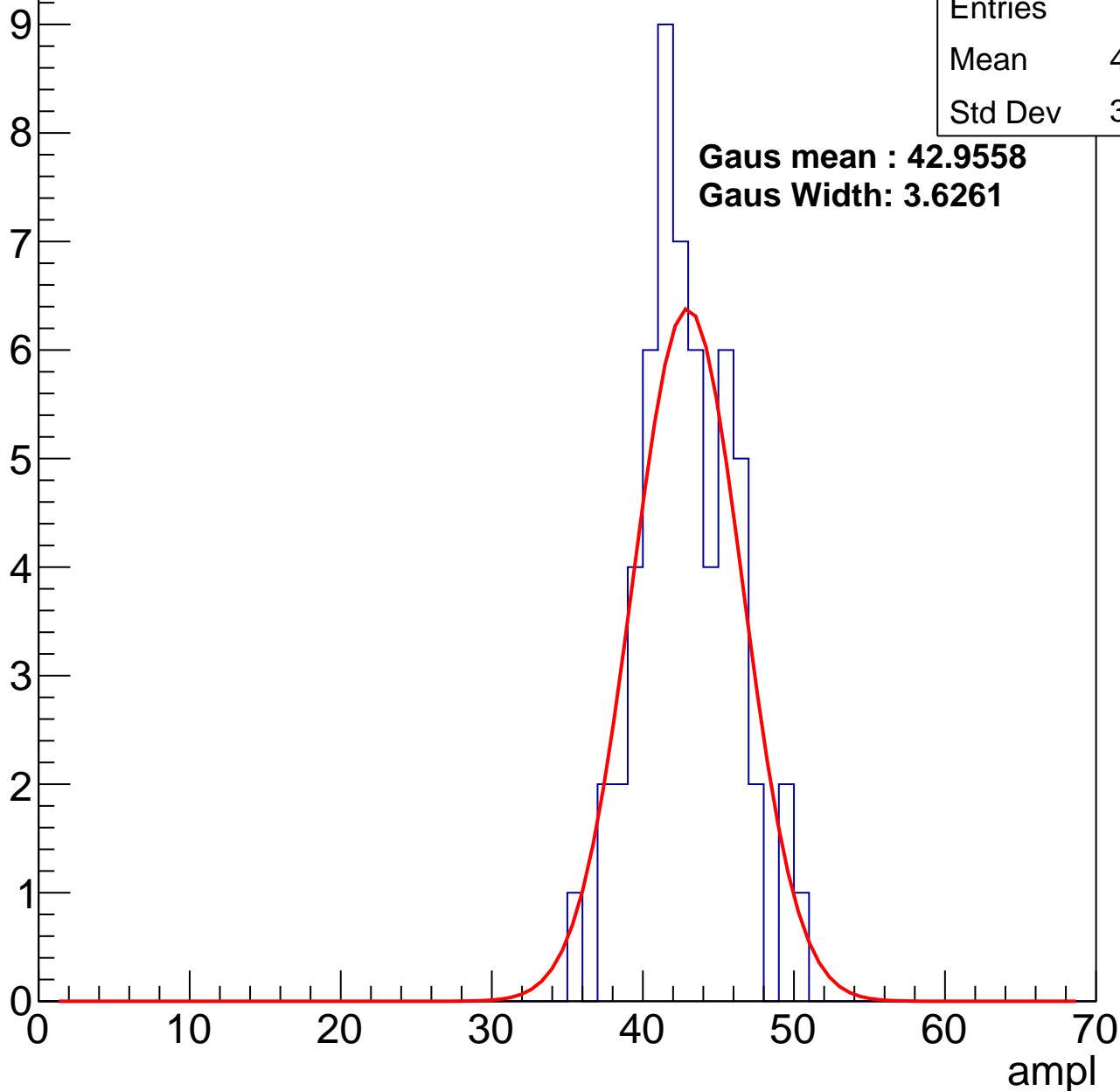
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	57
Mean	42.46
Std Dev	3.124

Gaus mean : 42.9558

Gaus Width: 3.6261

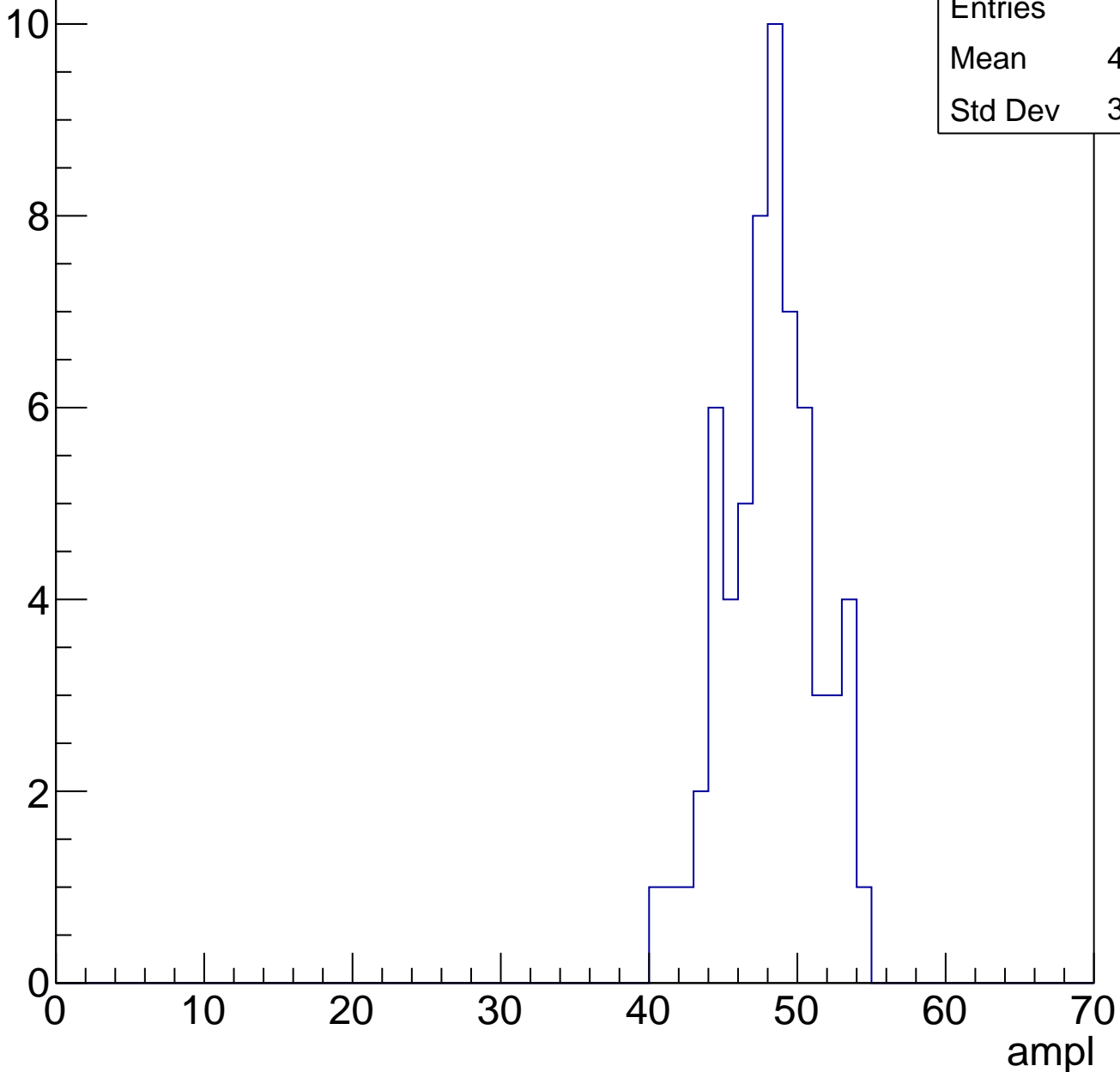


B1L003S, U21-ch124, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	62
Mean	47.69
Std Dev	3.098

Entry

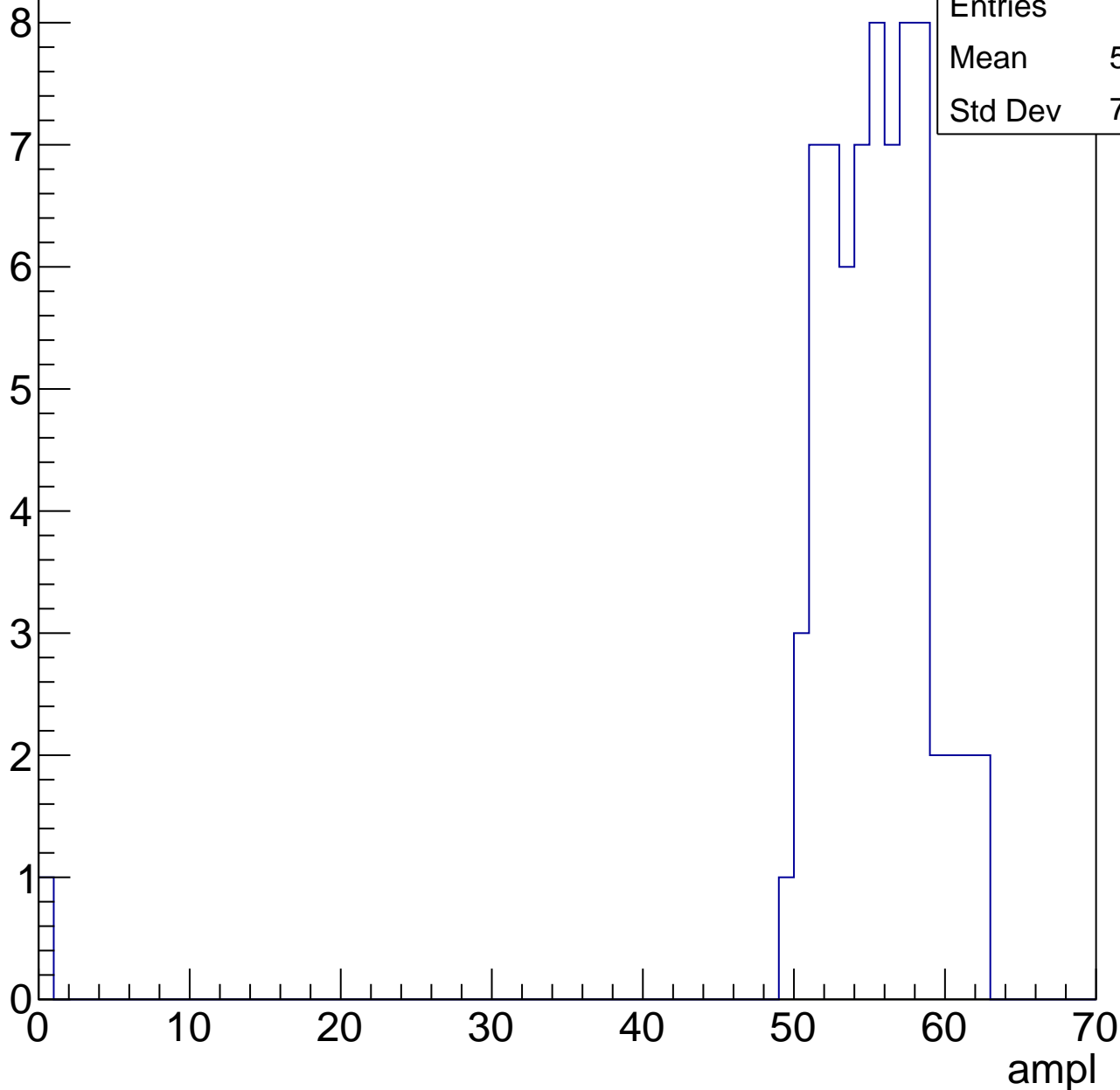


B1L003S, U21-ch124, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

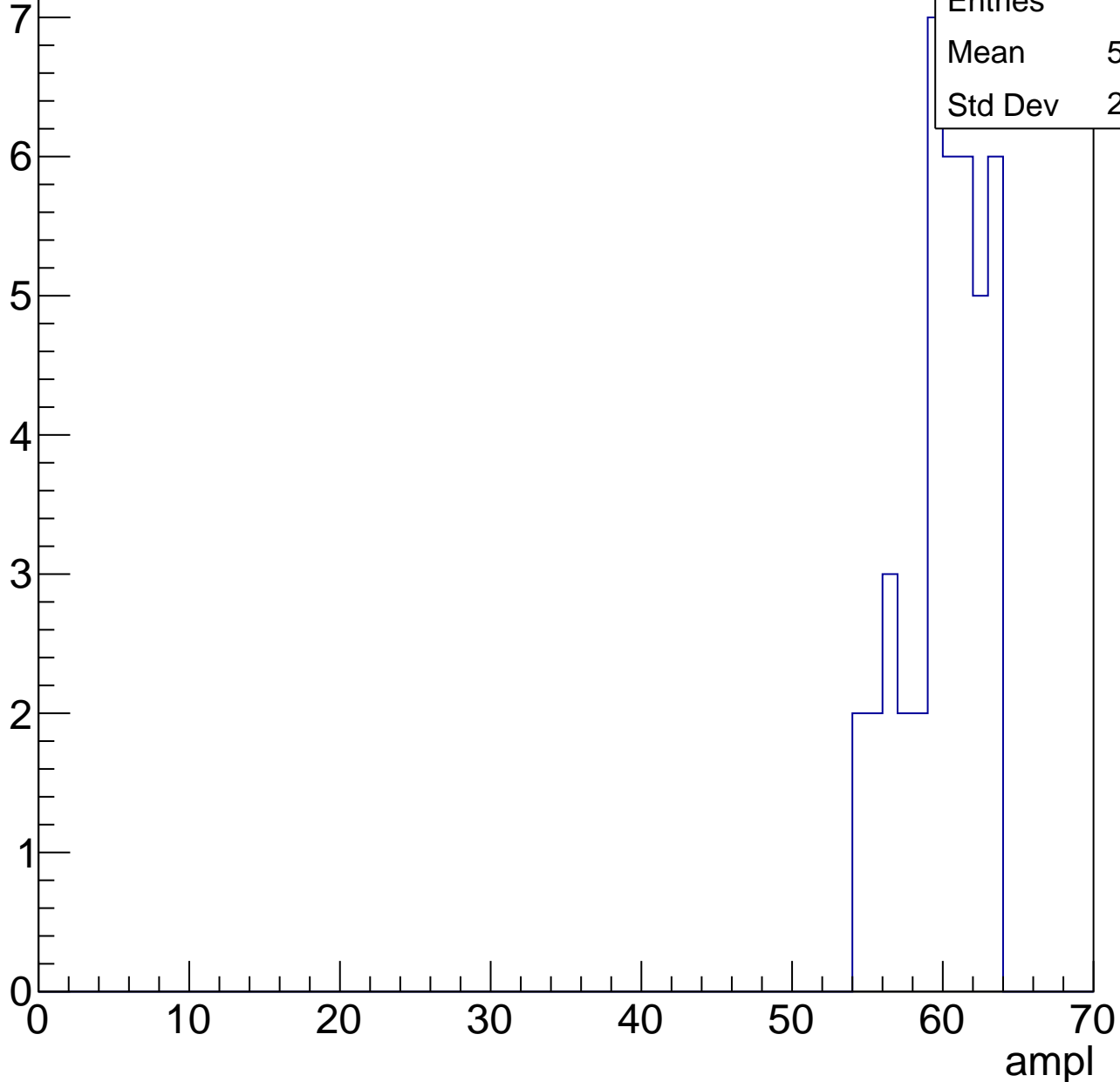
Entries	71
Mean	54.25
Std Dev	7.185



B1L003S, U21-ch124, adc5

calib_packv5_042523_0143.root, FC#13, port D2

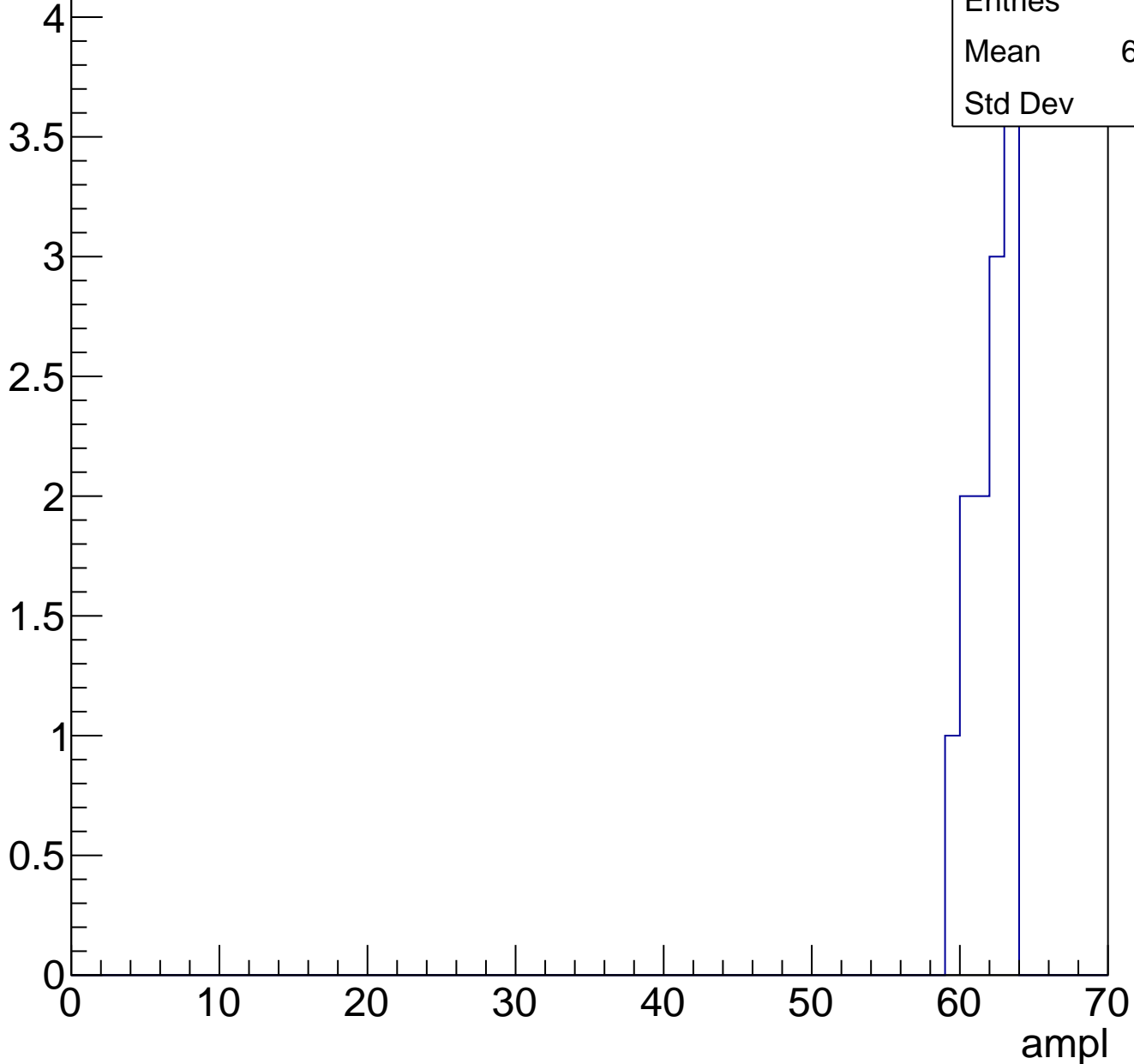
Entry



B1L003S, U21-ch124, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry

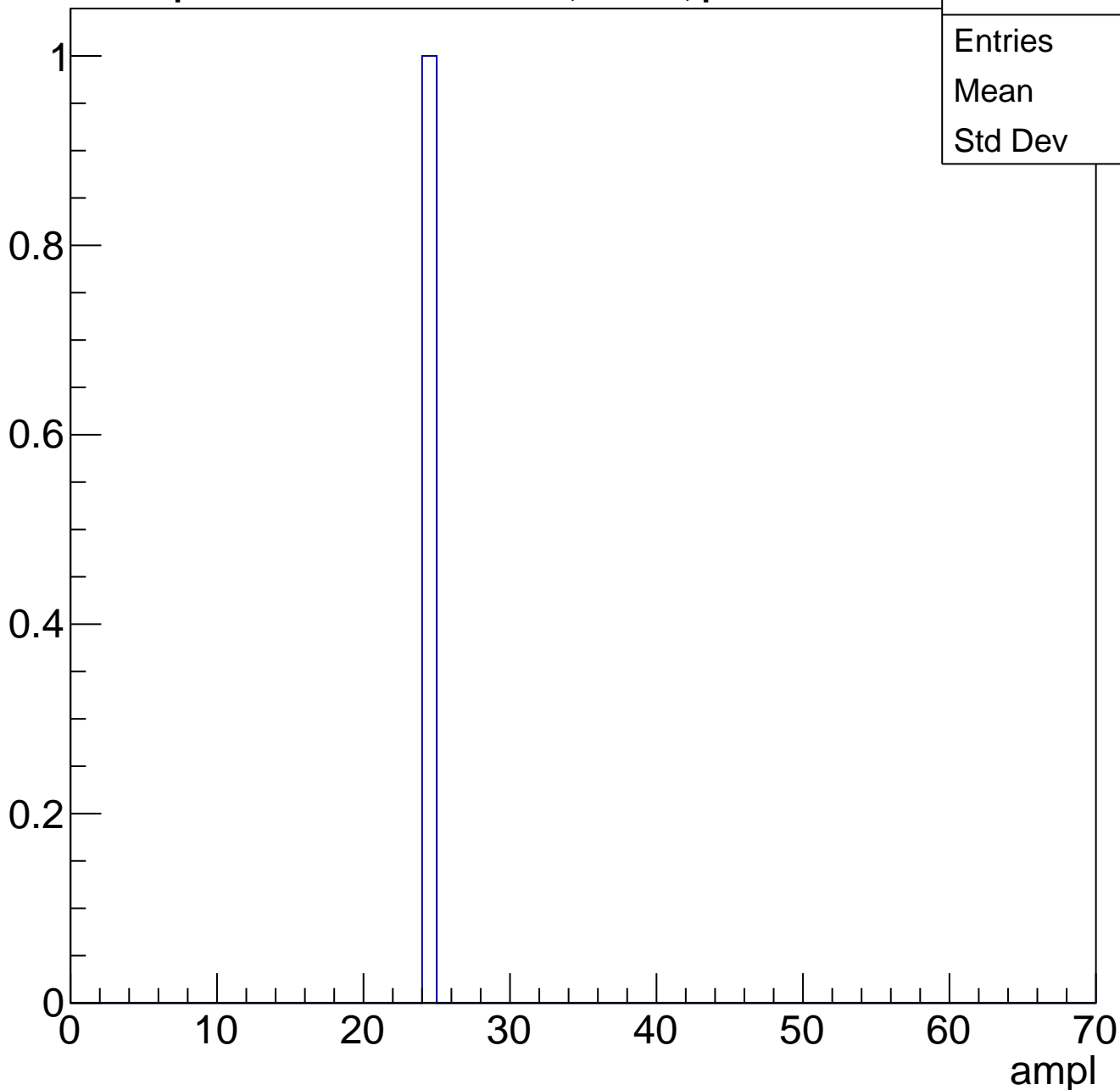


Entries	12
Mean	61.58
Std Dev	1.32

B1L003S, U21-ch124, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch125, adc0

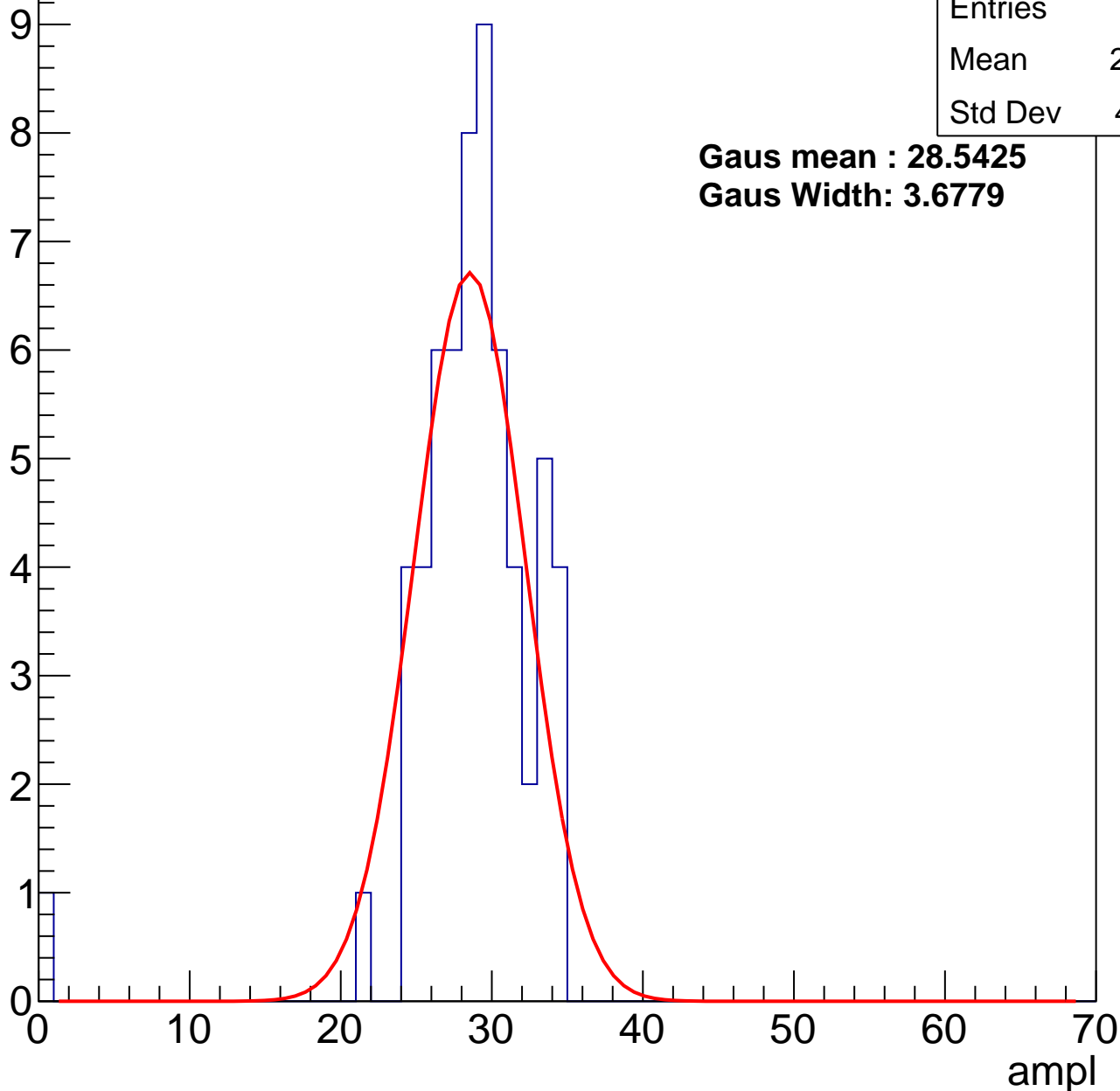
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	28.15
Std Dev	4.711

Gaus mean : 28.5425

Gaus Width: 3.6779



B1L003S, U21-ch125, adc1

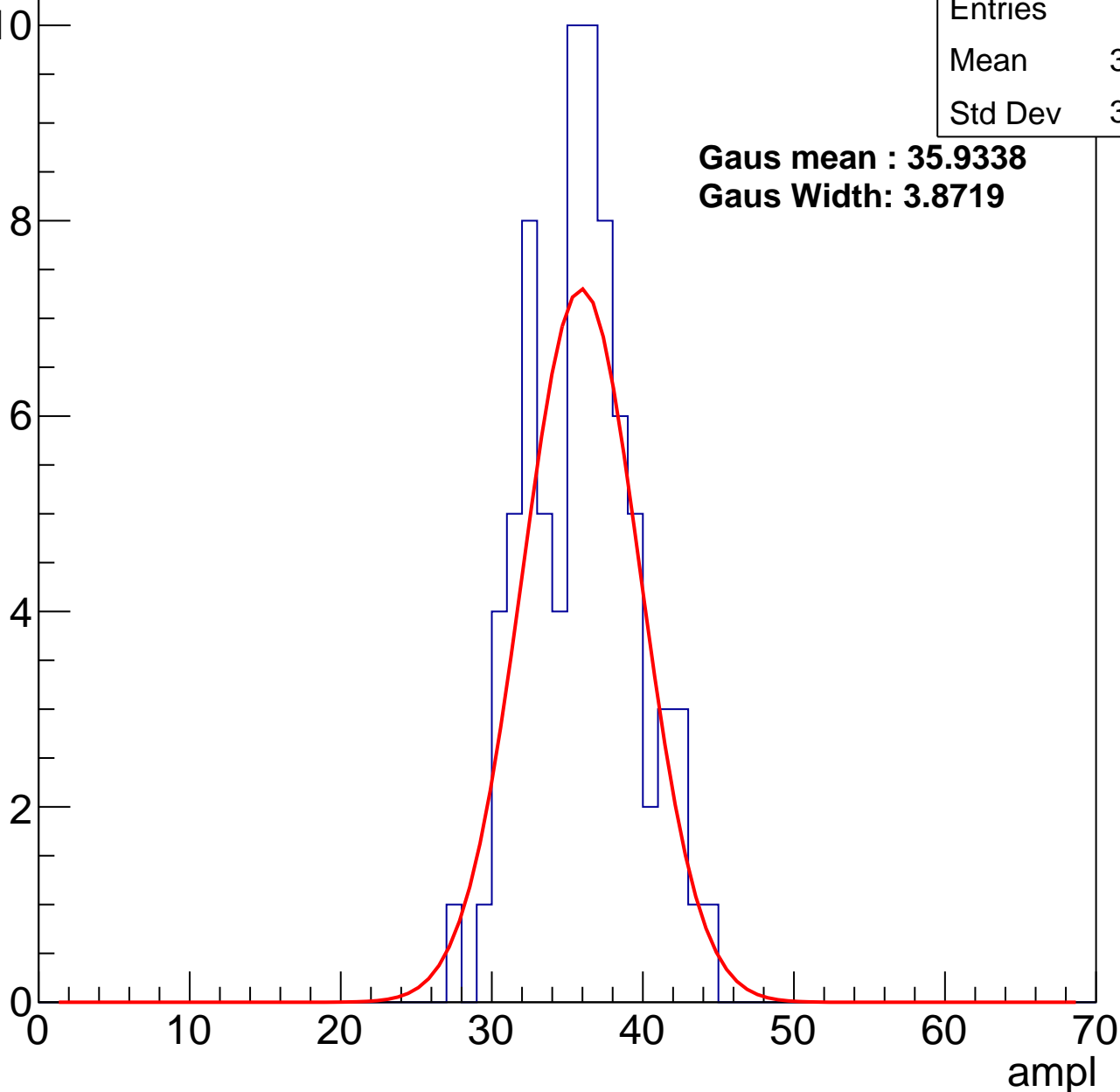
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	77
Mean	35.49
Std Dev	3.573

Gaus mean : 35.9338

Gaus Width: 3.8719



B1L003S, U21-ch125, adc2

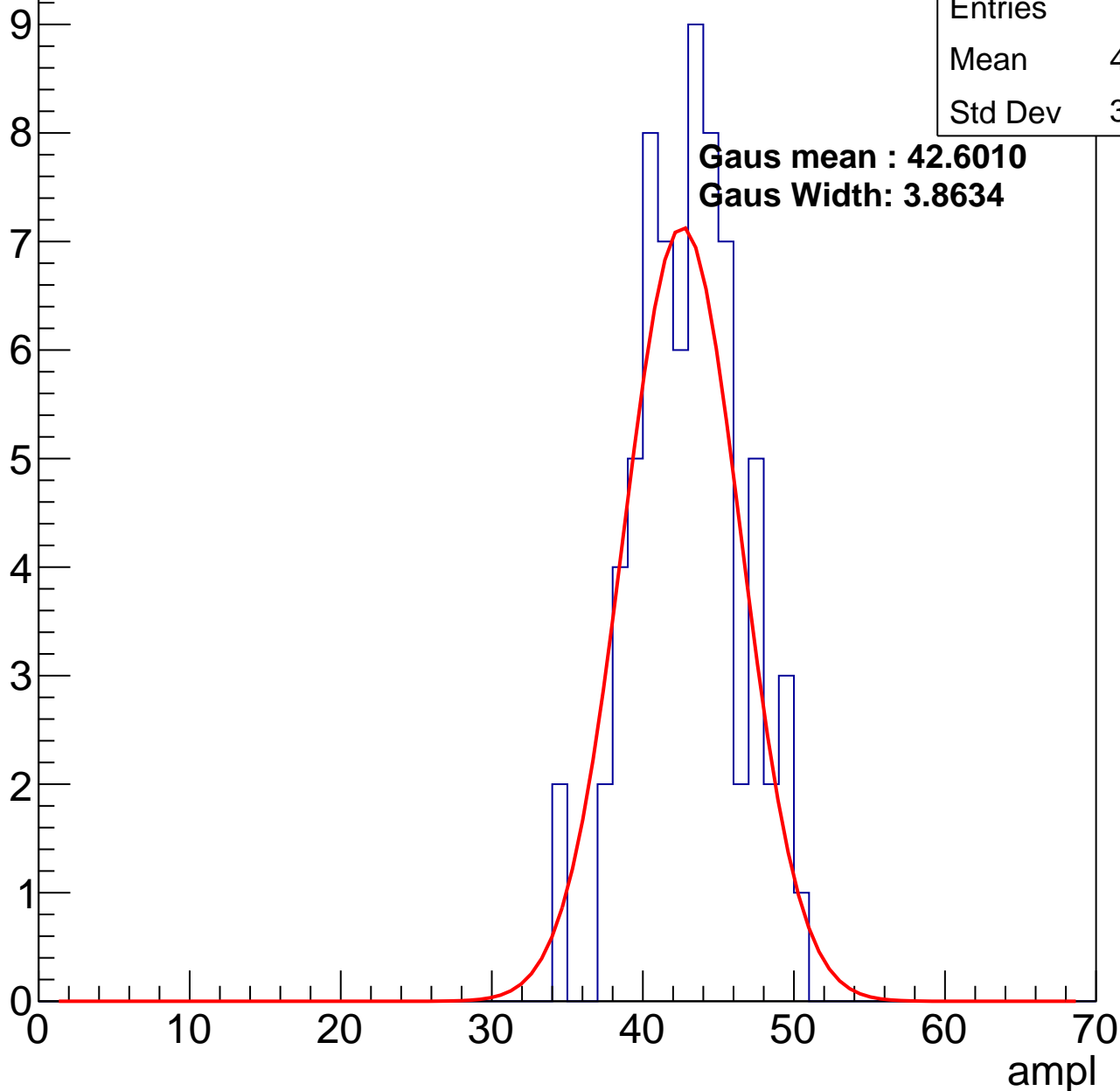
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	71
Mean	42.56
Std Dev	3.455

Gaus mean : 42.6010

Gaus Width: 3.8634

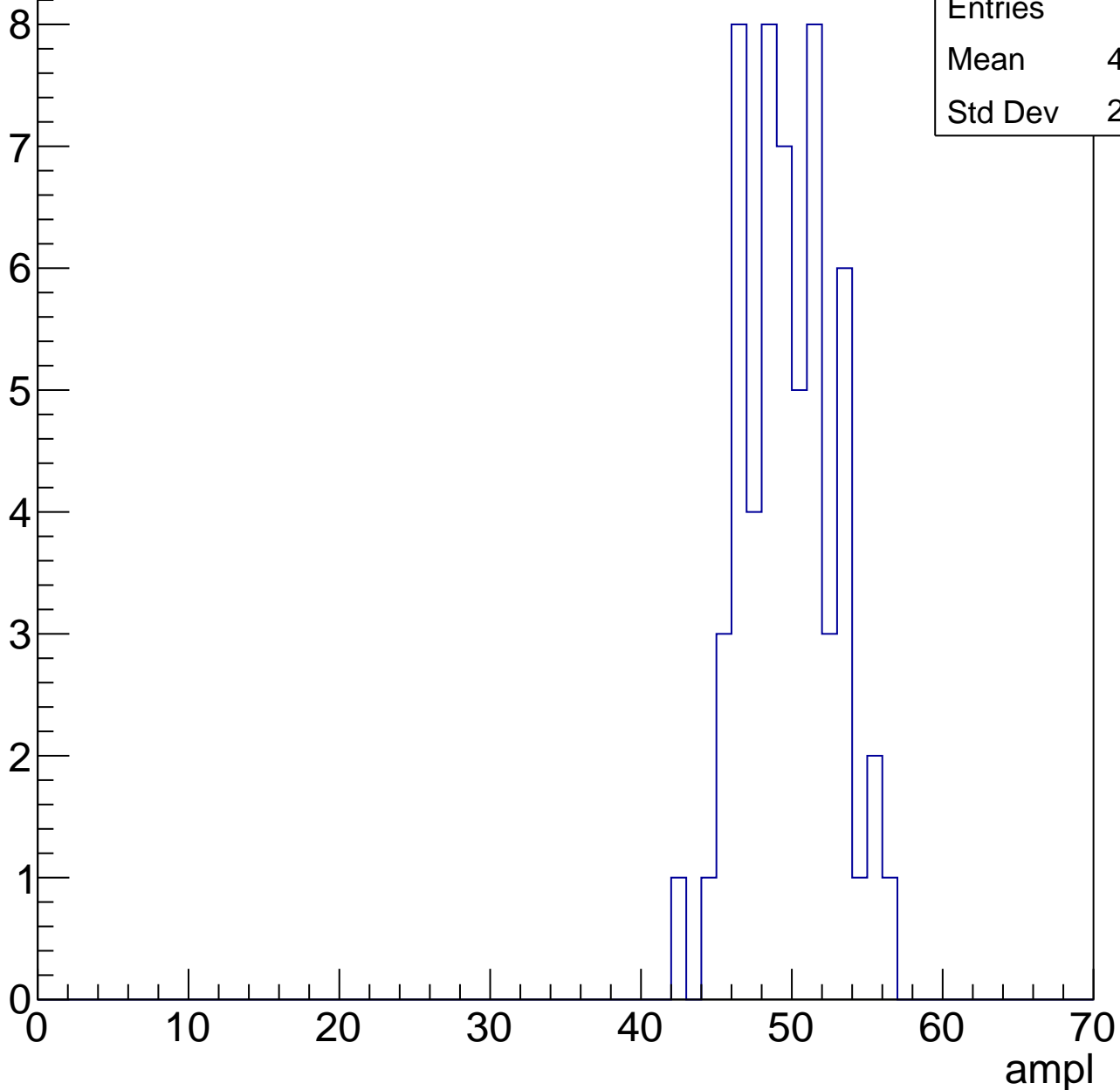


B1L003S, U21-ch125, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	58
Mean	49.24
Std Dev	2.996

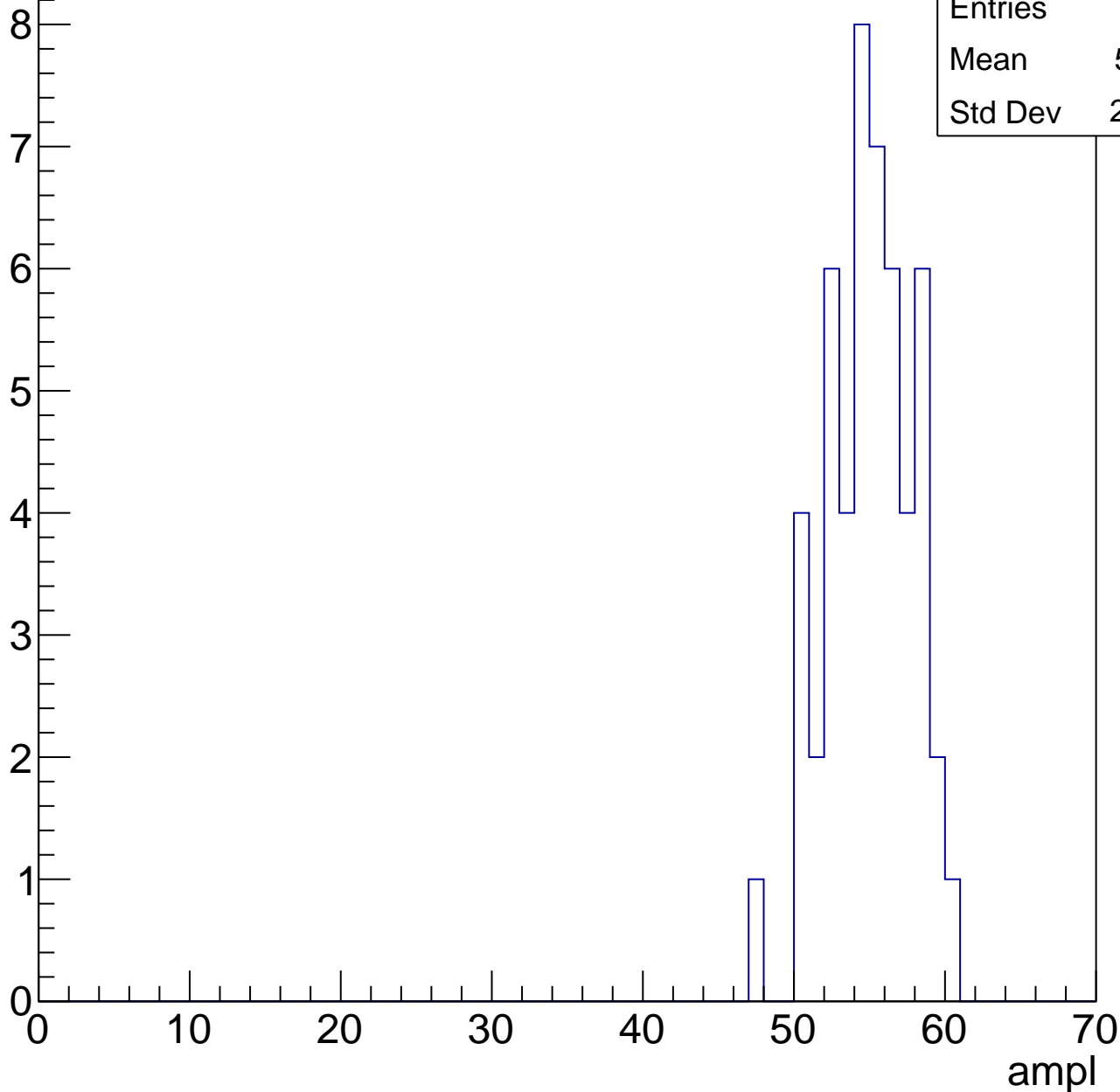


B1L003S, U21-ch125, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	51
Mean	54.51
Std Dev	2.782

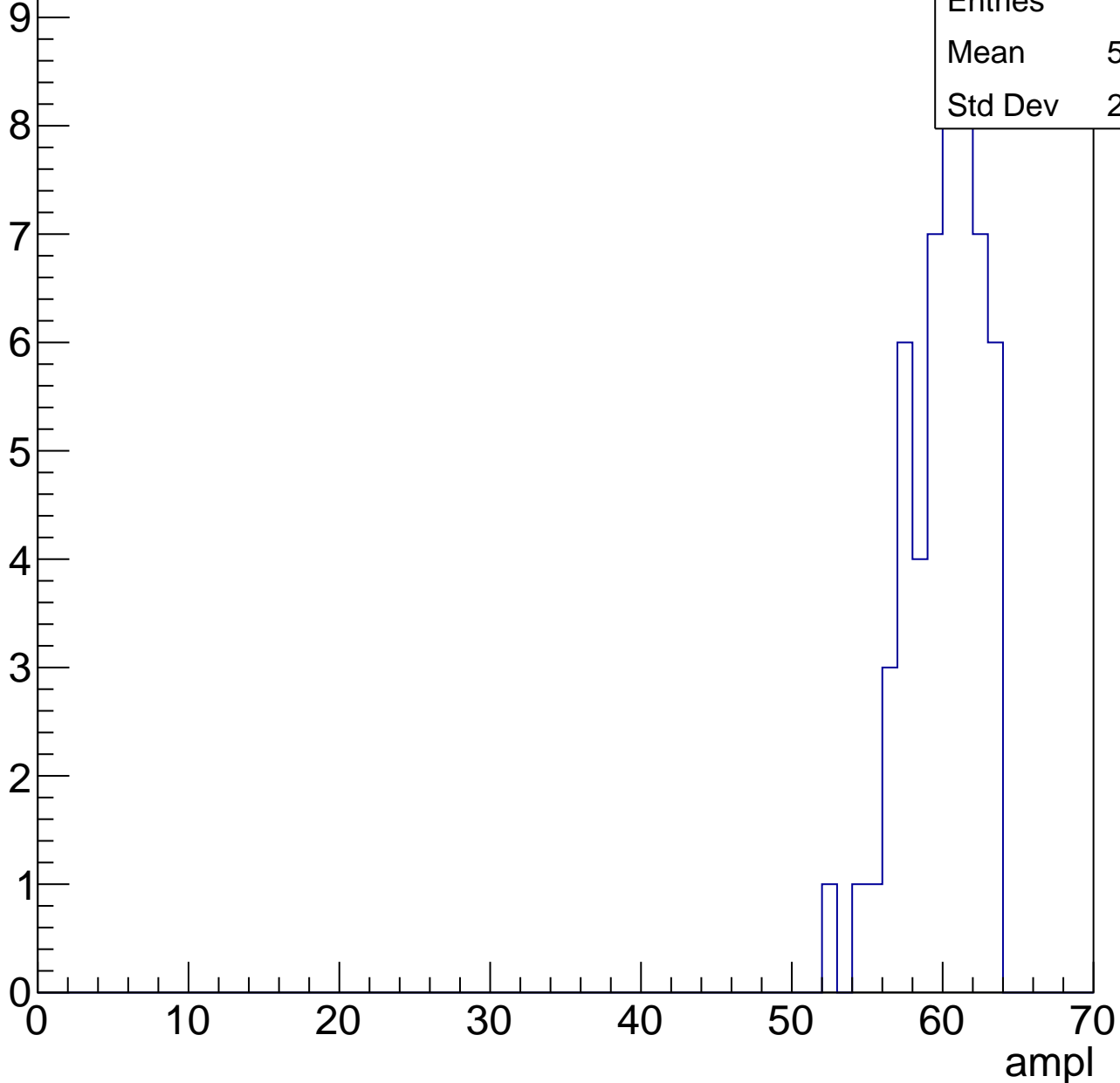


B1L003S, U21-ch125, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry

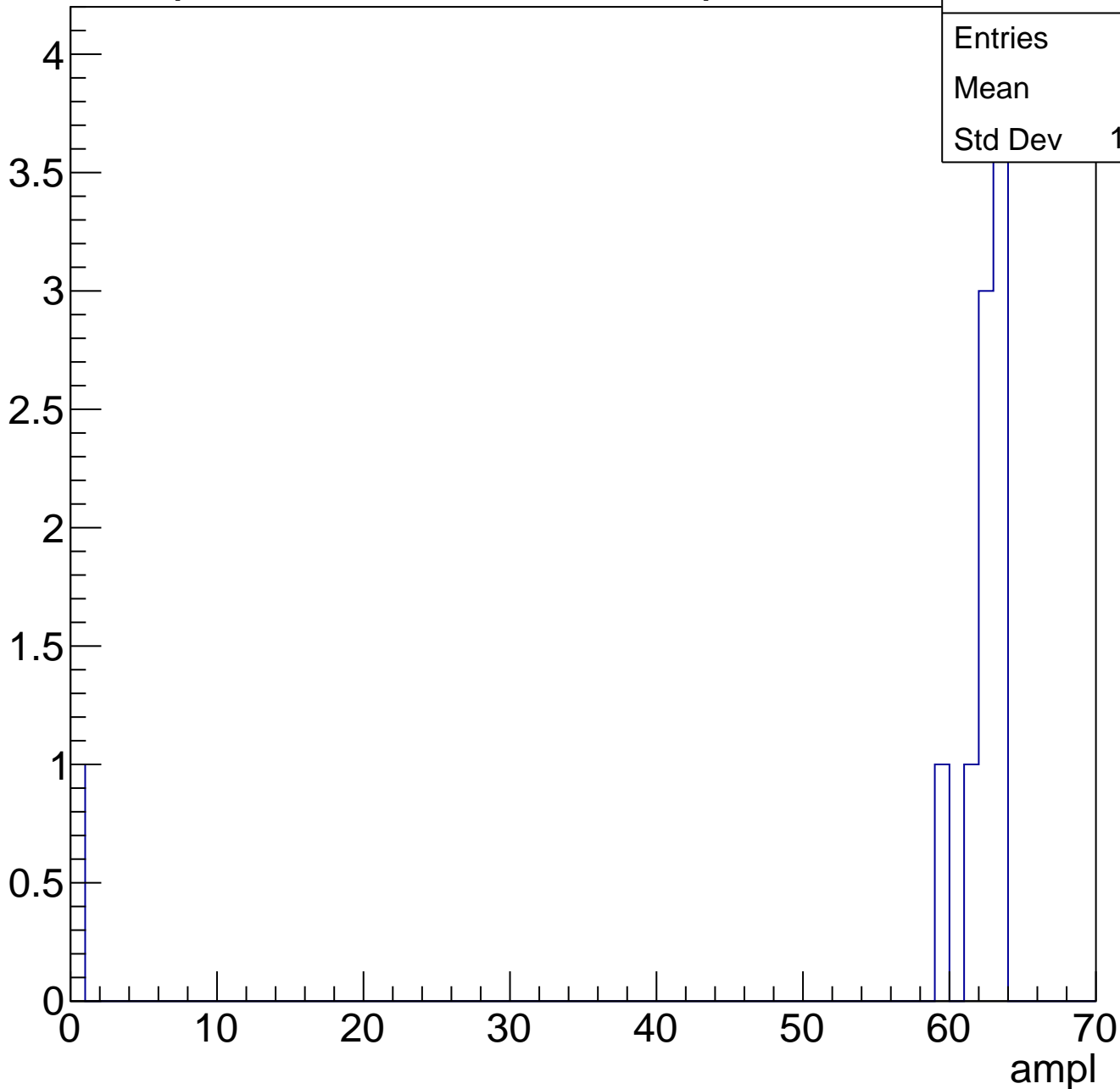
Entries	54
Mean	59.57
Std Dev	2.477



B1L003S, U21-ch125, adc6

calib_packv5_042523_0143.root, FC#13, port D2

Entry



B1L003S, U21-ch125, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch126, adc0

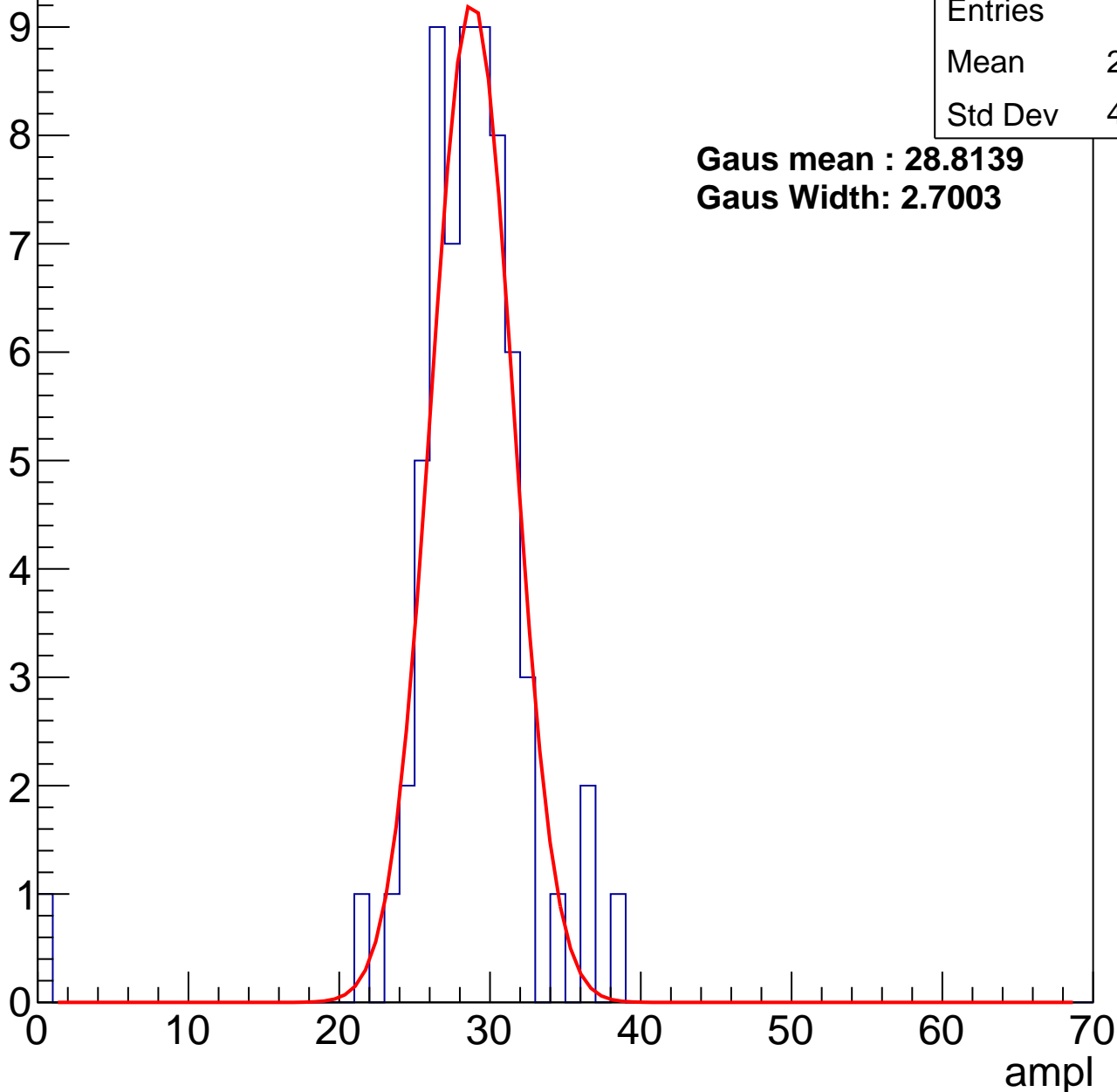
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	65
Mean	27.98
Std Dev	4.616

Gaus mean : 28.8139

Gaus Width: 2.7003



B1L003S, U21-ch126, adc1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	83
Mean	35.29
Std Dev	3.494

Gaus mean : 35.9706

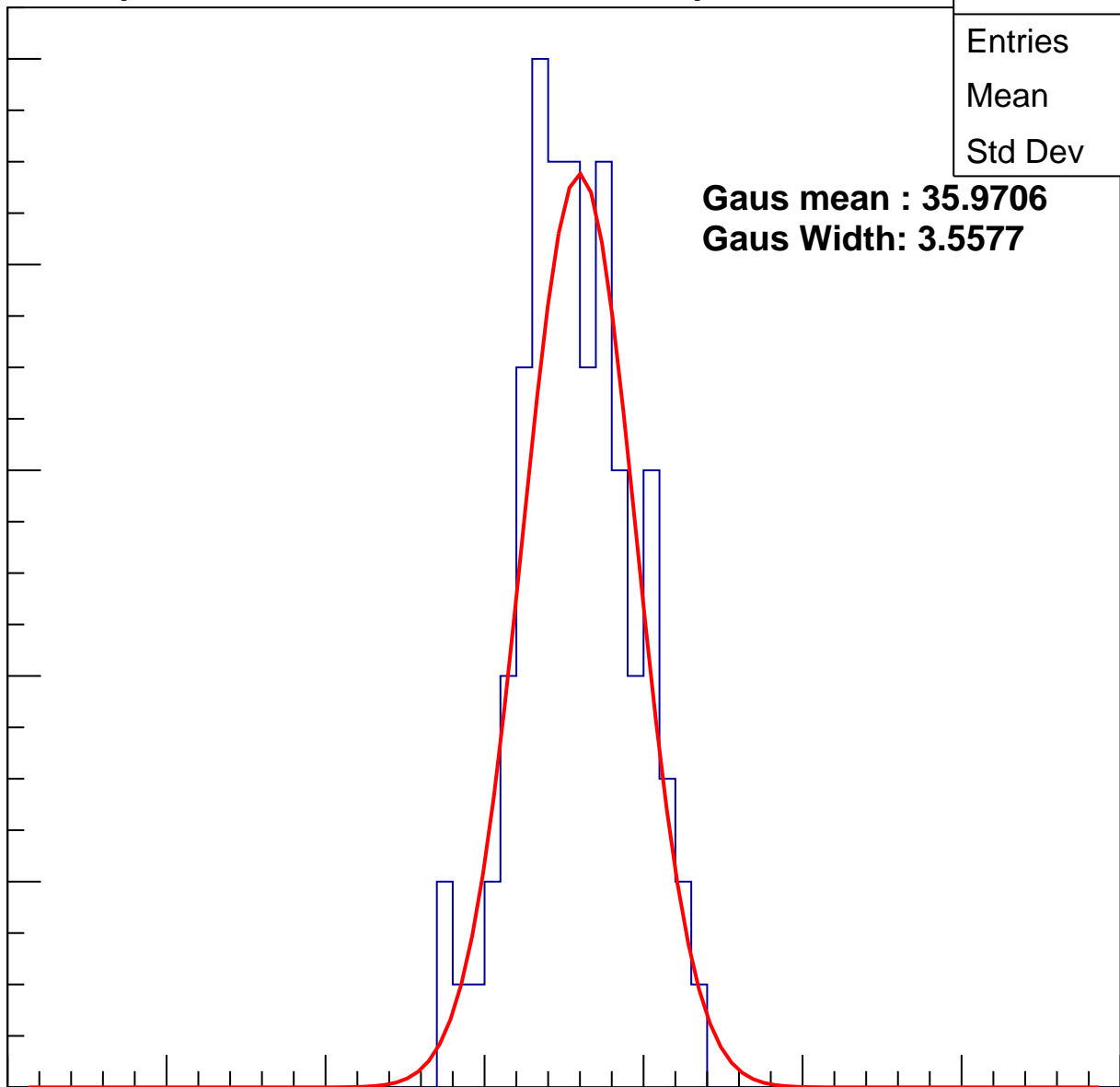
Gaus Width: 3.5577

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch126, adc2

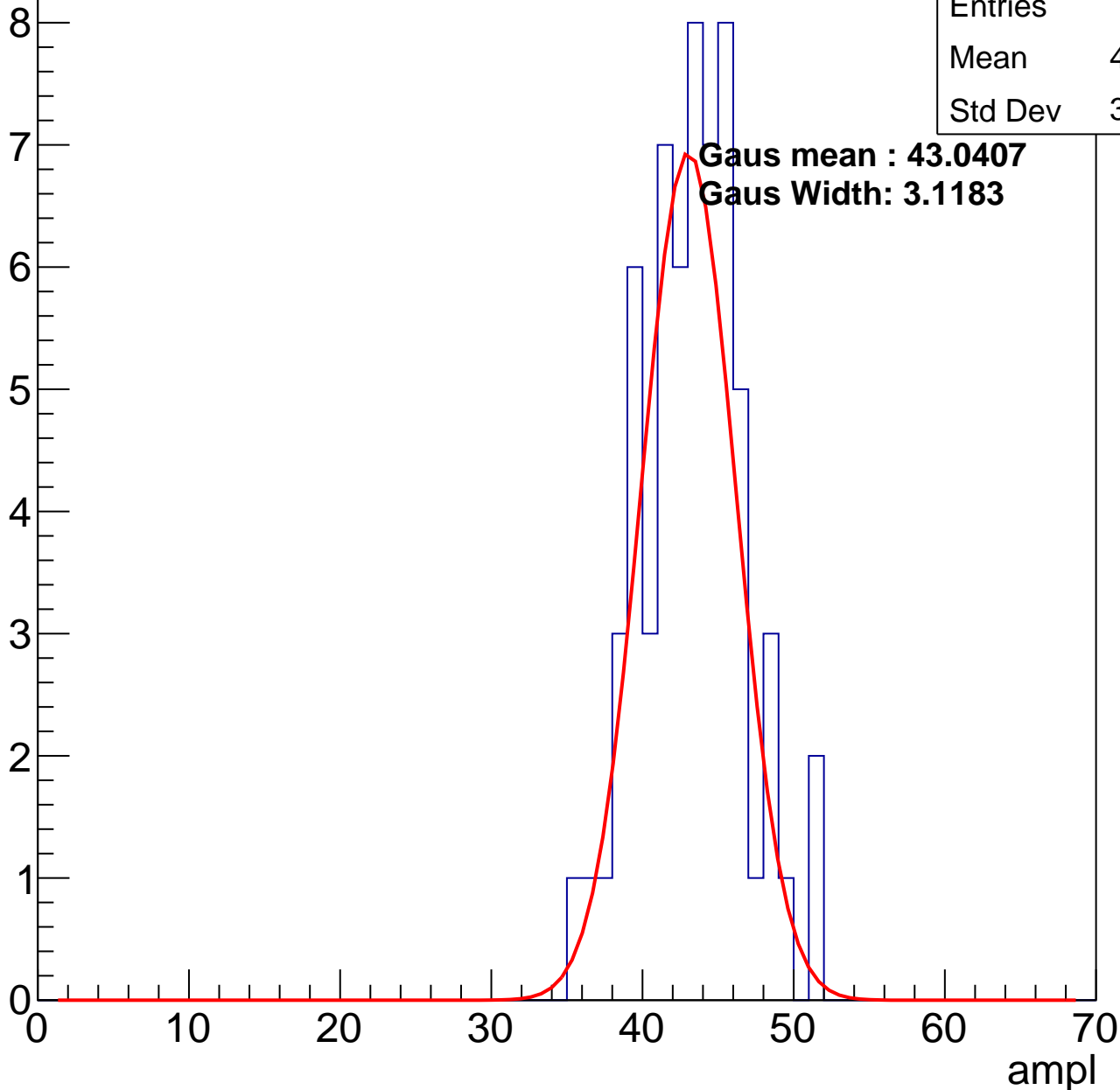
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	63
Mean	42.84
Std Dev	3.377

Gaus mean : 43.0407

Gaus Width: 3.1183

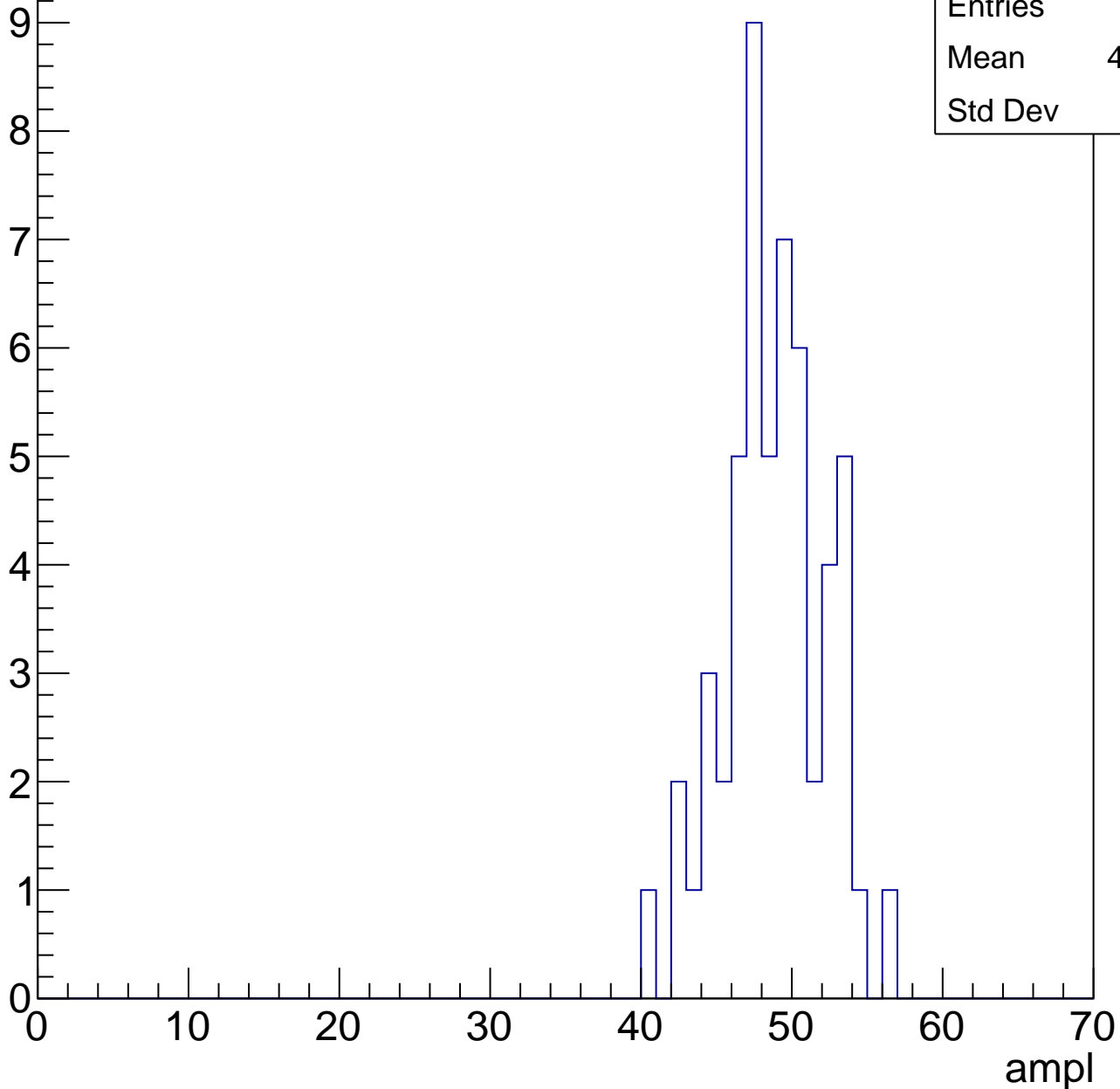


B1L003S, U21-ch126, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

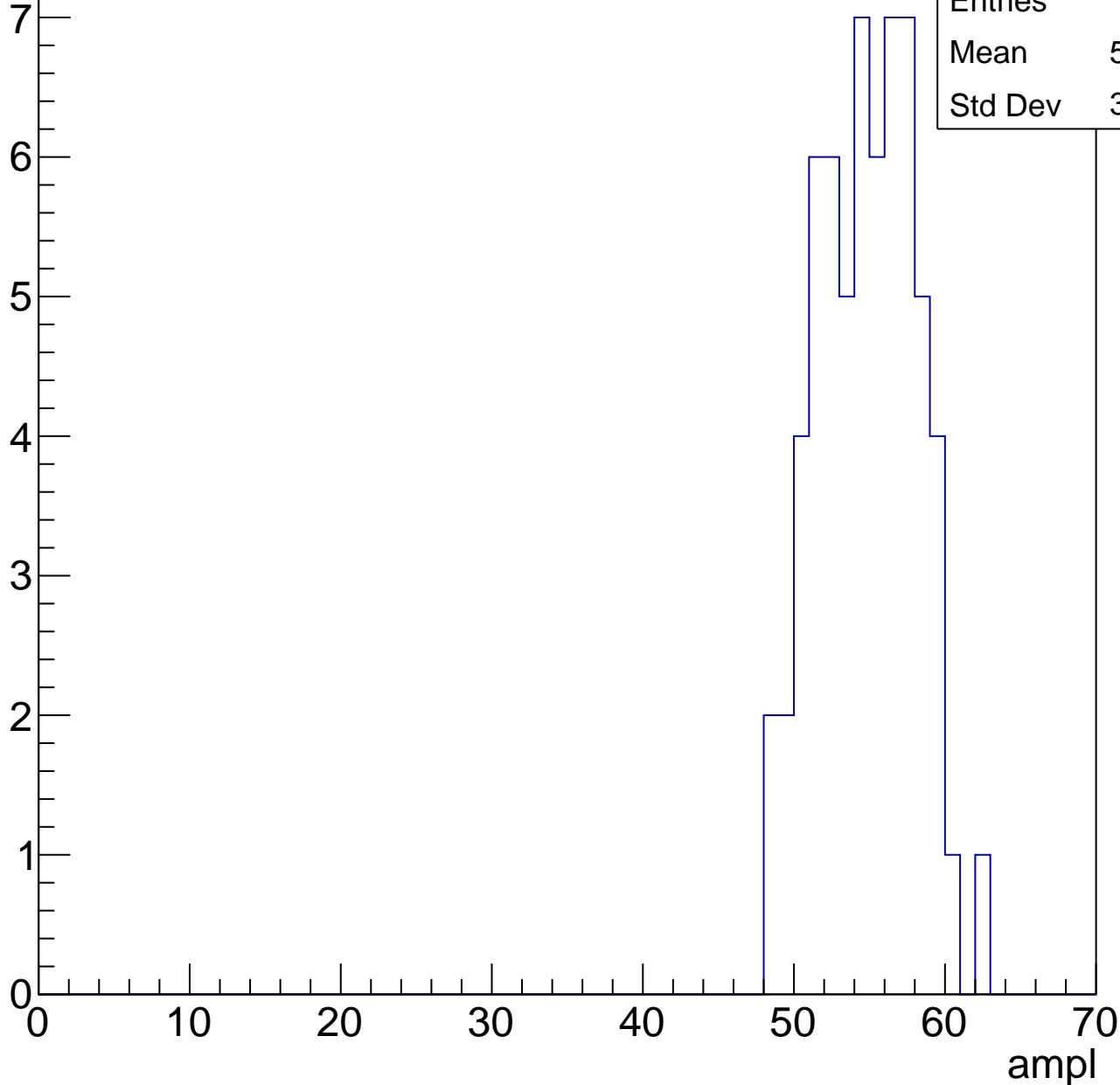
Entries	54
Mean	48.33
Std Dev	3.3



B1L003S, U21-ch126, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	63
Mean	54.35
Std Dev	3.183

B1L003S, U21-ch126, adc5

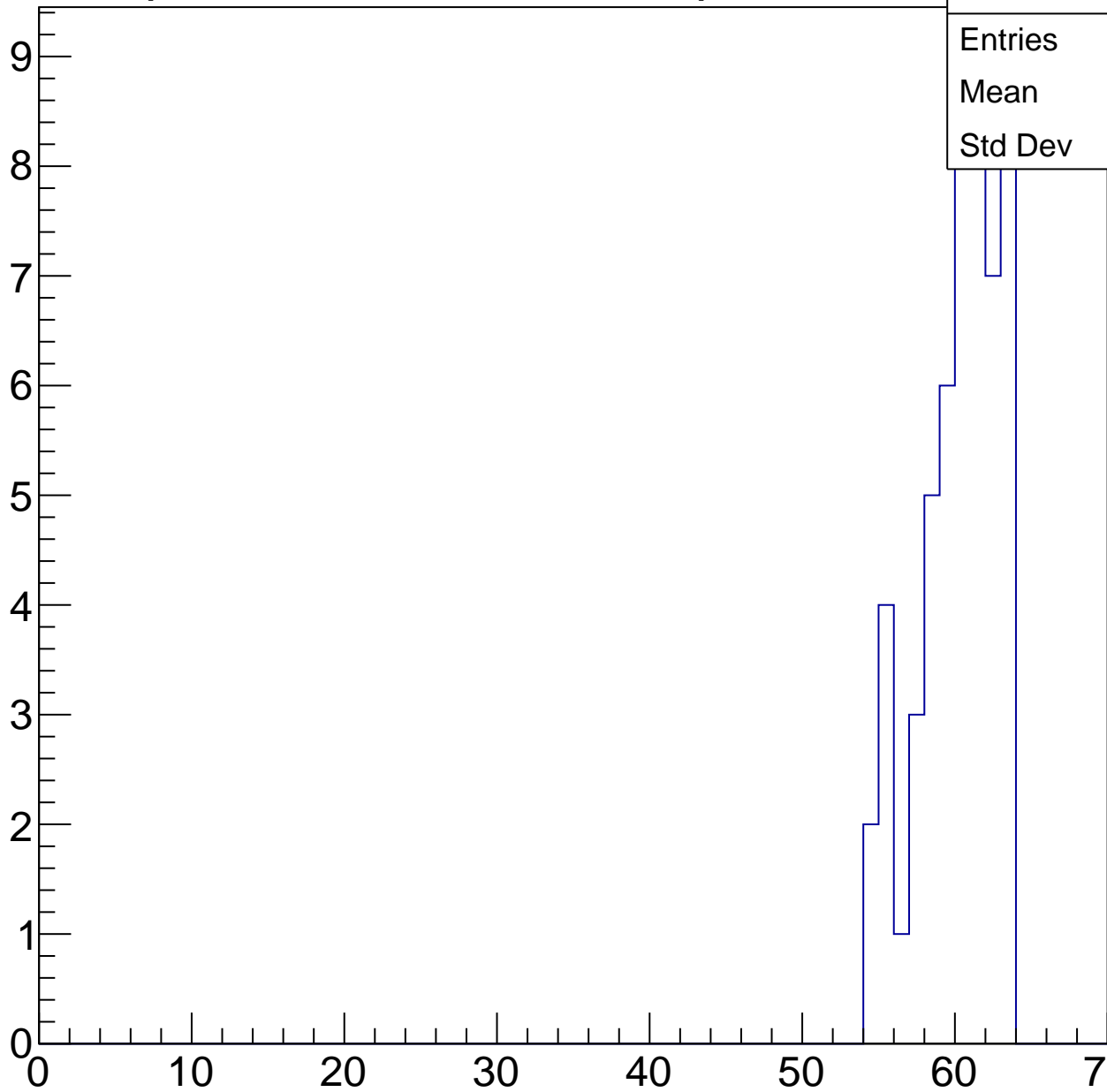
calib_packv5_042523_0143.root, FC#13, port D2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	53
Mean	59.74
Std Dev	2.556

ampl



B1L003S, U21-ch126, adc6

calib_packv5_042523_0143.root, FC#13, port D2

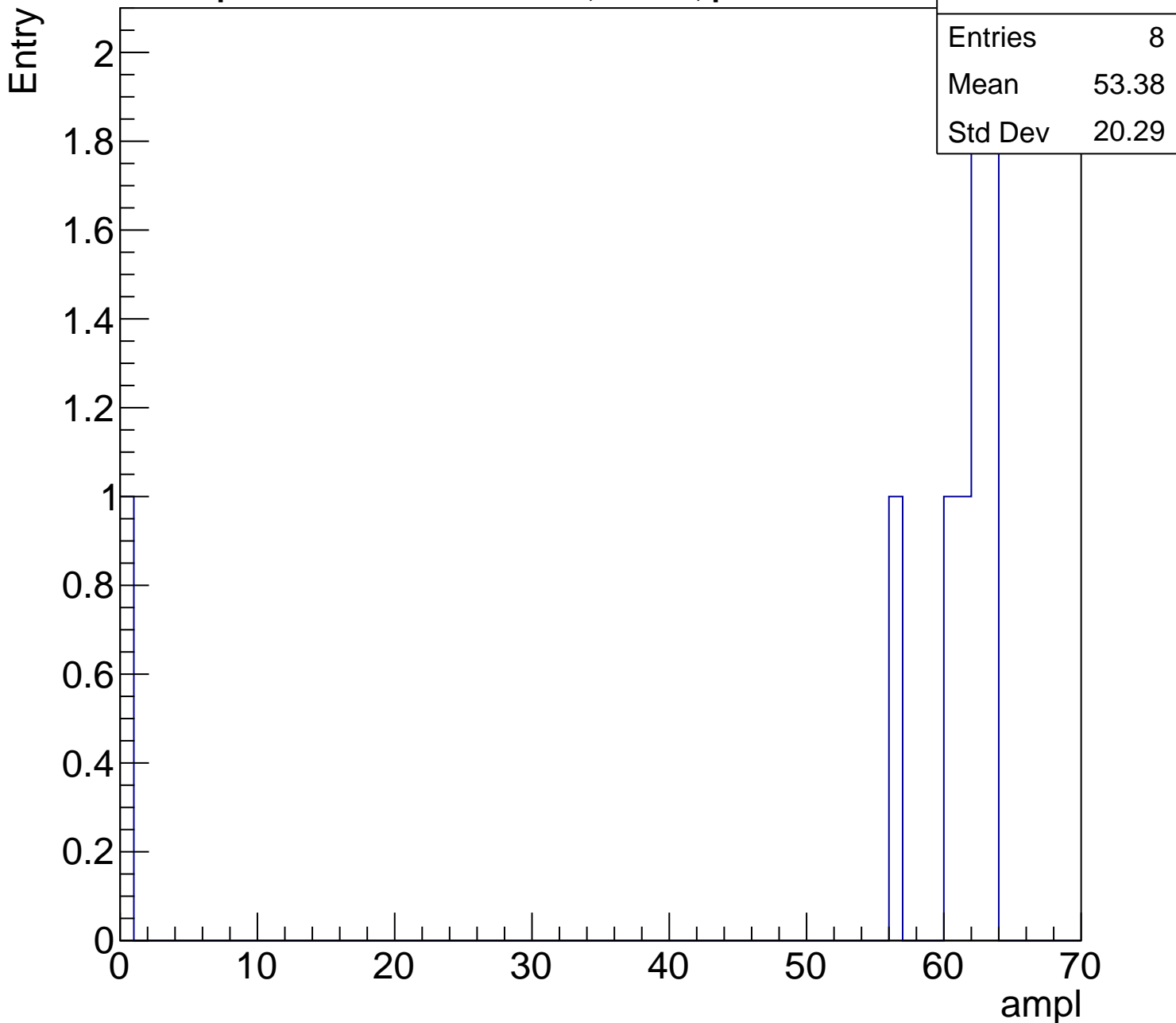
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	8
Mean	53.38
Std Dev	20.29

0 10 20 30 40 50 60 70

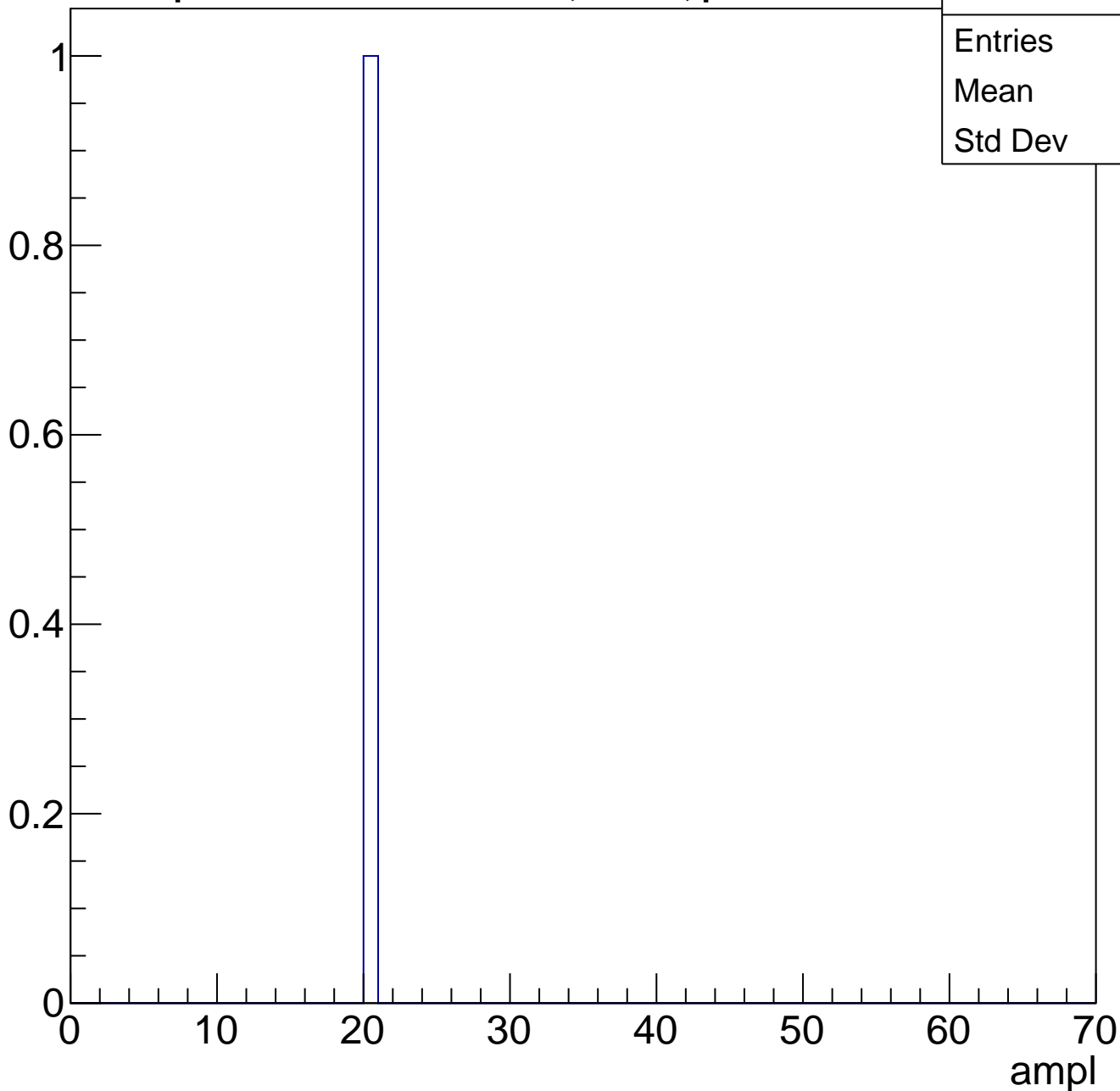
ampl



B1L003S, U21-ch126, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	1
Mean	20
Std Dev	0

B1L003S, U21-ch127, adc0

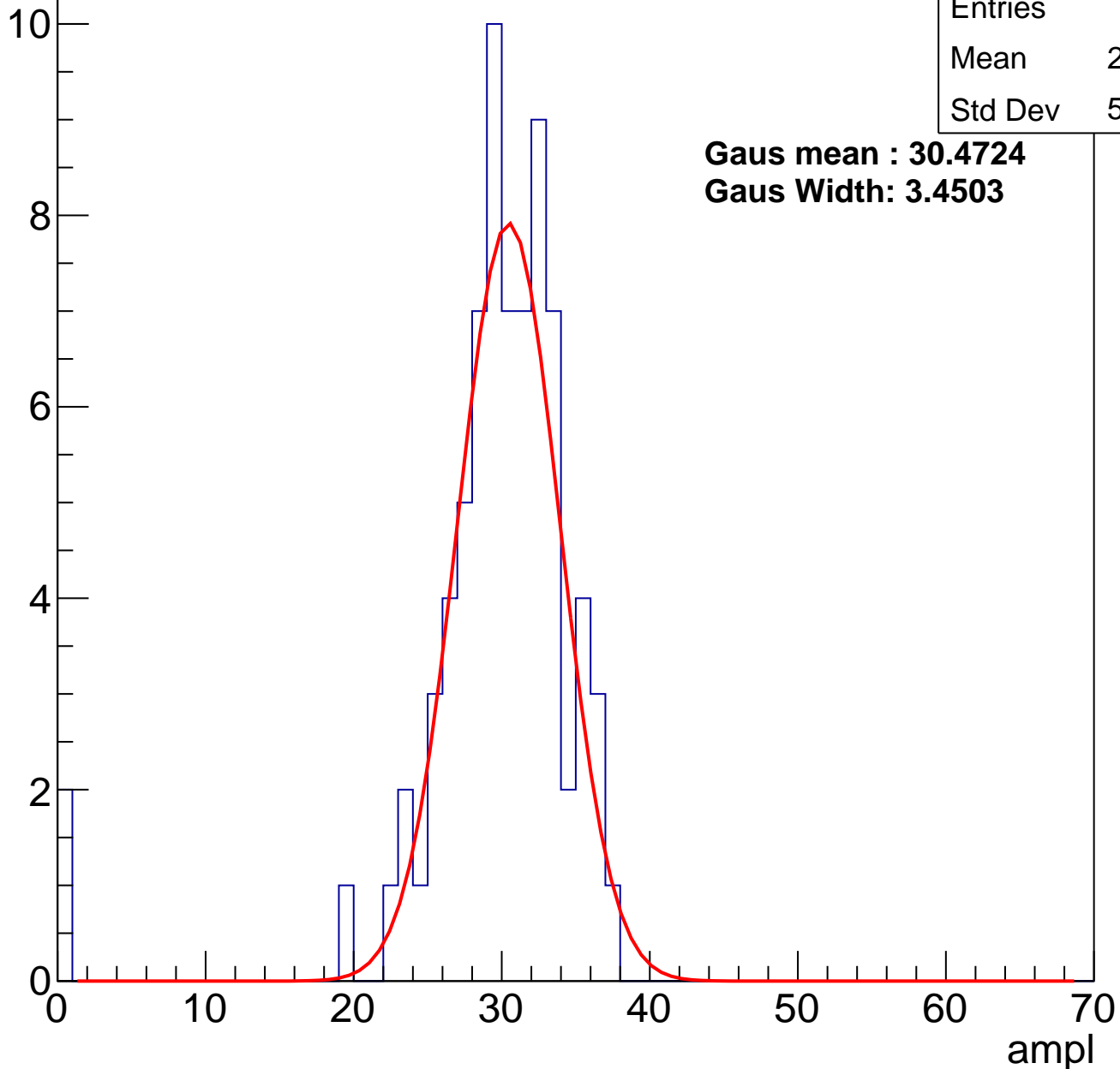
calib_packv5_042523_0143.root, FC#13, port D2

Entries	76
Mean	29.08
Std Dev	5.929

Gaus mean : 30.4724

Gaus Width: 3.4503

Entry



B1L003S, U21-ch127, adc1

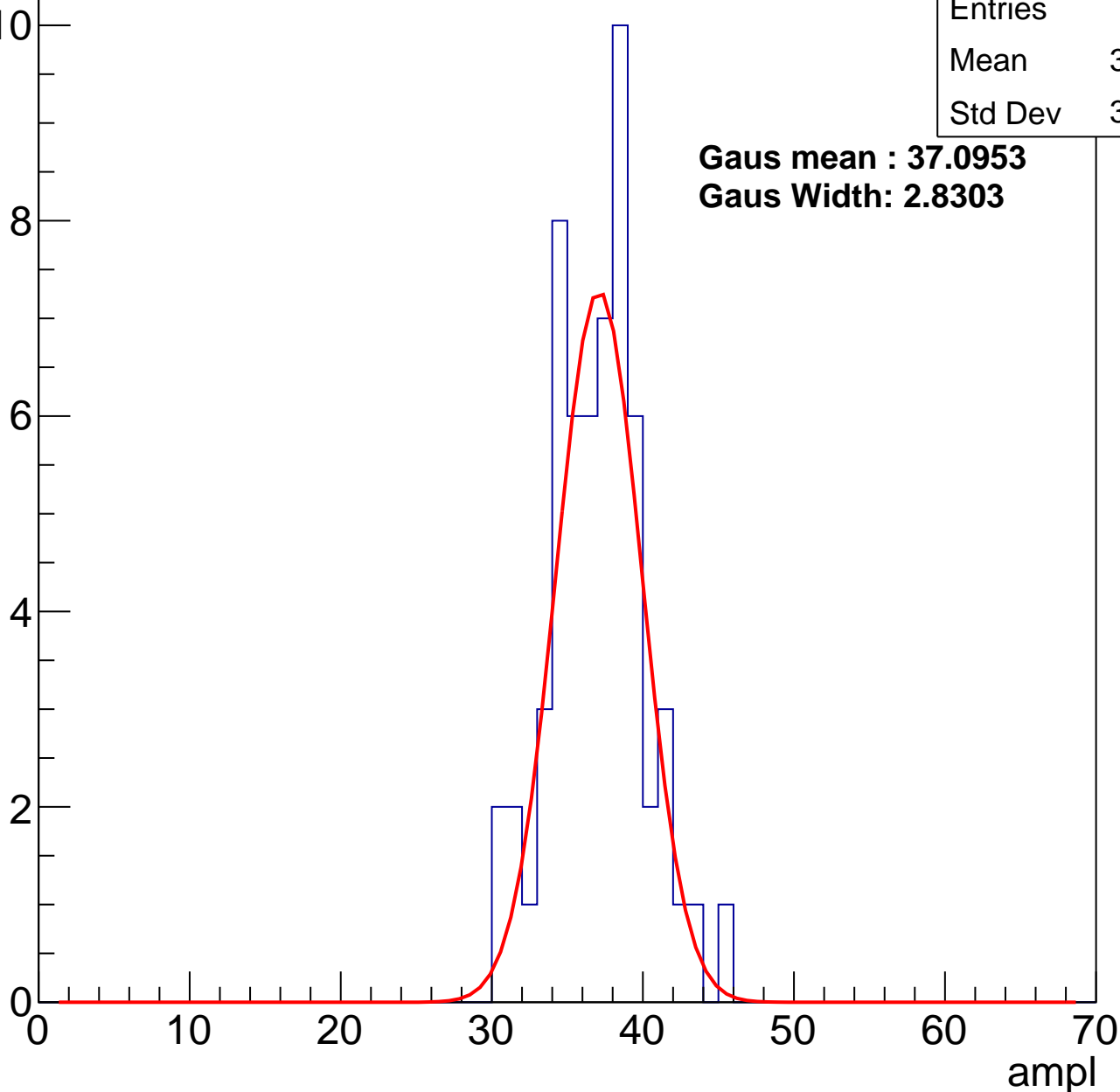
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	59
Mean	36.56
Std Dev	3.077

Gaus mean : 37.0953

Gaus Width: 2.8303



B1L003S, U21-ch127, adc2

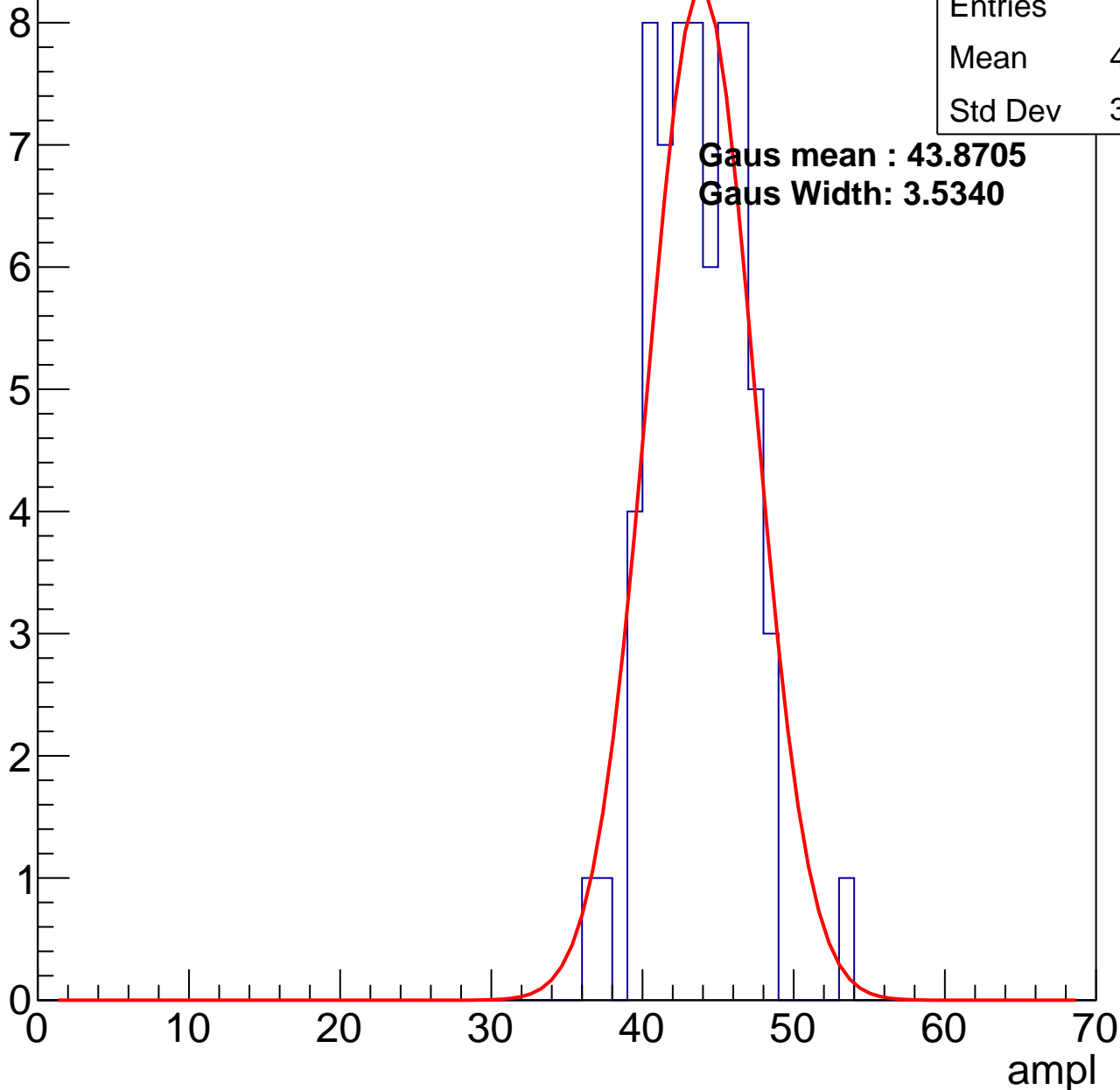
calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	68
Mean	43.24
Std Dev	3.015

Gaus mean : 43.8705

Gaus Width: 3.5340

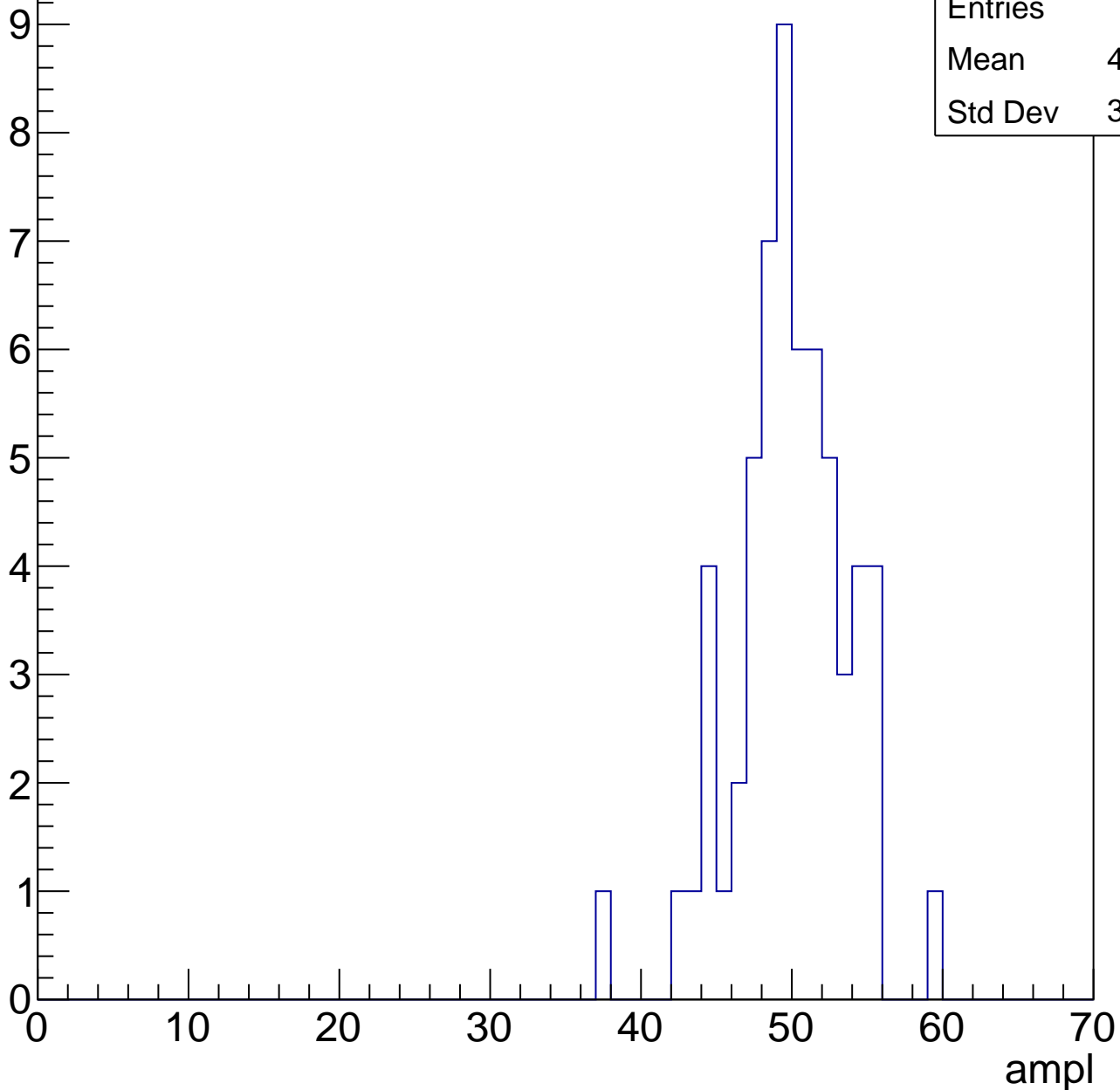


B1L003S, U21-ch127, adc3

calib_packv5_042523_0143.root, FC#13, port D2

Entry

Entries	60
Mean	49.45
Std Dev	3.752

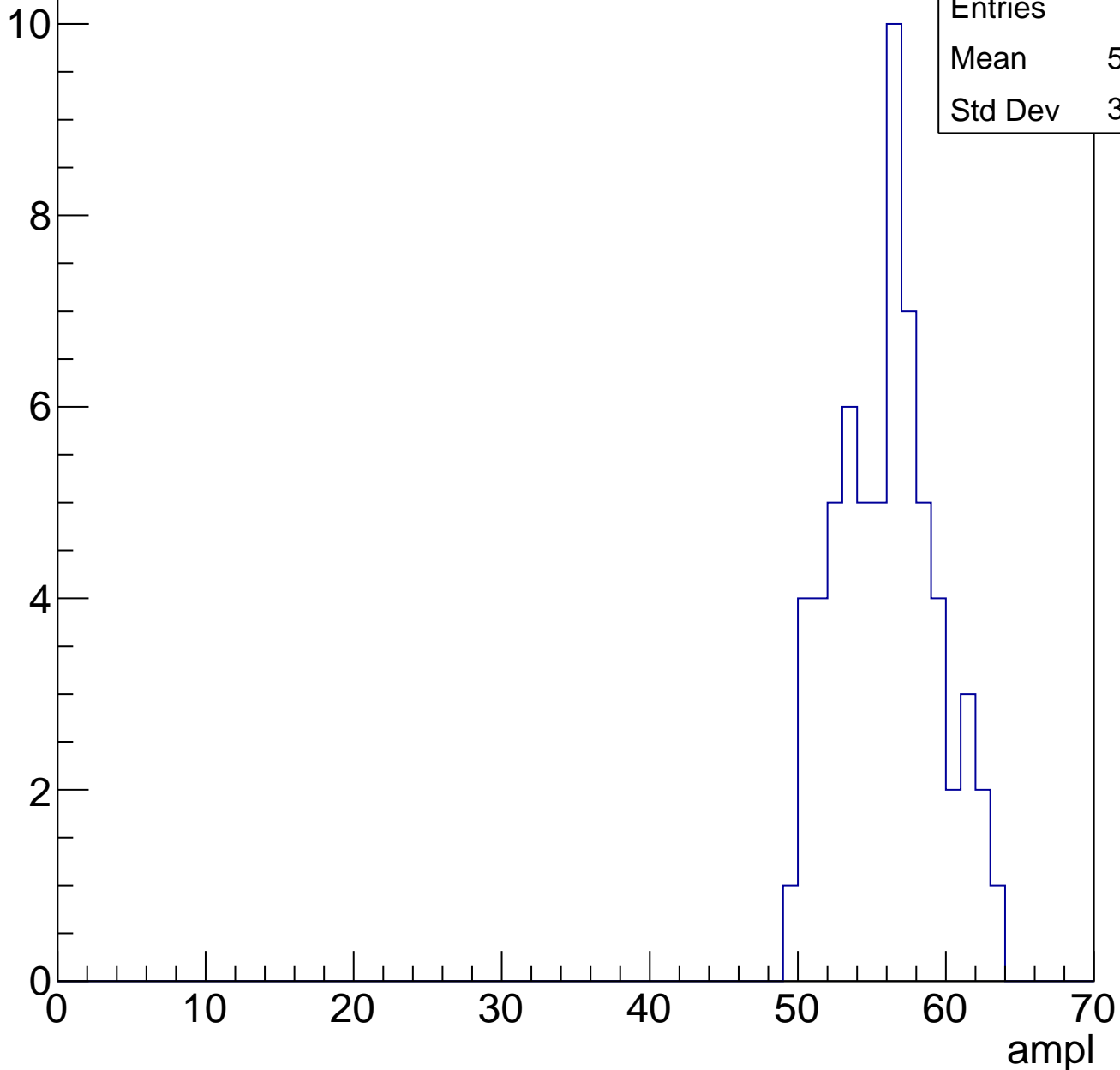


B1L003S, U21-ch127, adc4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	64
Mean	55.48
Std Dev	3.368

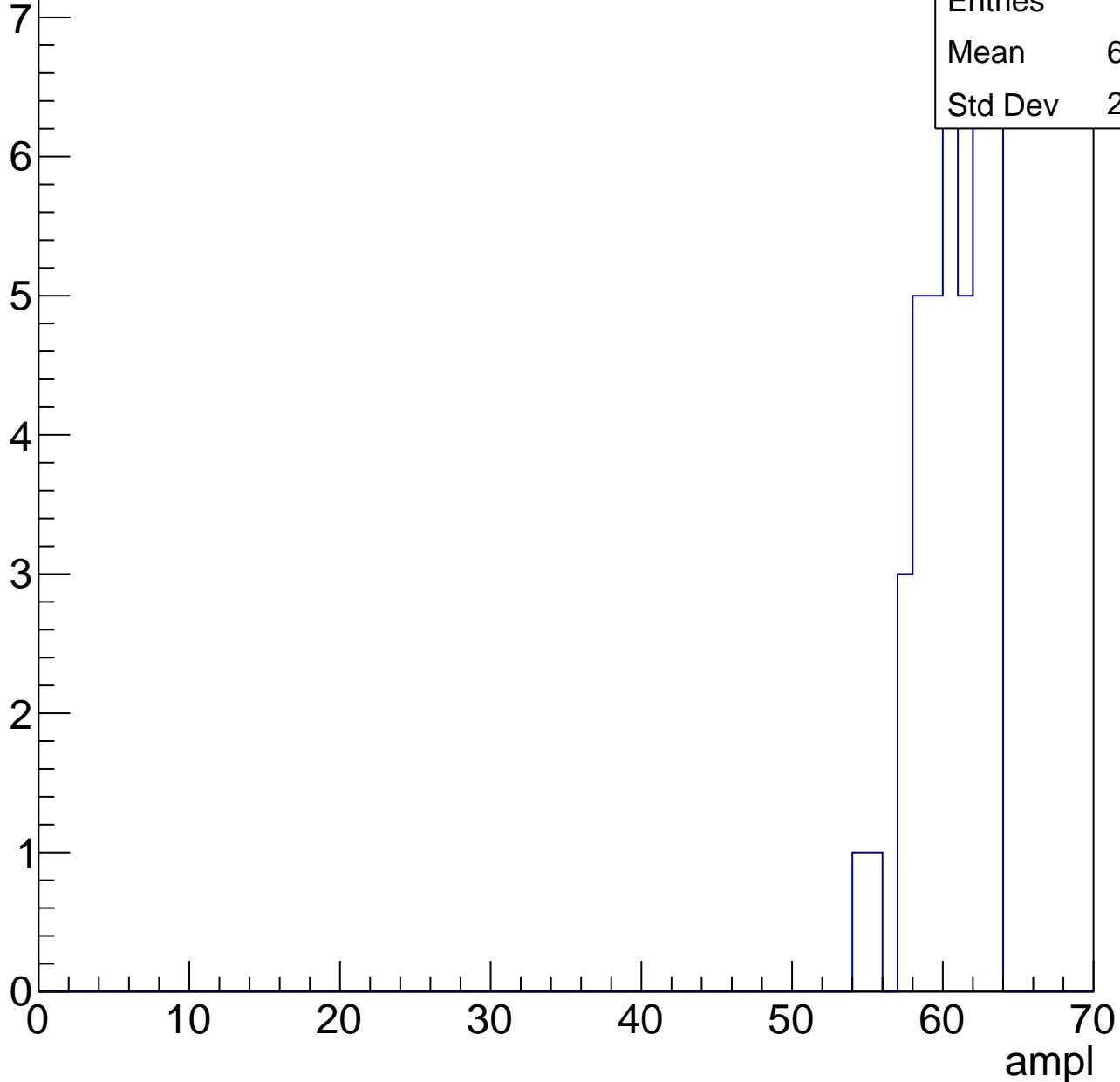
Entry



B1L003S, U21-ch127, adc5

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	41
Mean	60.12
Std Dev	2.254

B1L003S, U21-ch127, adc6

calib_packv5_042523_0143.root, FC#13, port D2

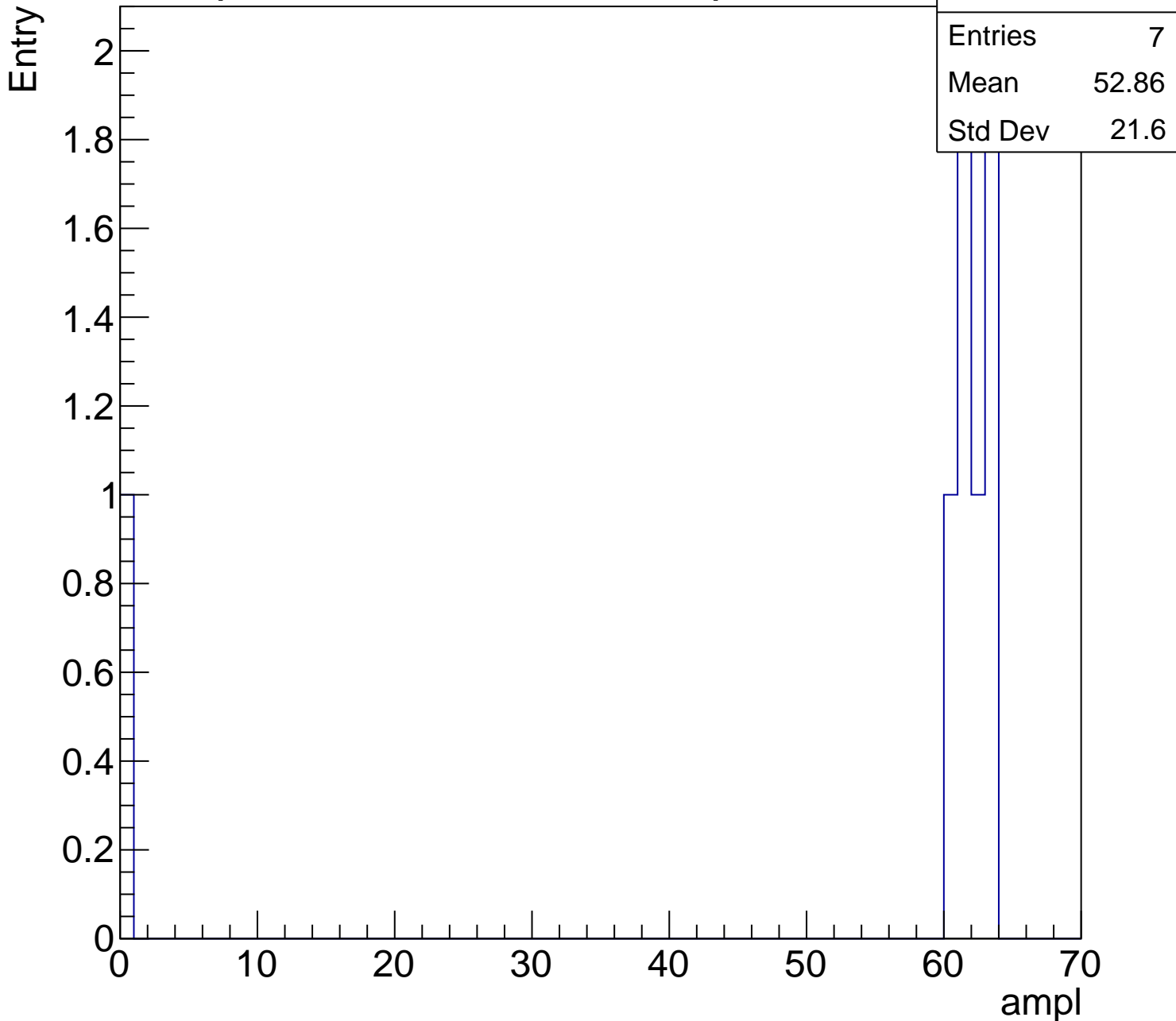
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	7
Mean	52.86
Std Dev	21.6

0 10 20 30 40 50 60 70

ampl



B1L003S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0

B1L003S, U21-ch127, adc7

calib_packv5_042523_0143.root, FC#13, port D2

Entry



Entries	0
Mean	0
Std Dev	0