

B1L103S, U10-ch0, adc0

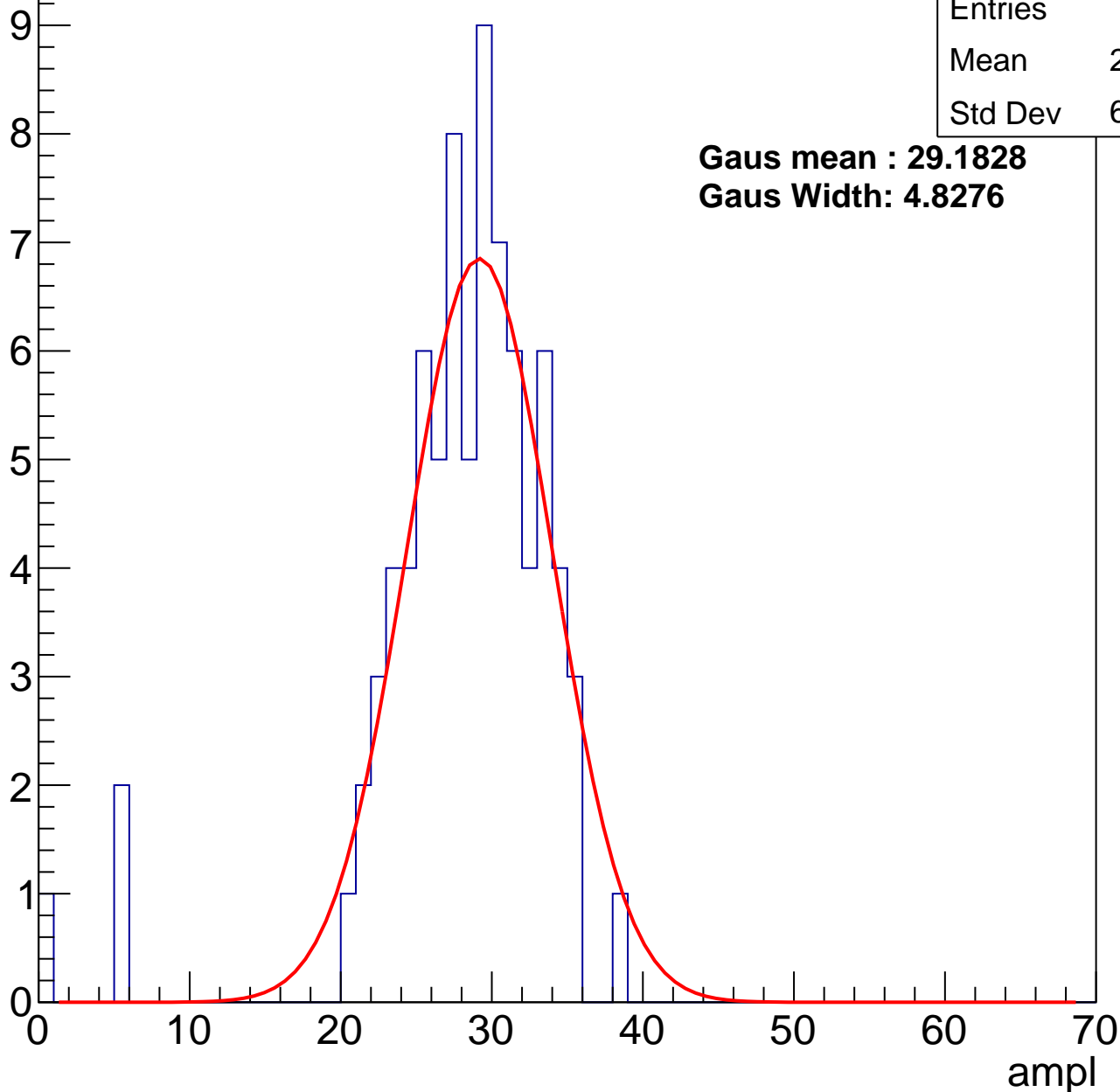
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	27.46
Std Dev	6.116

Gaus mean : 29.1828

Gaus Width: 4.8276



B1L103S, U10-ch0, adc1

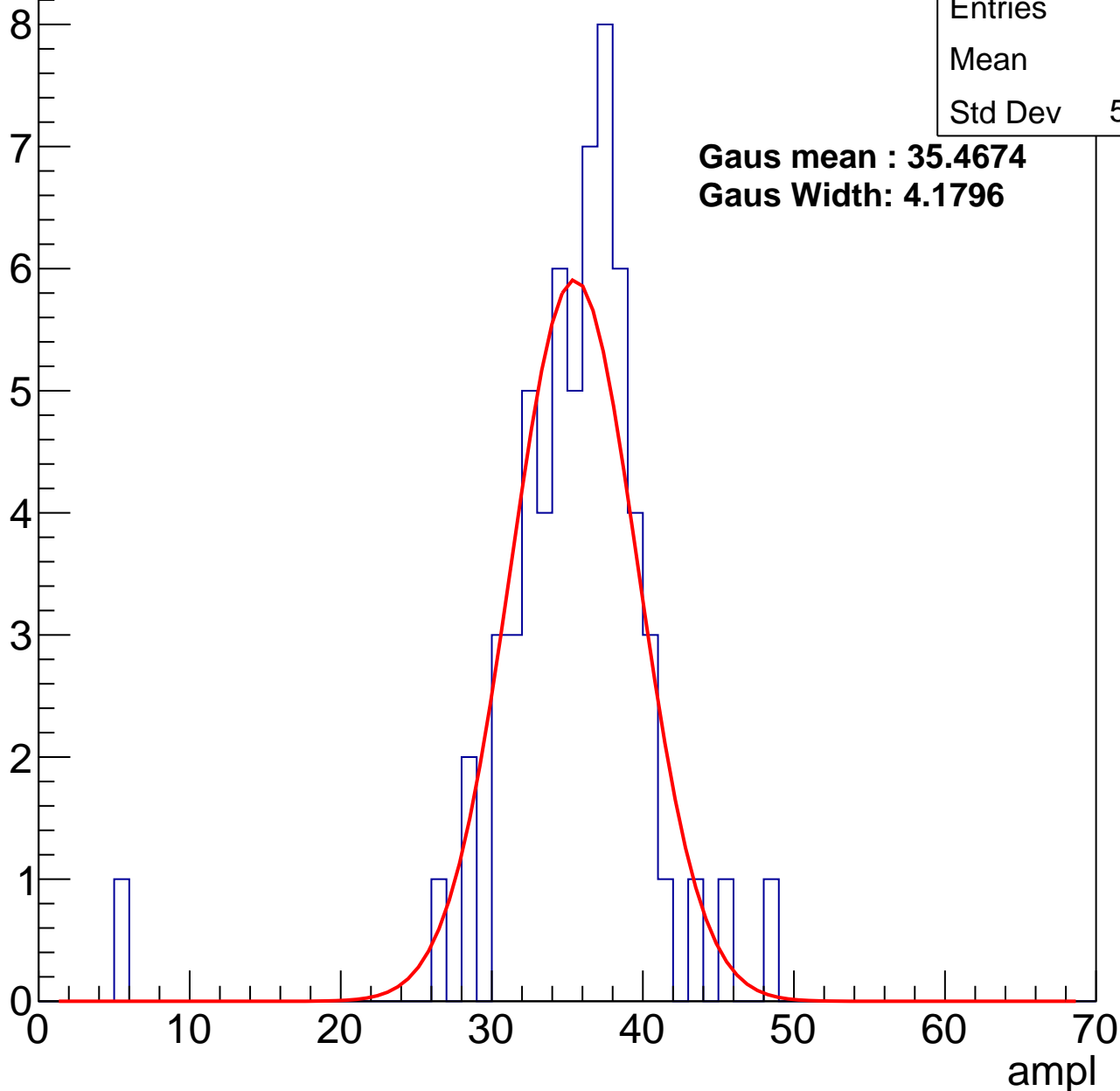
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35
Std Dev	5.492

Gaus mean : 35.4674

Gaus Width: 4.1796



B1L103S, U10-ch0, adc2

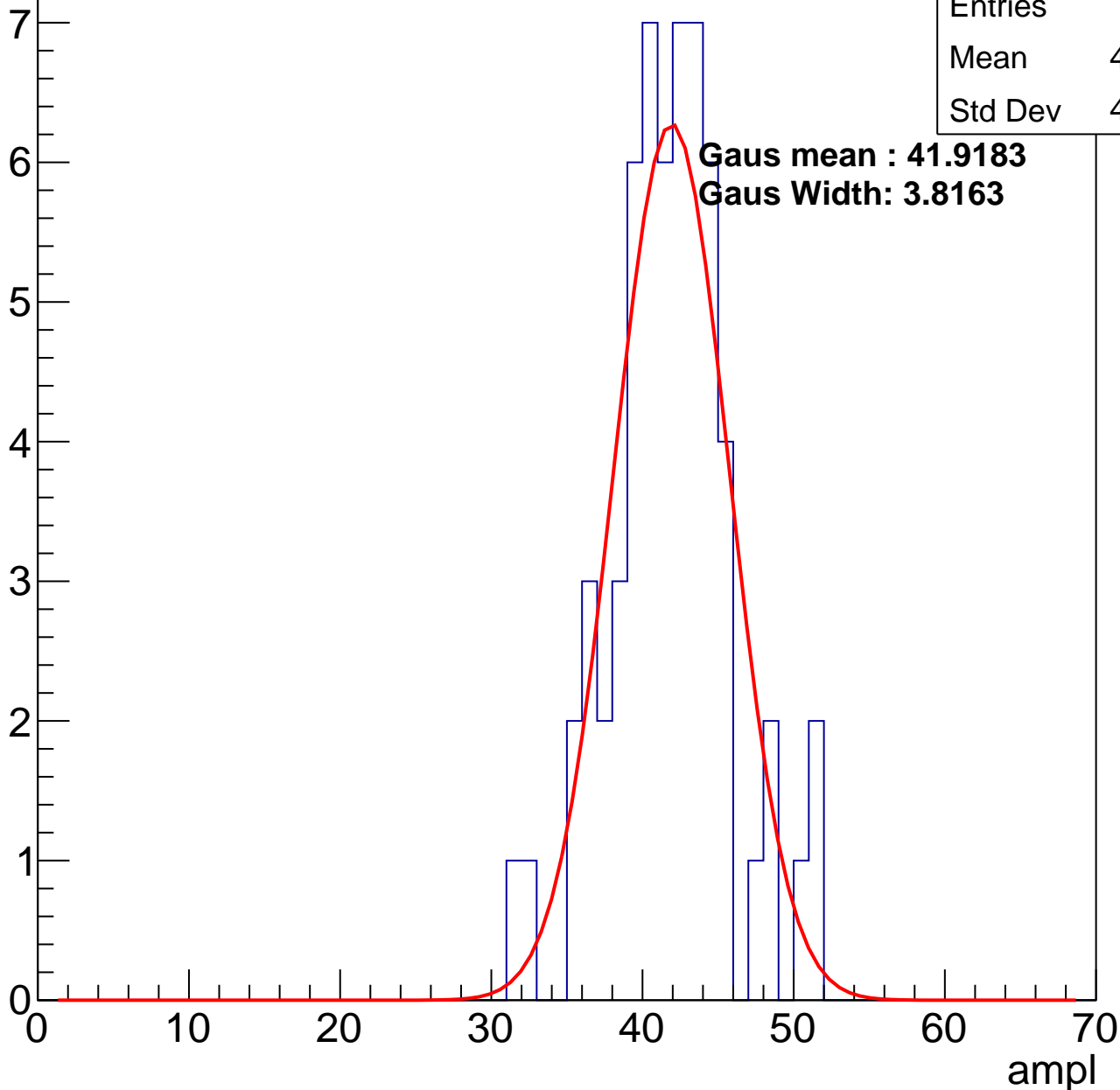
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.36
Std Dev	4.004

Gaus mean : 41.9183

Gaus Width: 3.8163

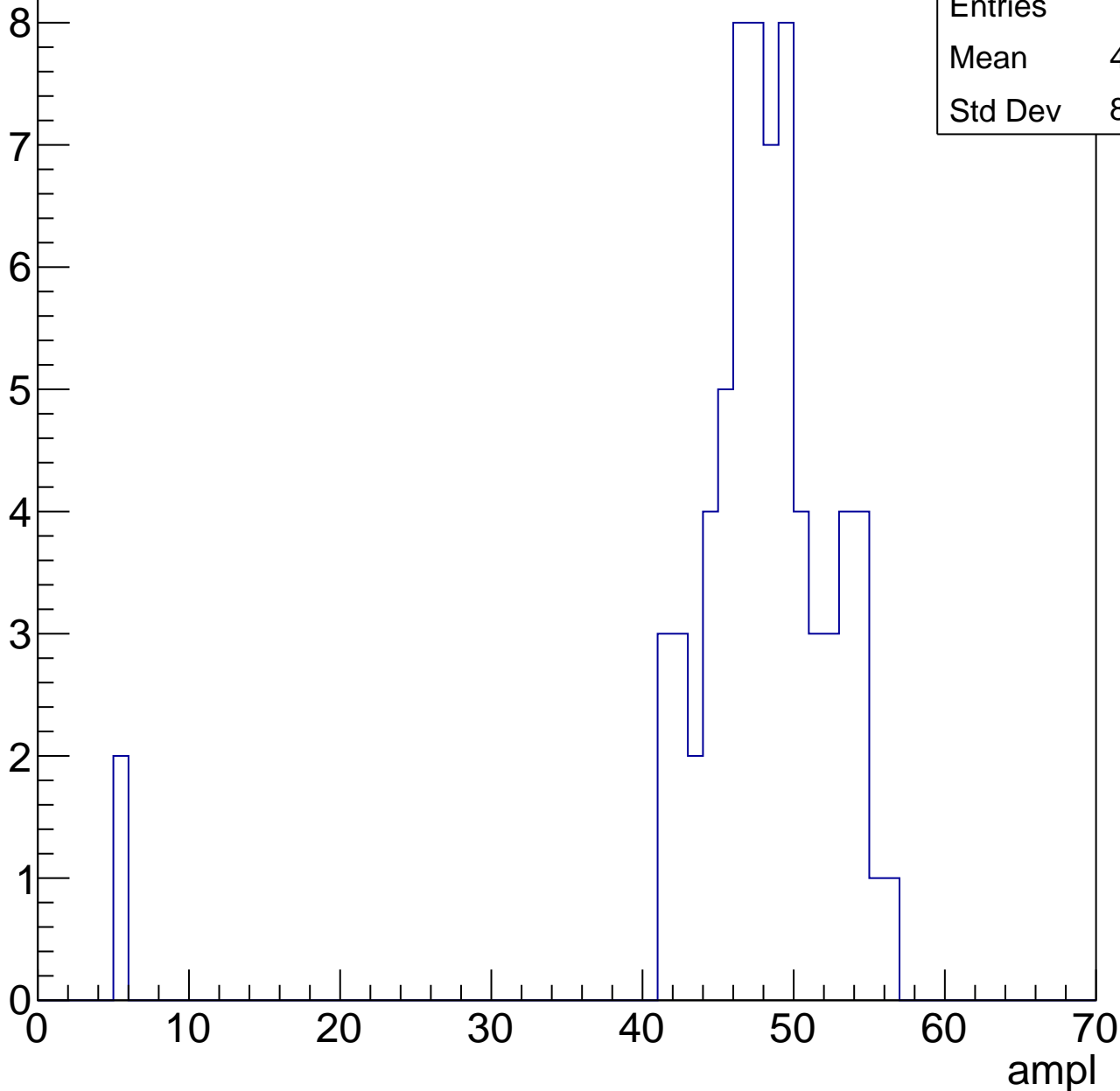


B1L103S, U10-ch0, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	46.66
Std Dev	8.002

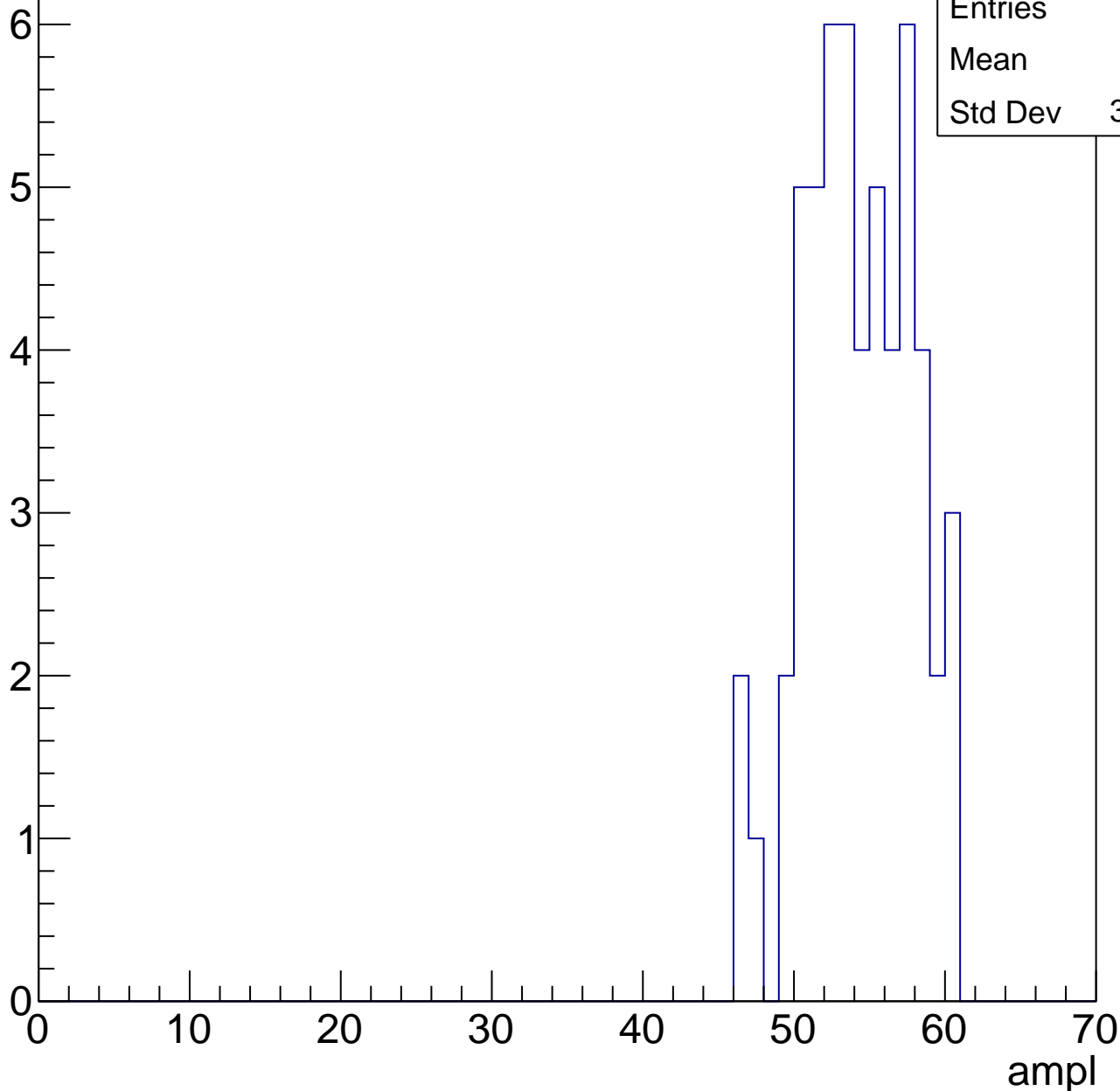


B1L103S, U10-ch0, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	53.8
Std Dev	3.508

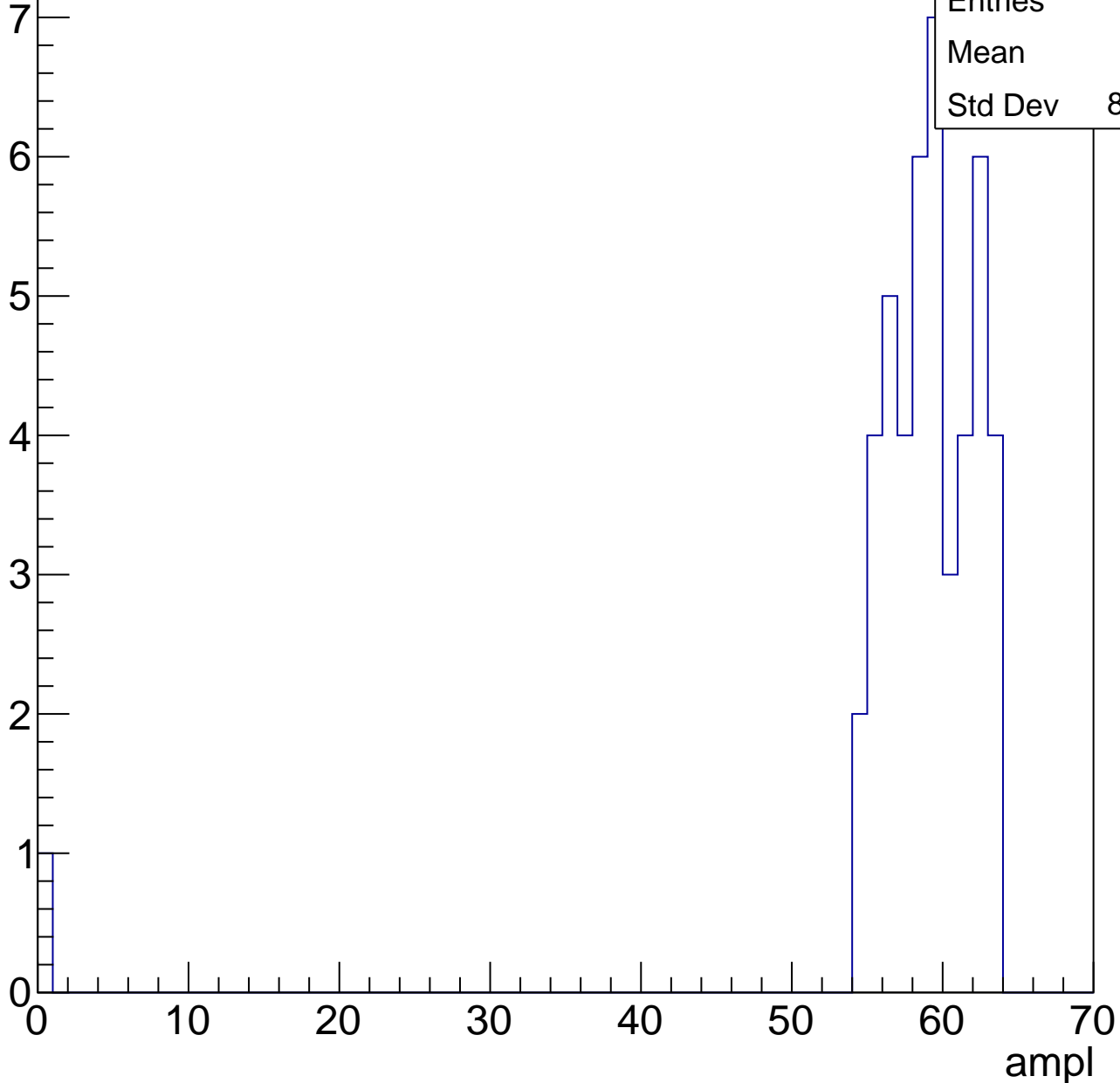


B1L103S, U10-ch0, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	57.5
Std Dev	8.963

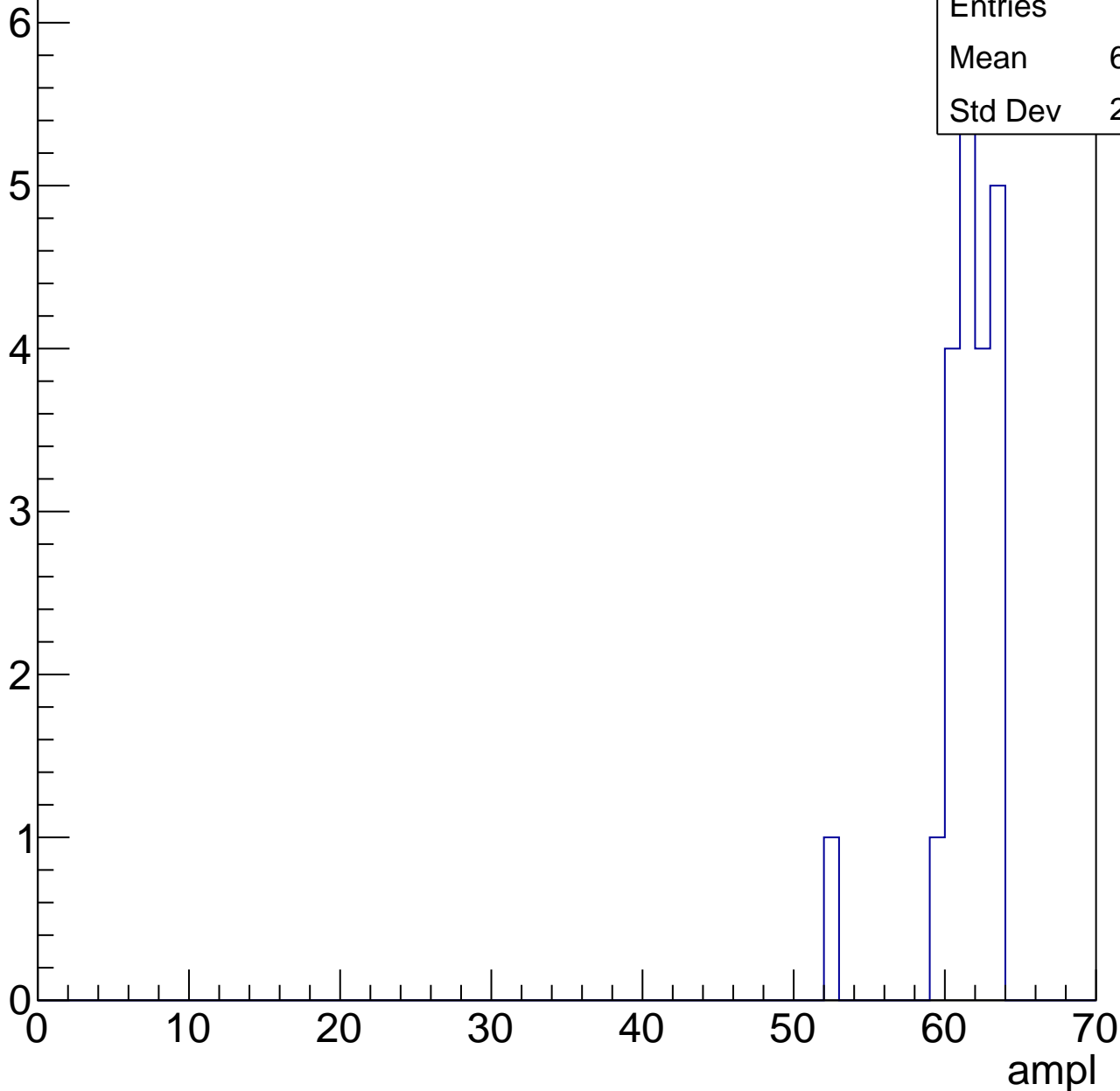


B1L103S, U10-ch0, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	60.95
Std Dev	2.319



B1L103S, U10-ch0, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch1, adc0

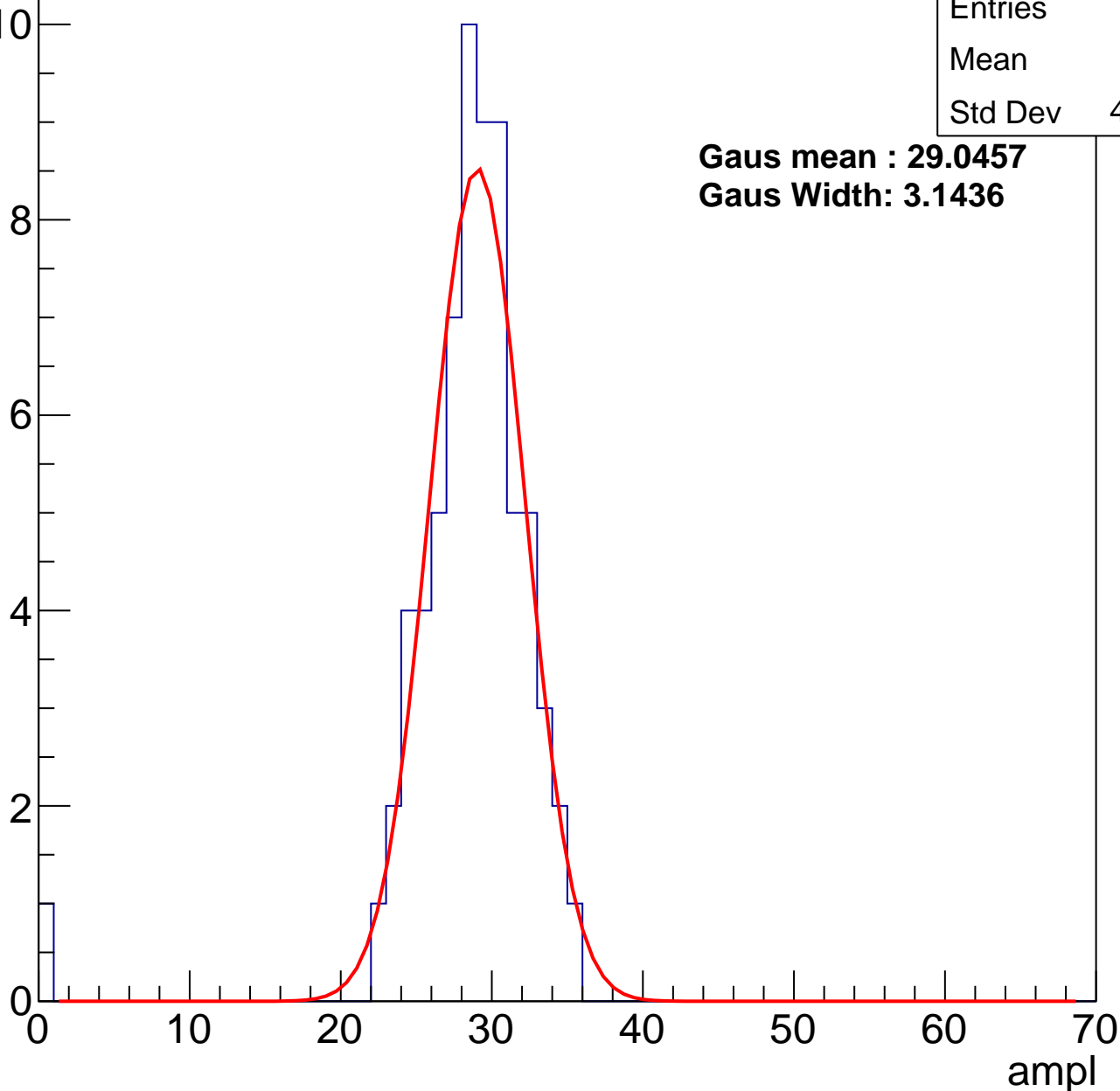
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.1
Std Dev	4.476

Gaus mean : 29.0457

Gaus Width: 3.1436



B1L103S, U10-ch1, adc1

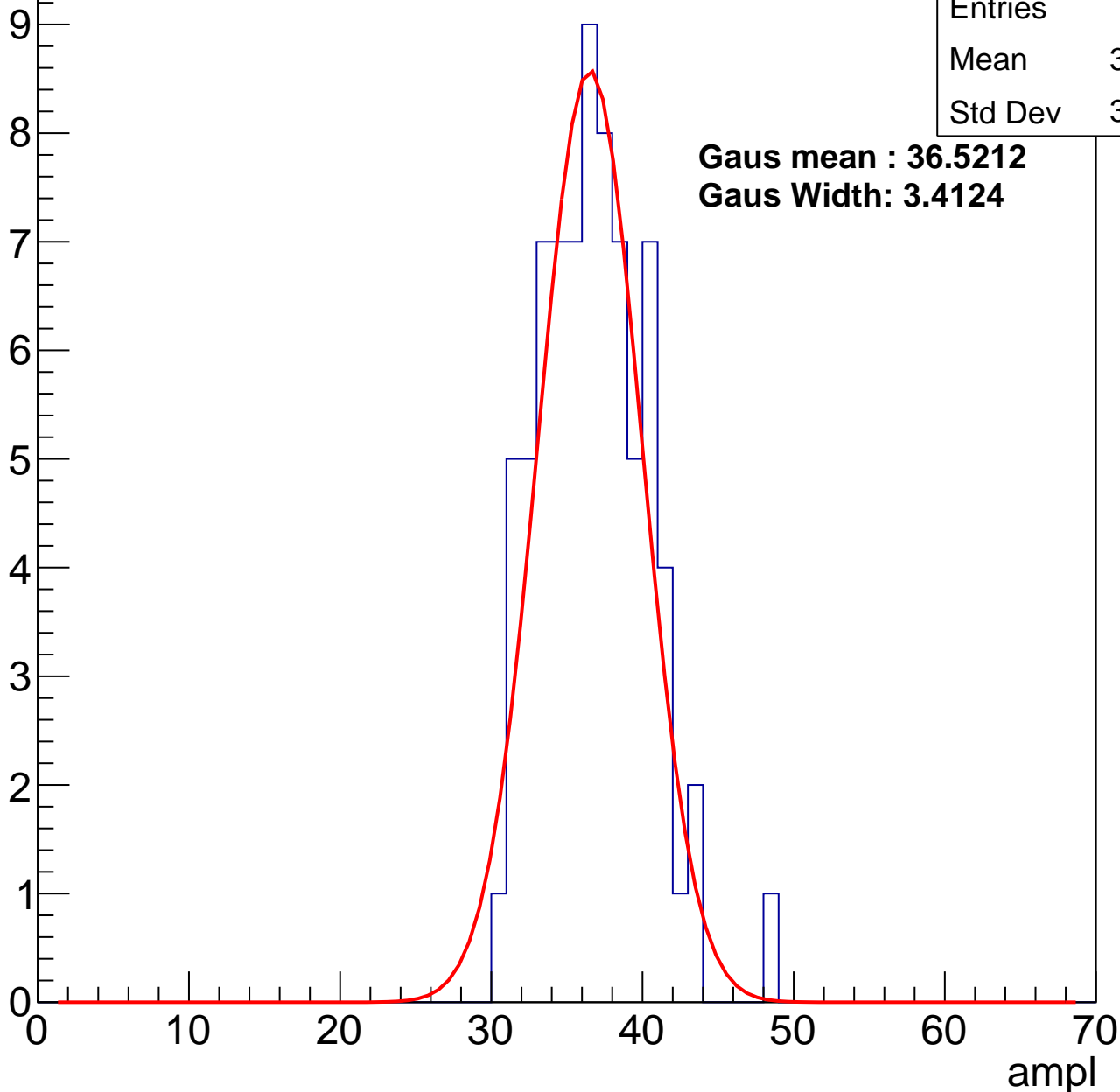
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	36.32
Std Dev	3.446

Gaus mean : 36.5212

Gaus Width: 3.4124



B1L103S, U10-ch1, adc2

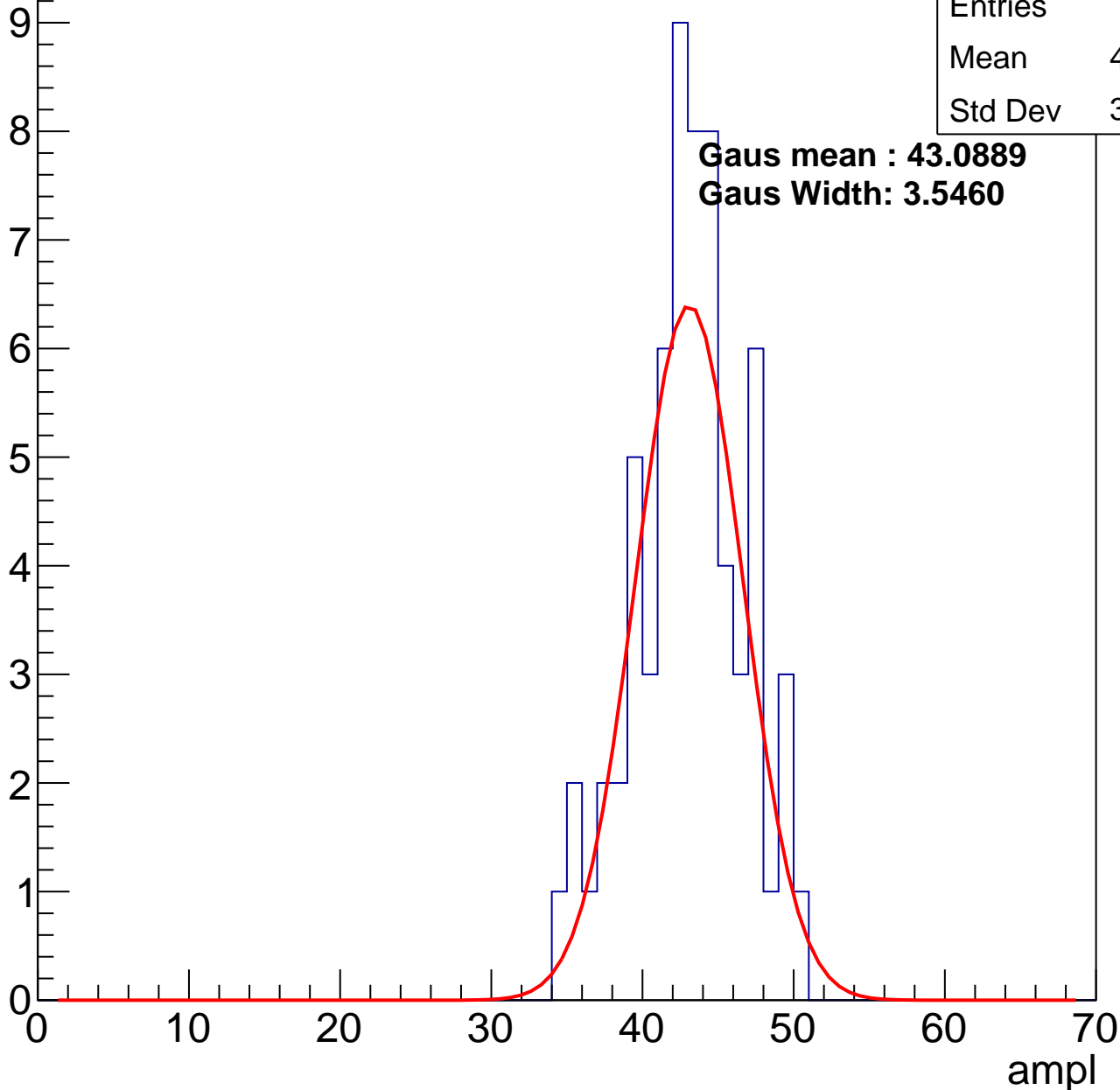
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.62
Std Dev	3.594

Gaus mean : 43.0889

Gaus Width: 3.5460

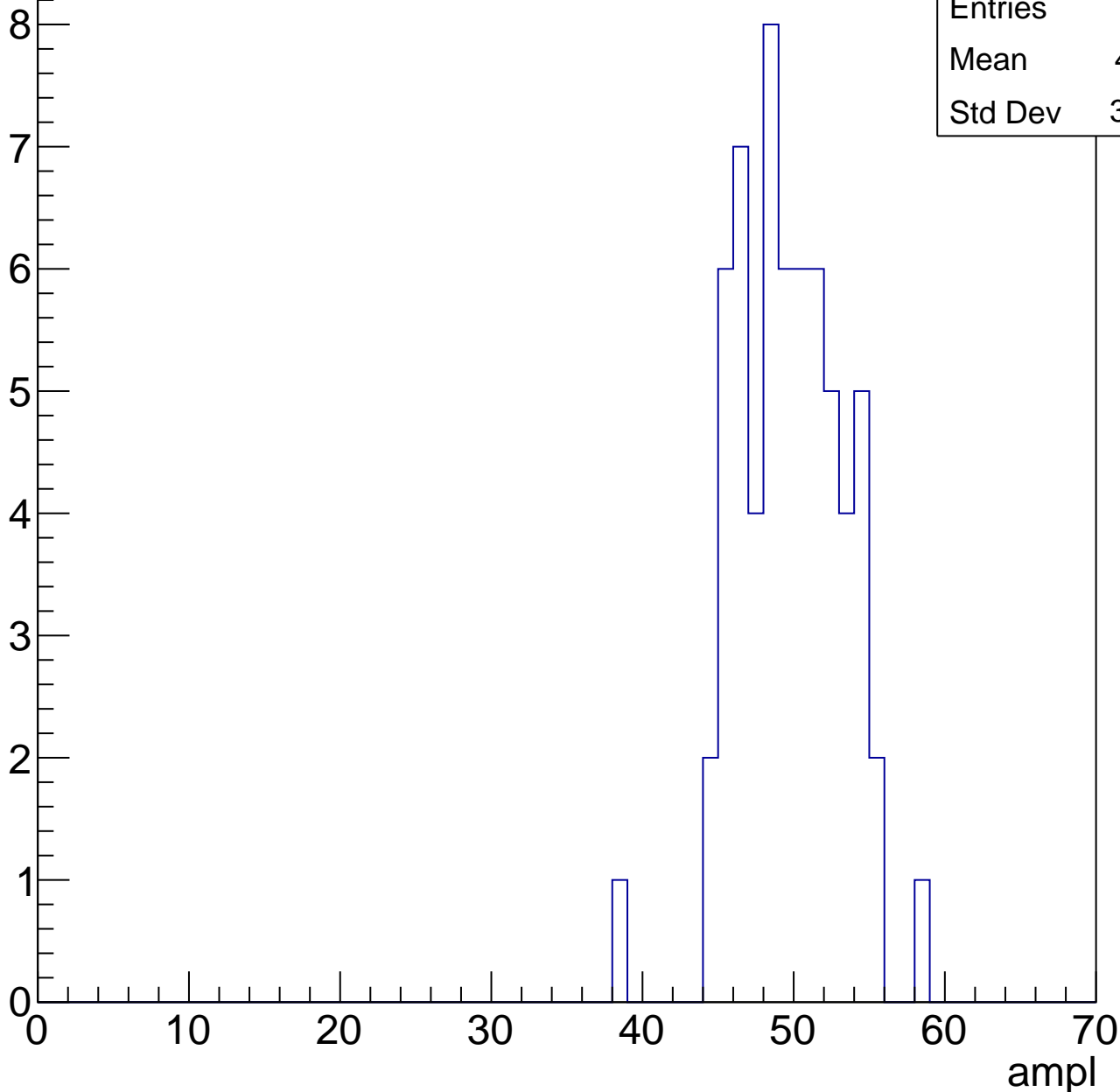


B1L103S, U10-ch1, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.21
Std Dev	3.492

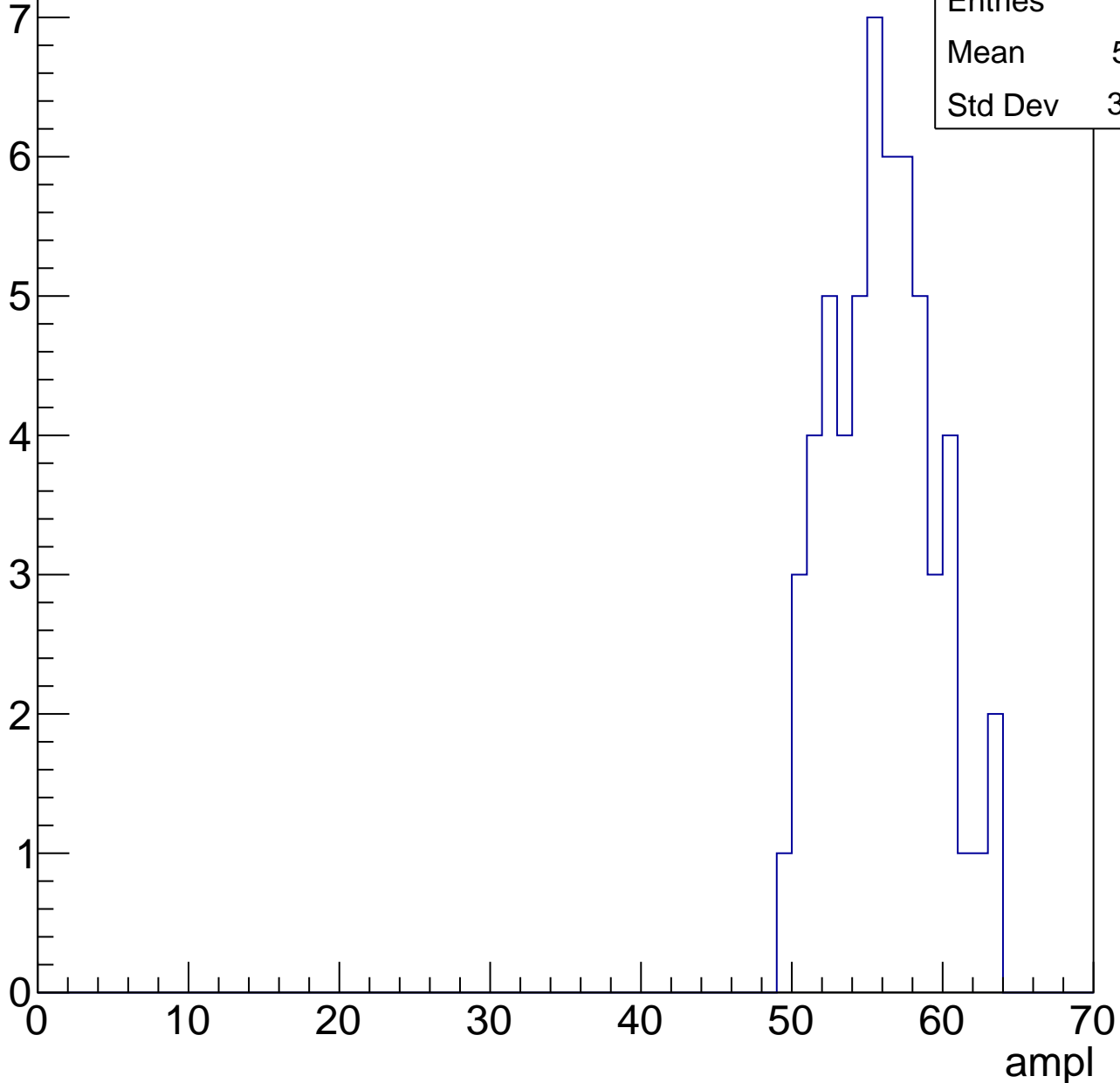


B1L103S, U10-ch1, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.51
Std Dev	3.409

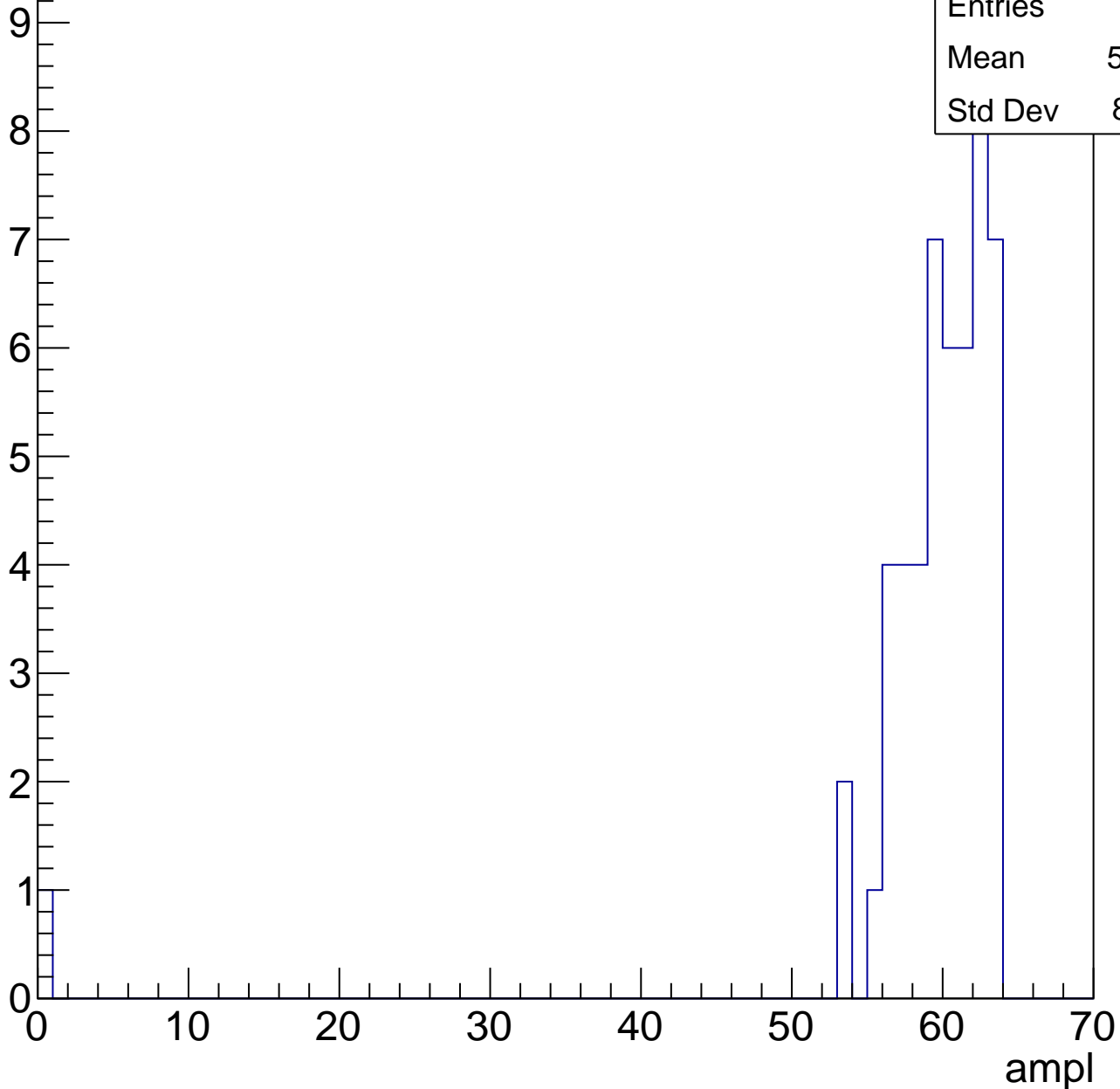


B1L103S, U10-ch1, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

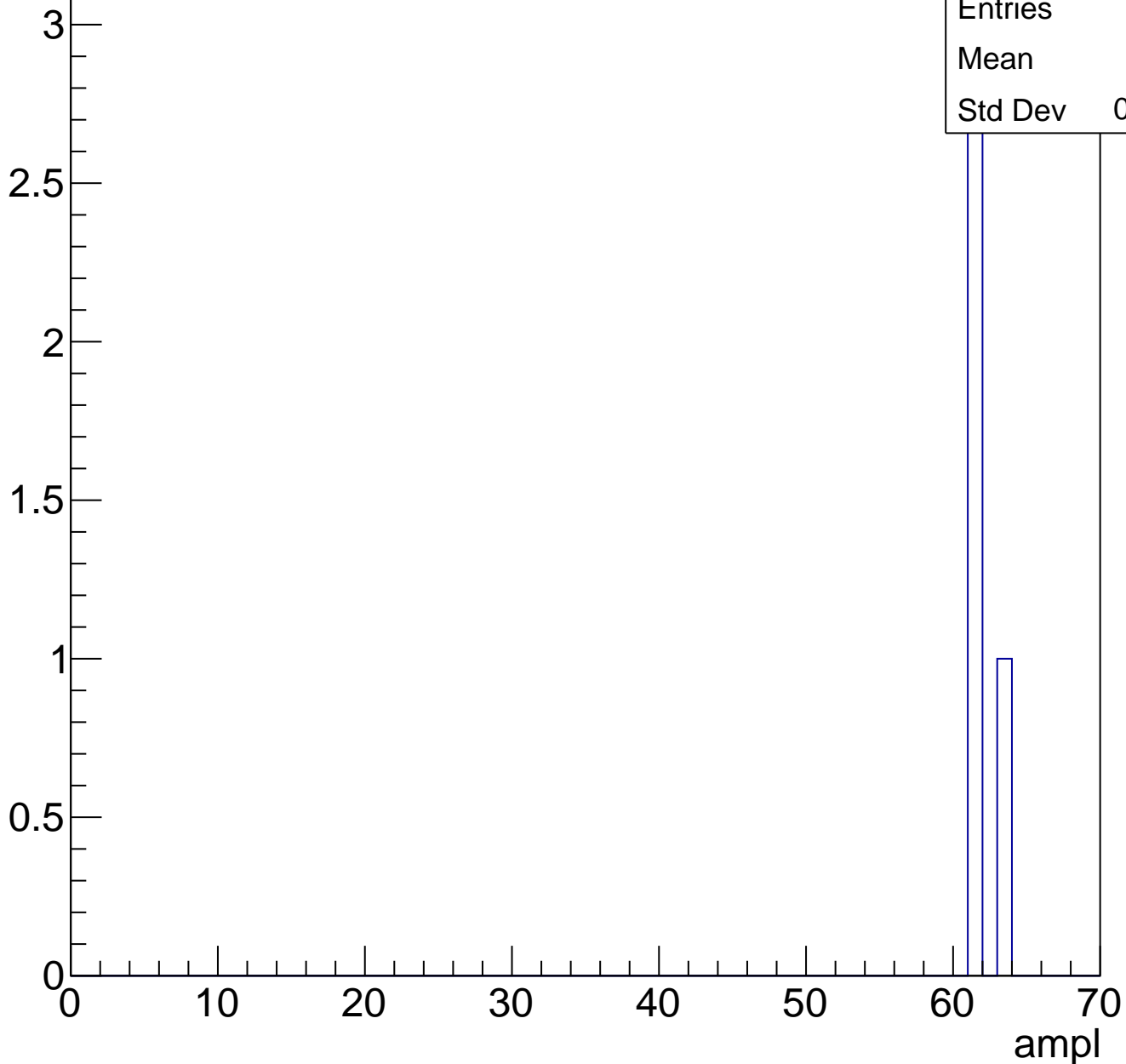
Entries	51
Mean	58.49
Std Dev	8.671



B1L103S, U10-ch1, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch1, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch2, adc0

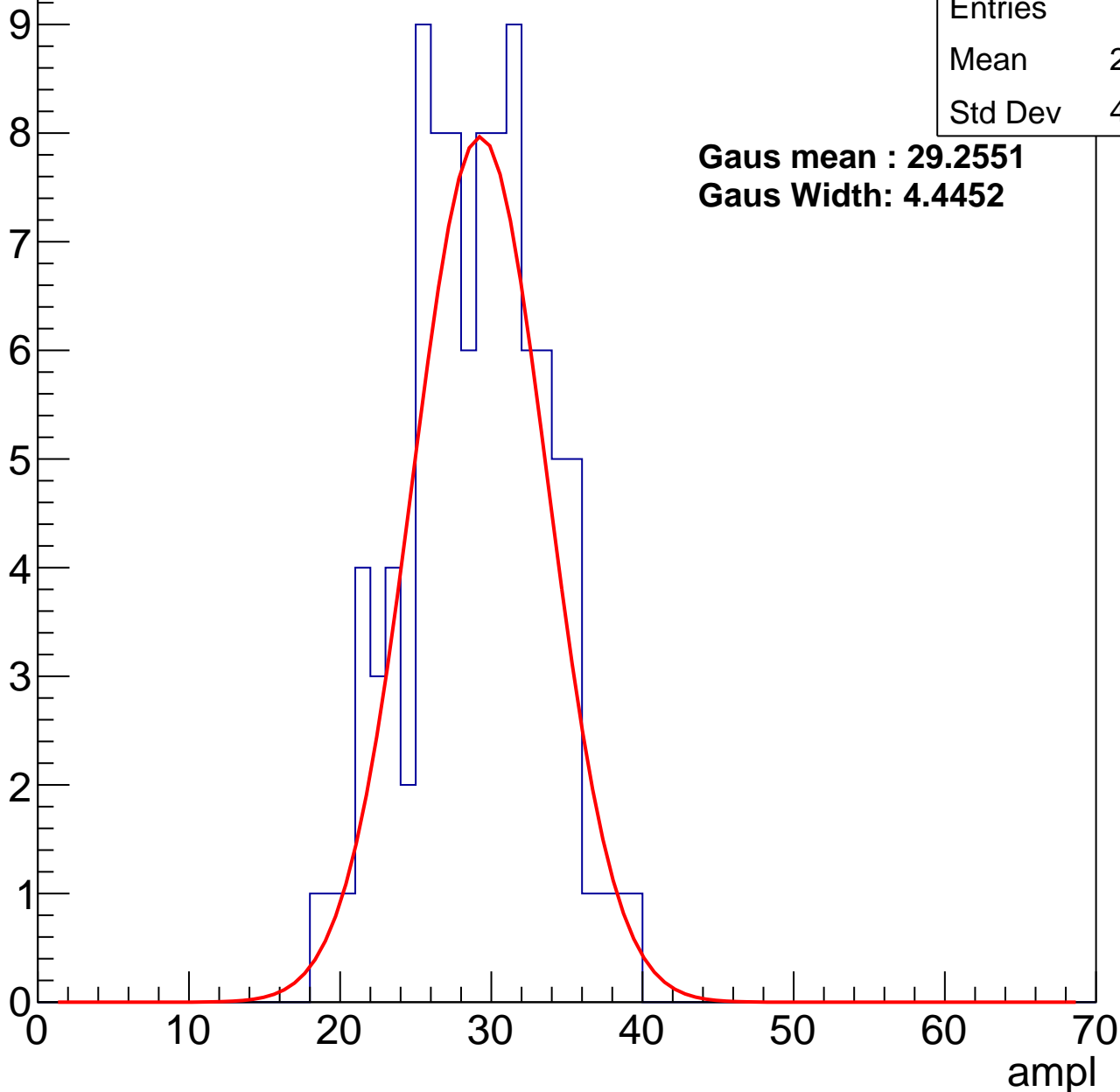
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	98
Mean	28.57
Std Dev	4.435

Gaus mean : 29.2551

Gaus Width: 4.4452



B1L103S, U10-ch2, adc1

calib_packv5_042523_0143.root, FC#7, port C2

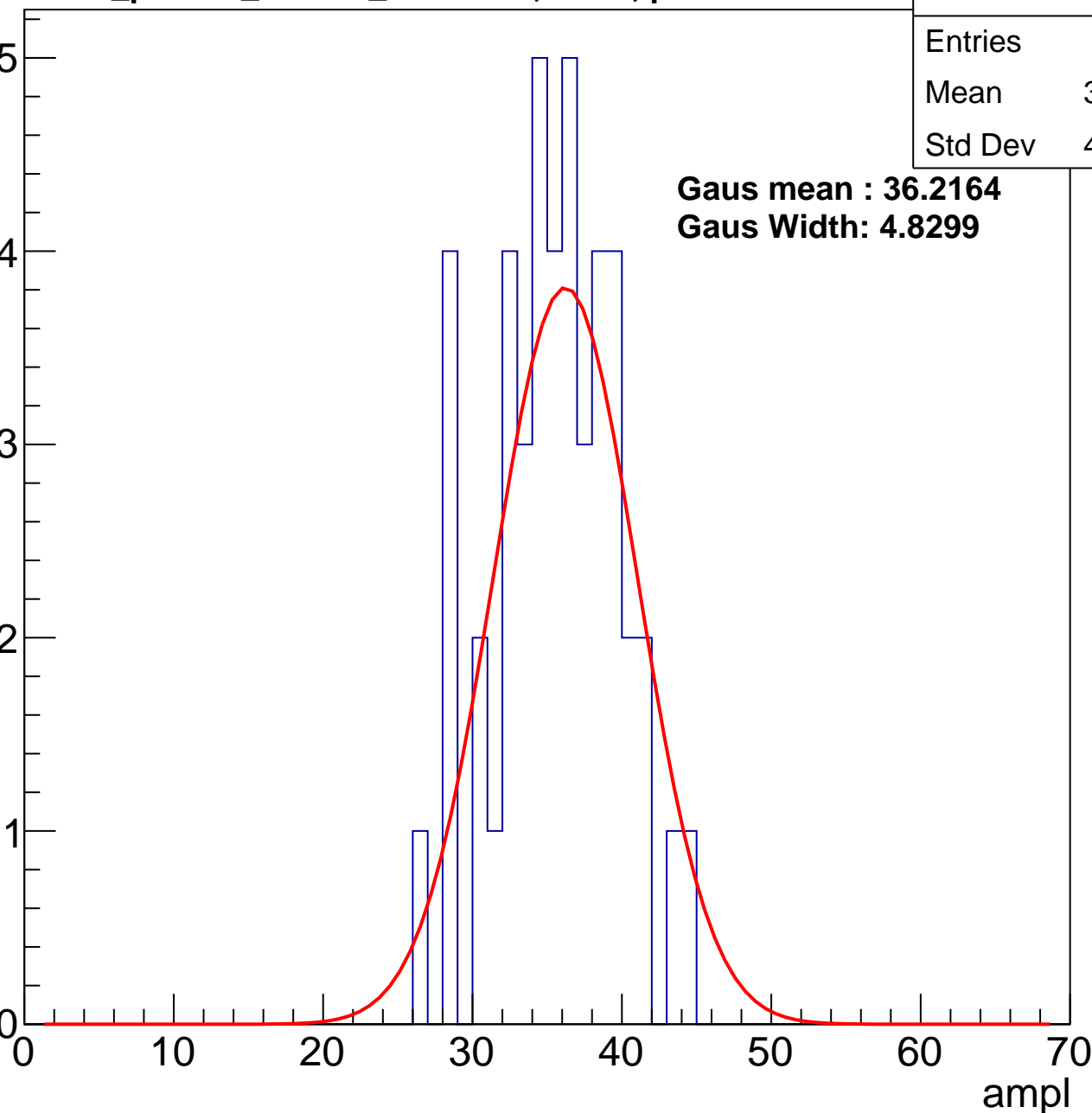
Entry

Entries	46
Mean	35.09
Std Dev	4.117

Gaus mean : 36.2164

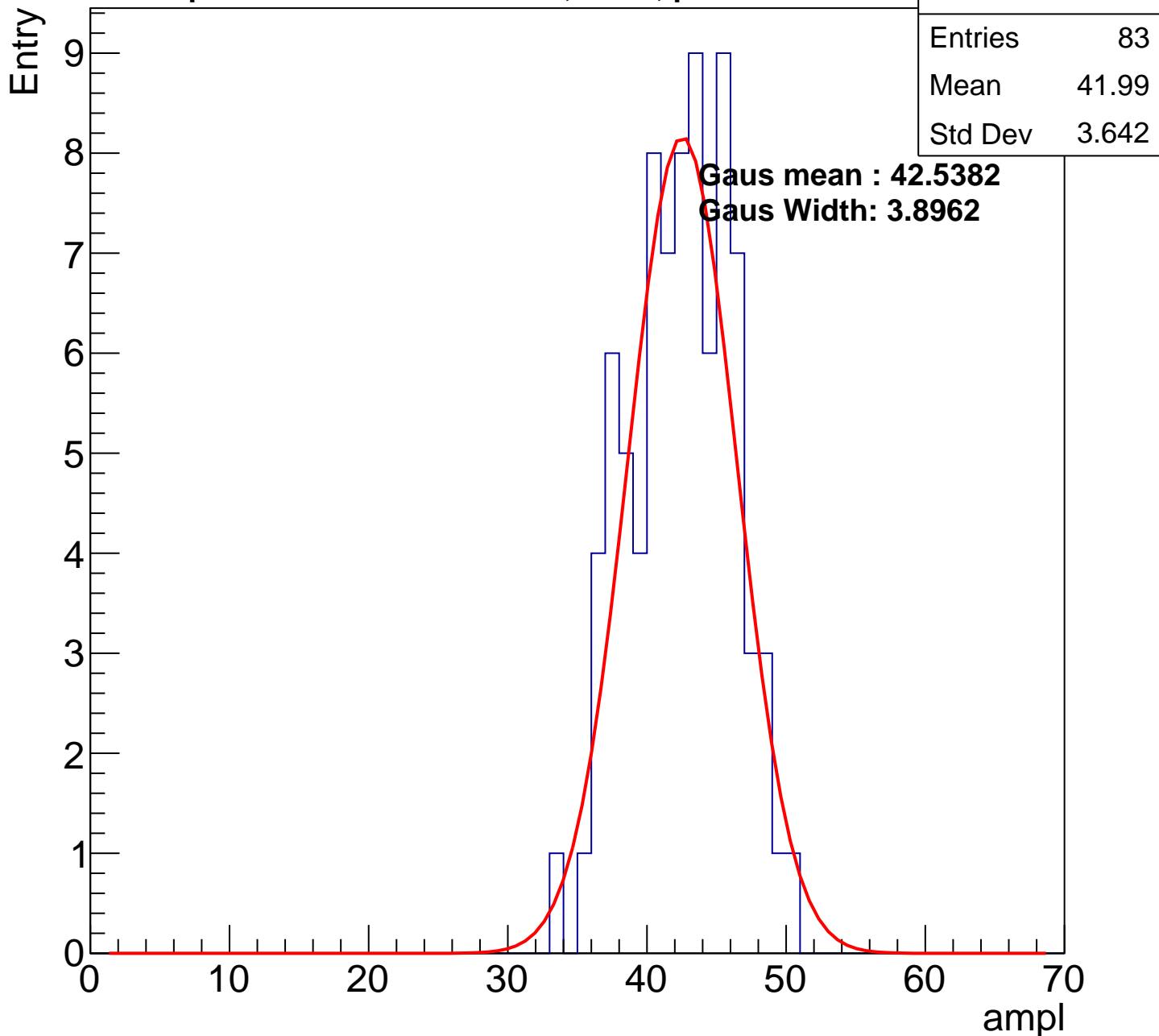
Gaus Width: 4.8299

5
4
3
2
1
0



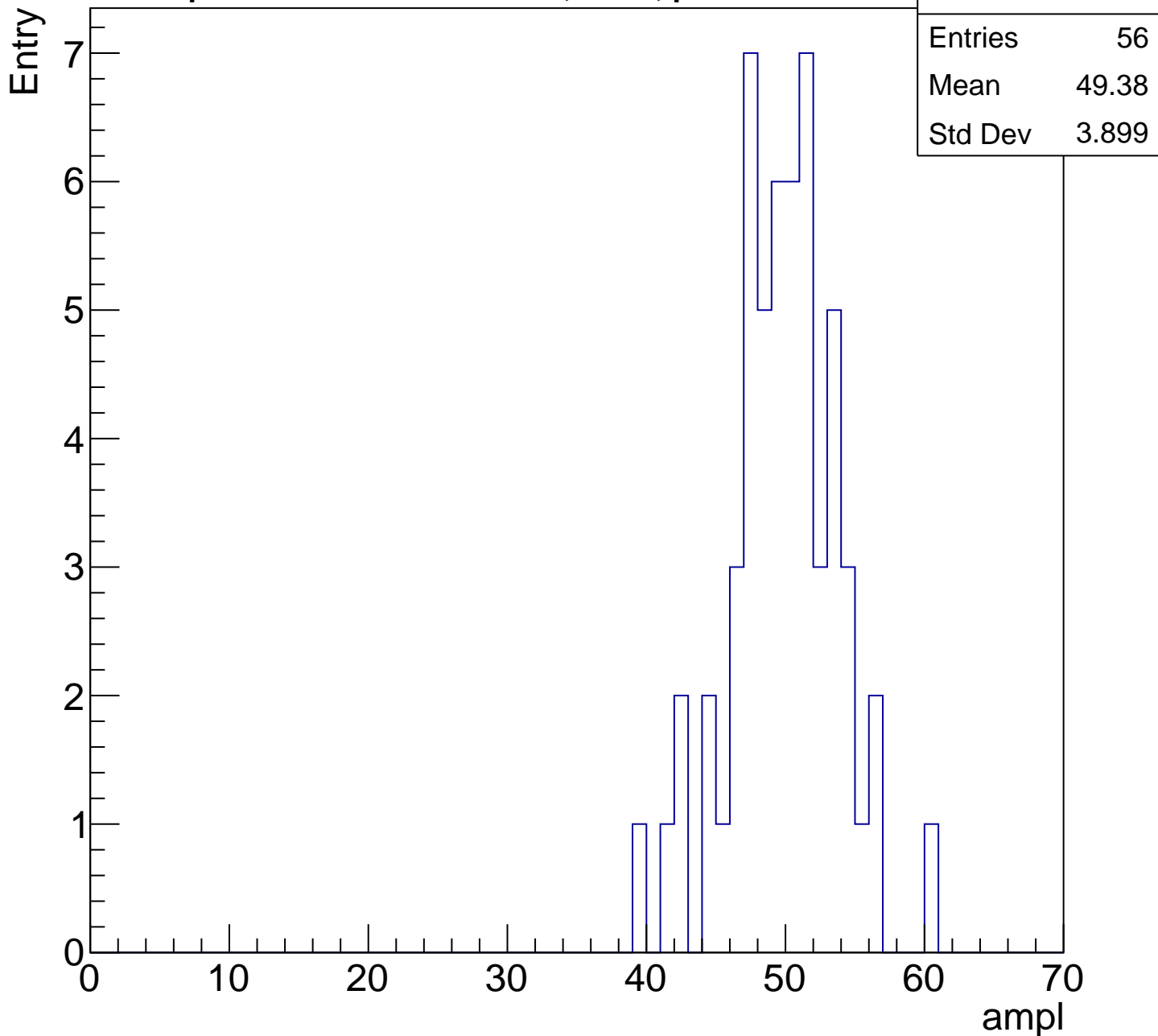
B1L103S, U10-ch2, adc2

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U10-ch2, adc3

calib_packv5_042523_0143.root, FC#7, port C2

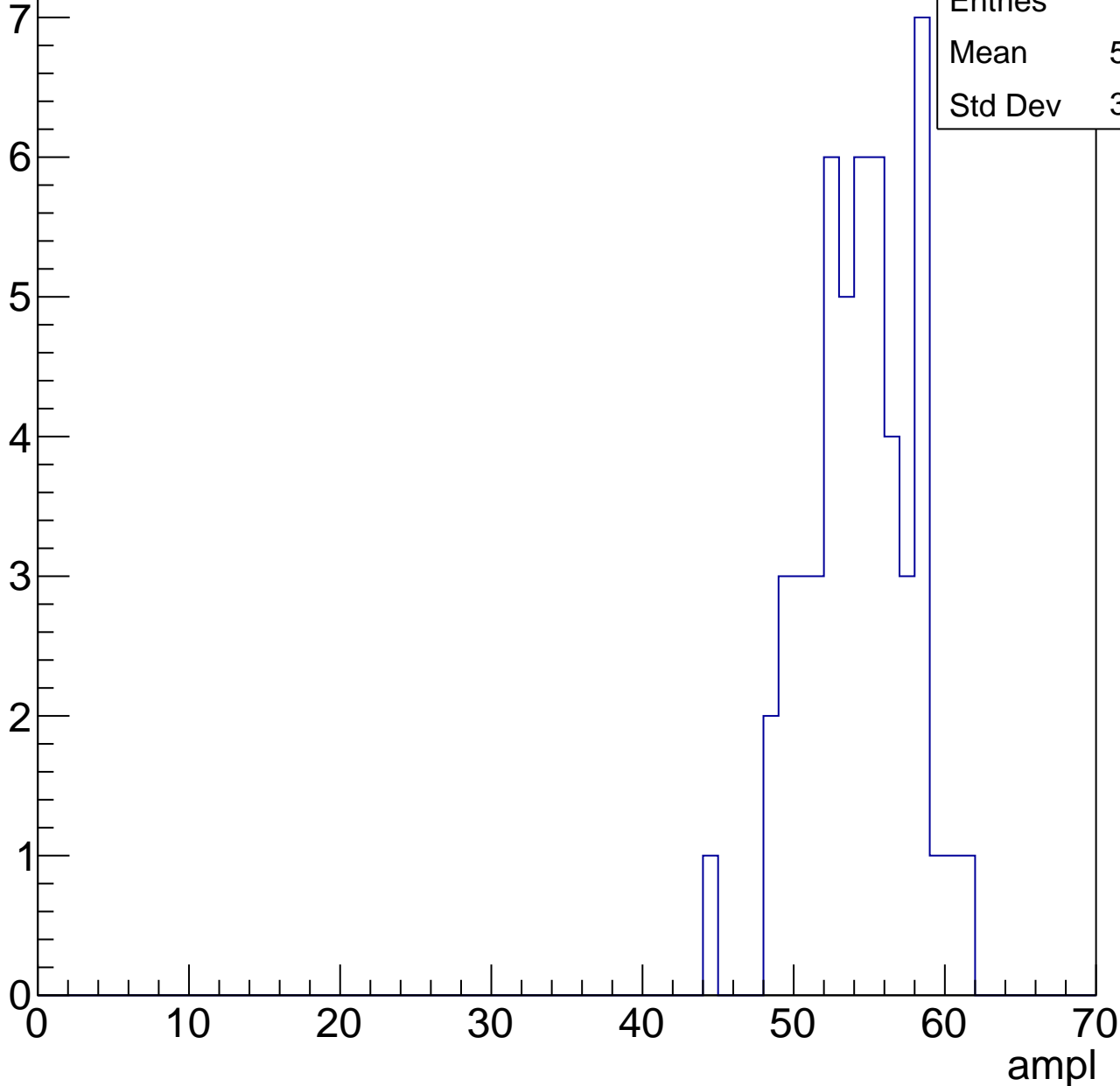


B1L103S, U10-ch2, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	53.88
Std Dev	3.457



B1L103S, U10-ch2, adc5

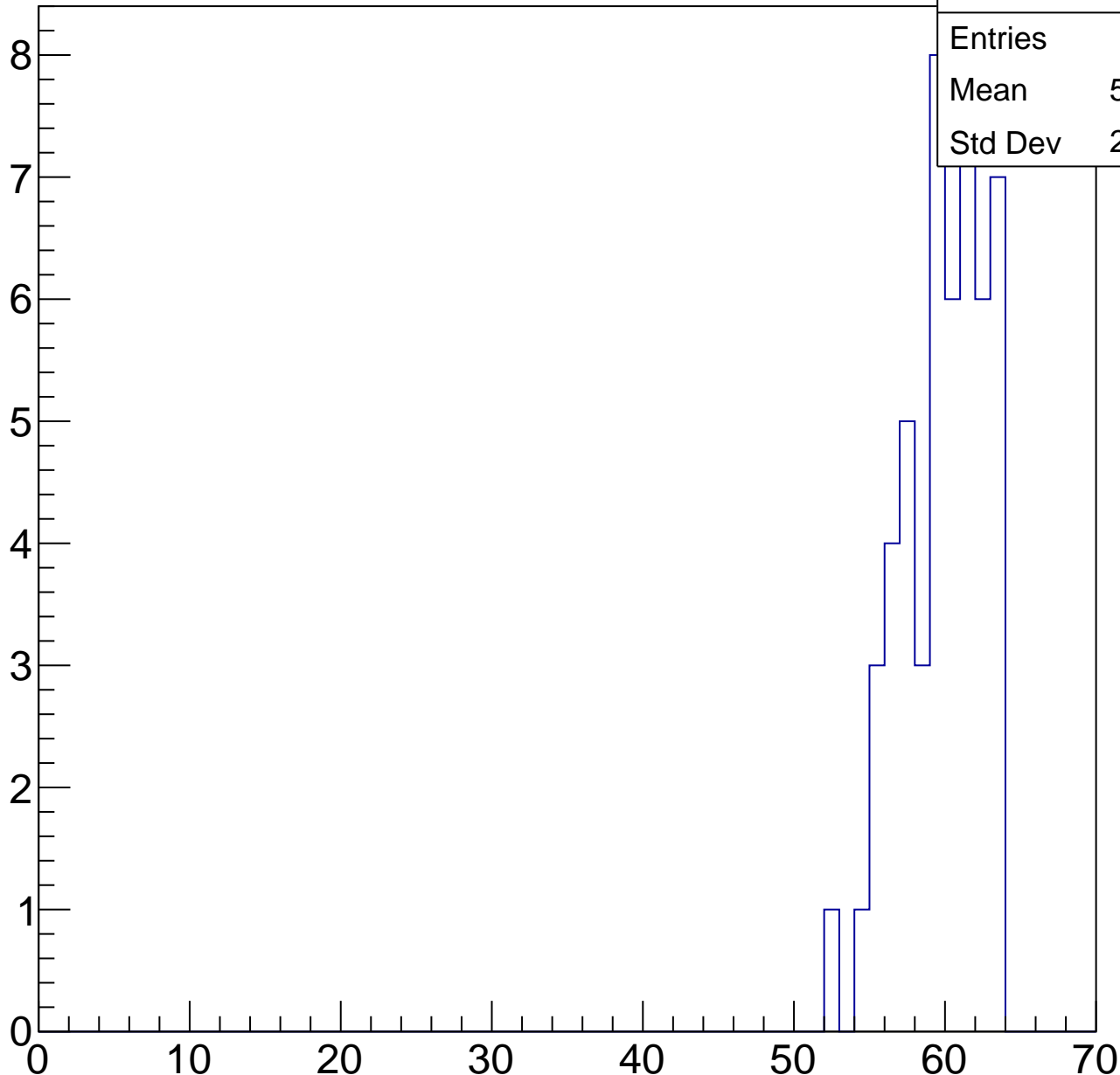
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	52
Mean	59.37
Std Dev	2.696

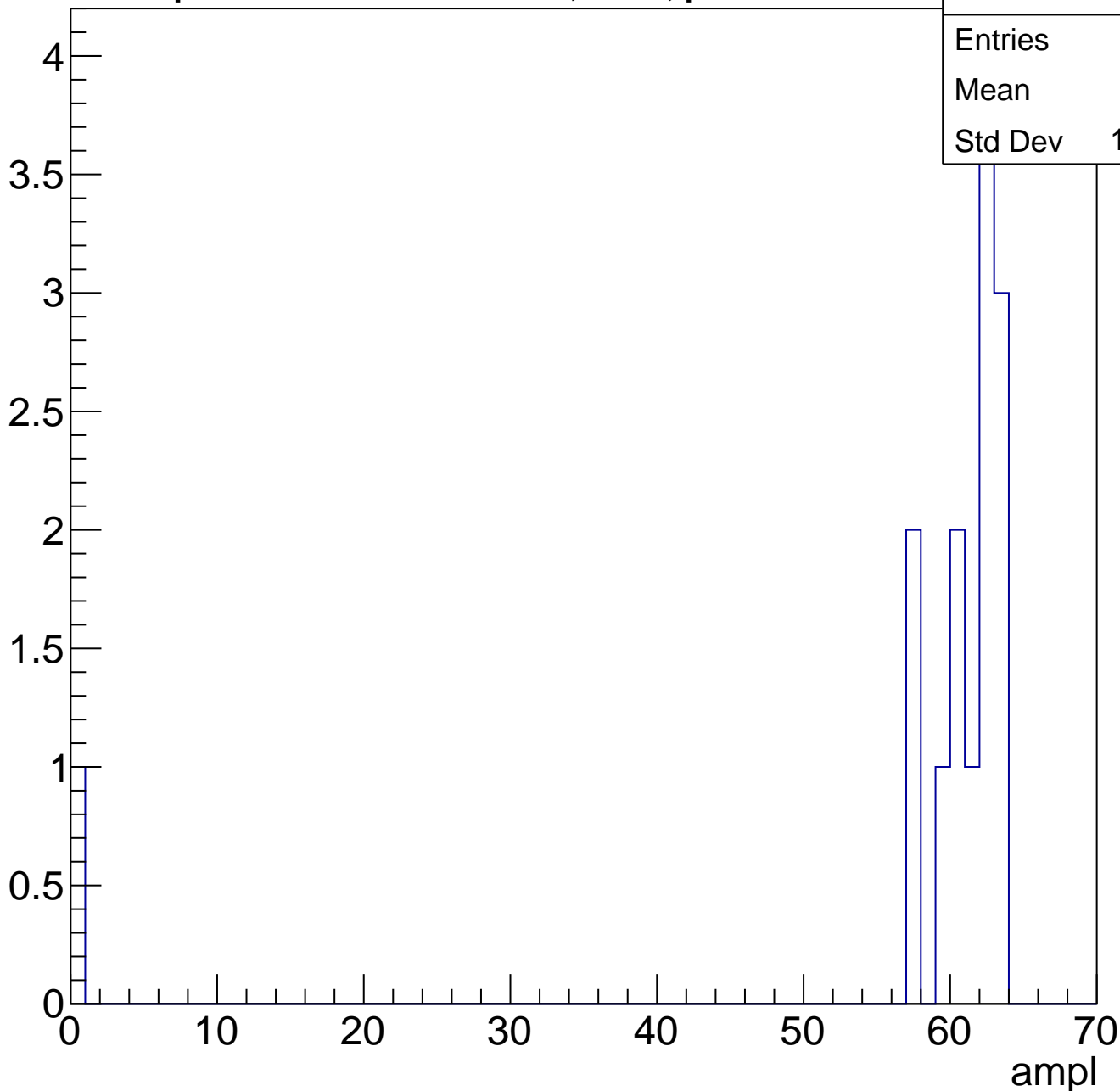
ampl



B1L103S, U10-ch2, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch2, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch3, adc0

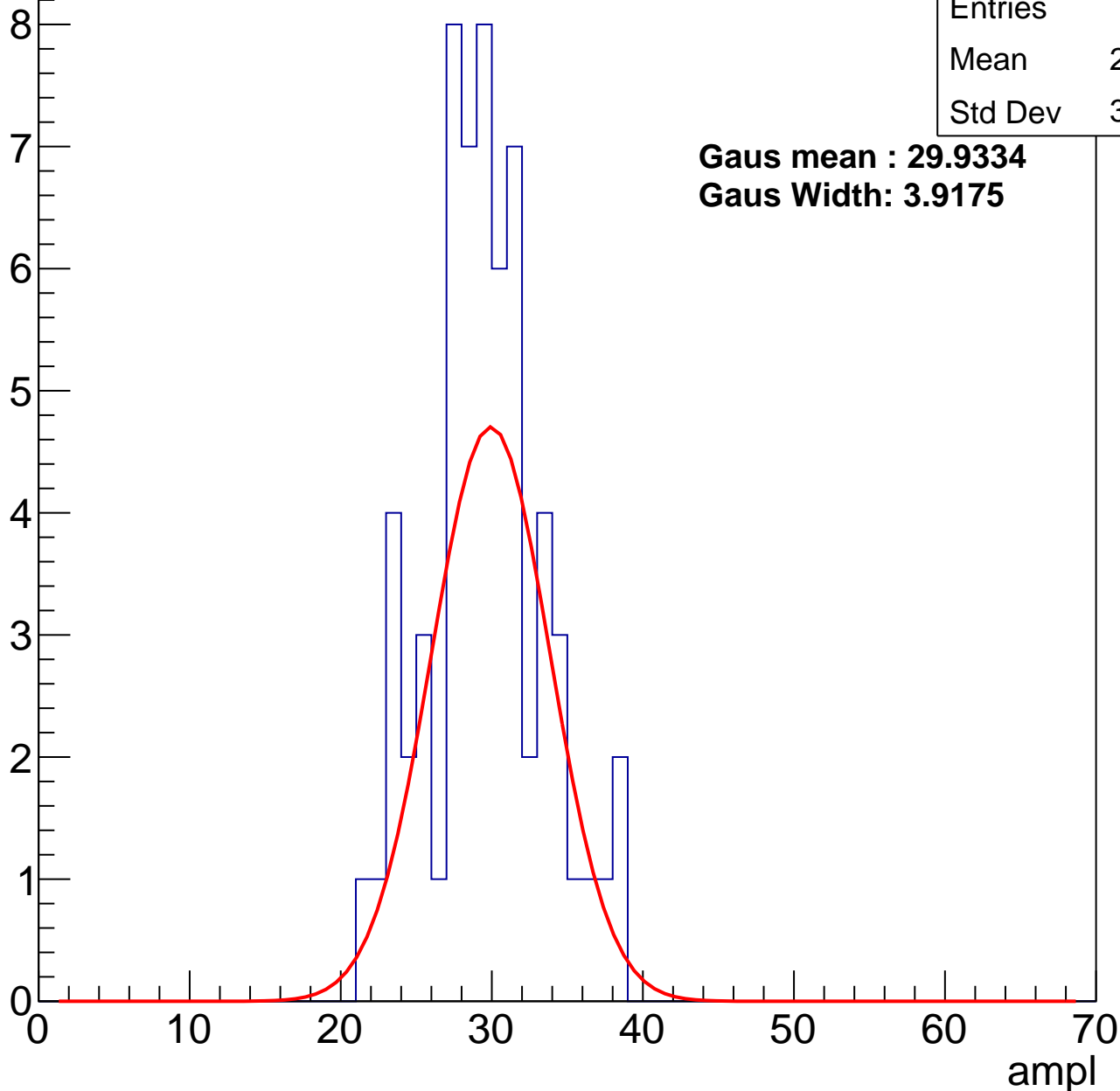
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.15
Std Dev	3.818

Gaus mean : 29.9334

Gaus Width: 3.9175



B1L103S, U10-ch3, adc1

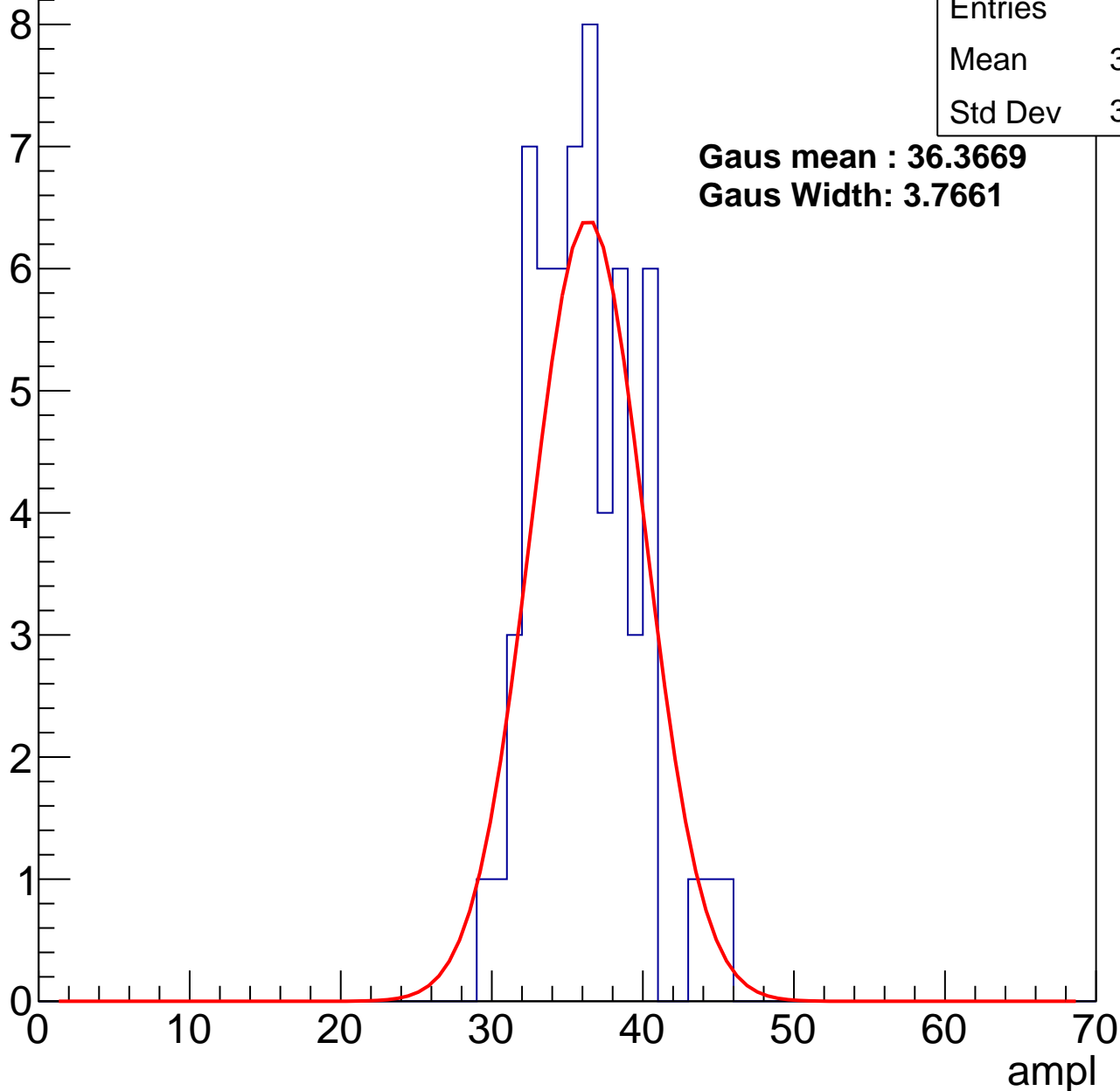
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.67
Std Dev	3.376

Gaus mean : 36.3669

Gaus Width: 3.7661



B1L103S, U10-ch3, adc2

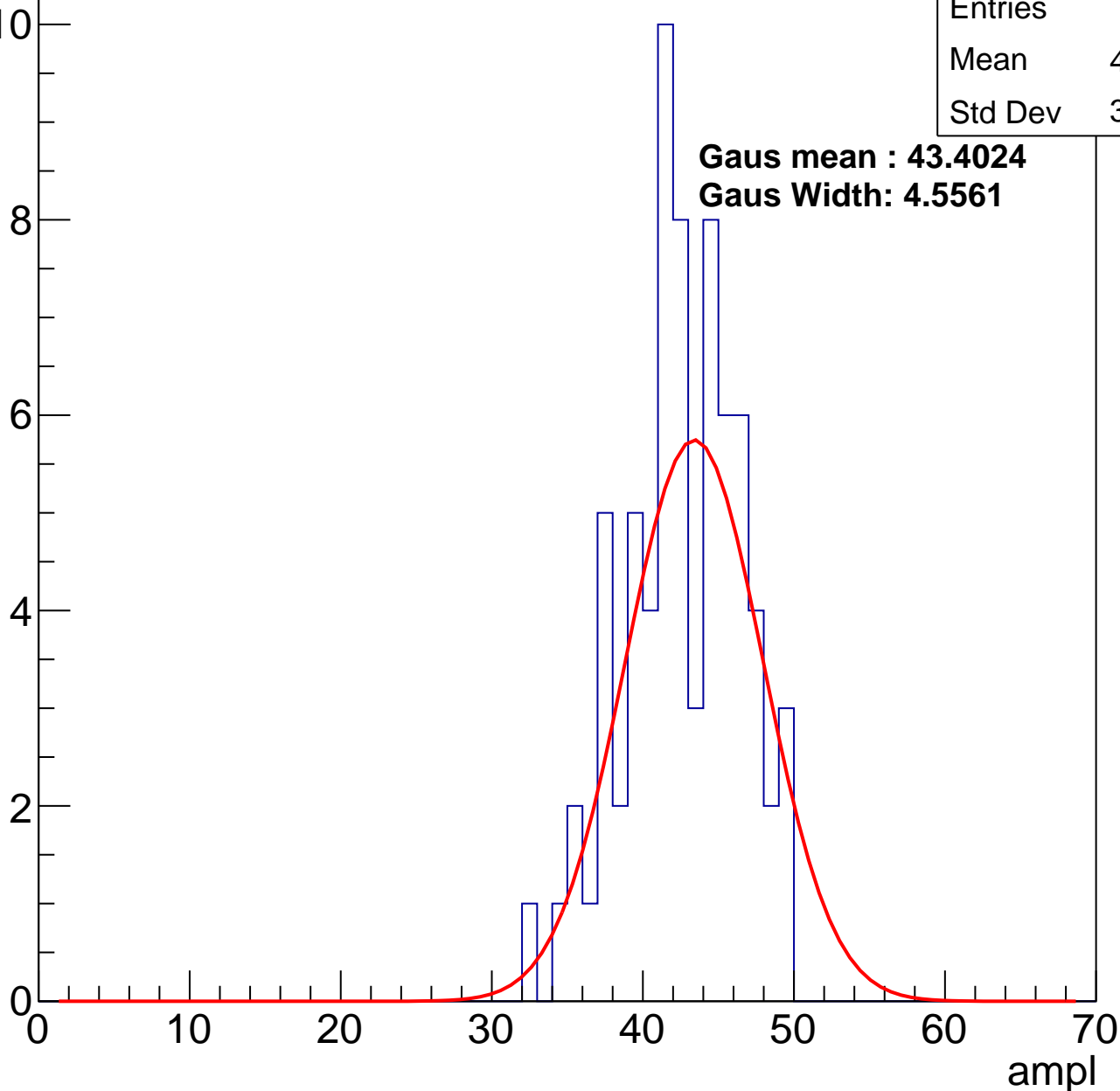
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.14
Std Dev	3.806

Gaus mean : 43.4024

Gaus Width: 4.5561

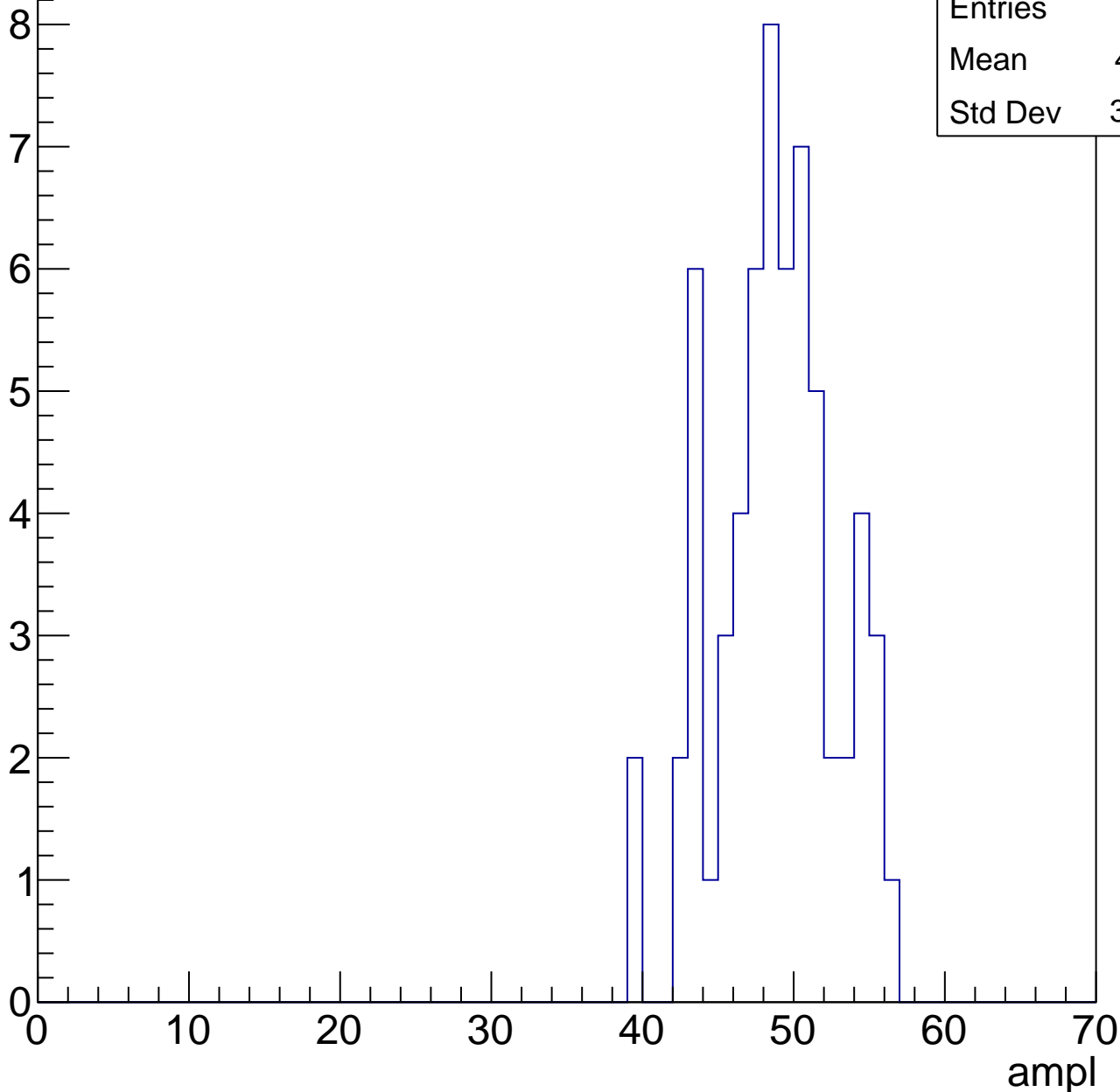


B1L103S, U10-ch3, adc3

calib_packv5_042523_0143.root, FC#7, port C2

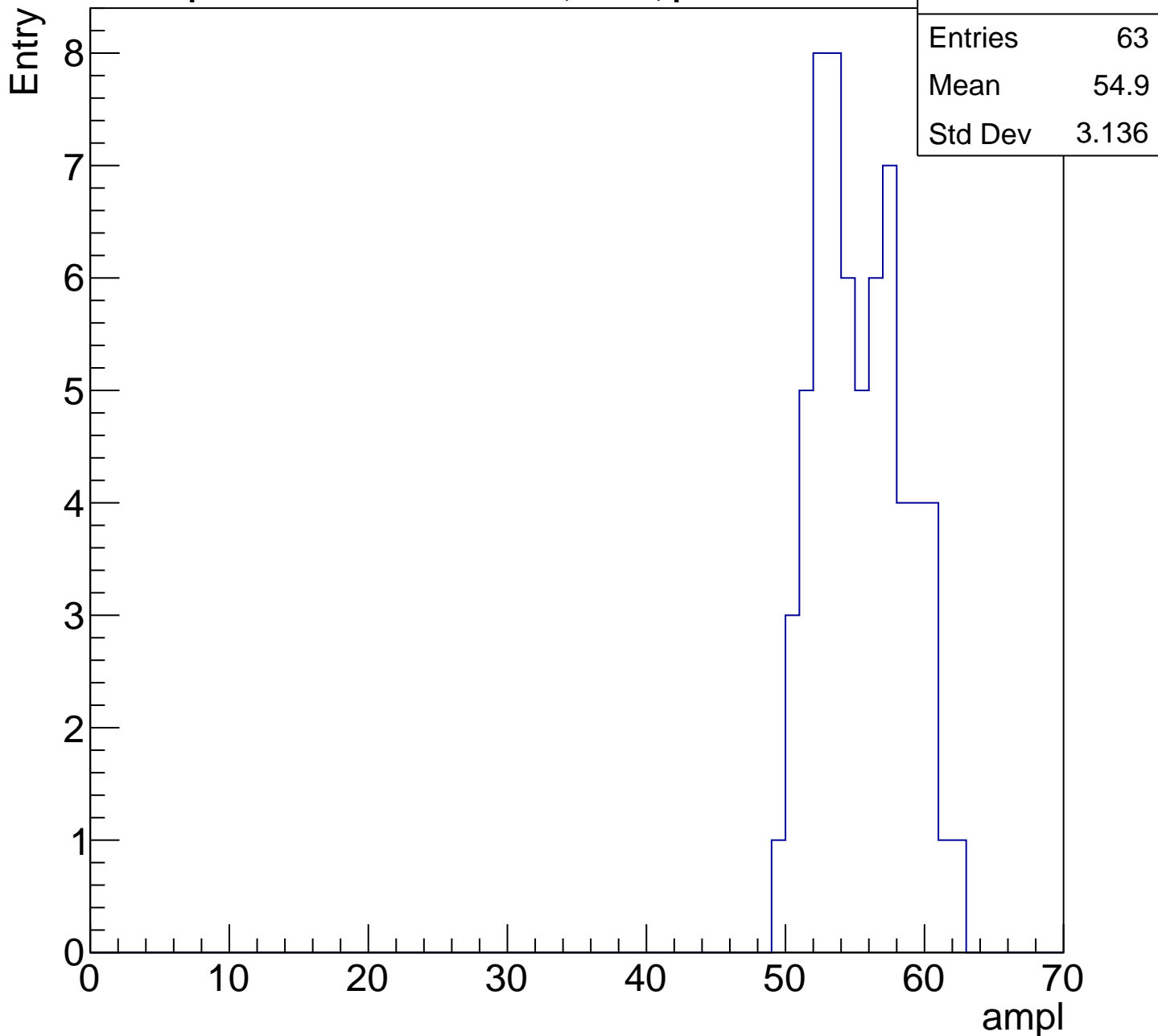
Entry

Entries	62
Mean	48.31
Std Dev	3.933



B1L103S, U10-ch3, adc4

calib_packv5_042523_0143.root, FC#7, port C2

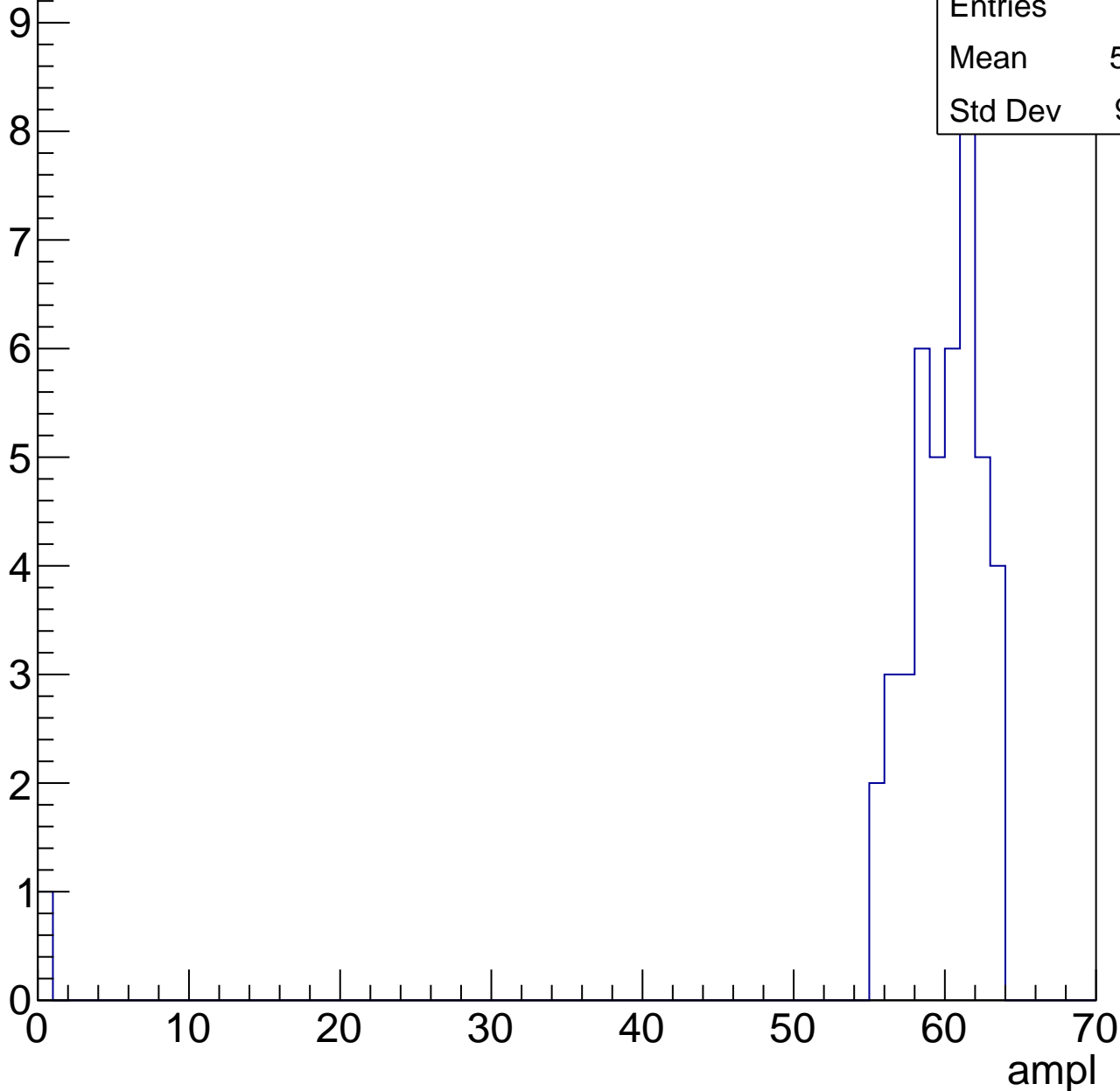


B1L103S, U10-ch3, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.25
Std Dev	9.151

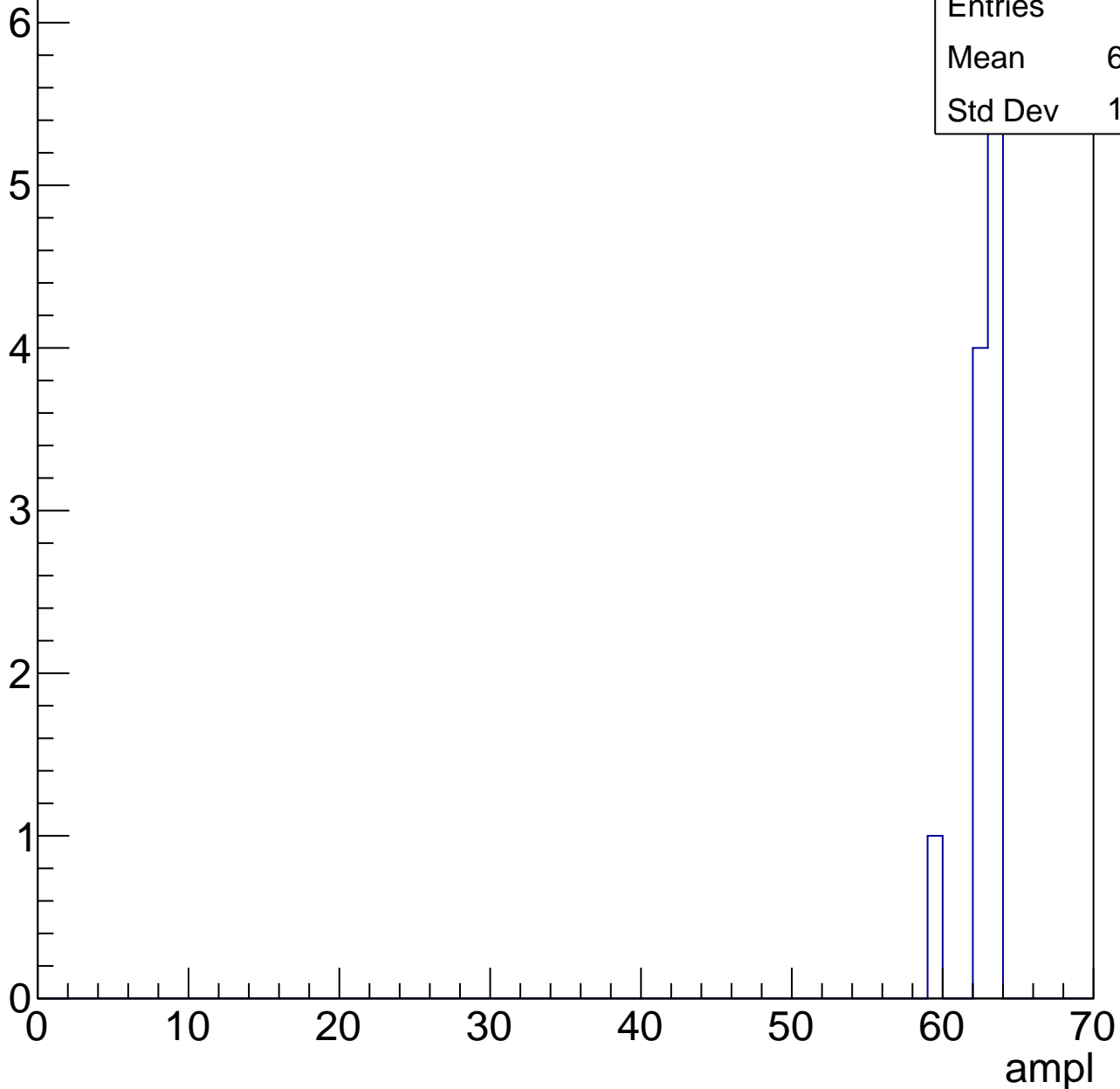


B1L103S, U10-ch3, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	62.27
Std Dev	1.135



B1L103S, U10-ch3, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch4, adc0

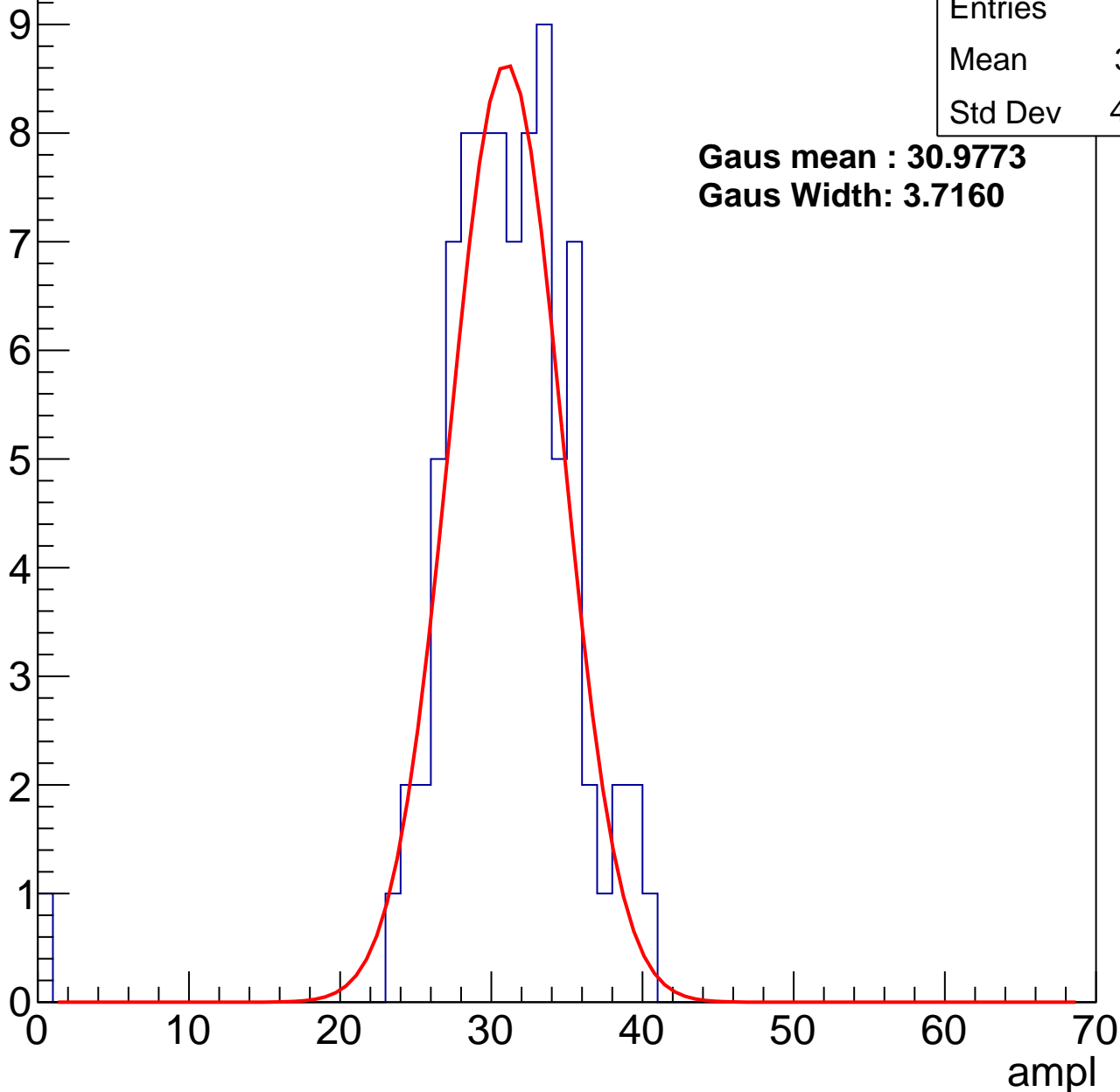
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	30.51
Std Dev	4.962

Gaus mean : 30.9773

Gaus Width: 3.7160



B1L103S, U10-ch4, adc1

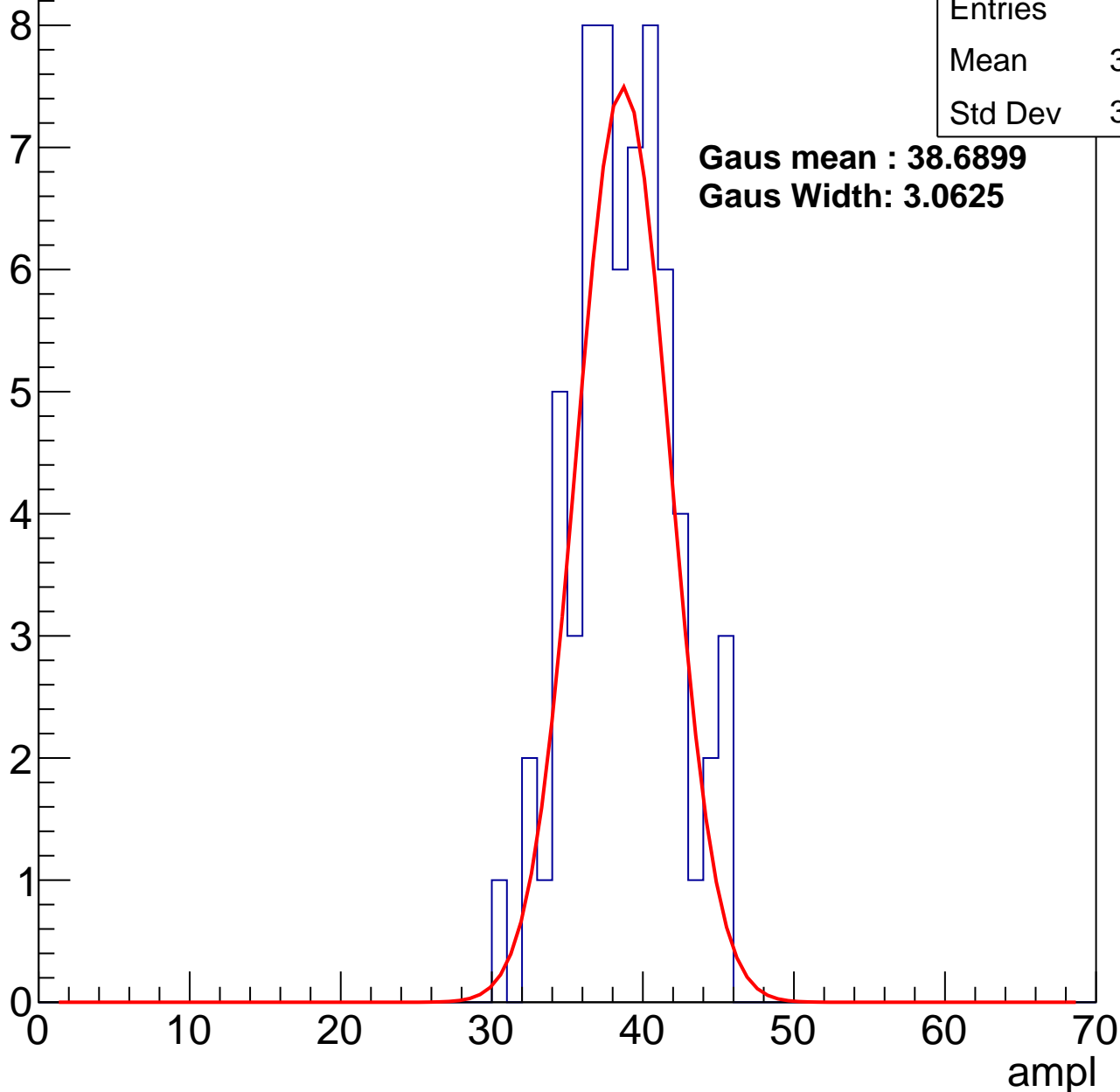
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	38.26
Std Dev	3.292

Gaus mean : 38.6899

Gaus Width: 3.0625



B1L103S, U10-ch4, adc2

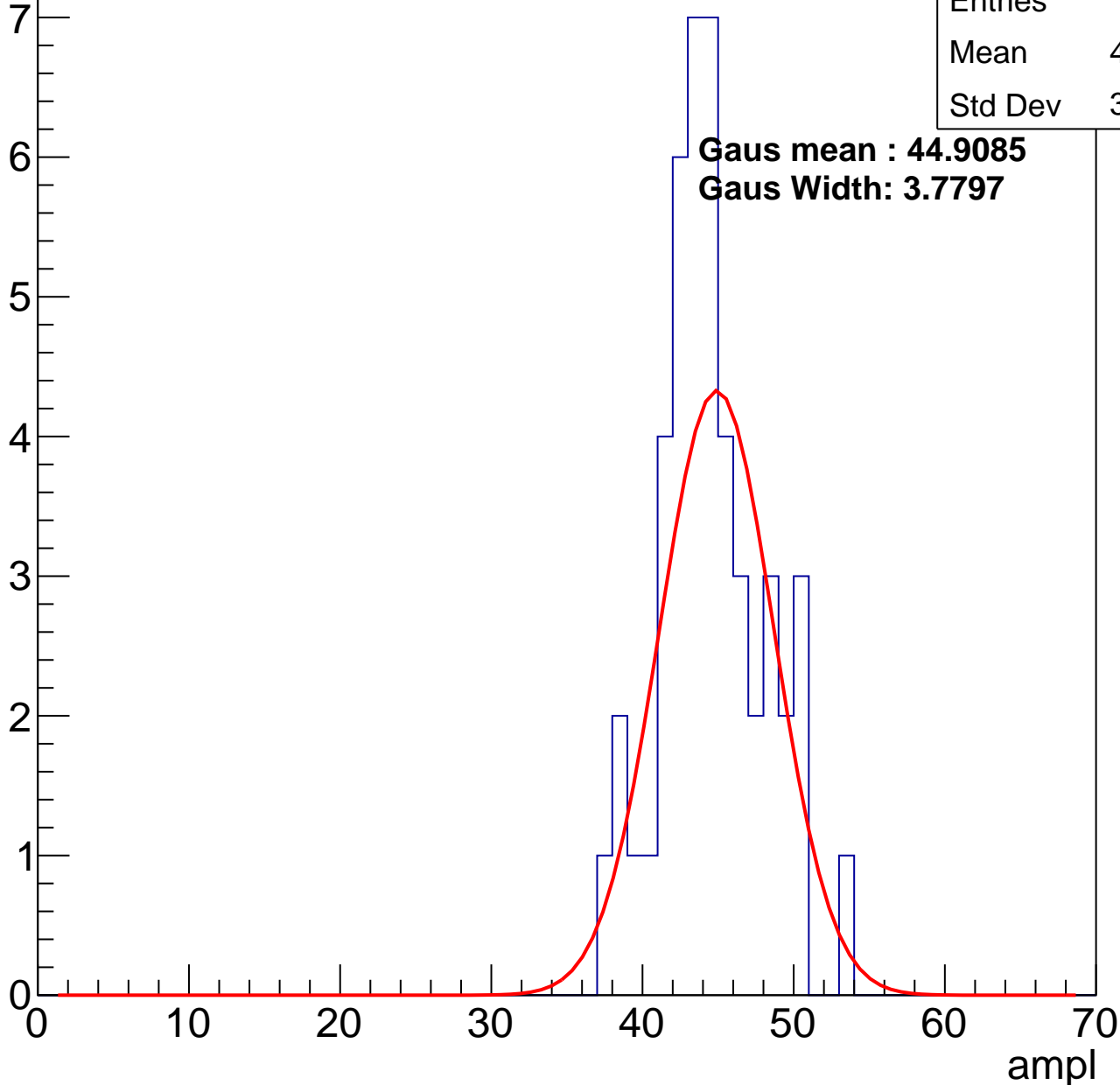
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	44.13
Std Dev	3.418

Gaus mean : 44.9085

Gaus Width: 3.7797

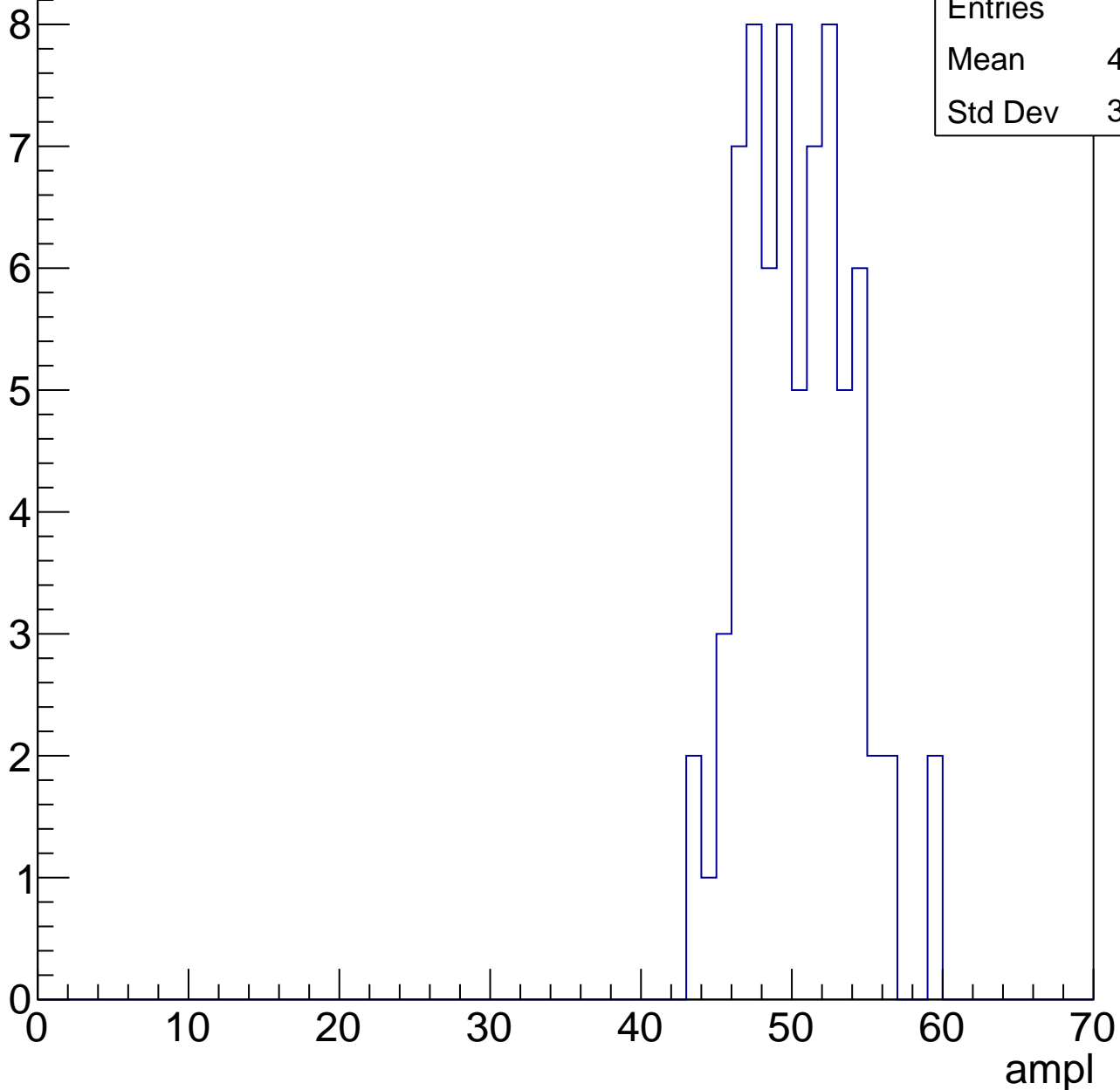


B1L103S, U10-ch4, adc3

calib_packv5_042523_0143.root, FC#7, port C2

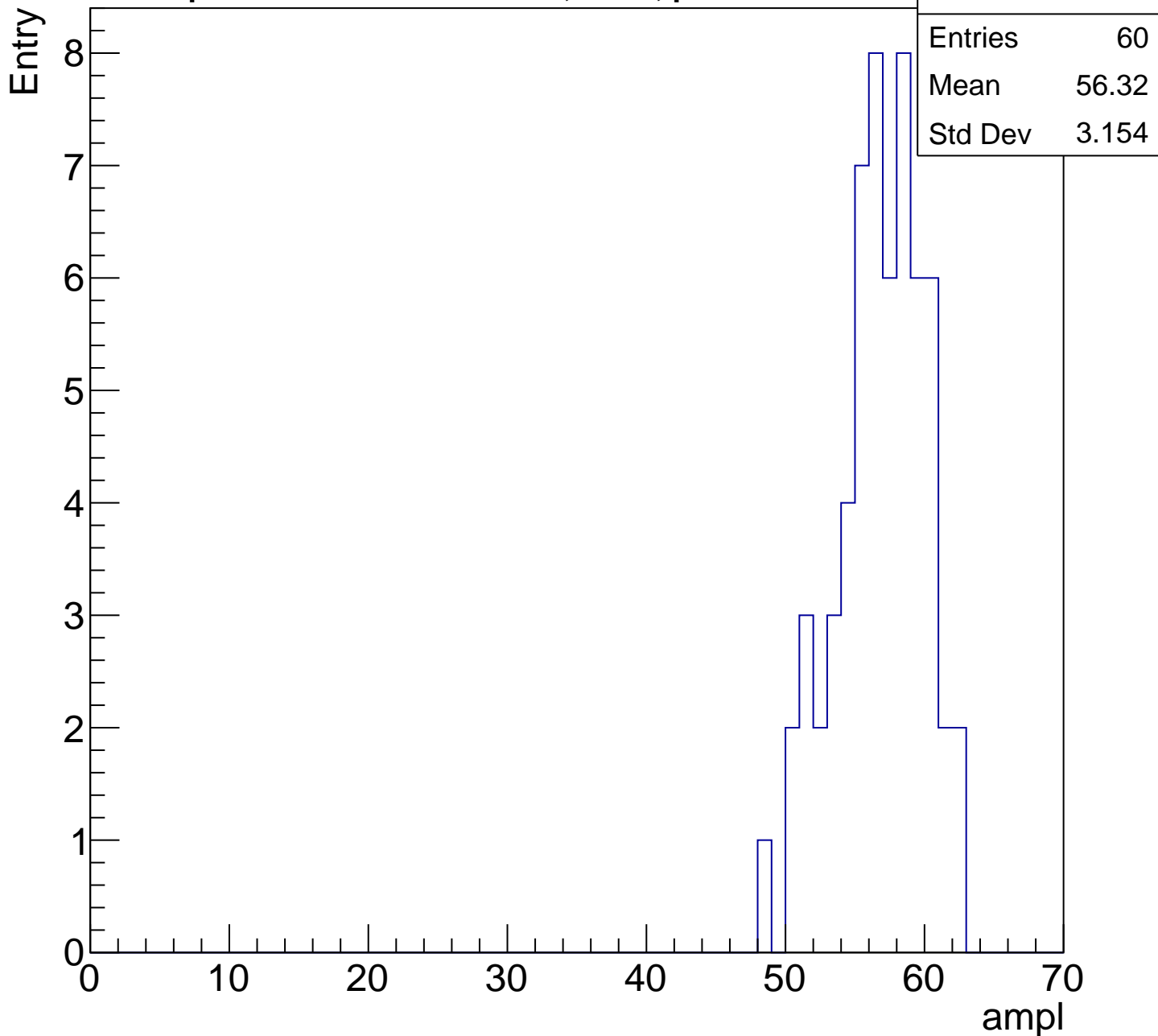
Entry

Entries	72
Mean	49.93
Std Dev	3.513



B1L103S, U10-ch4, adc4

calib_packv5_042523_0143.root, FC#7, port C2

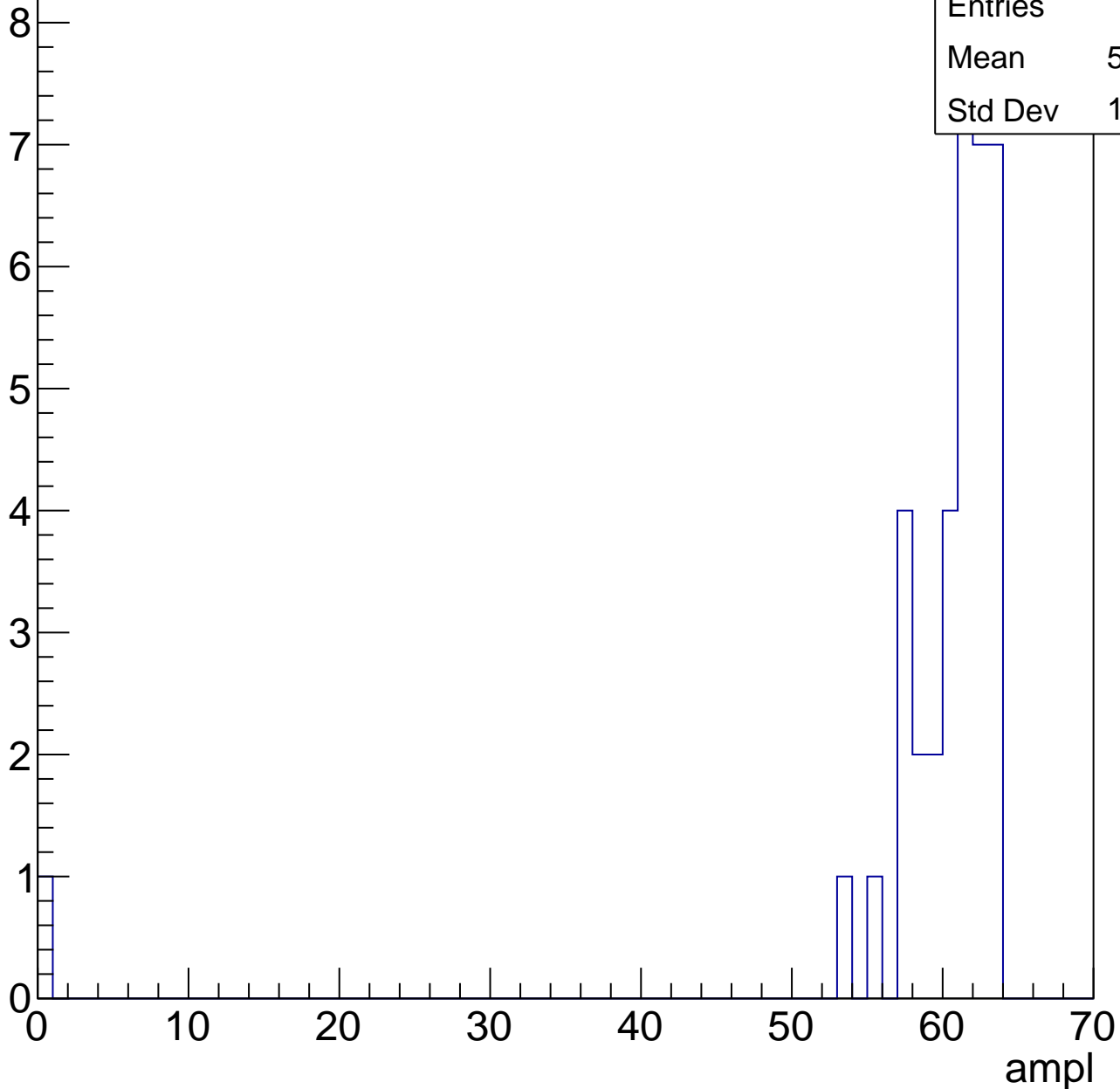


B1L103S, U10-ch4, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.73
Std Dev	10.08



B1L103S, U10-ch4, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch4, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch5, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	28.46
Std Dev	4.088

Gaus mean : 28.3528

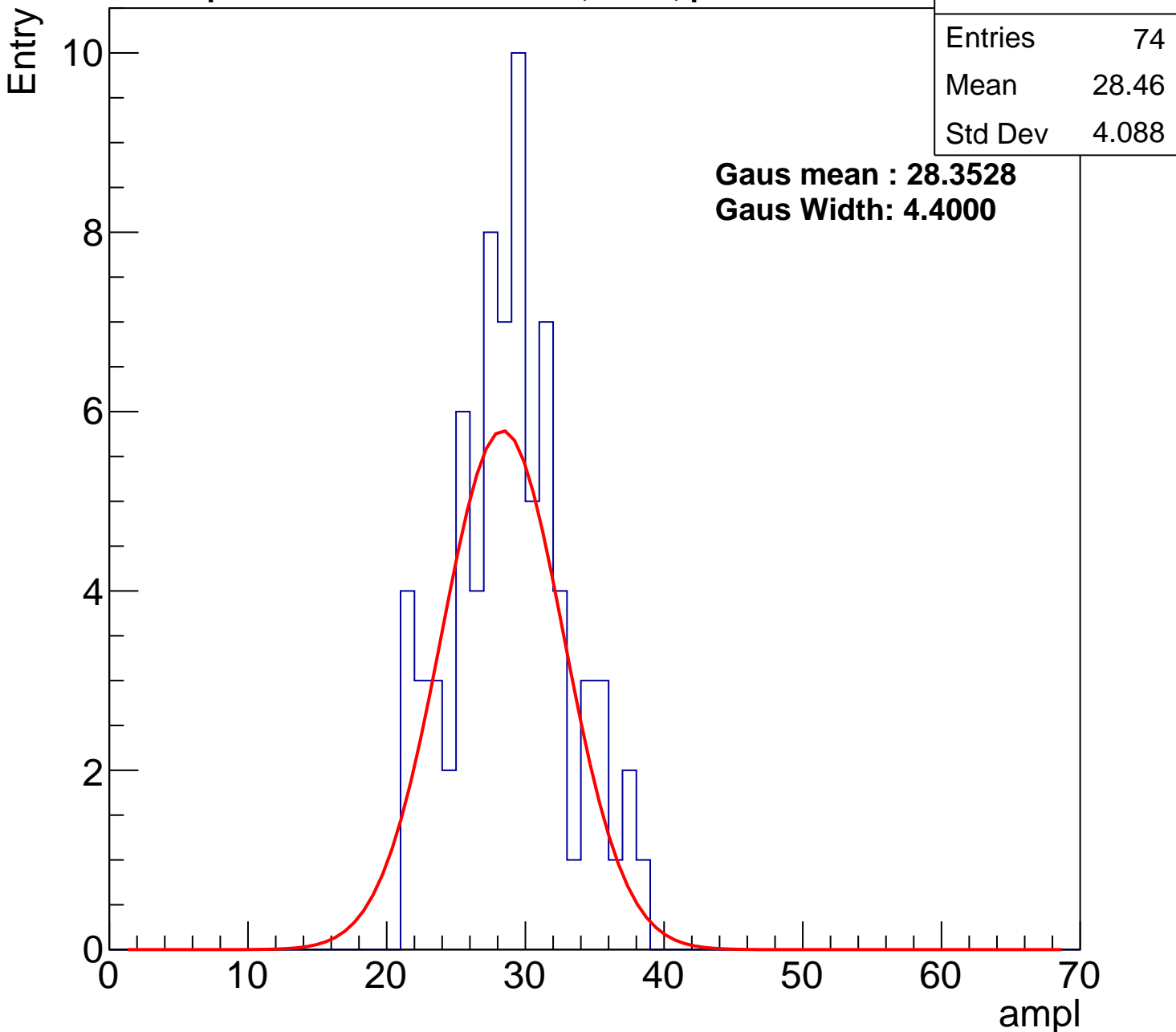
Gaus Width: 4.4000

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch5, adc1

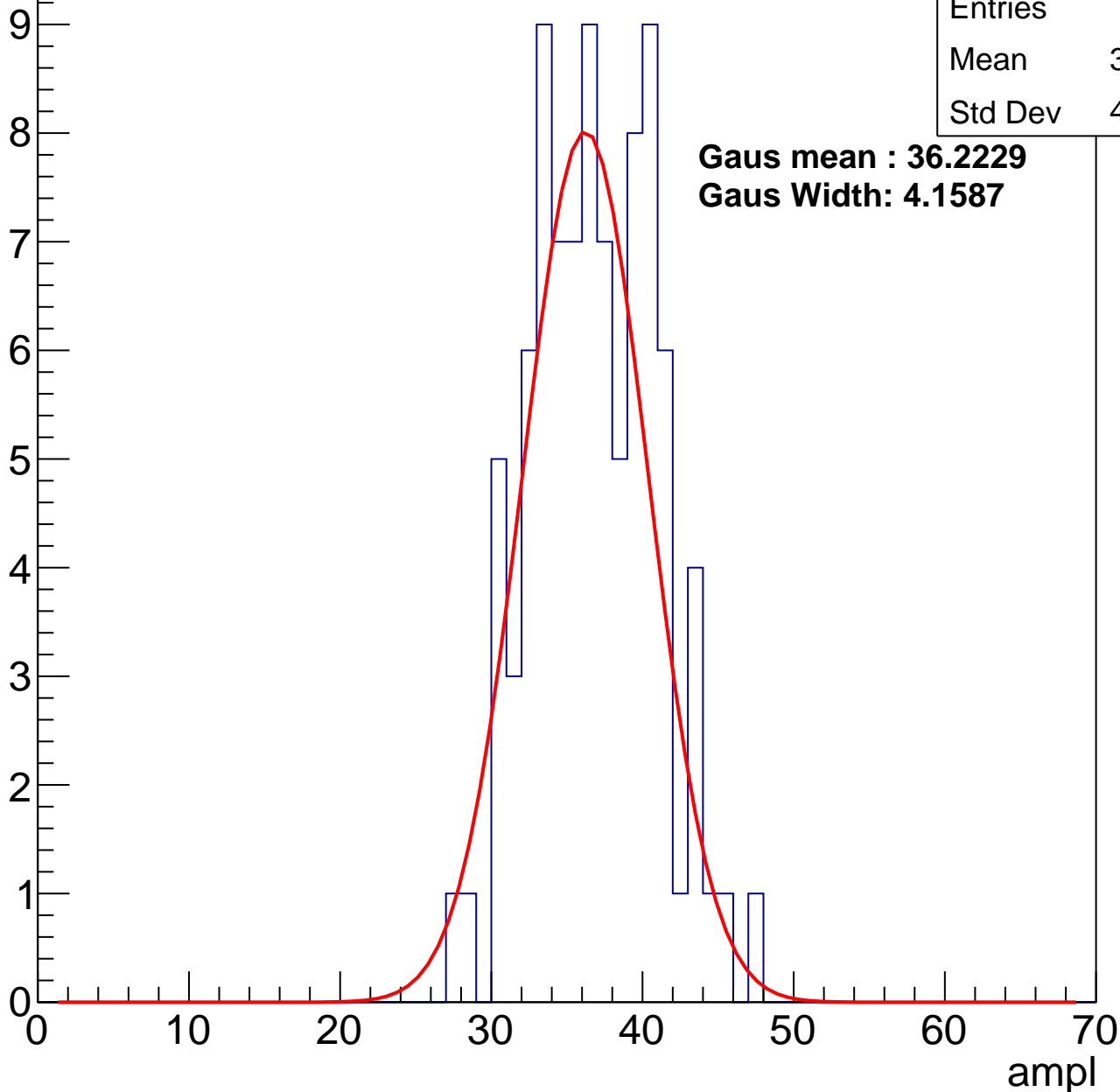
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	91
Mean	36.38
Std Dev	4.046

Gaus mean : 36.2229

Gaus Width: 4.1587



B1L103S, U10-ch5, adc2

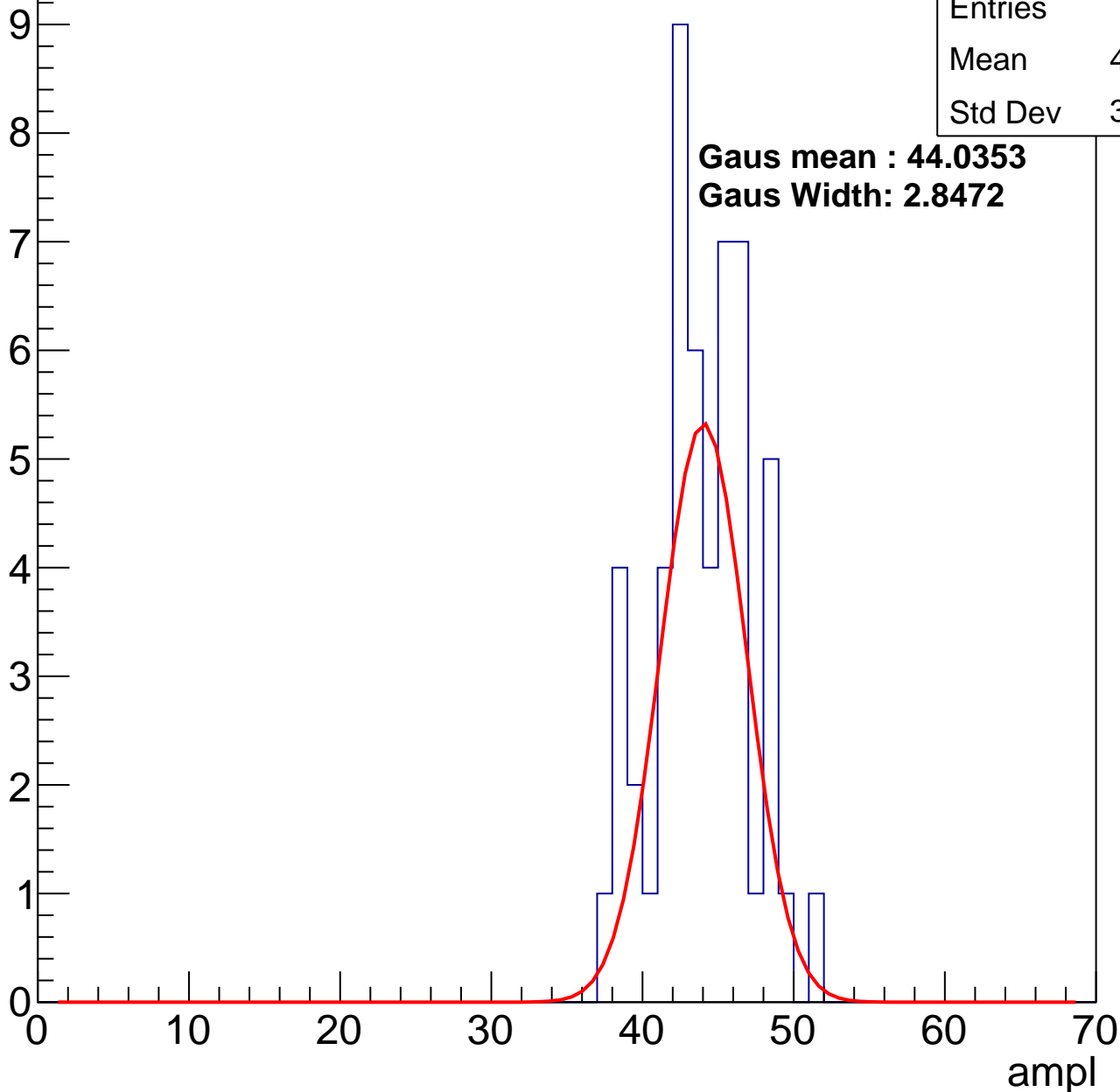
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	43.53
Std Dev	3.142

Gaus mean : 44.0353

Gaus Width: 2.8472

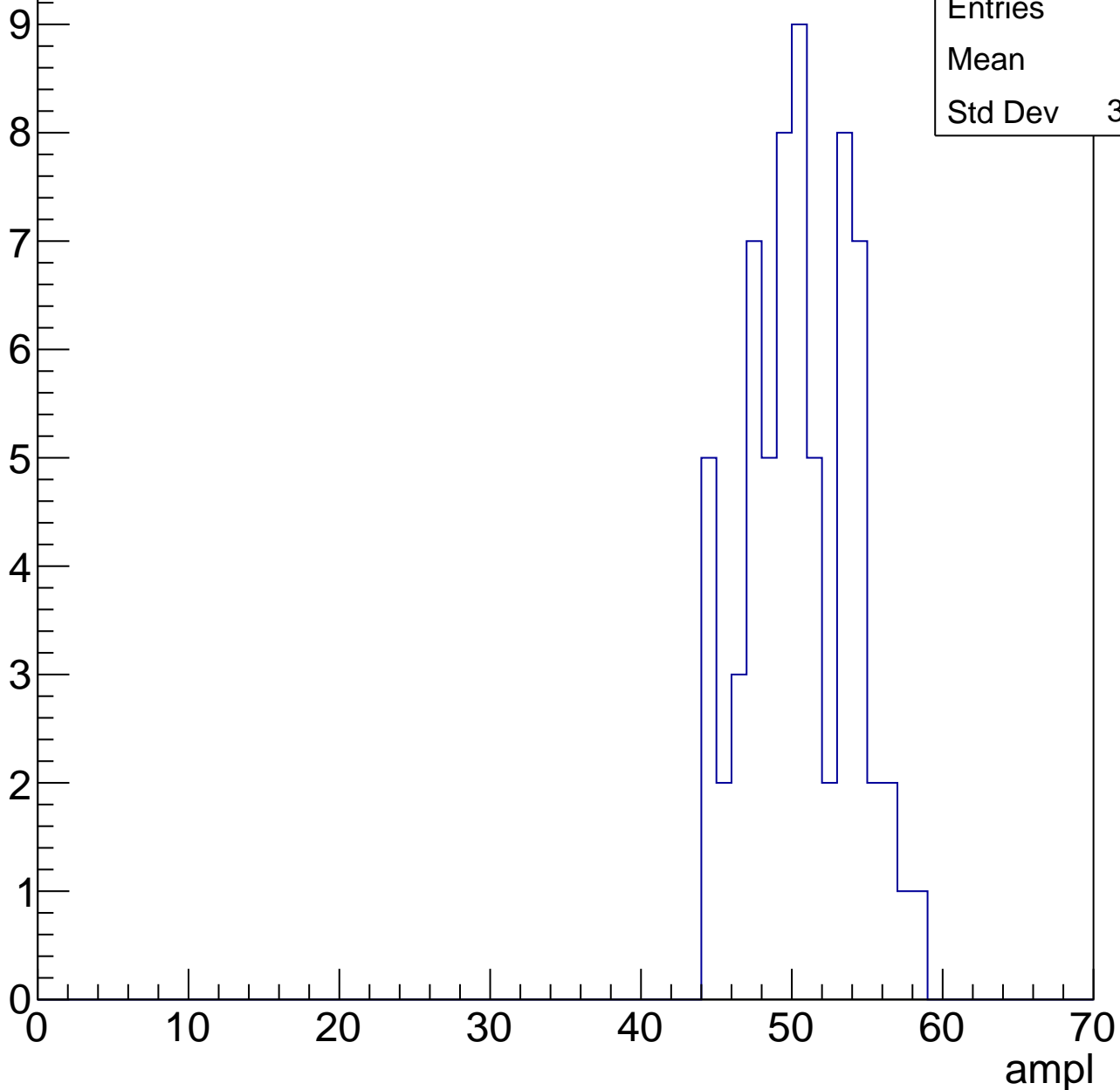


B1L103S, U10-ch5, adc3

calib_packv5_042523_0143.root, FC#7, port C2

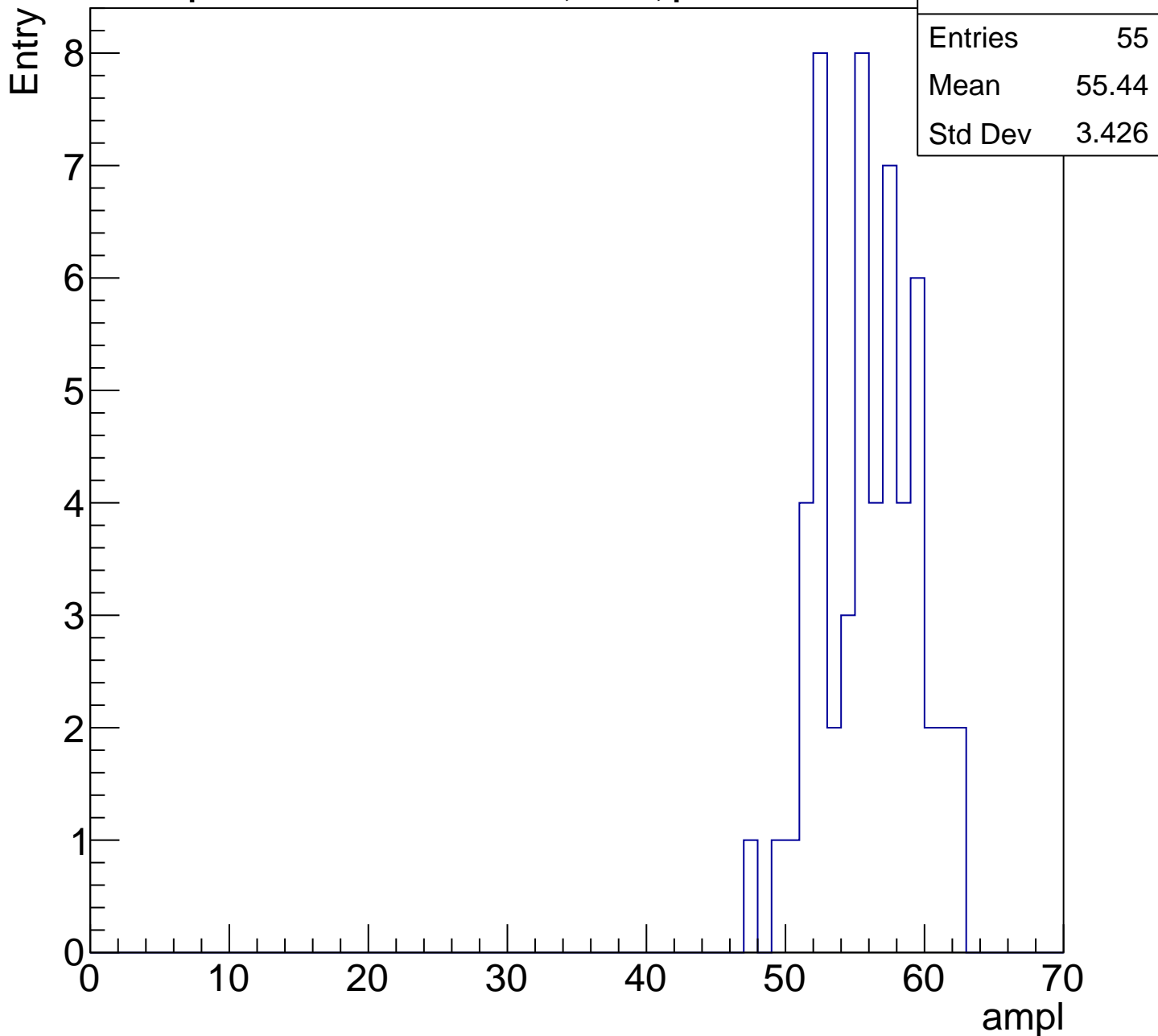
Entry

Entries	67
Mean	50.1
Std Dev	3.456



B1L103S, U10-ch5, adc4

calib_packv5_042523_0143.root, FC#7, port C2

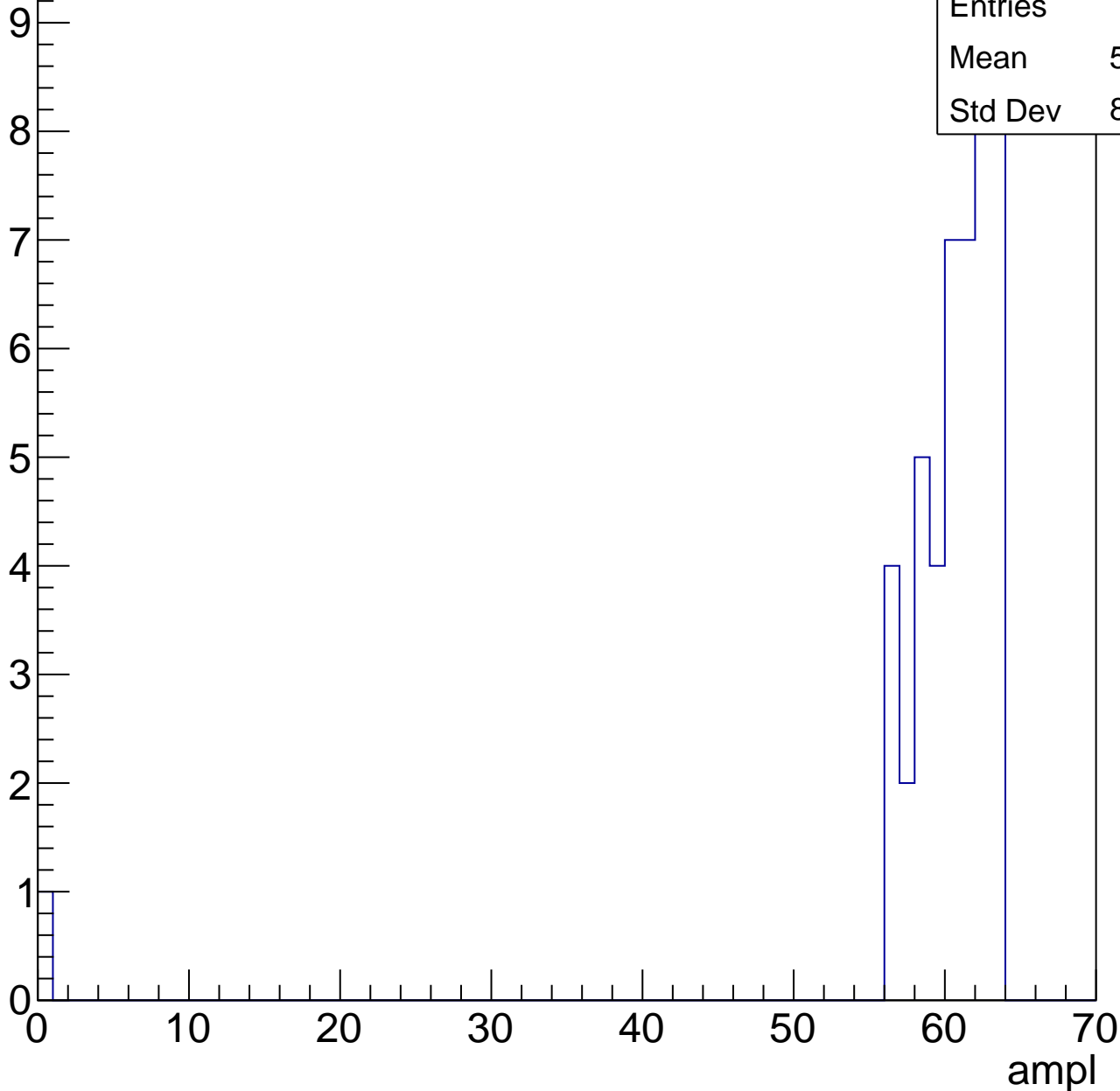


B1L103S, U10-ch5, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	59.02
Std Dev	8.969



B1L103S, U10-ch5, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	1.247

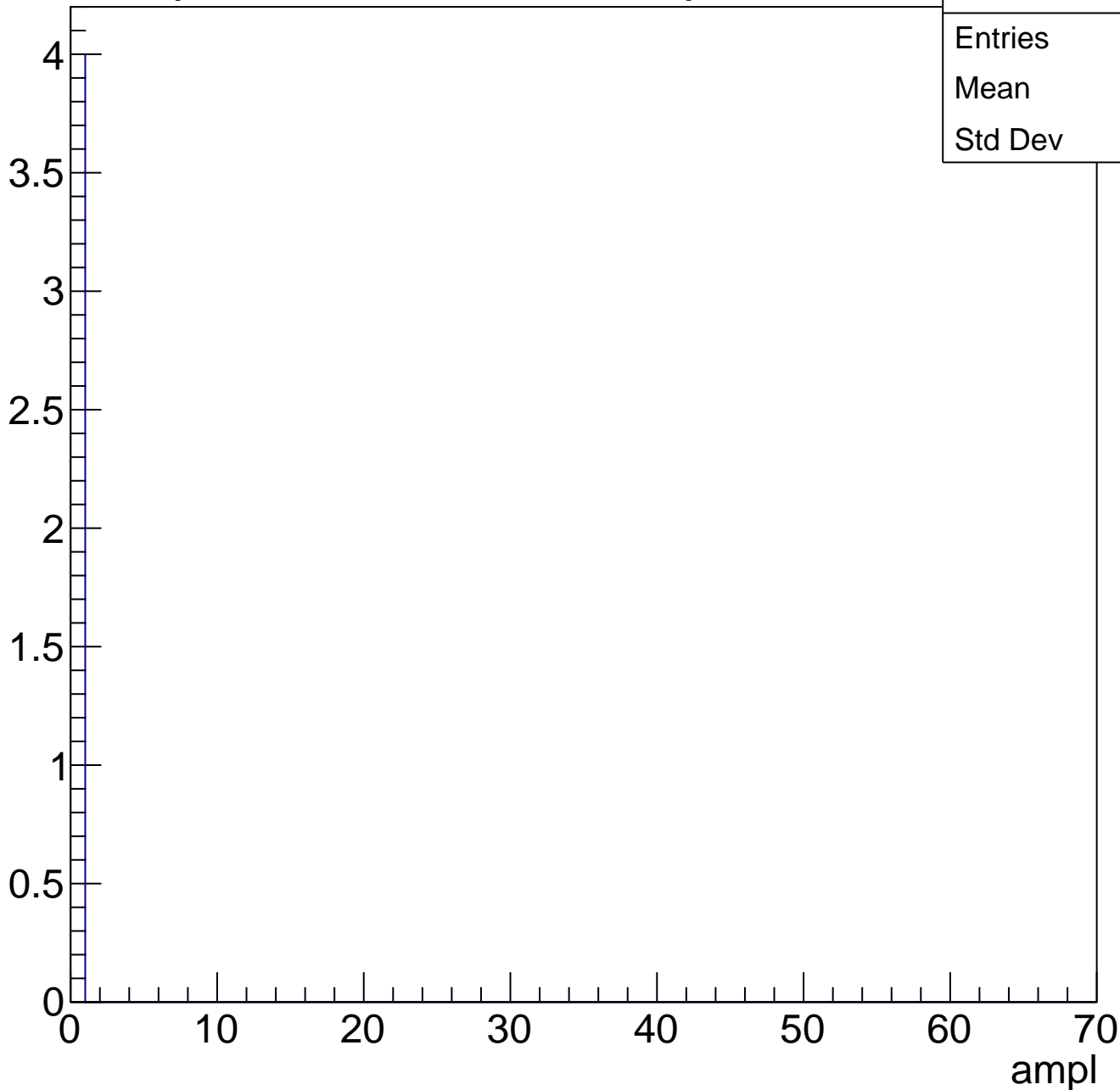
ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch5, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	0
Std Dev	0

B1L103S, U10-ch6, adc0

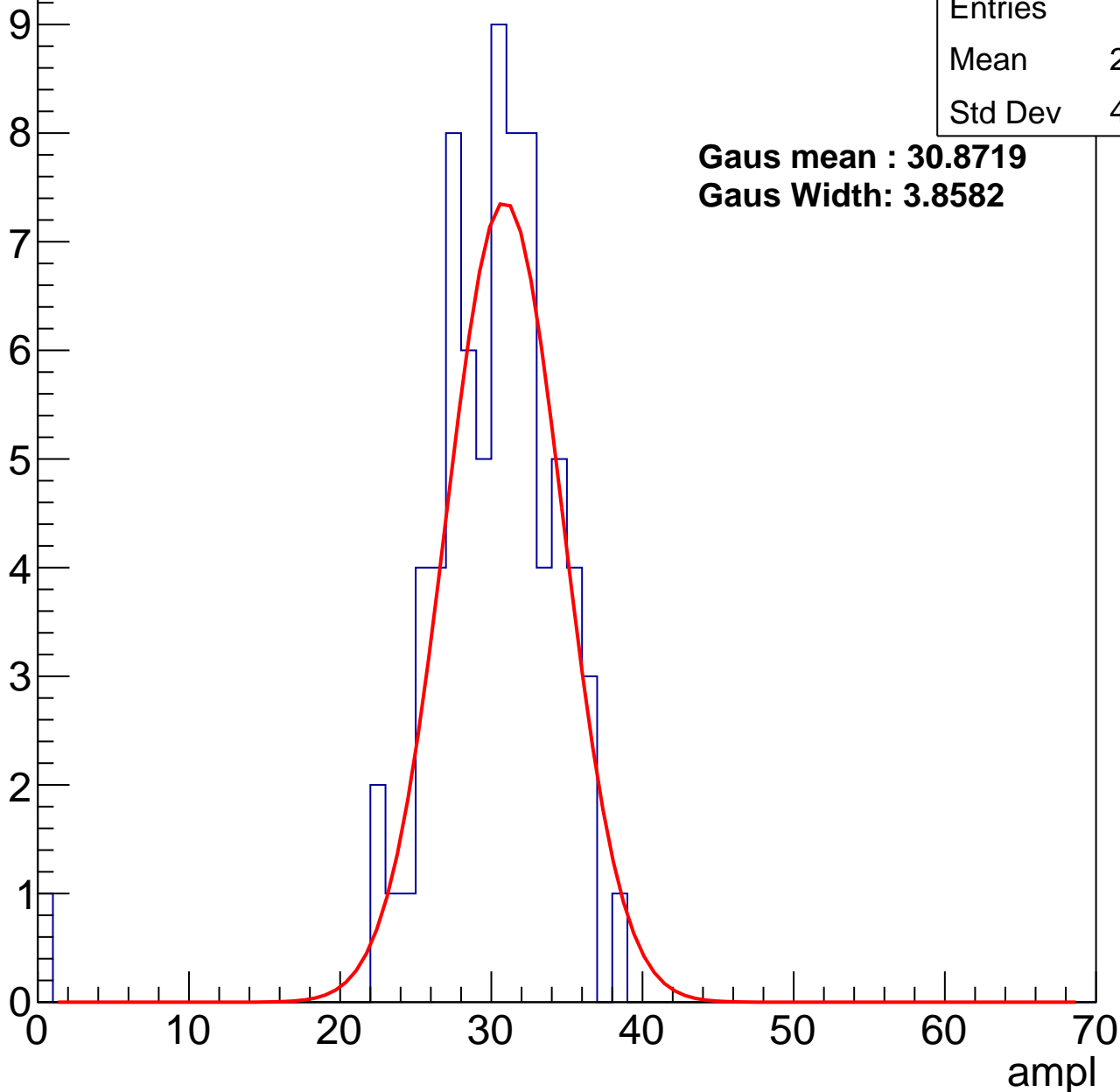
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	29.54
Std Dev	4.919

Gaus mean : 30.8719

Gaus Width: 3.8582



B1L103S, U10-ch6, adc1

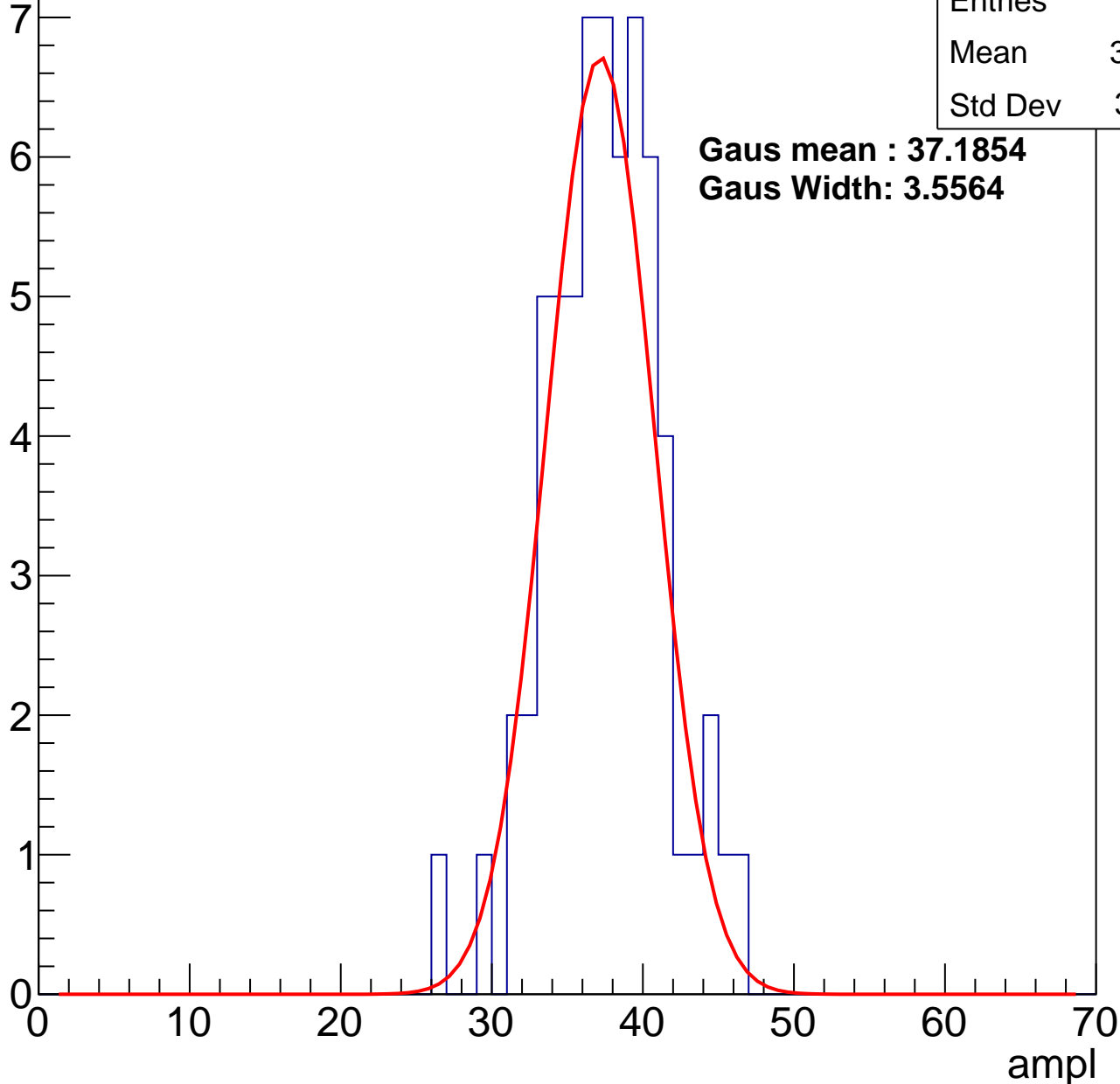
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.05
Std Dev	3.781

Gaus mean : 37.1854

Gaus Width: 3.5564



B1L103S, U10-ch6, adc2

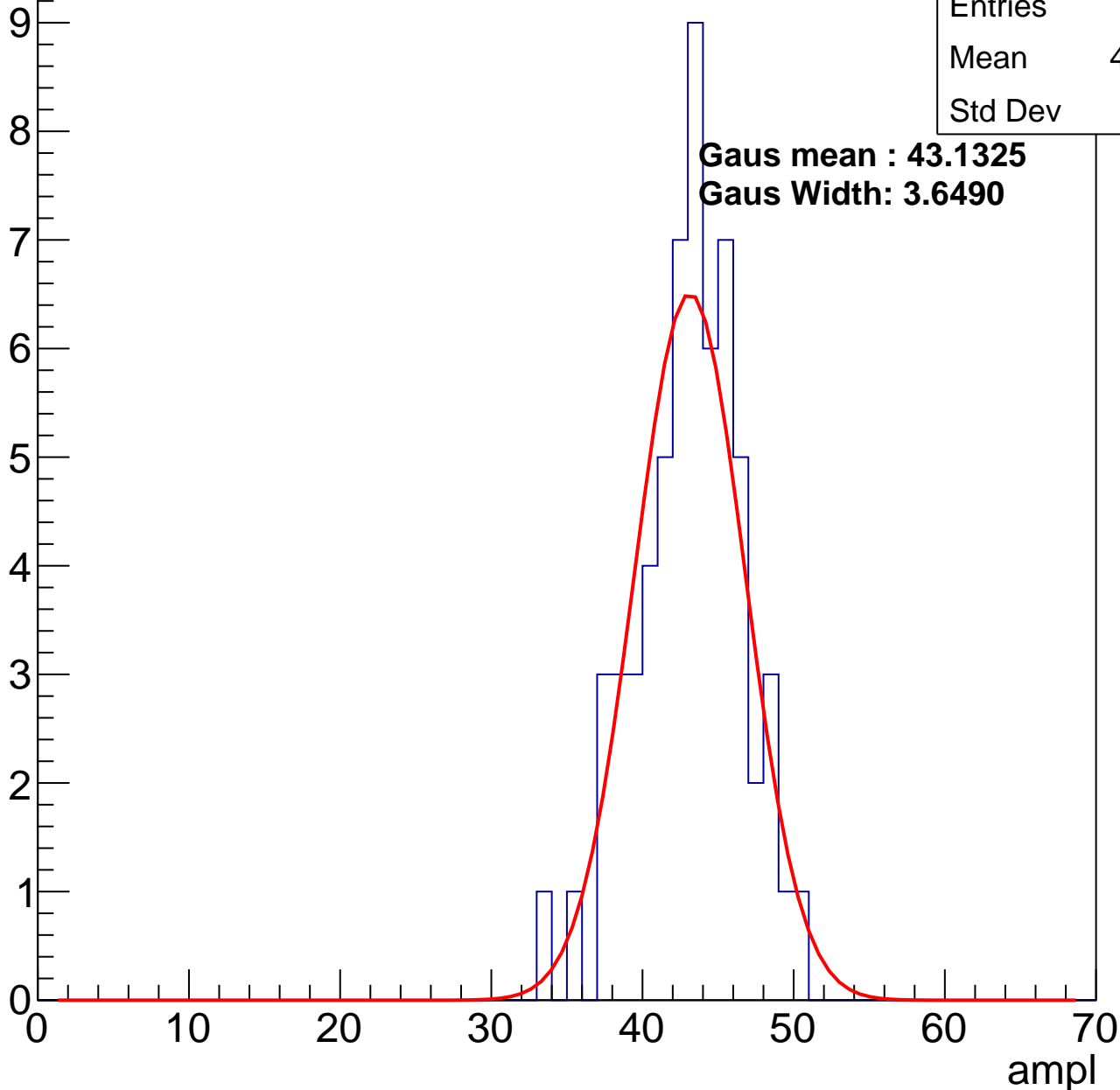
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	42.66
Std Dev	3.44

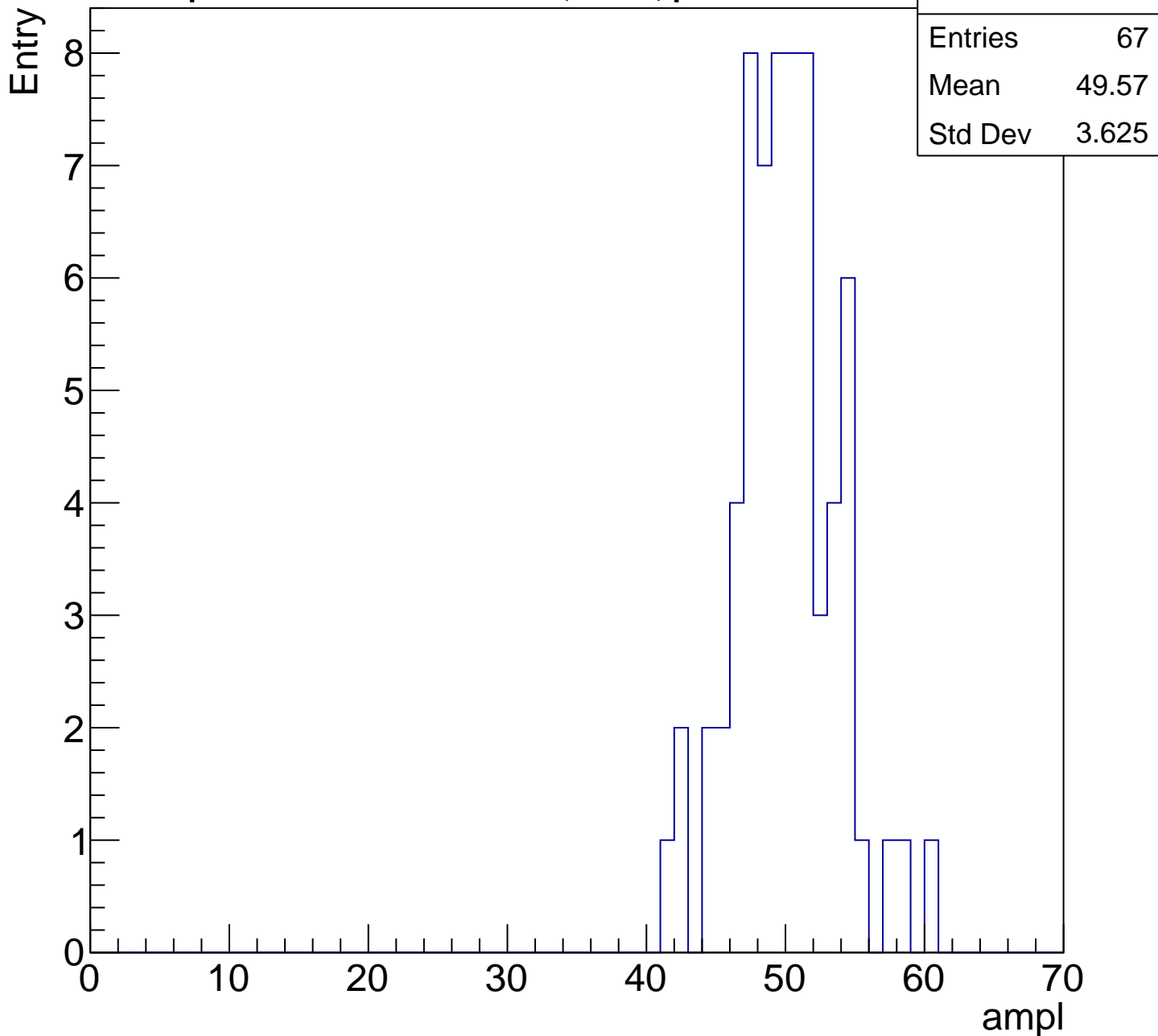
Gaus mean : 43.1325

Gaus Width: 3.6490



B1L103S, U10-ch6, adc3

calib_packv5_042523_0143.root, FC#7, port C2

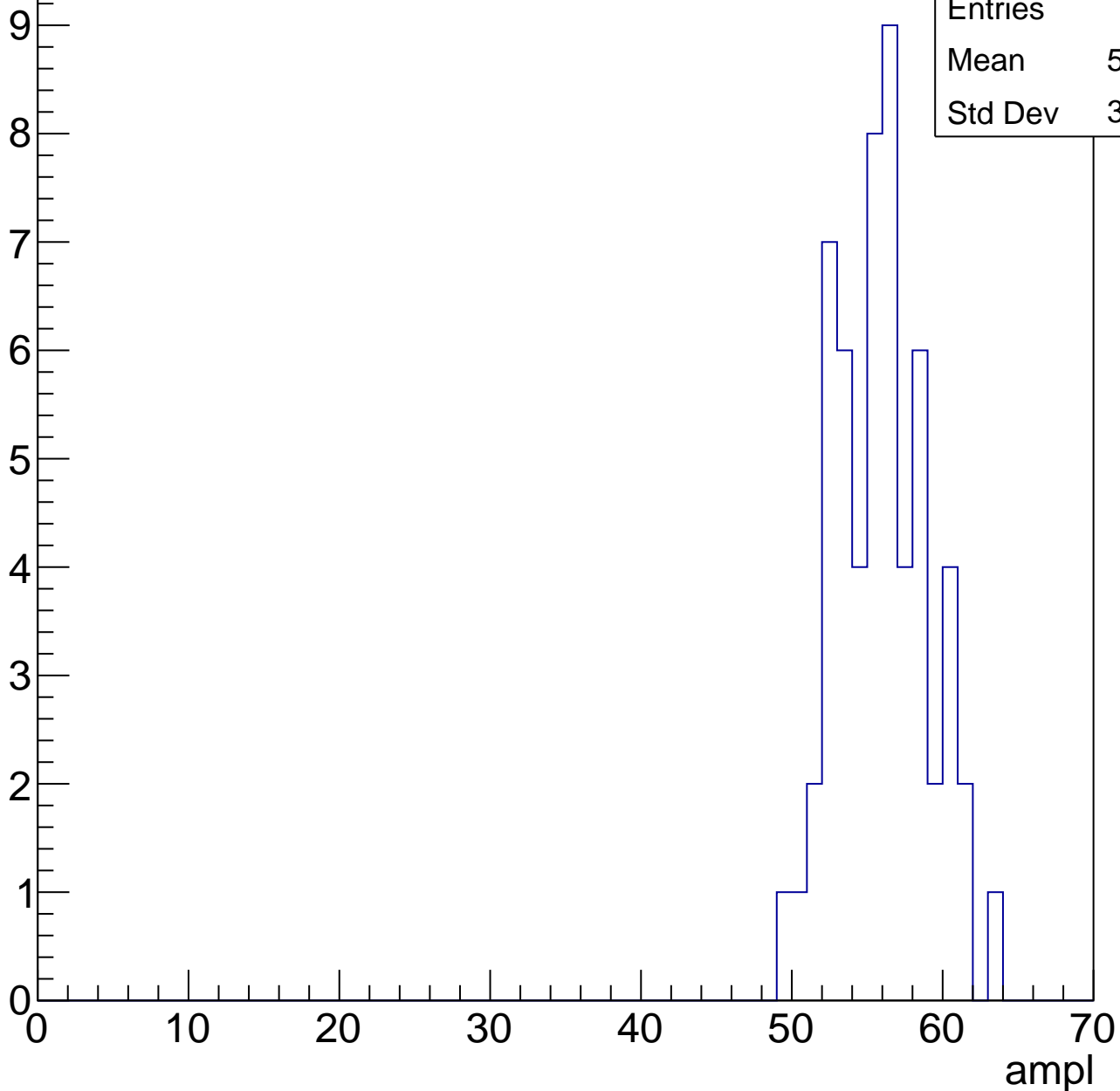


B1L103S, U10-ch6, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.47
Std Dev	3.015



B1L103S, U10-ch6, adc5

calib_packv5_042523_0143.root, FC#7, port C2

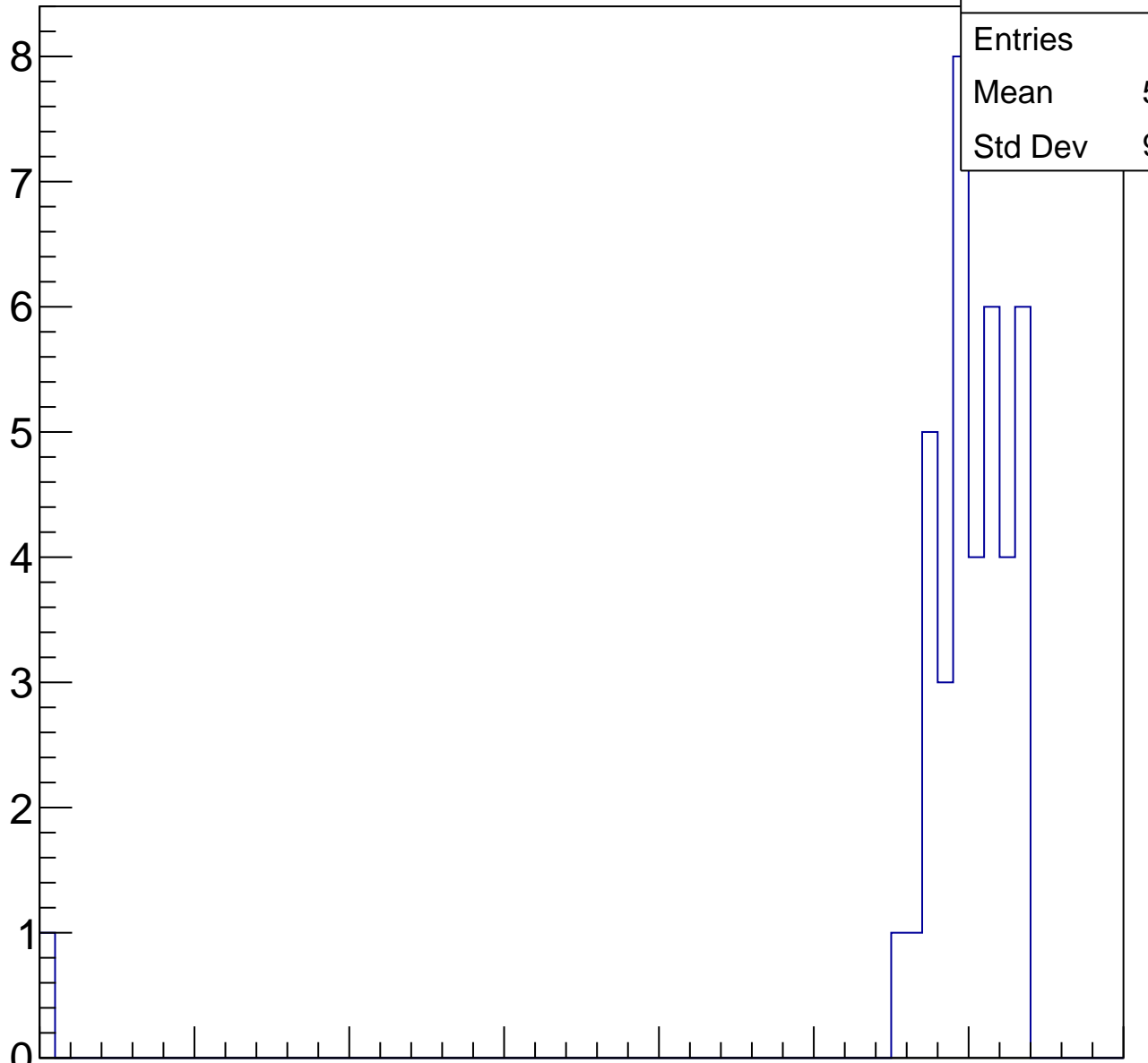
Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	58.31
Std Dev	9.701

ampl

0 10 20 30 40 50 60 70

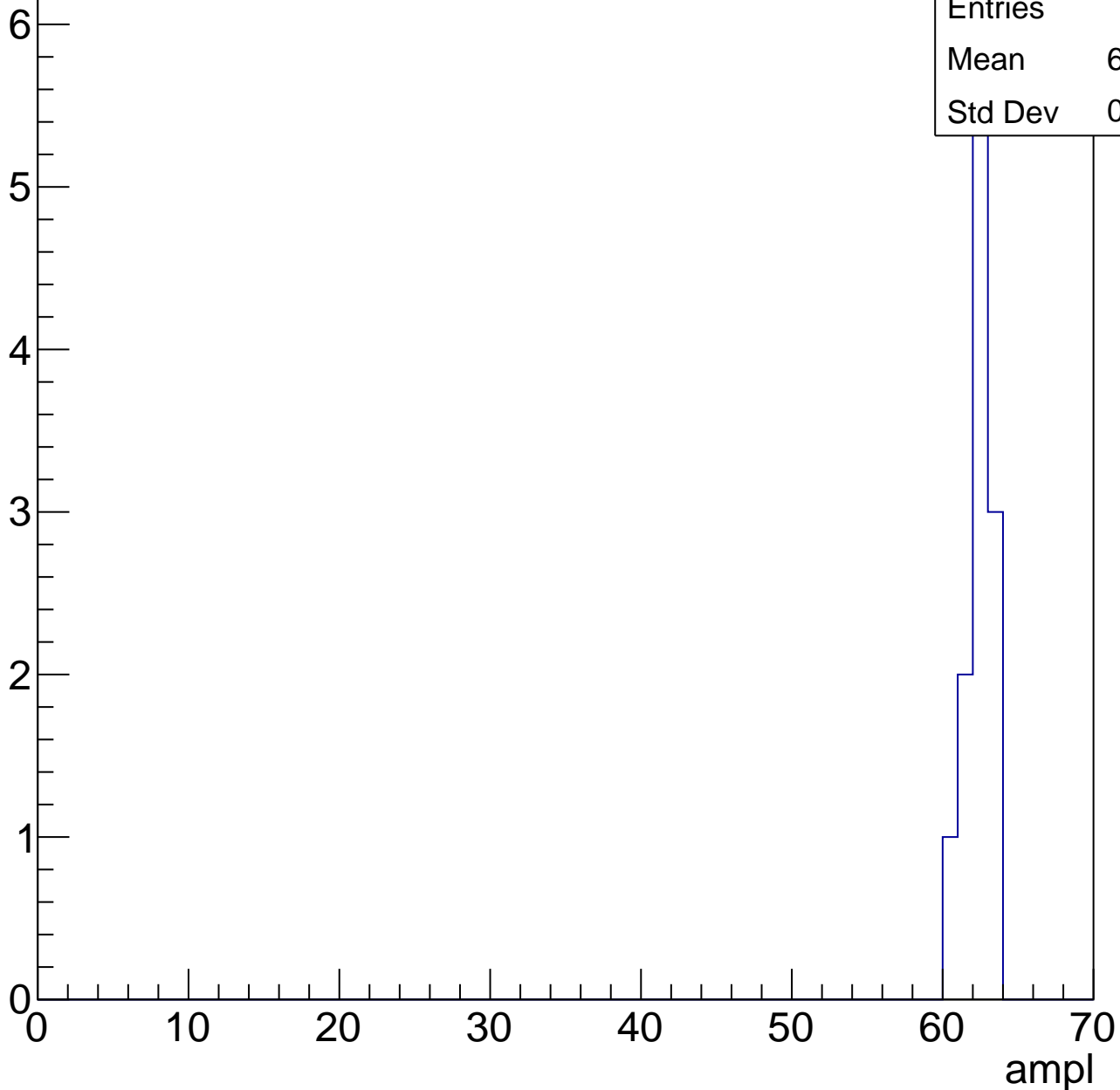


B1L103S, U10-ch6, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.92
Std Dev	0.862



B1L103S, U10-ch6, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch7, adc0

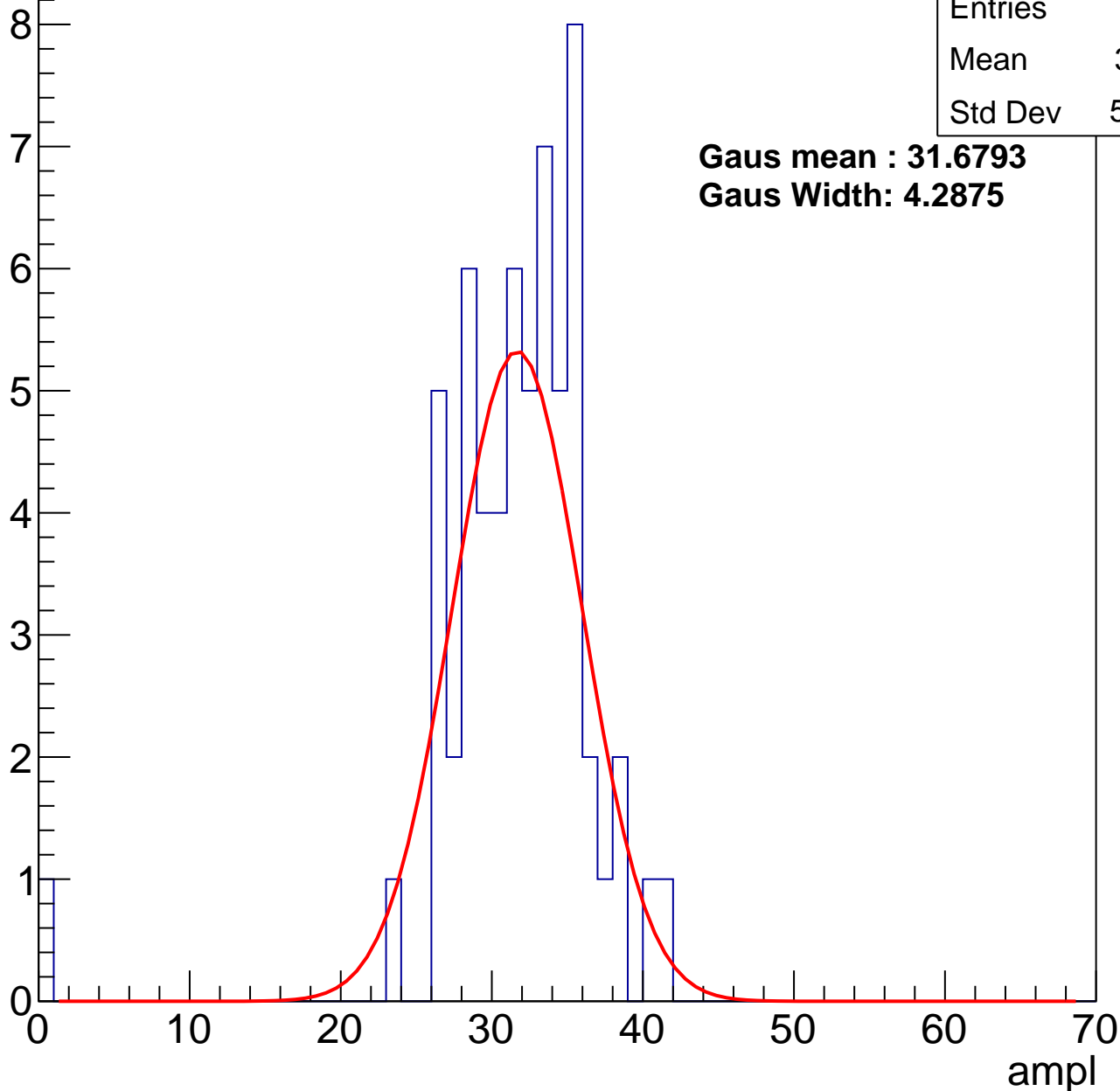
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	31.21
Std Dev	5.469

Gaus mean : 31.6793

Gaus Width: 4.2875



B1L103S, U10-ch7, adc1

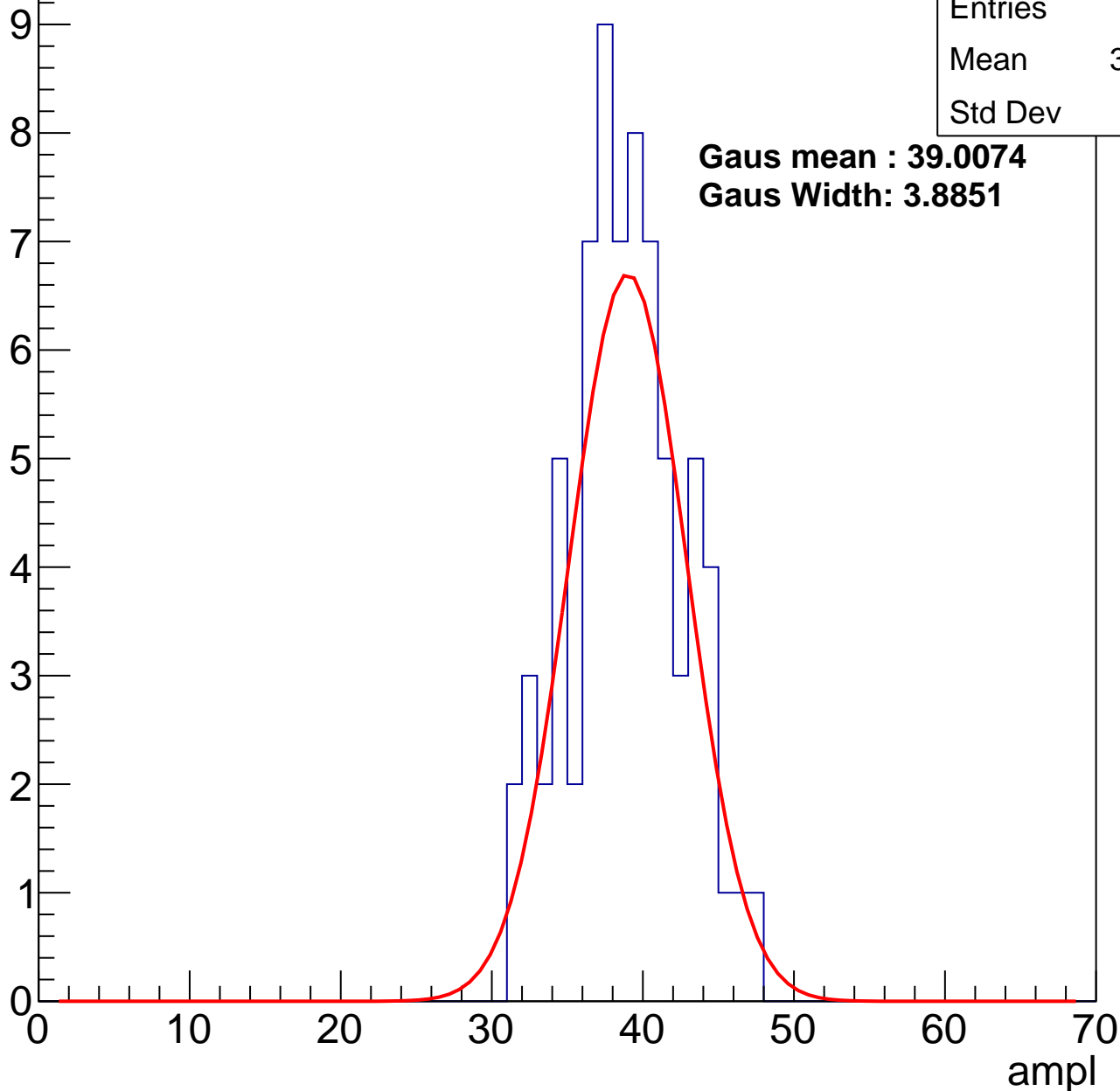
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	38.43
Std Dev	3.67

Gaus mean : 39.0074

Gaus Width: 3.8851



B1L103S, U10-ch7, adc2

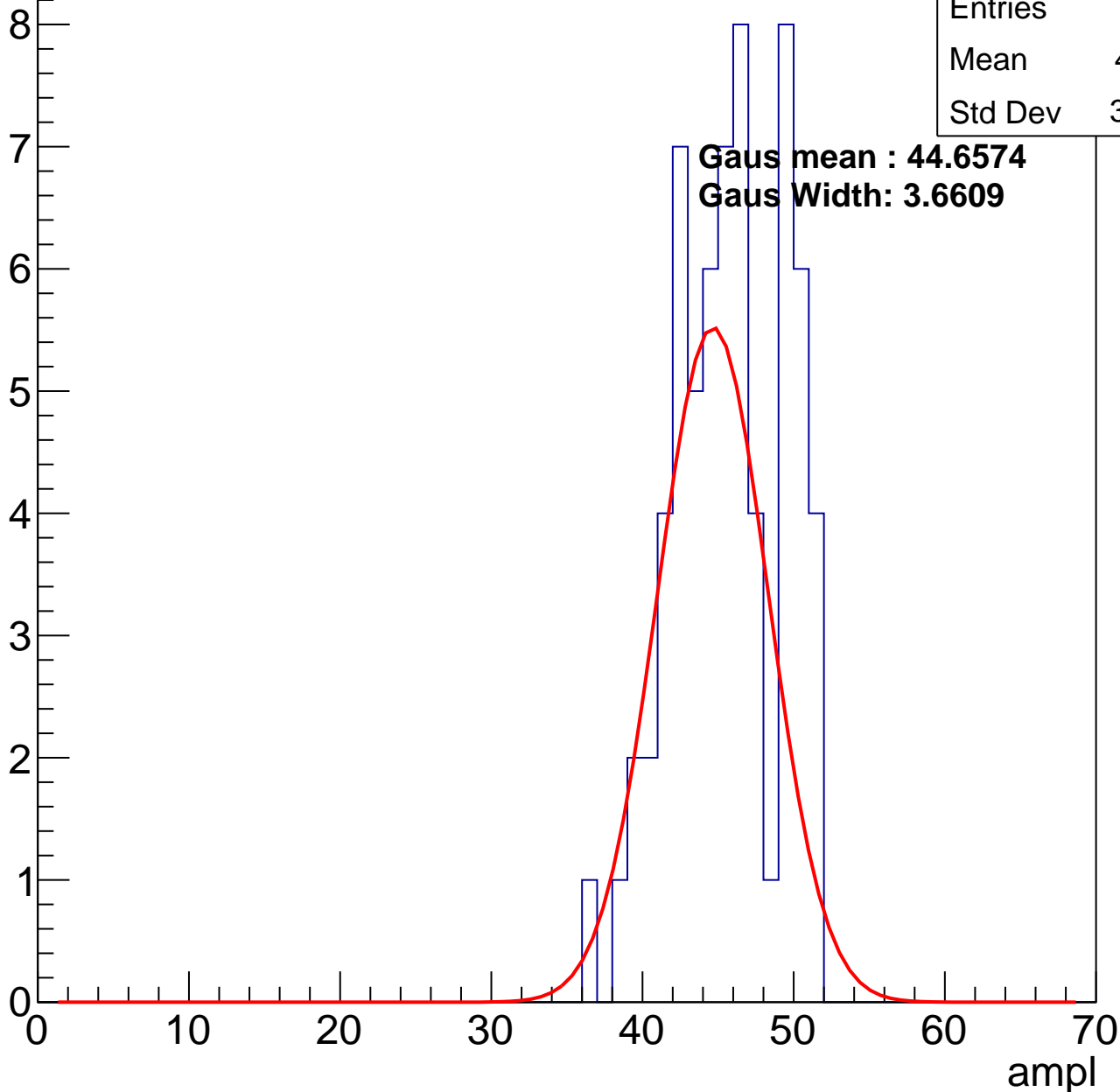
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	45.21
Std Dev	3.604

Gaus mean : 44.6574

Gaus Width: 3.6609

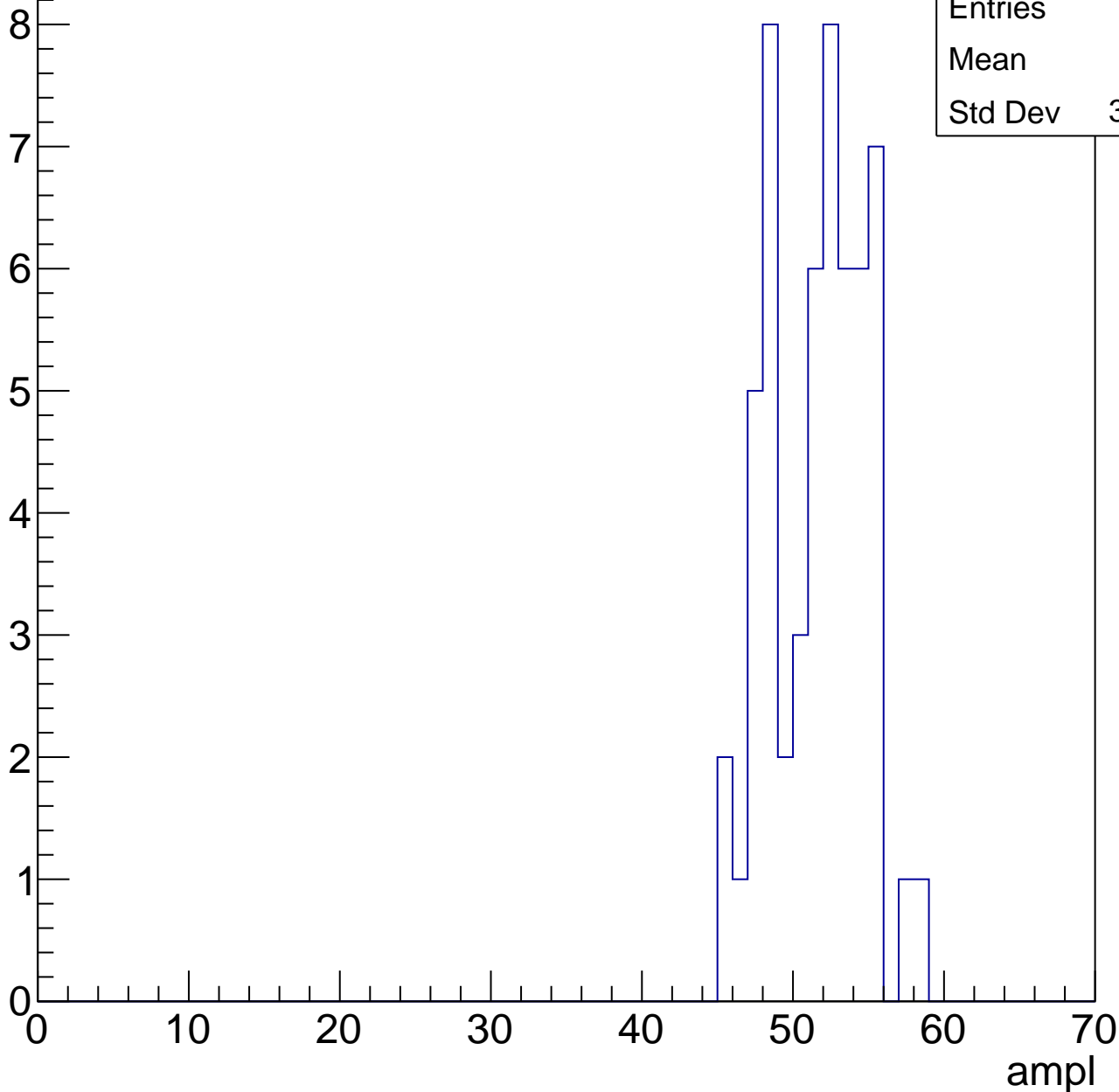


B1L103S, U10-ch7, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	51.2
Std Dev	3.108

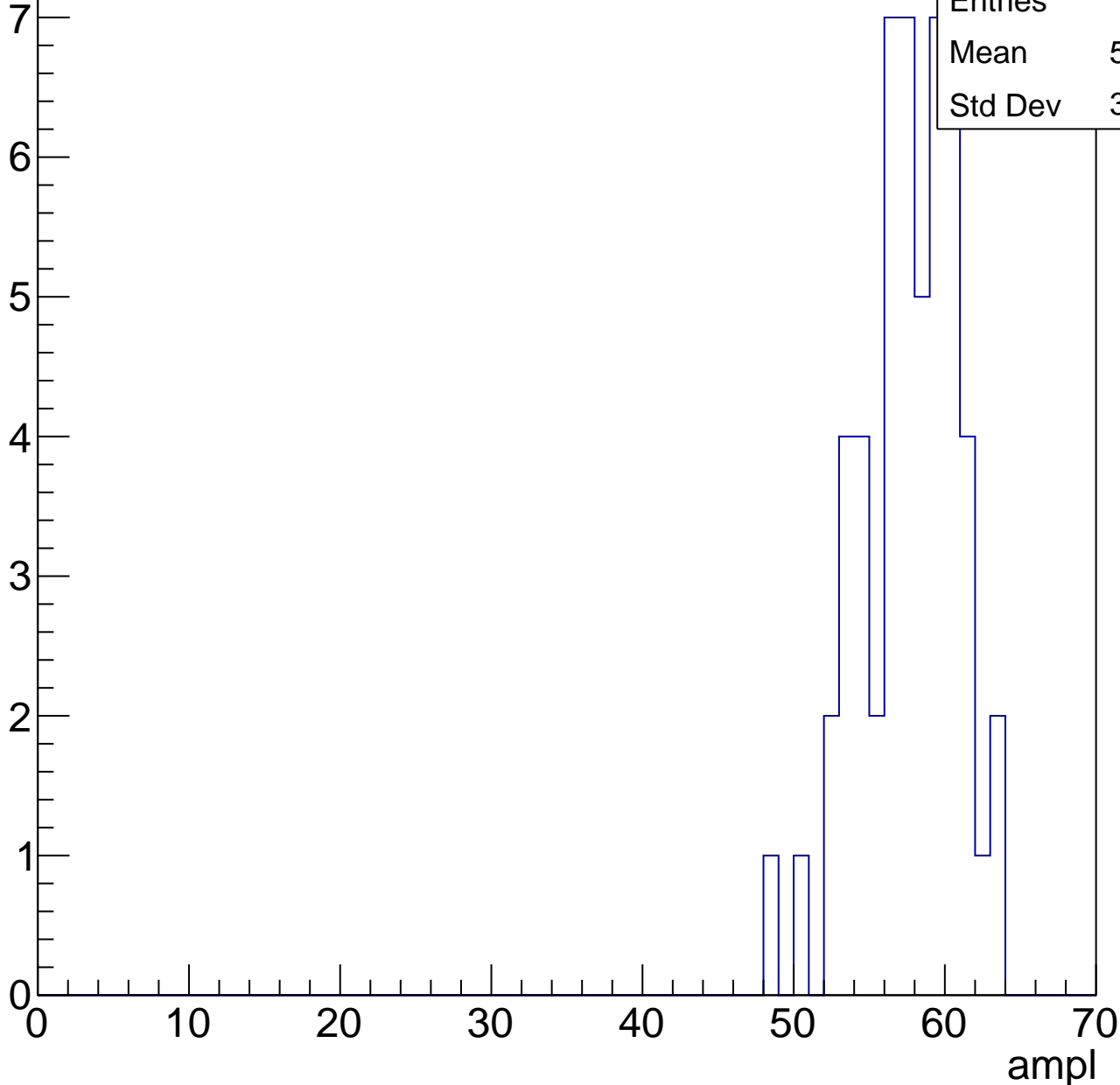


B1L103S, U10-ch7, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	57.15
Std Dev	3.194

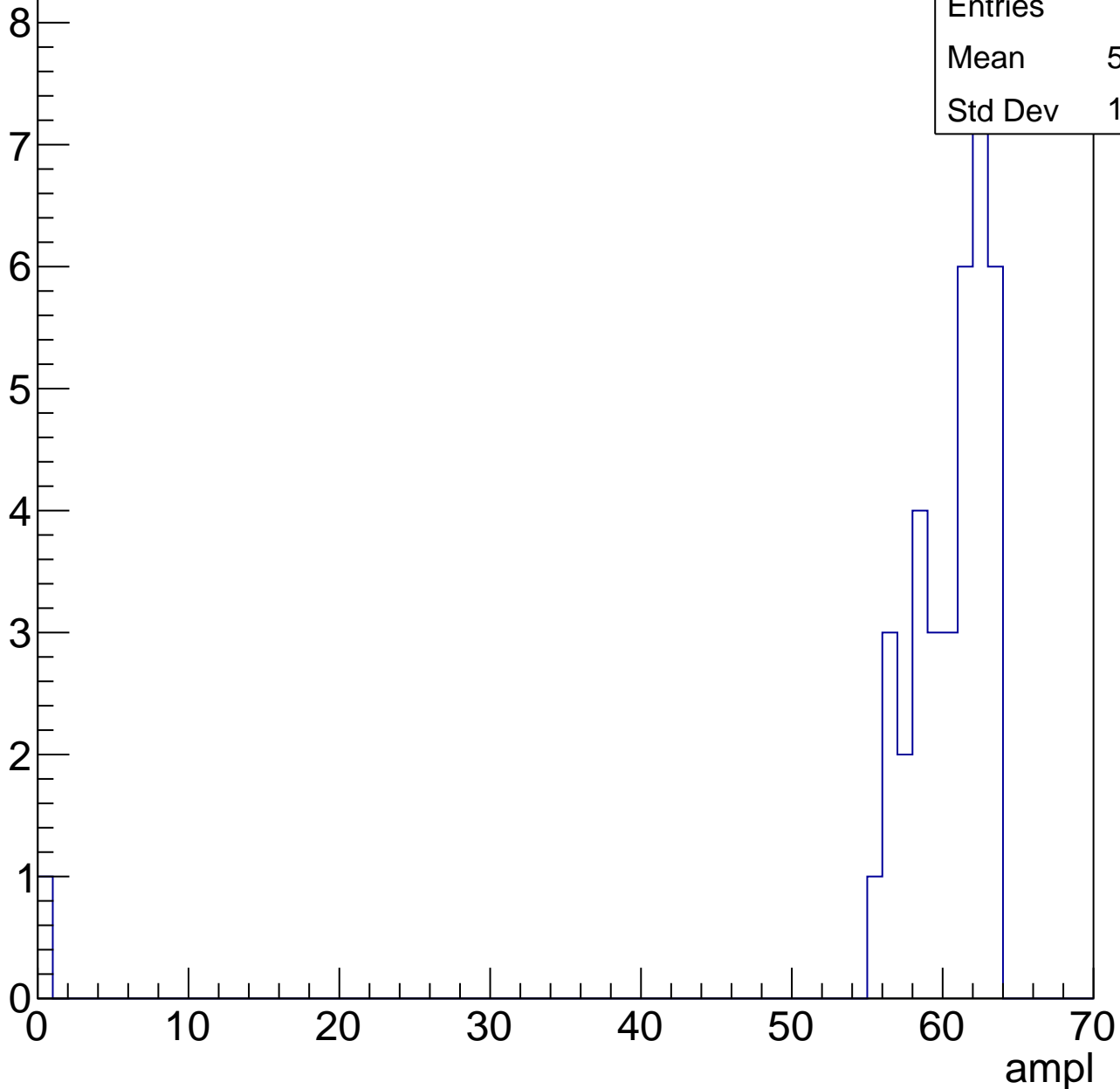


B1L103S, U10-ch7, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.54
Std Dev	10.03



B1L103S, U10-ch7, adc6

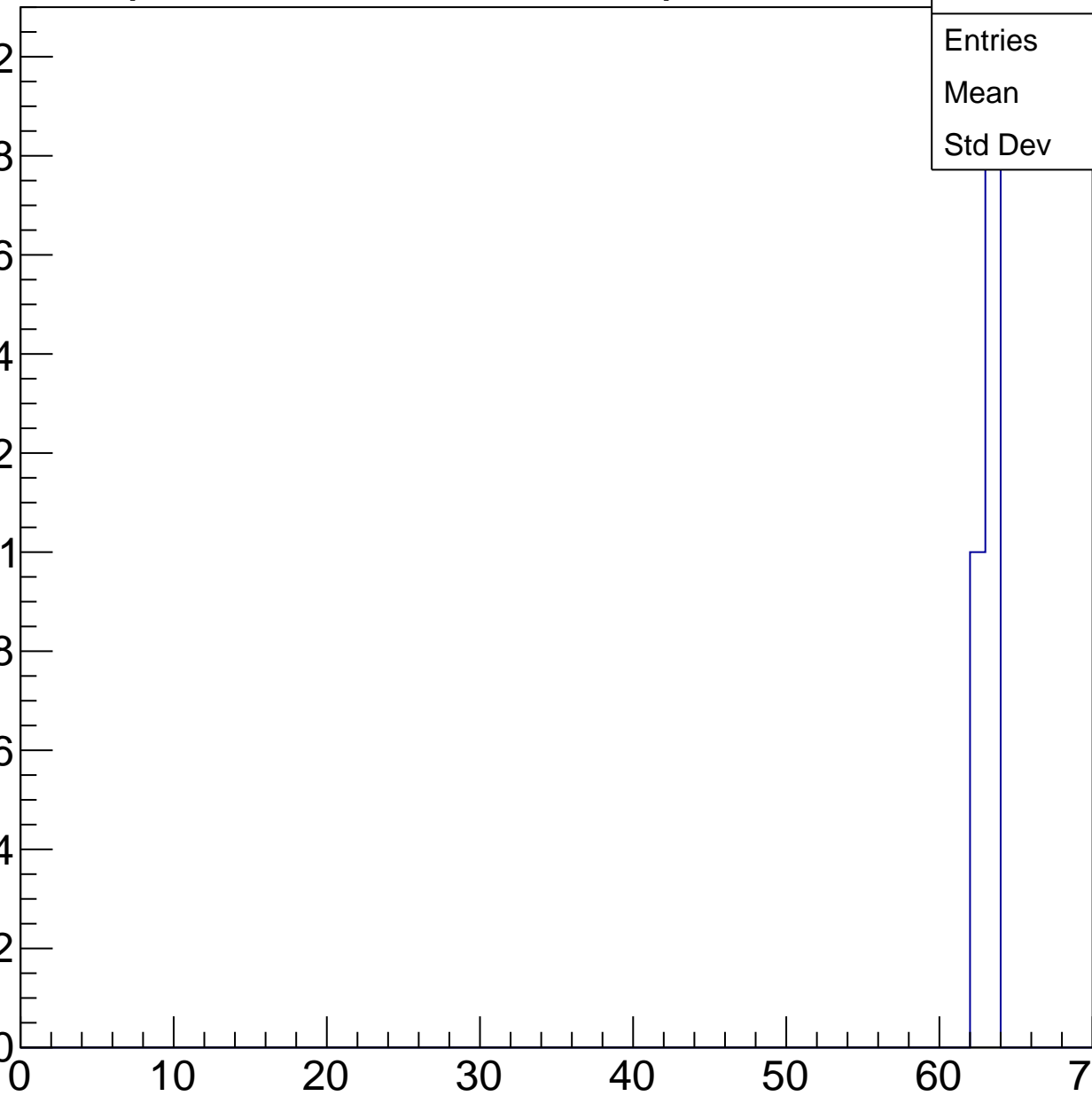
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B1L103S, U10-ch7, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch8, adc0

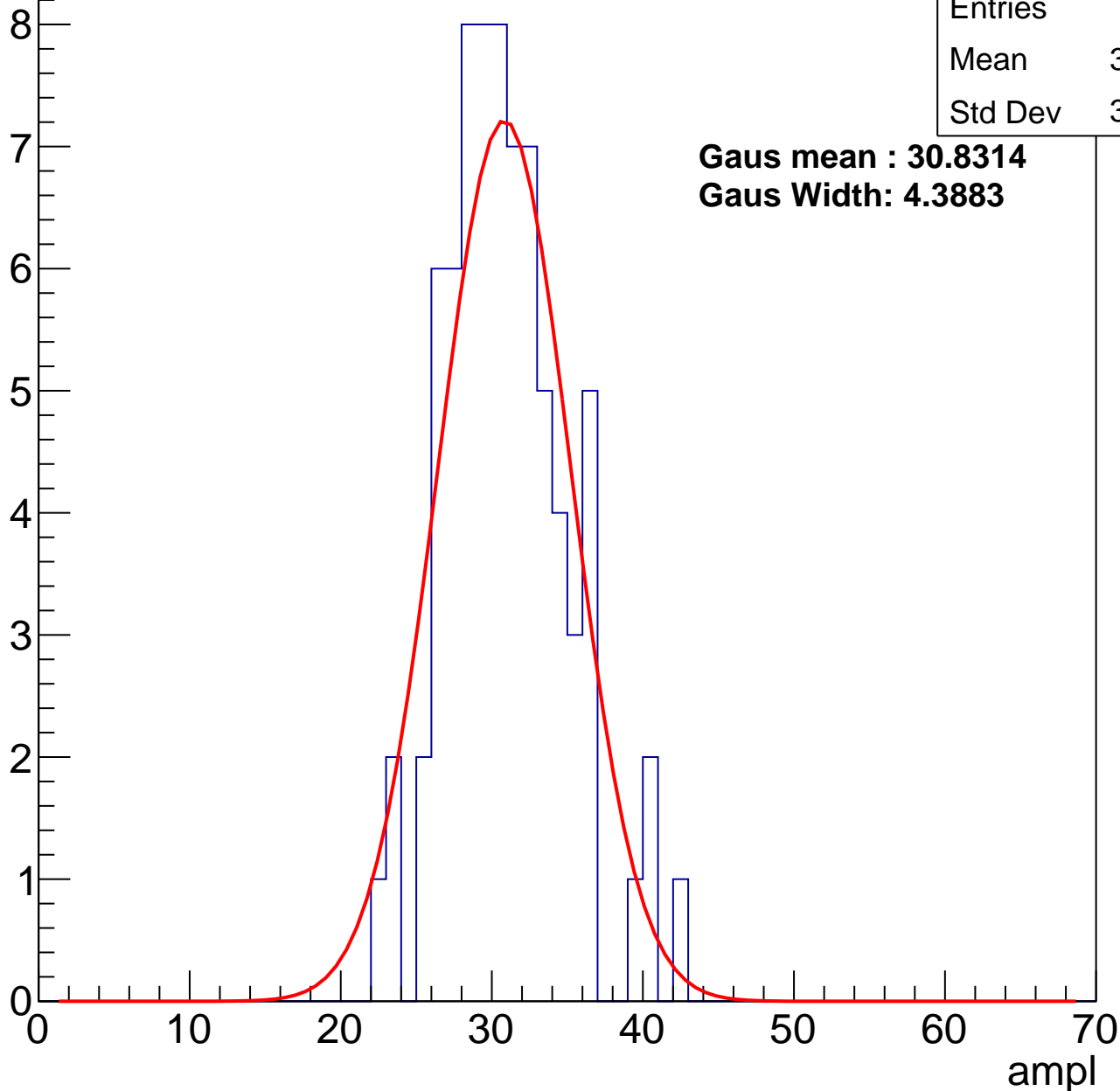
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	30.53
Std Dev	3.982

Gaus mean : 30.8314

Gaus Width: 4.3883



B1L103S, U10-ch8, adc1

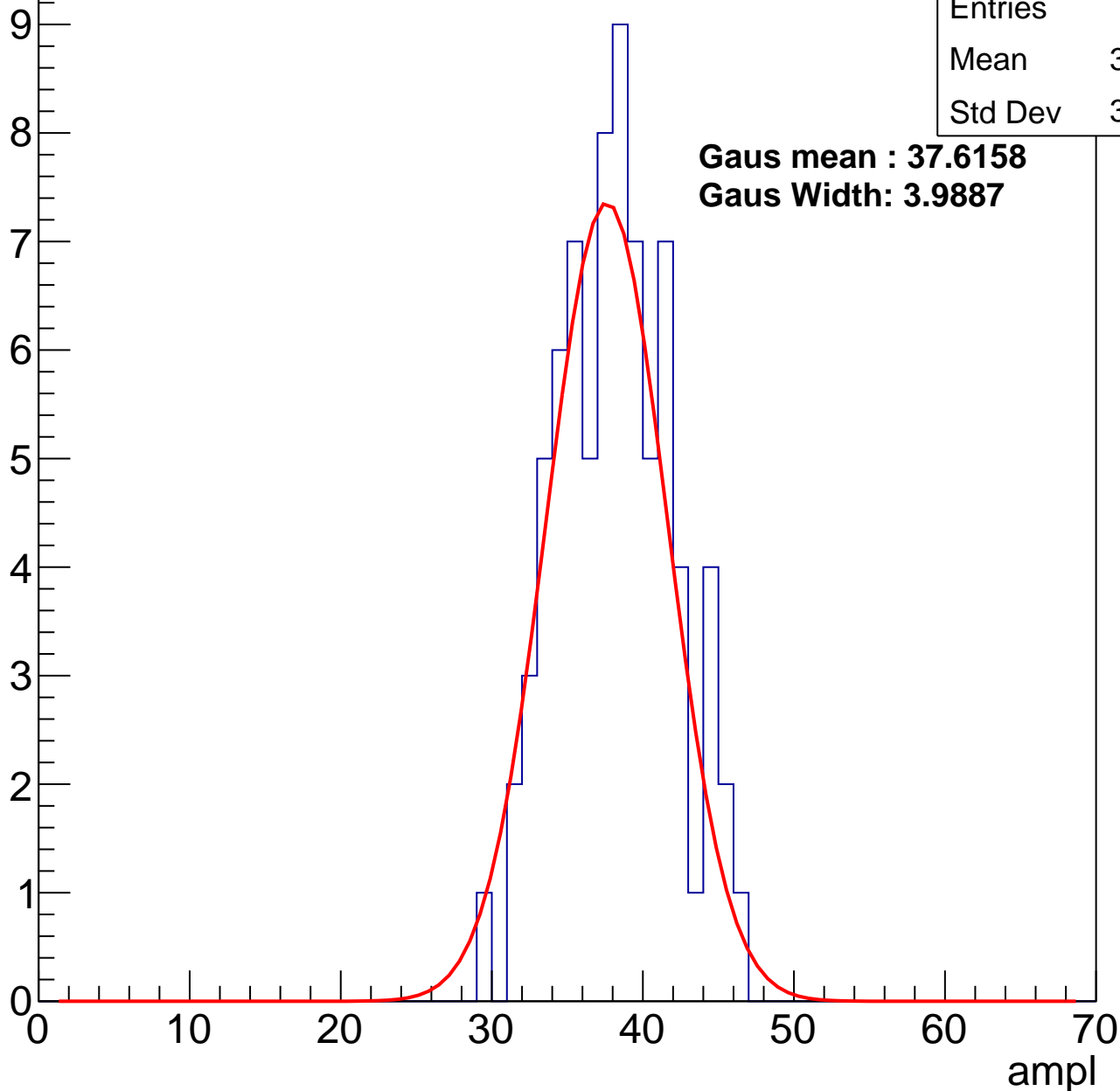
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	37.69
Std Dev	3.739

Gaus mean : 37.6158

Gaus Width: 3.9887



B1L103S, U10-ch8, adc2

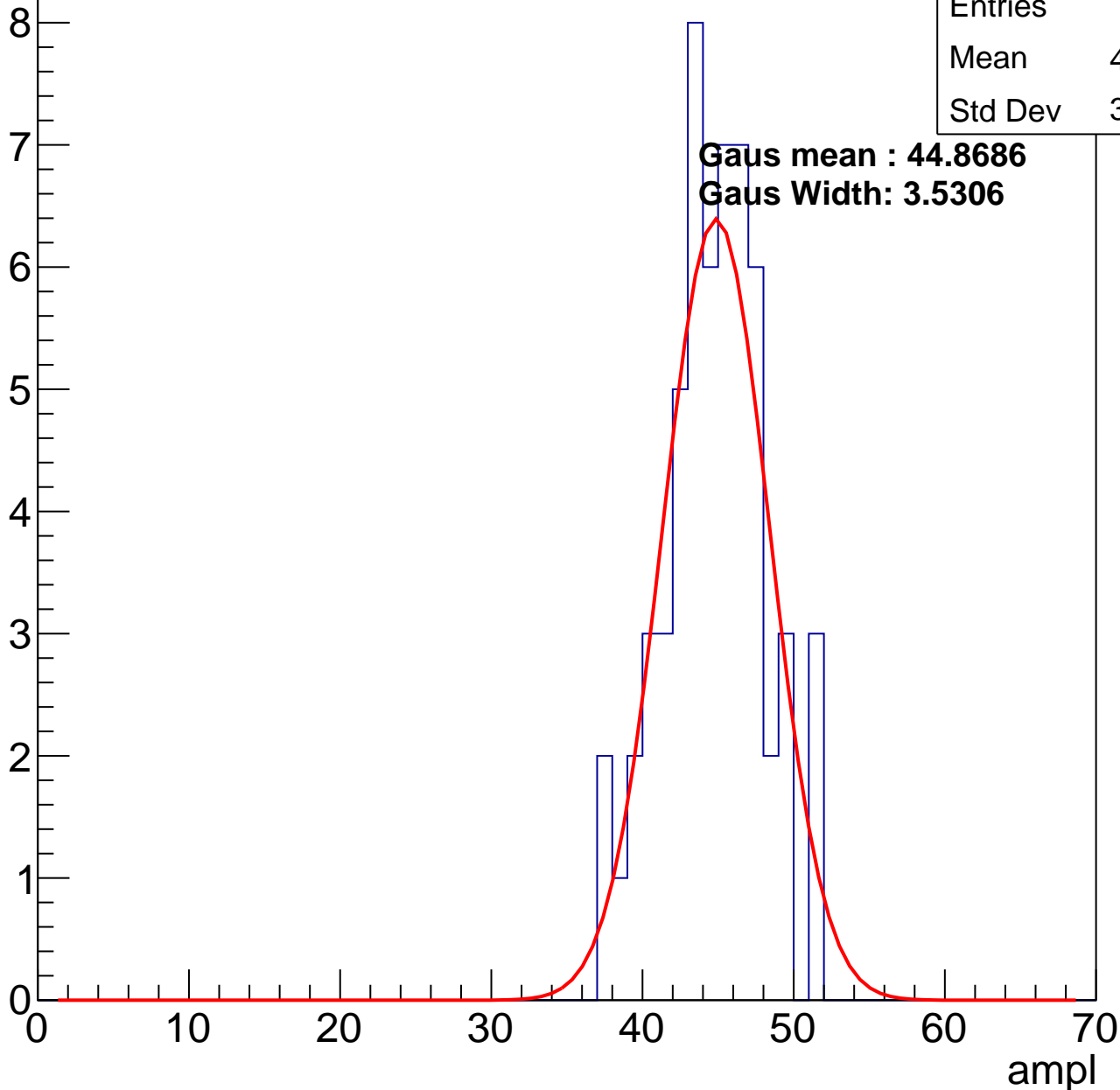
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	44.24
Std Dev	3.287

Gaus mean : 44.8686

Gaus Width: 3.5306

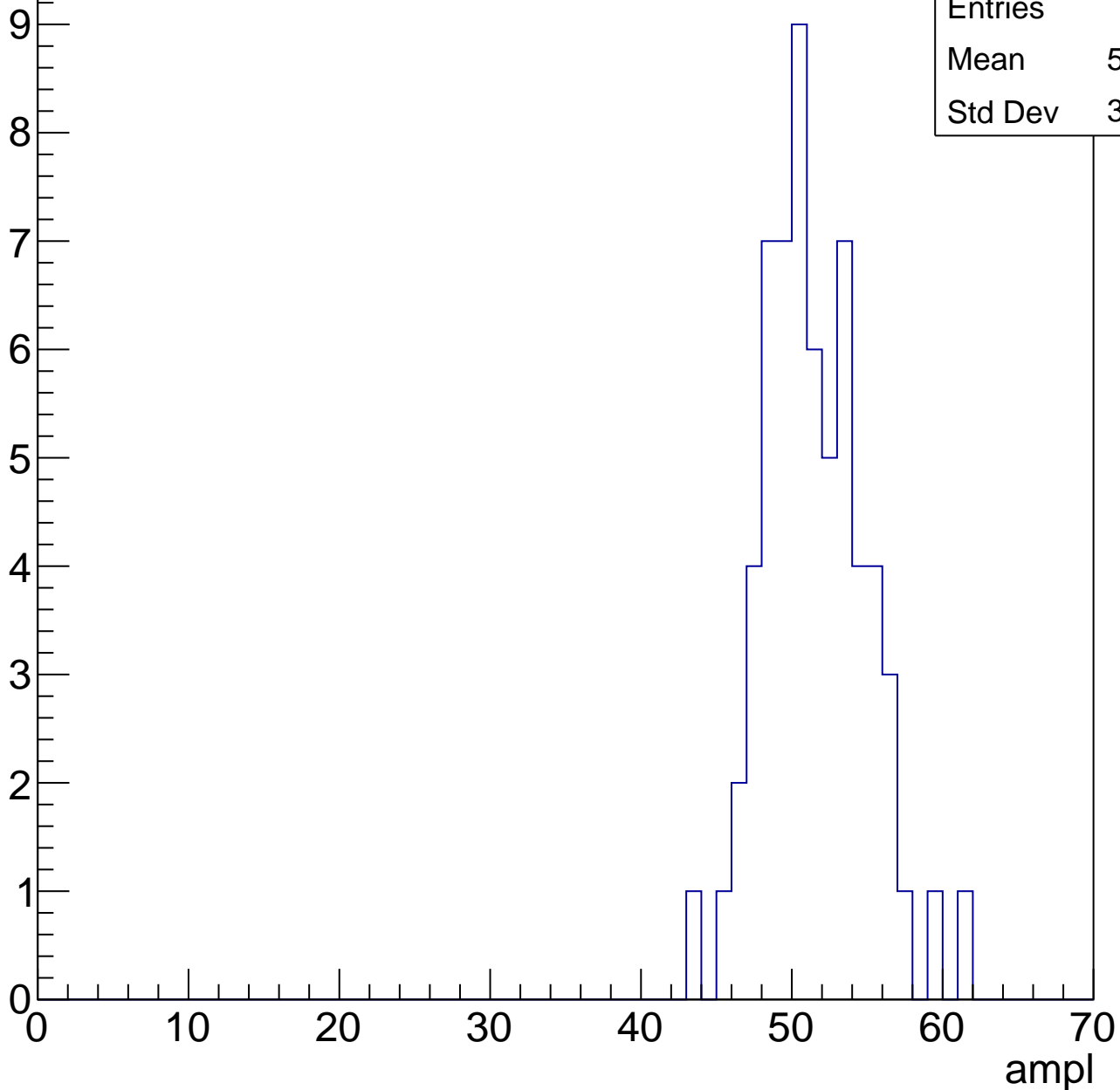


B1L103S, U10-ch8, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	51.03
Std Dev	3.385

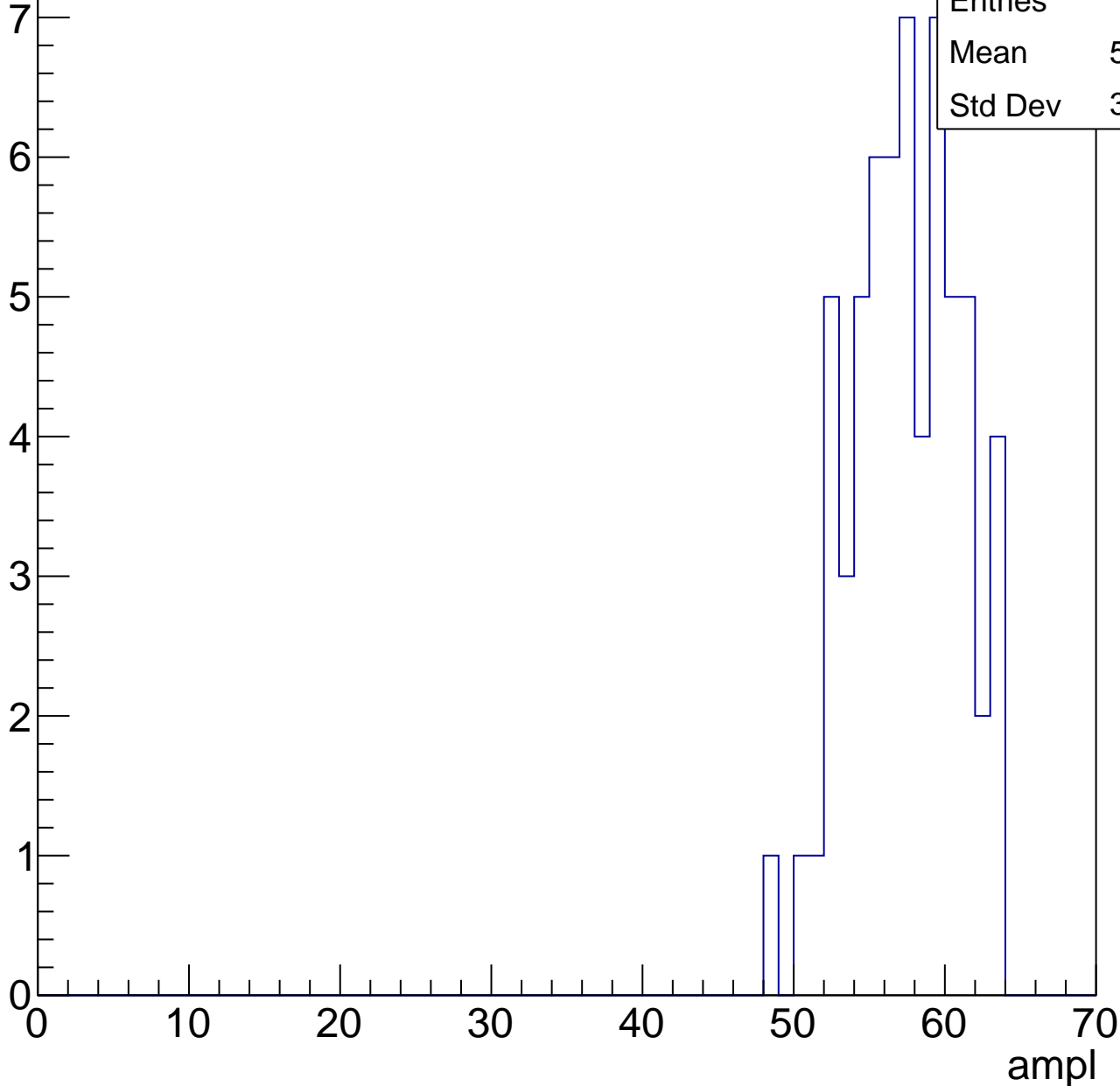


B1L103S, U10-ch8, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	56.92
Std Dev	3.512

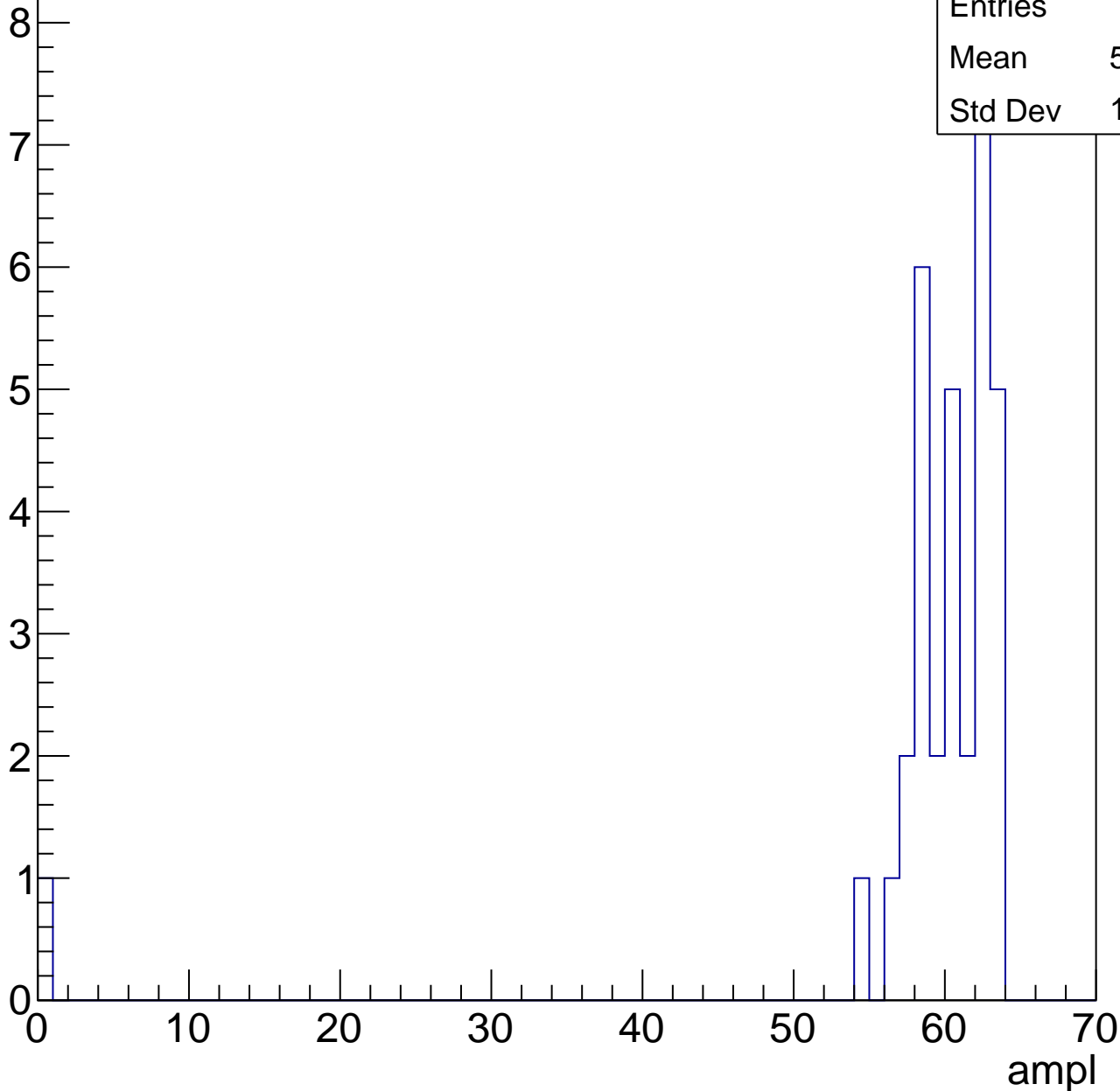


B1L103S, U10-ch8, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.27
Std Dev	10.56



B1L103S, U10-ch8, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch8, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch9, adc0

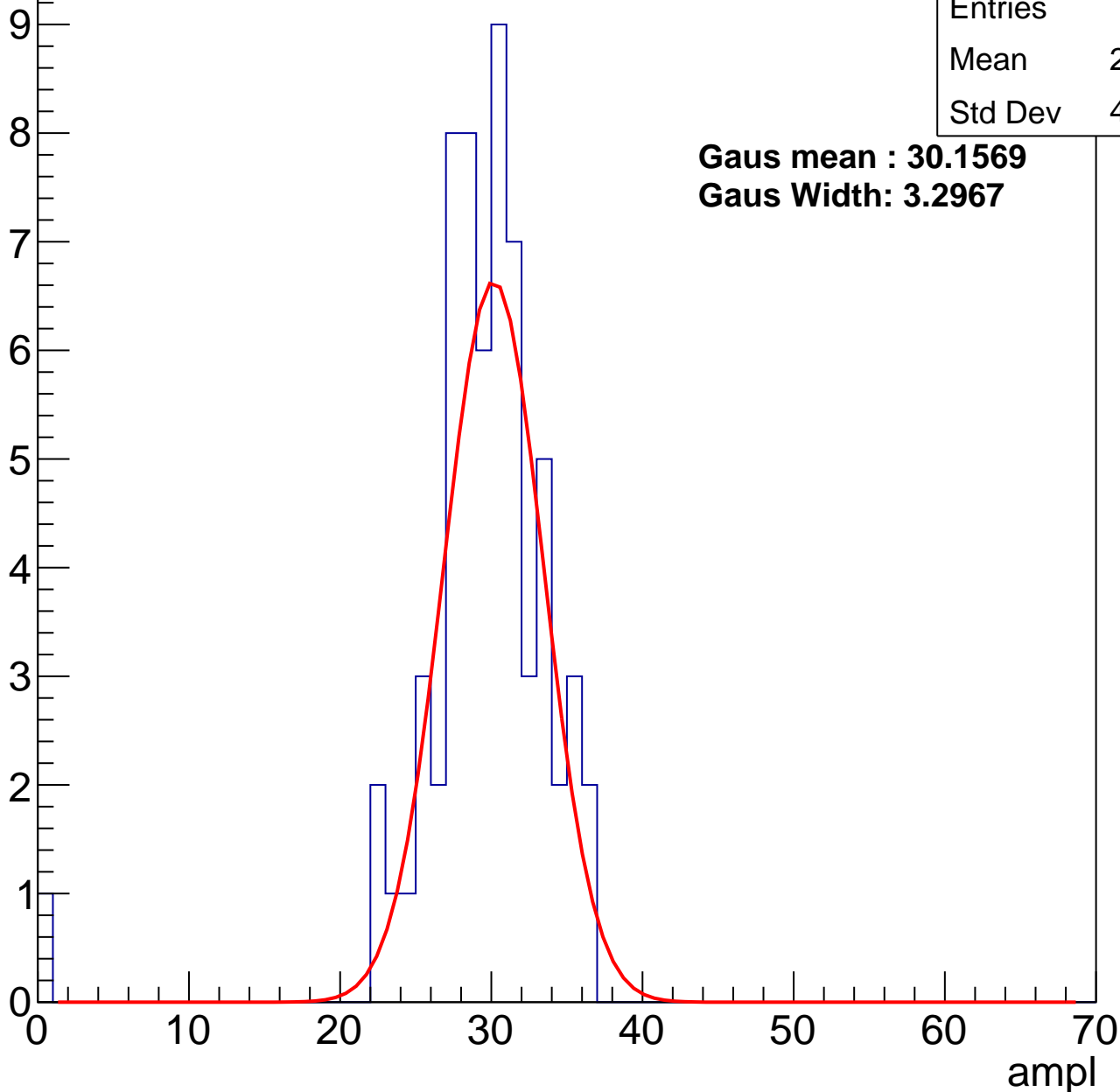
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	28.97
Std Dev	4.889

Gaus mean : 30.1569

Gaus Width: 3.2967



B1L103S, U10-ch9, adc1

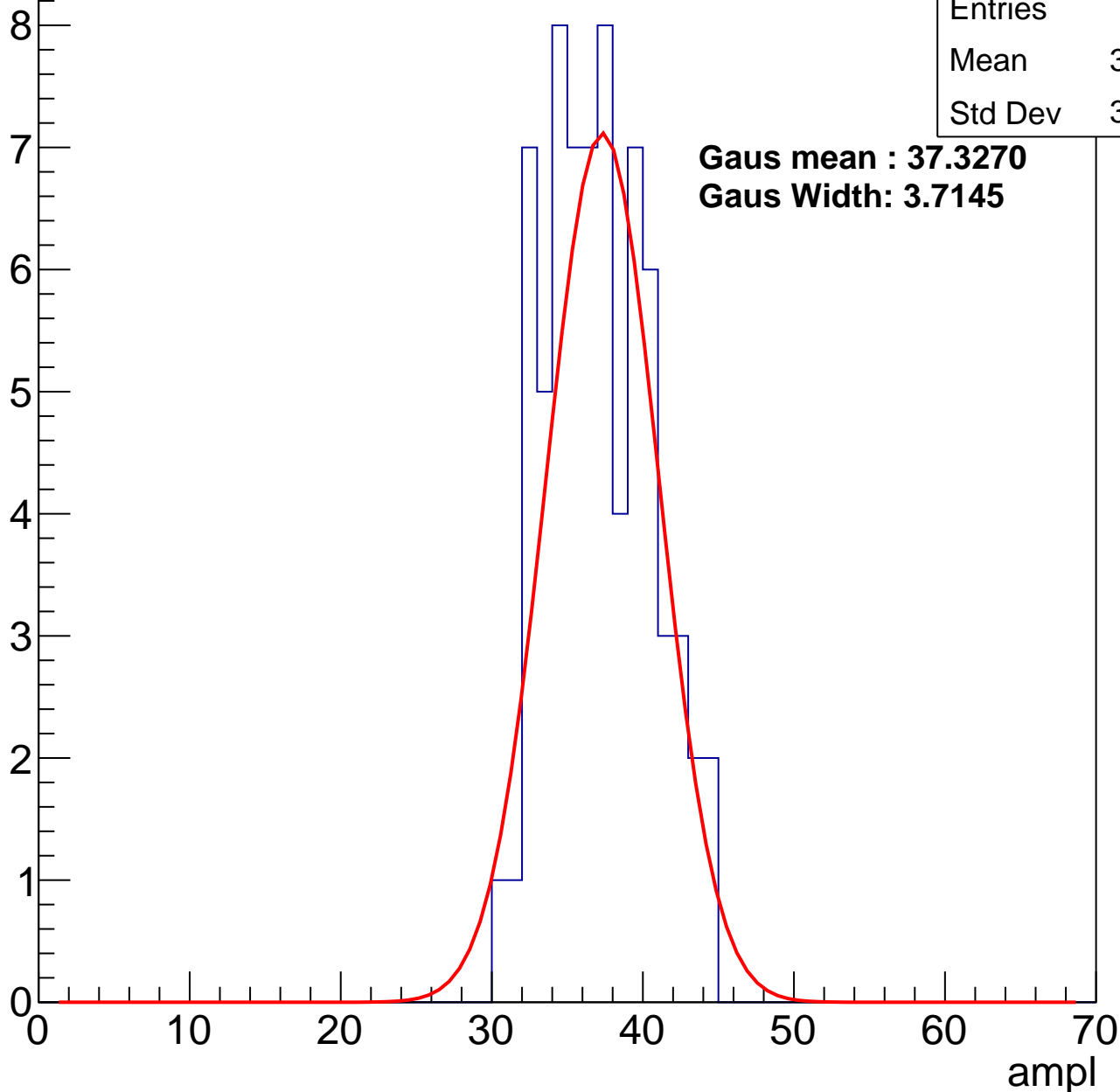
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.66
Std Dev	3.394

Gaus mean : 37.3270

Gaus Width: 3.7145



B1L103S, U10-ch9, adc2

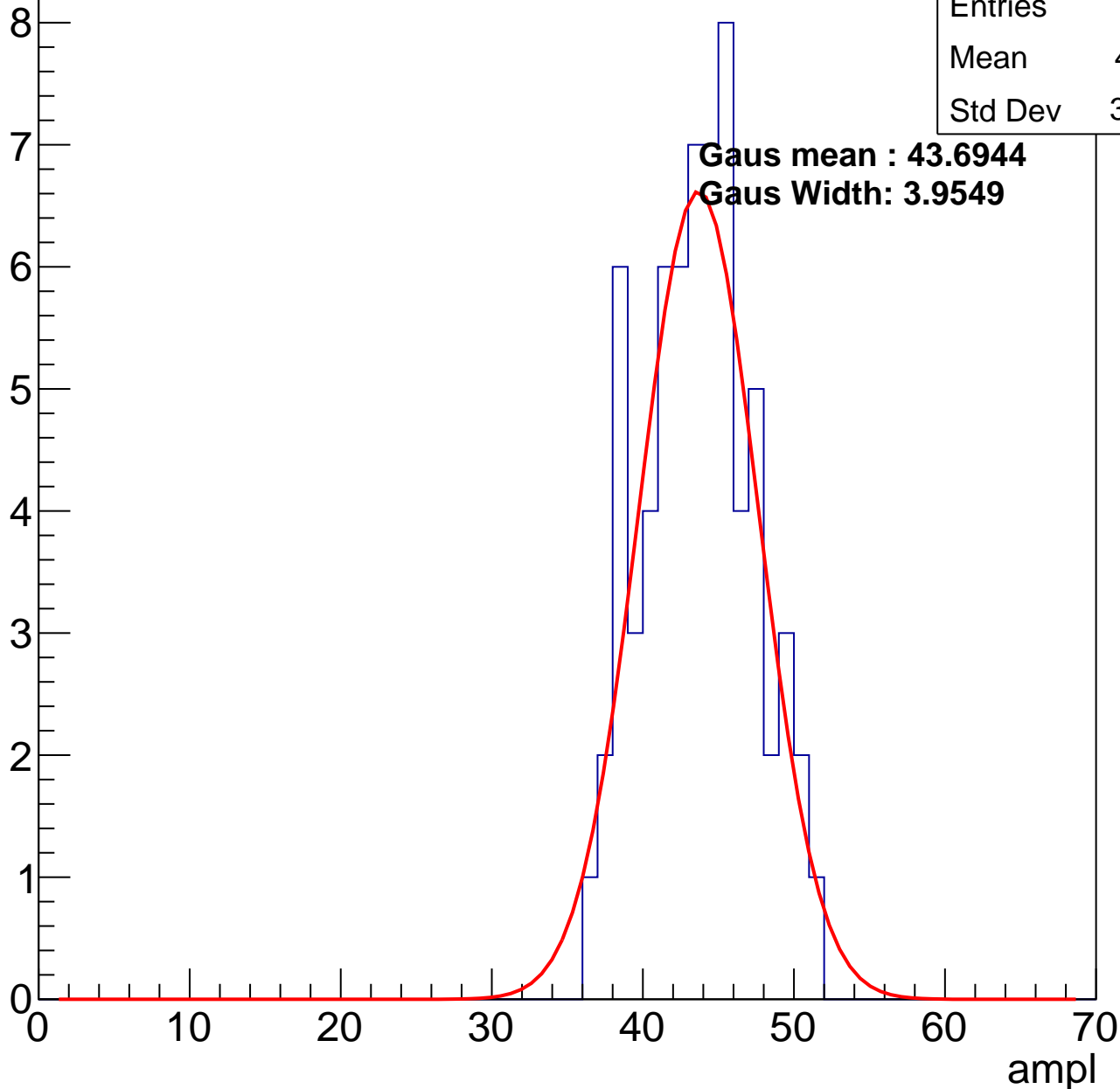
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.21
Std Dev	3.577

Gaus mean : 43.6944

Gaus Width: 3.9549

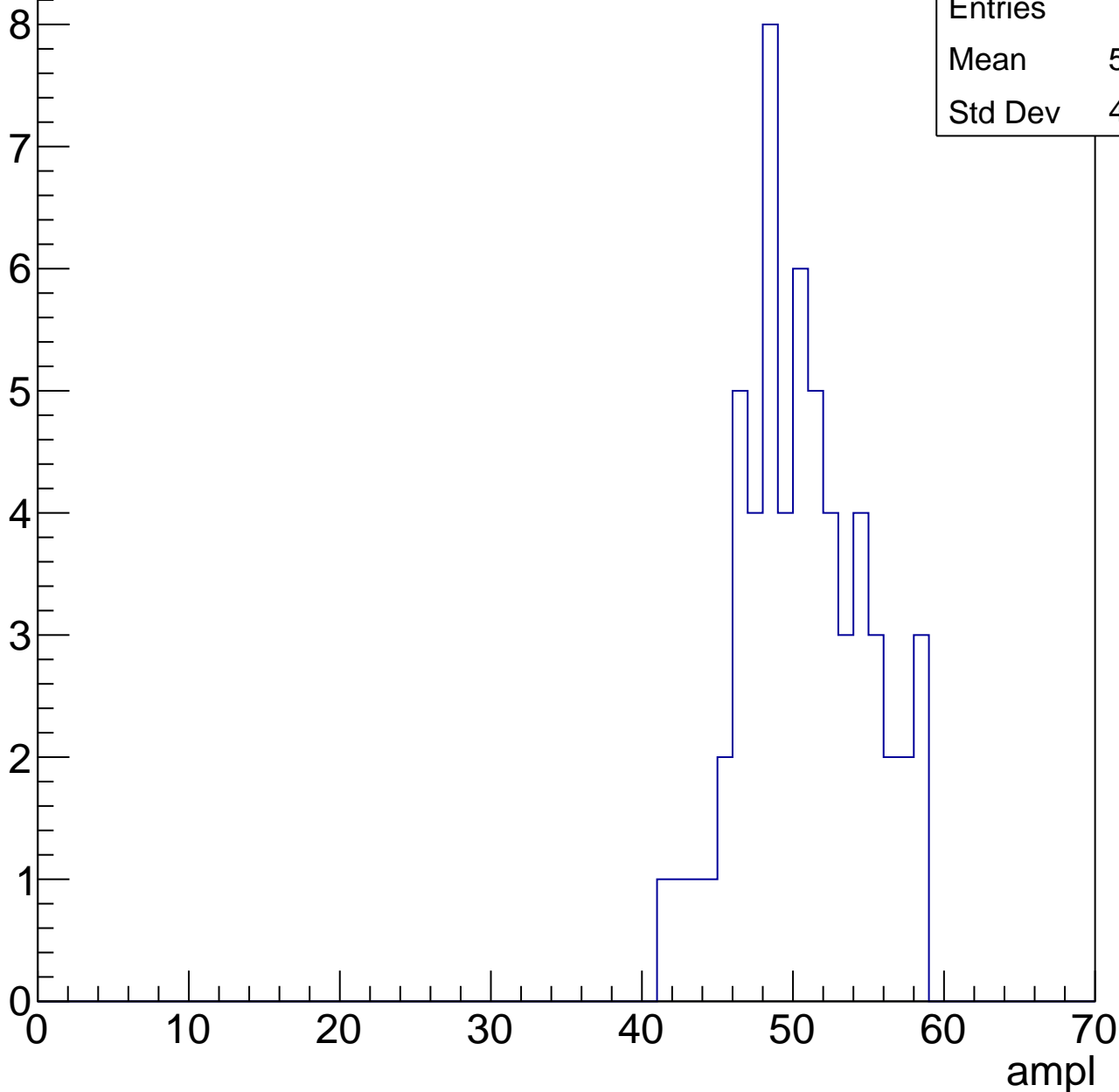


B1L103S, U10-ch9, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.19
Std Dev	4.073

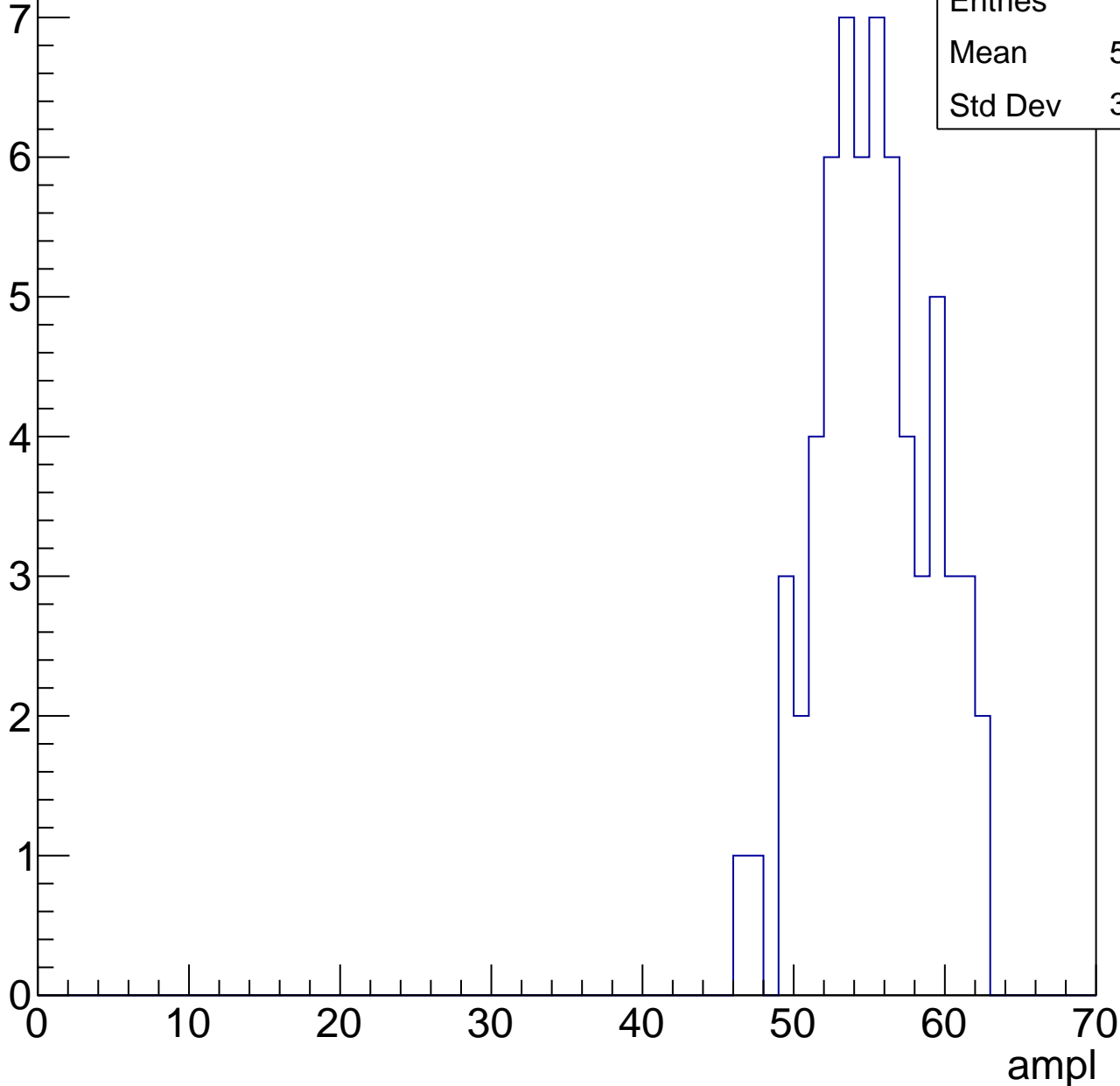


B1L103S, U10-ch9, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	54.86
Std Dev	3.703

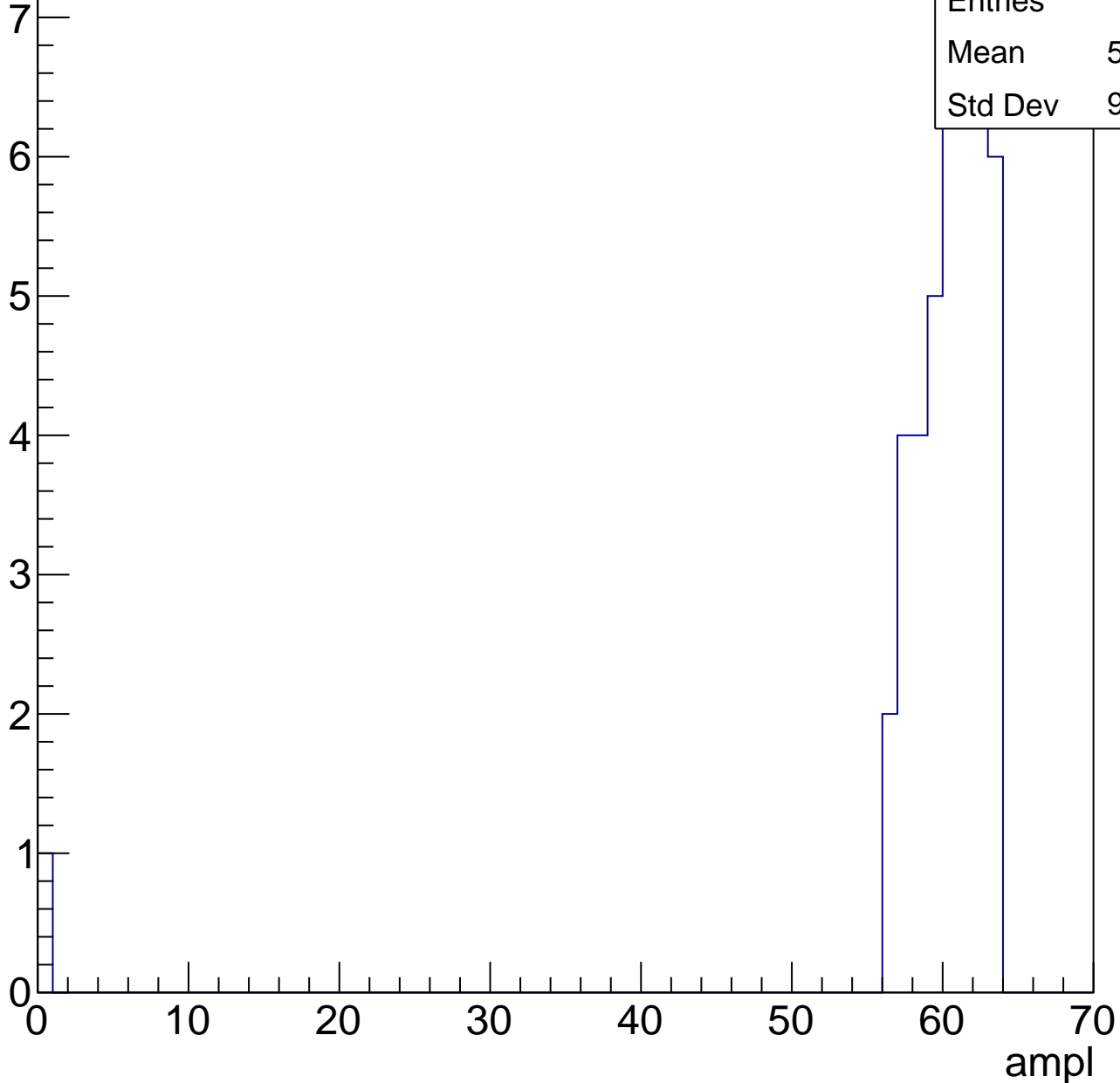


B1L103S, U10-ch9, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

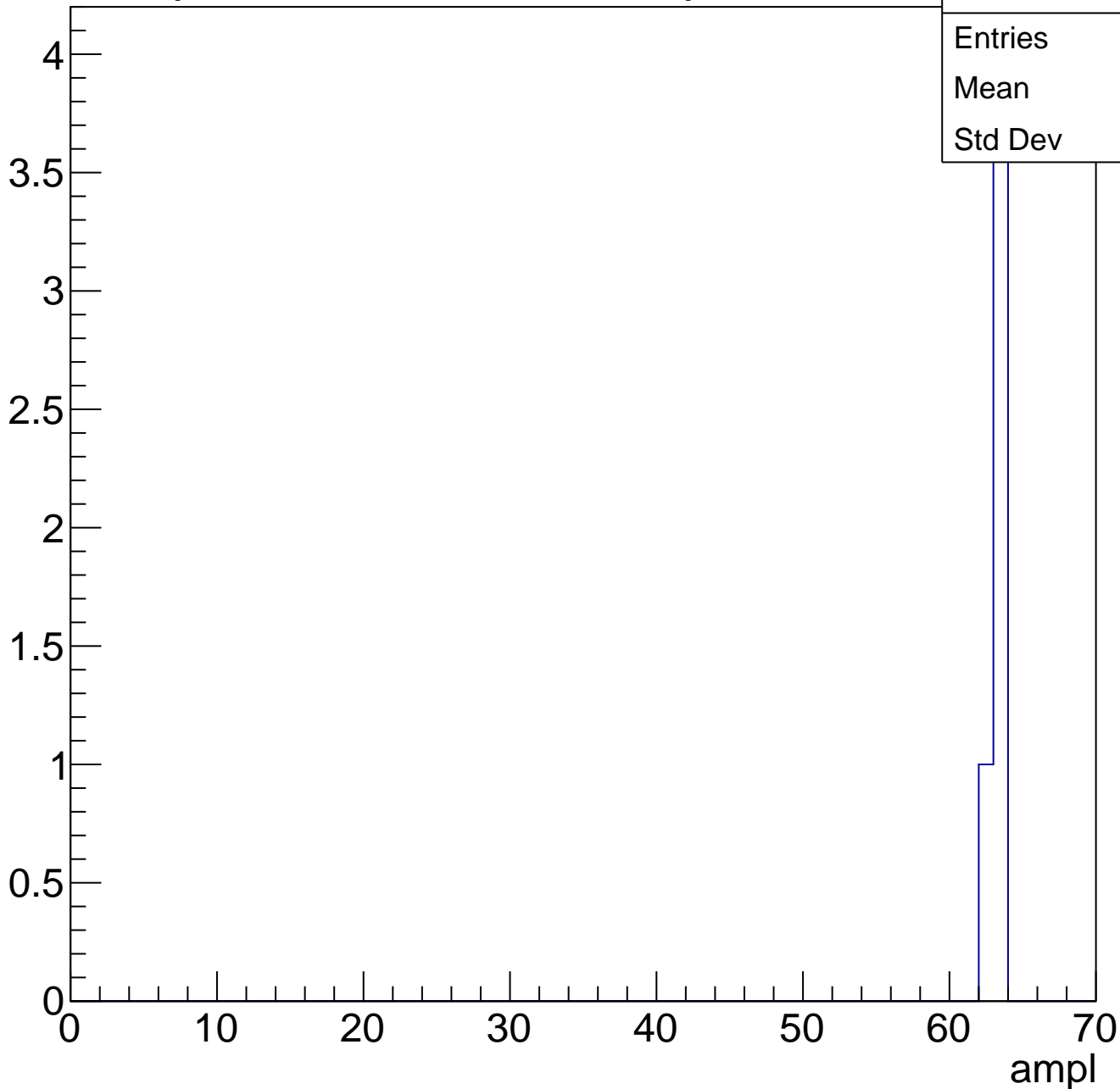
Entries	43
Mean	58.74
Std Dev	9.289



B1L103S, U10-ch9, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch9, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U10-ch10, adc0

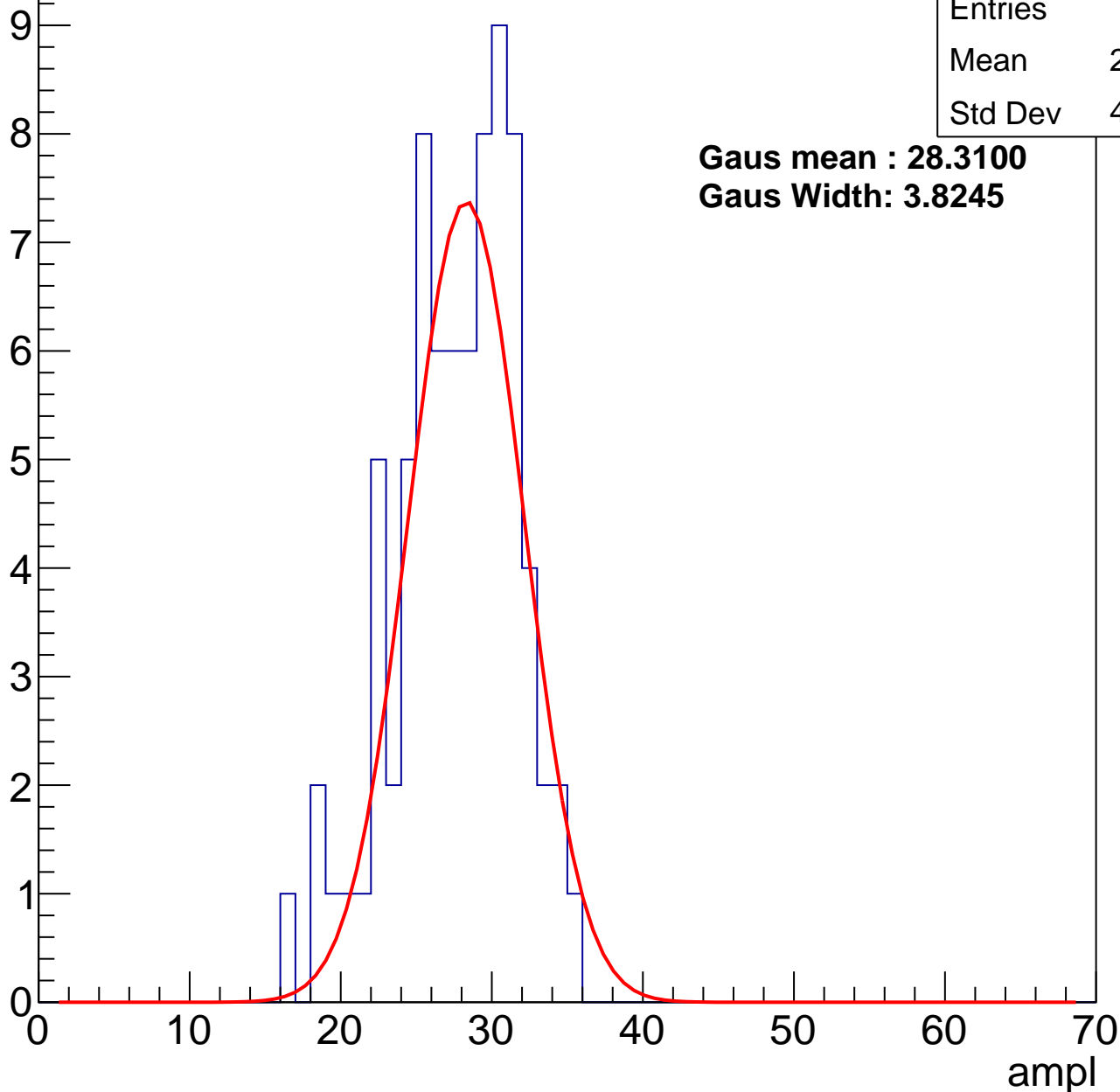
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	27.19
Std Dev	4.003

Gaus mean : 28.3100

Gaus Width: 3.8245



B1L103S, U10-ch10, adc1

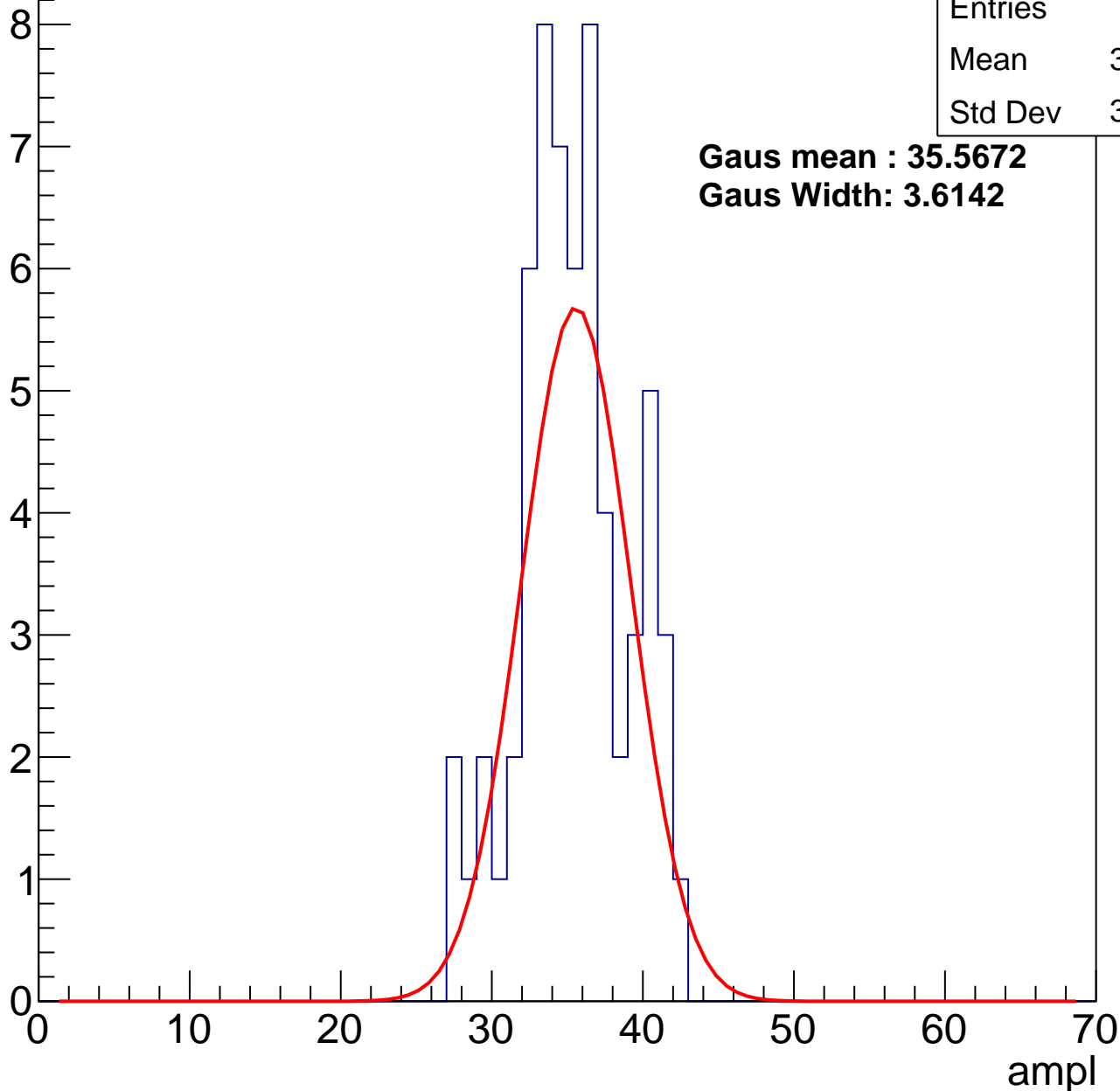
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	34.92
Std Dev	3.554

Gaus mean : 35.5672

Gaus Width: 3.6142



B1L103S, U10-ch10, adc2

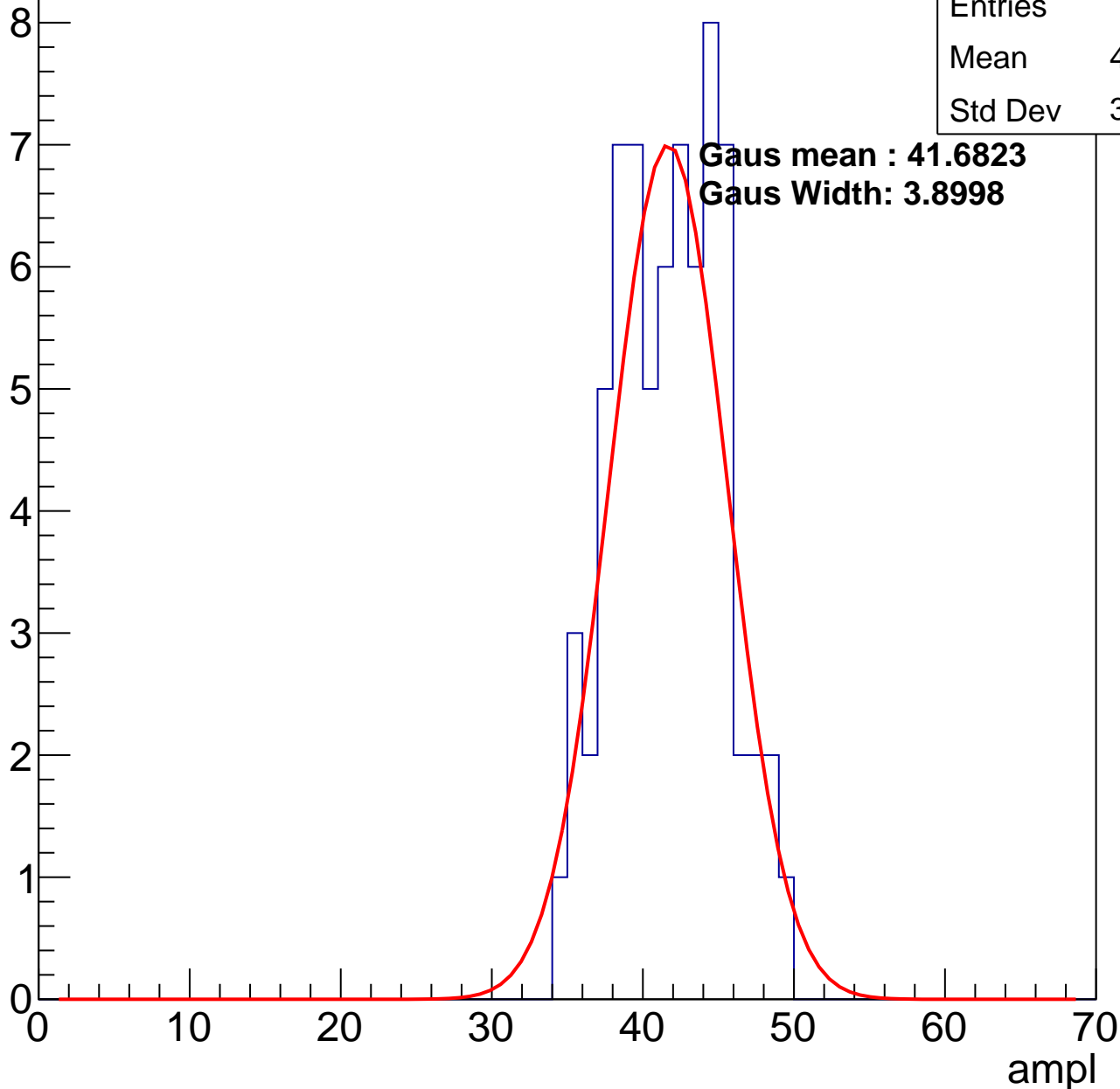
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	41.28
Std Dev	3.513

Gaus mean : 41.6823

Gaus Width: 3.8998

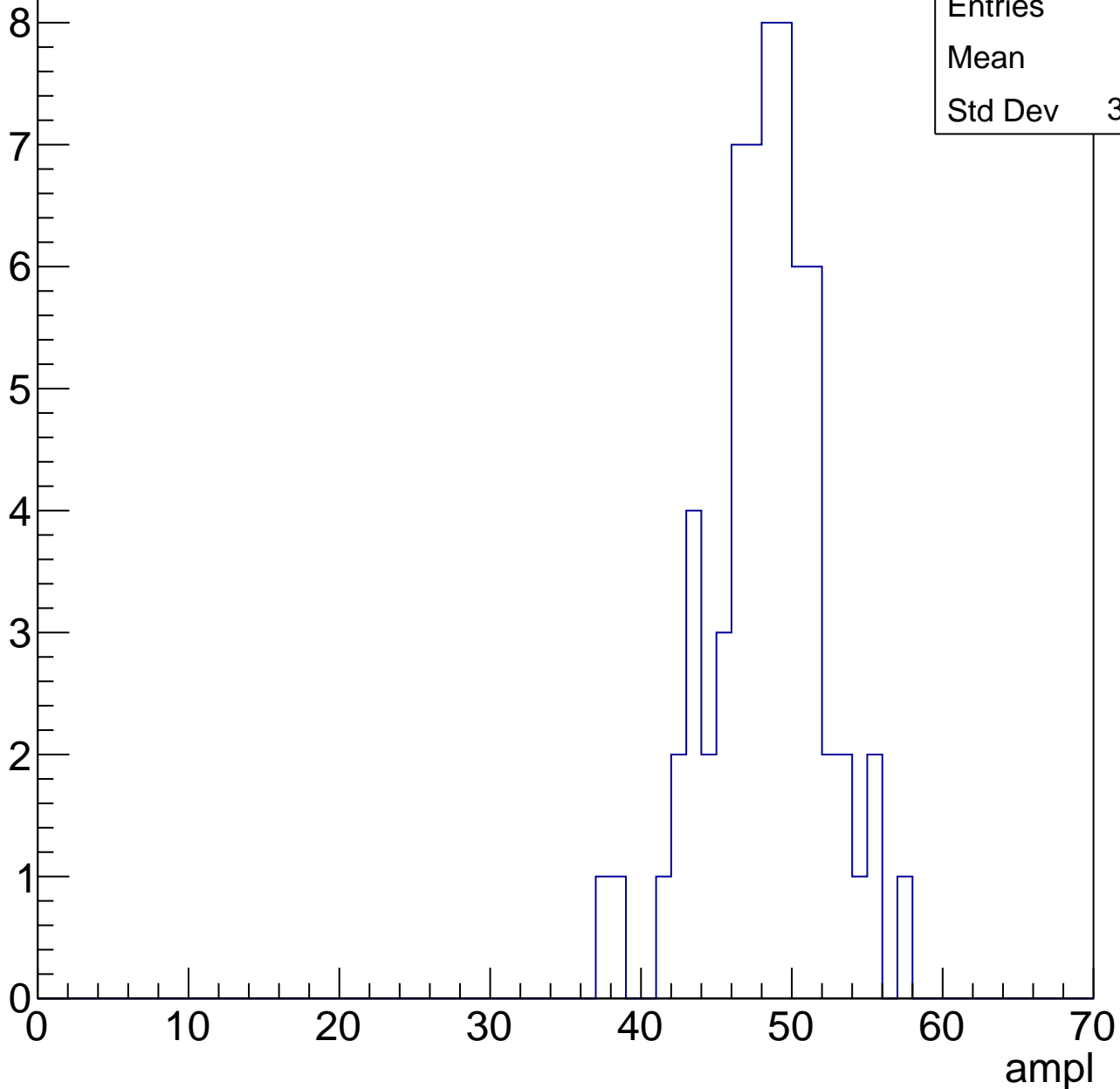


B1L103S, U10-ch10, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	47.8
Std Dev	3.784

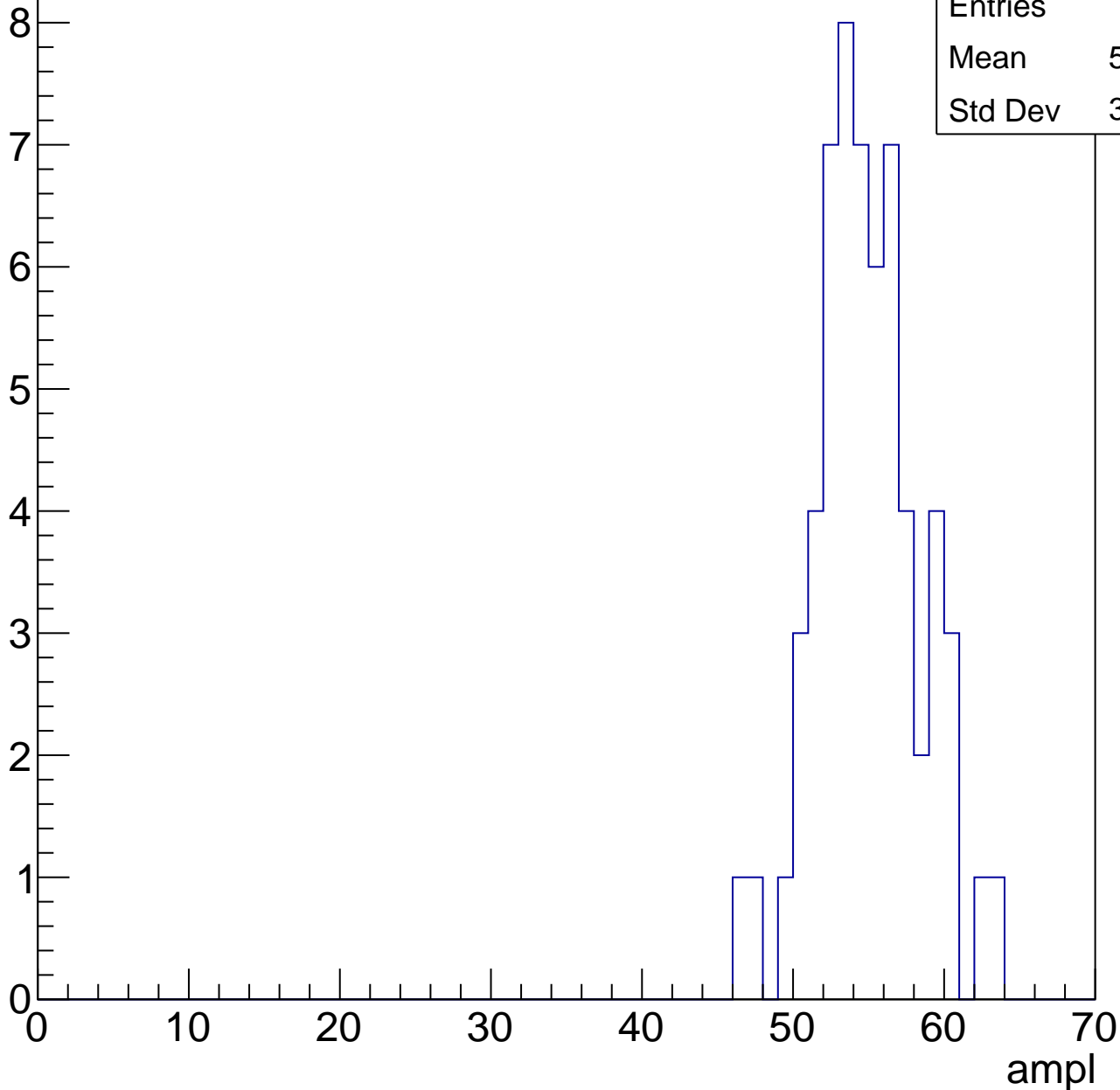


B1L103S, U10-ch10, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	54.48
Std Dev	3.413

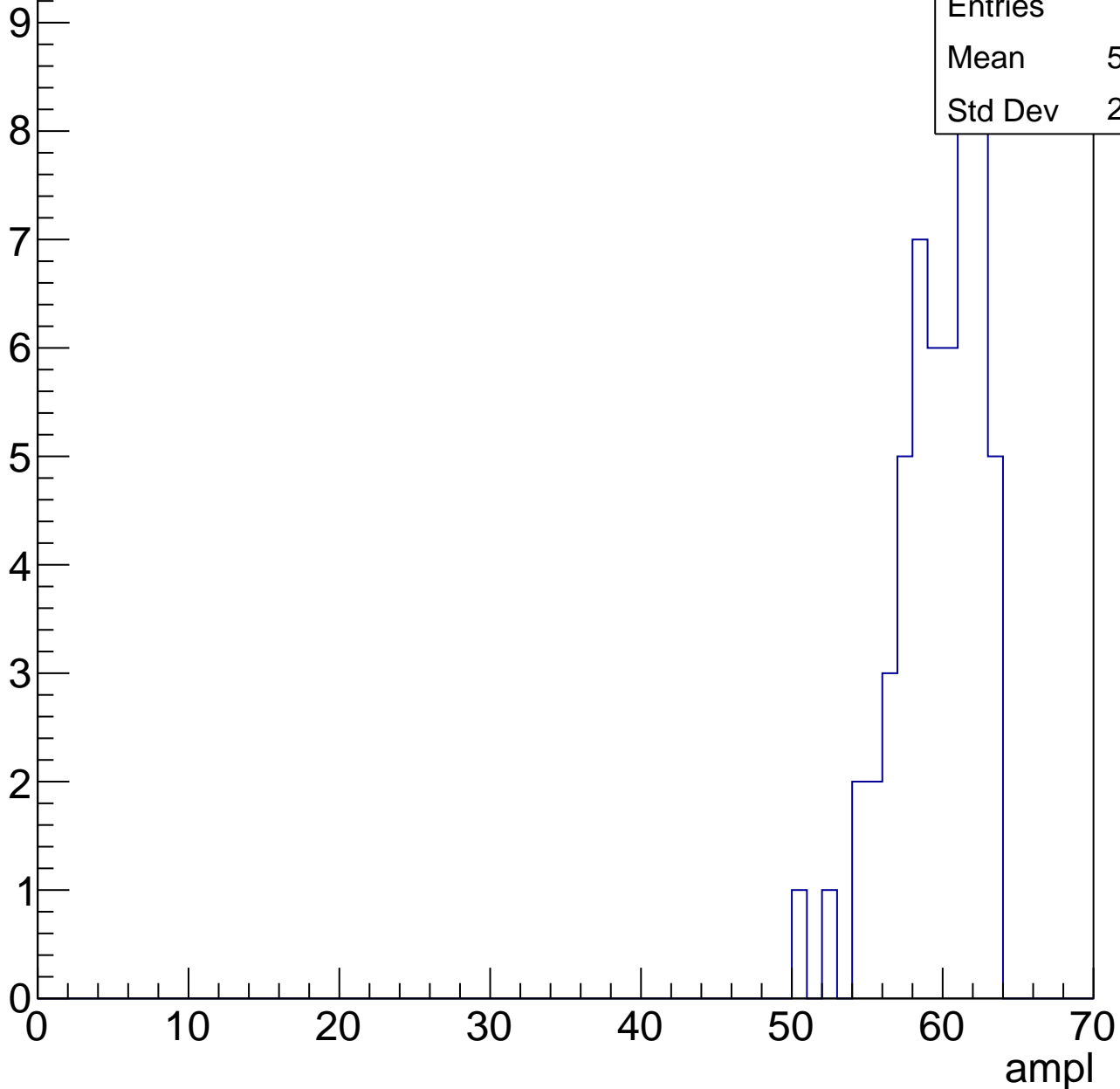


B1L103S, U10-ch10, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

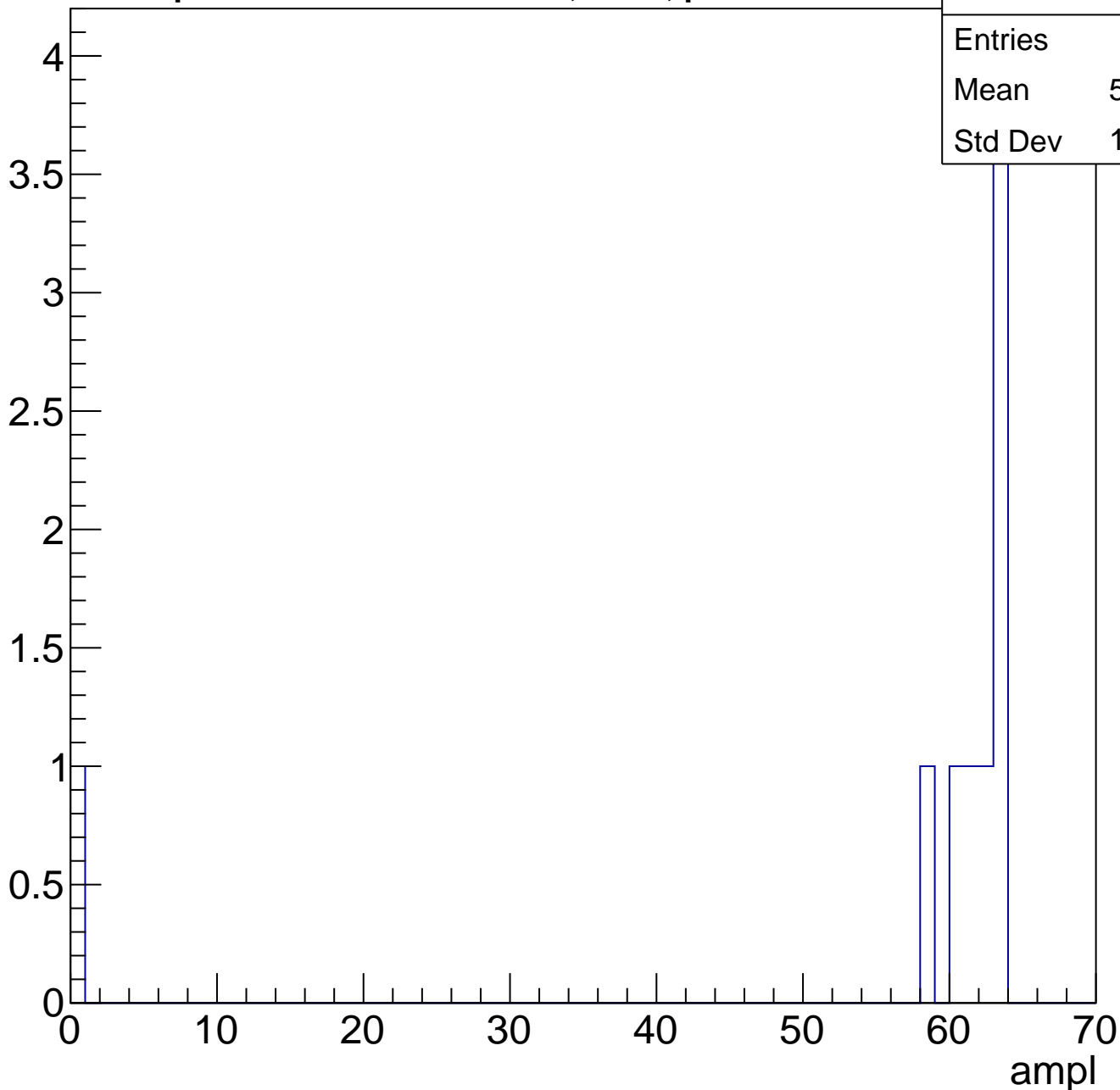
Entries	55
Mean	59.15
Std Dev	2.888



B1L103S, U10-ch10, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch10, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch11, adc0

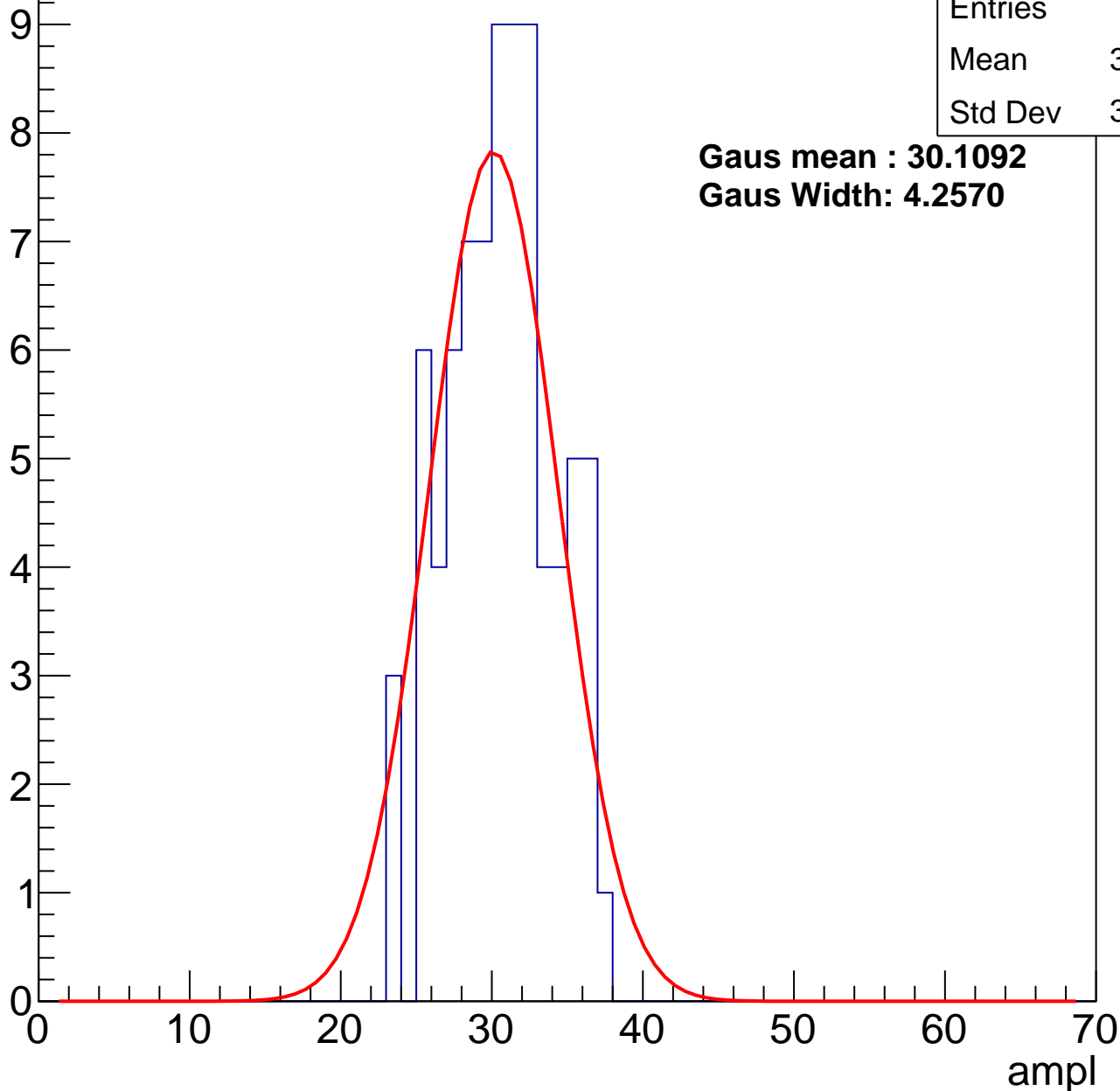
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	30.14
Std Dev	3.463

Gaus mean : 30.1092

Gaus Width: 4.2570



B1L103S, U10-ch11, adc1

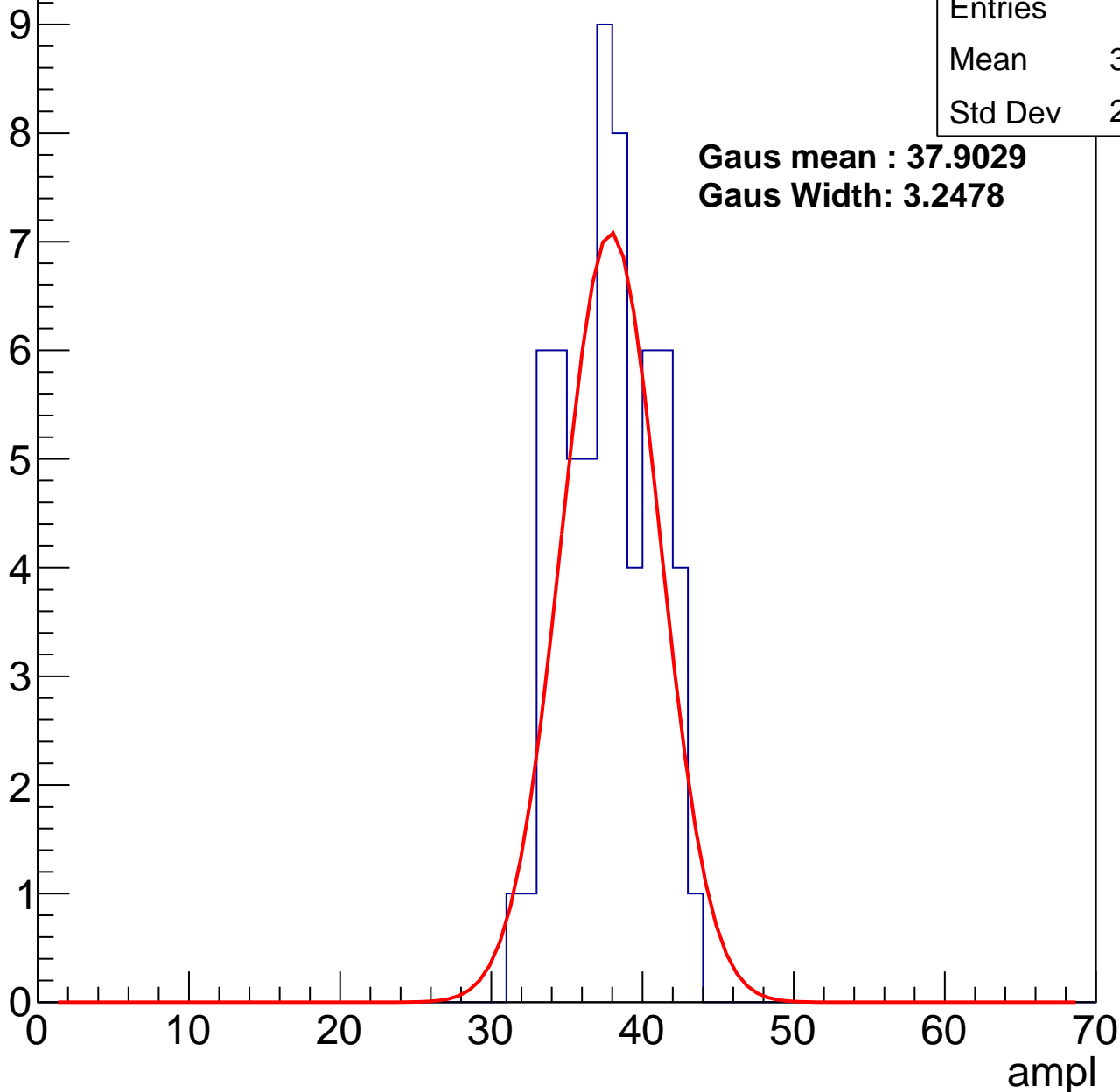
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	37.26
Std Dev	2.956

Gaus mean : 37.9029

Gaus Width: 3.2478



B1L103S, U10-ch11, adc2

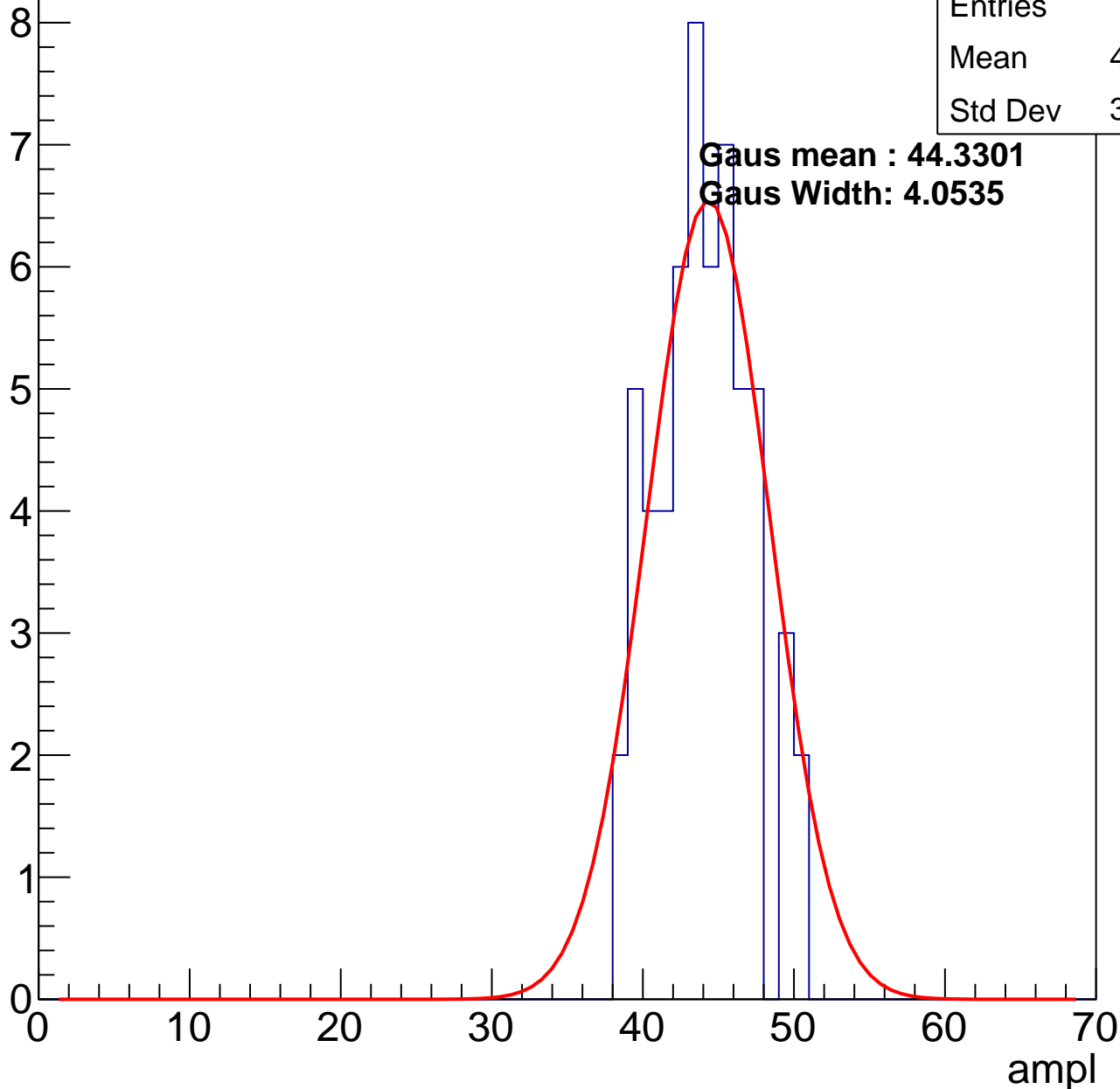
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.54
Std Dev	3.067

Gaus mean : 44.3301

Gaus Width: 4.0535

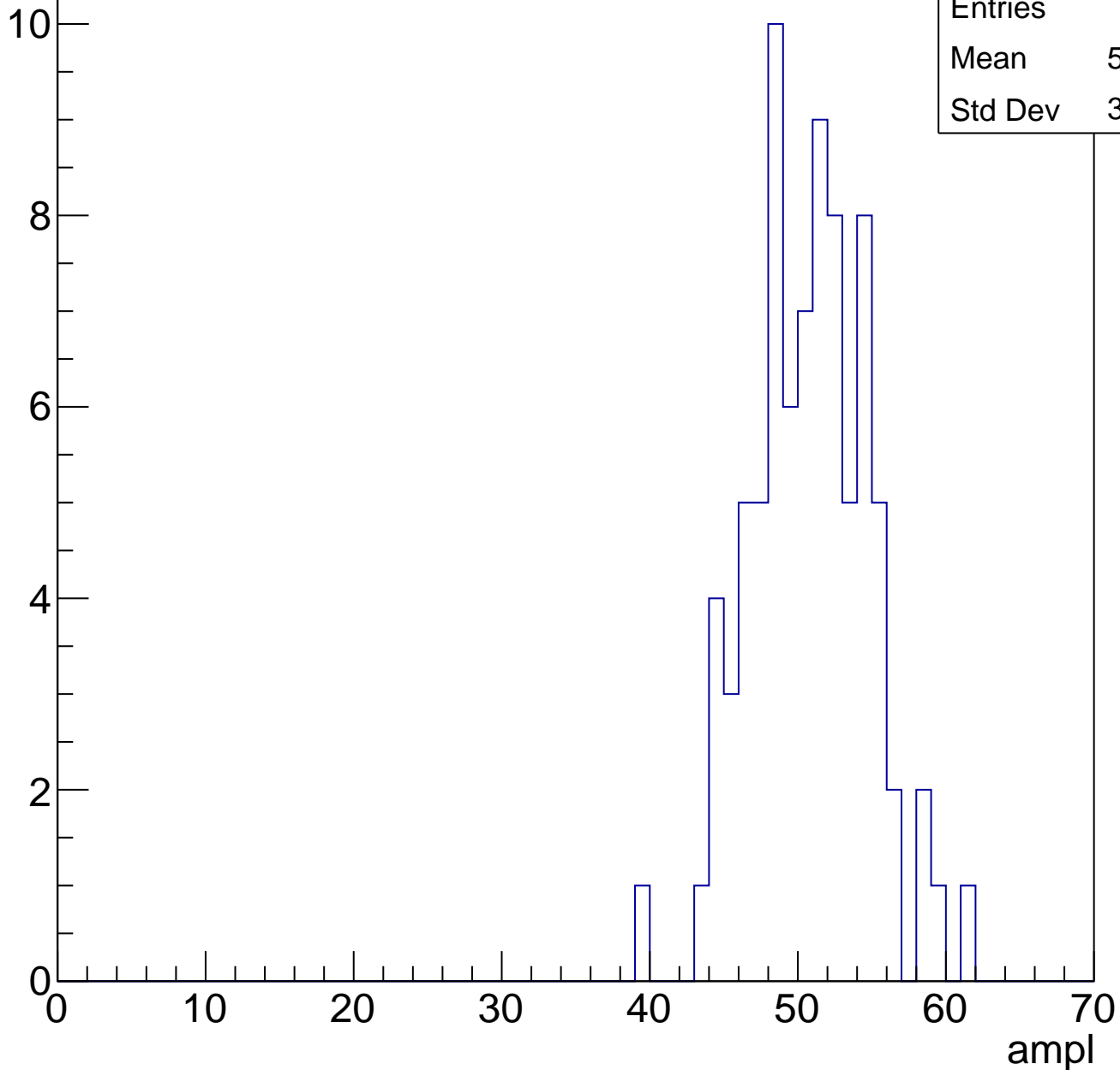


B1L103S, U10-ch11, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	50.33
Std Dev	3.946

Entry

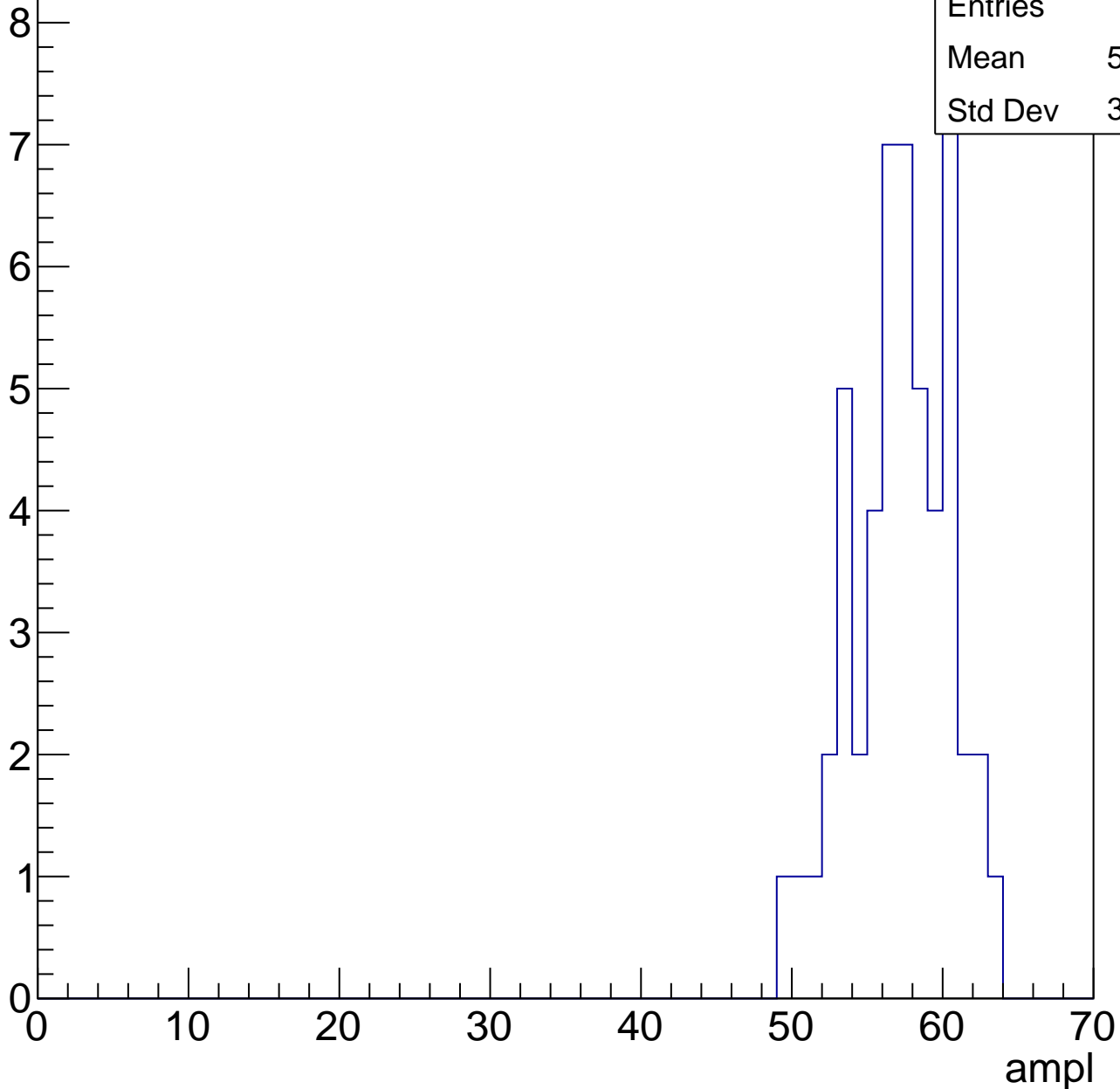


B1L103S, U10-ch11, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	56.79
Std Dev	3.189

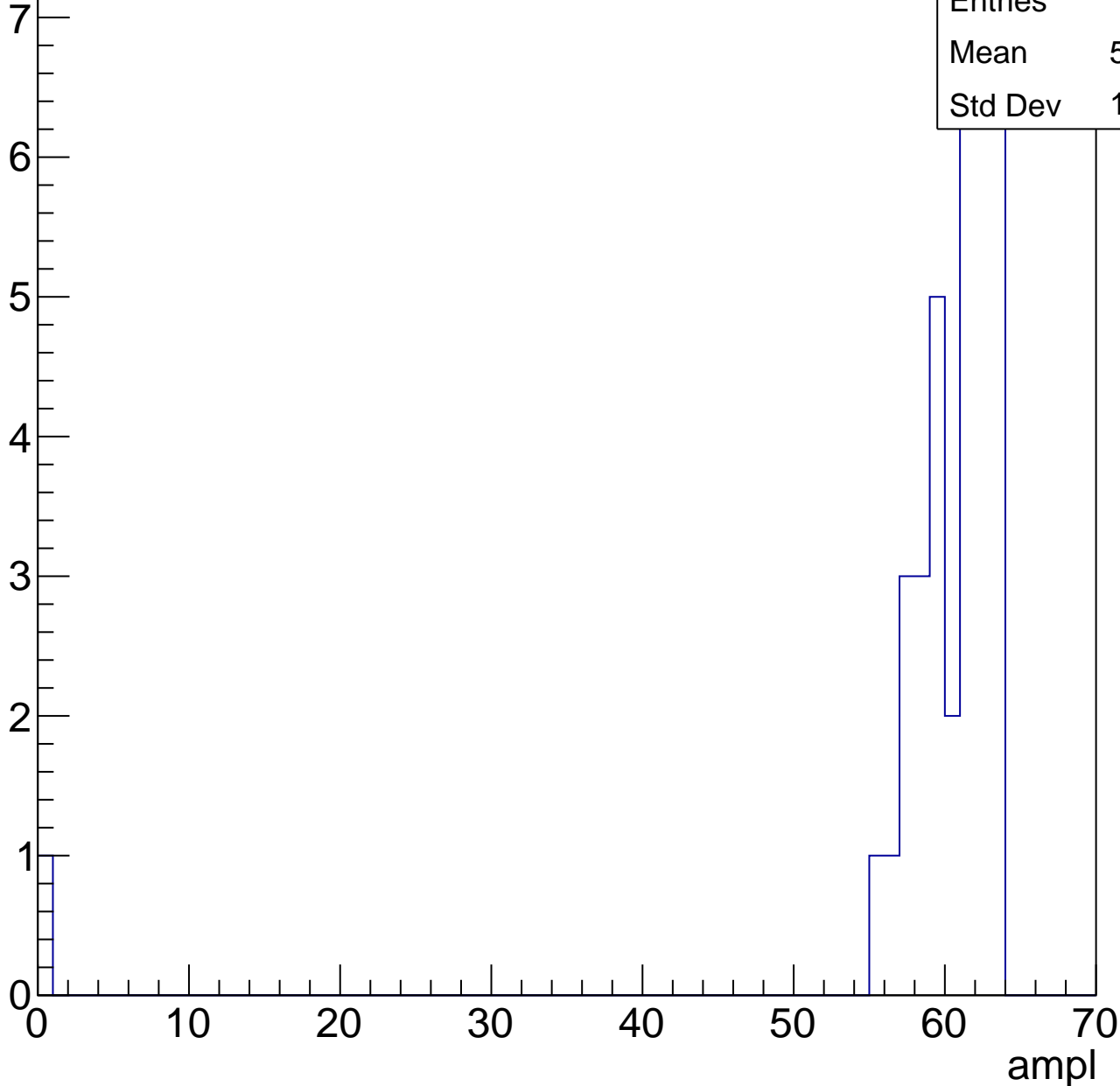


B1L103S, U10-ch11, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	58.73
Std Dev	10.03



B1L103S, U10-ch11, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch11, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch12, adc0

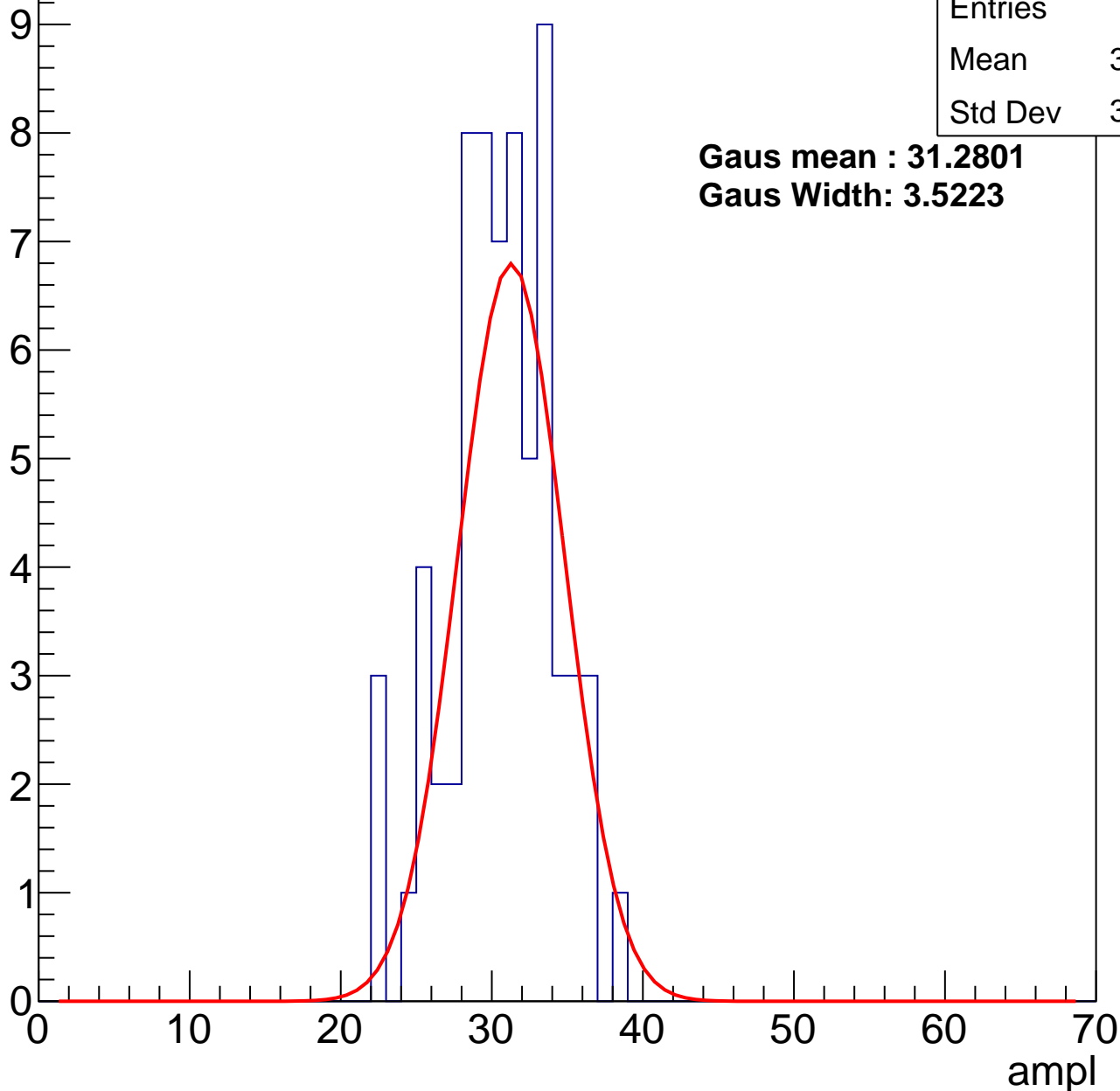
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.15
Std Dev	3.499

Gaus mean : 31.2801

Gaus Width: 3.5223



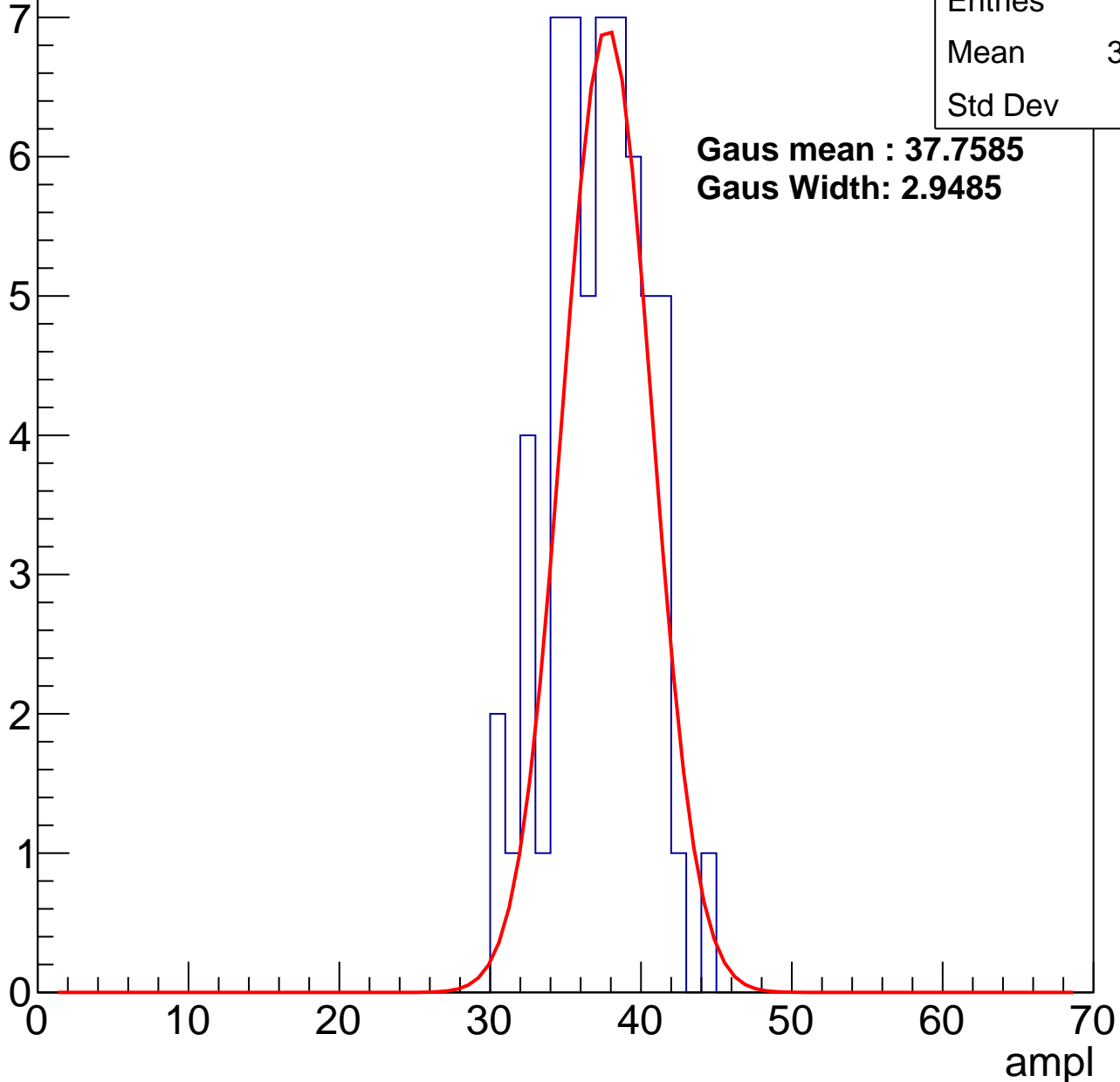
B1L103S, U10-ch12, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	36.69
Std Dev	3.11

Gaus mean : 37.7585
Gaus Width: 2.9485



B1L103S, U10-ch12, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	43.4
Std Dev	3.739

Gaus mean : 43.7412

Gaus Width: 4.0554

Entry

10

8

6

4

2

0

0

10

20

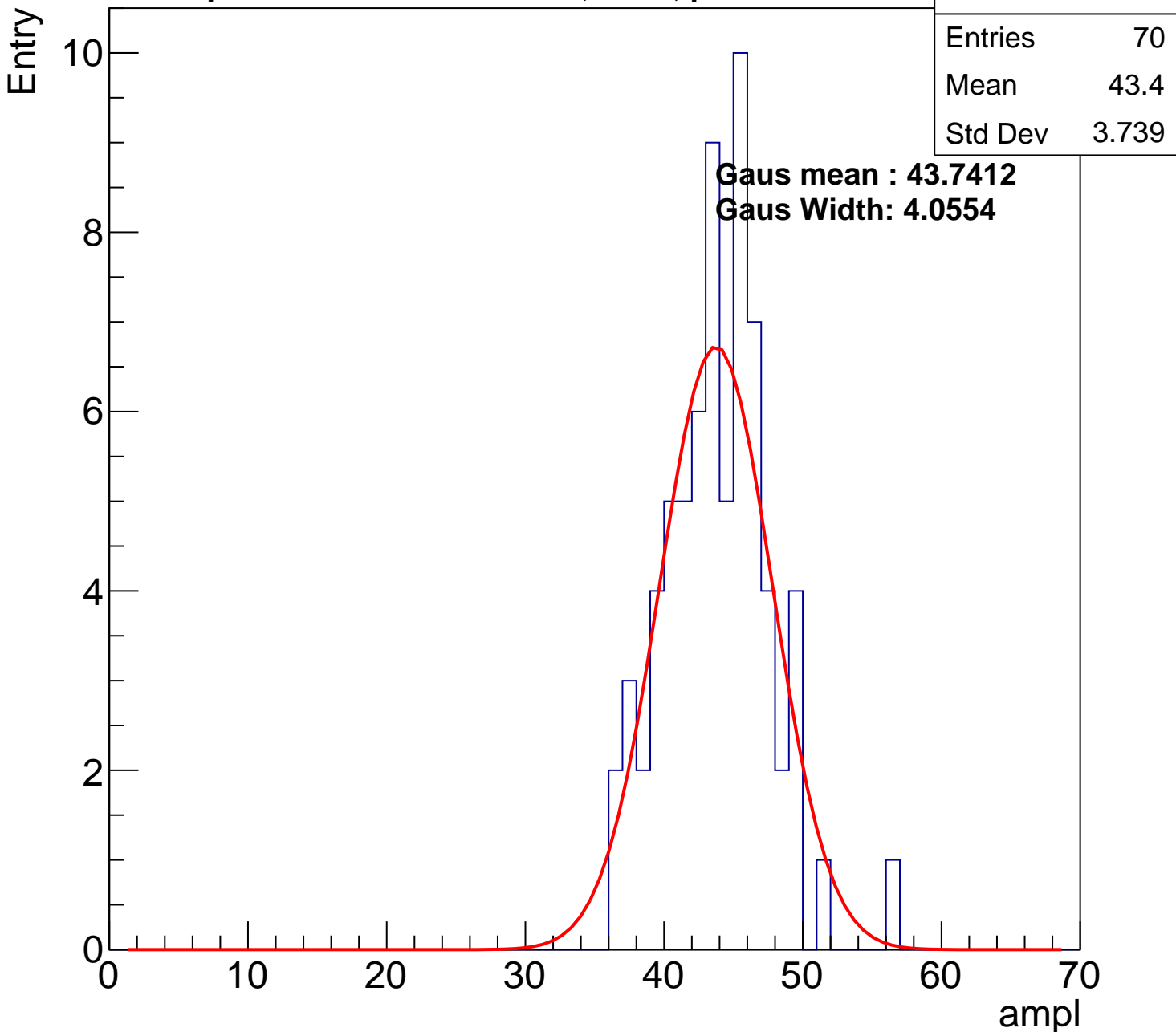
30

40

50

60

ampl

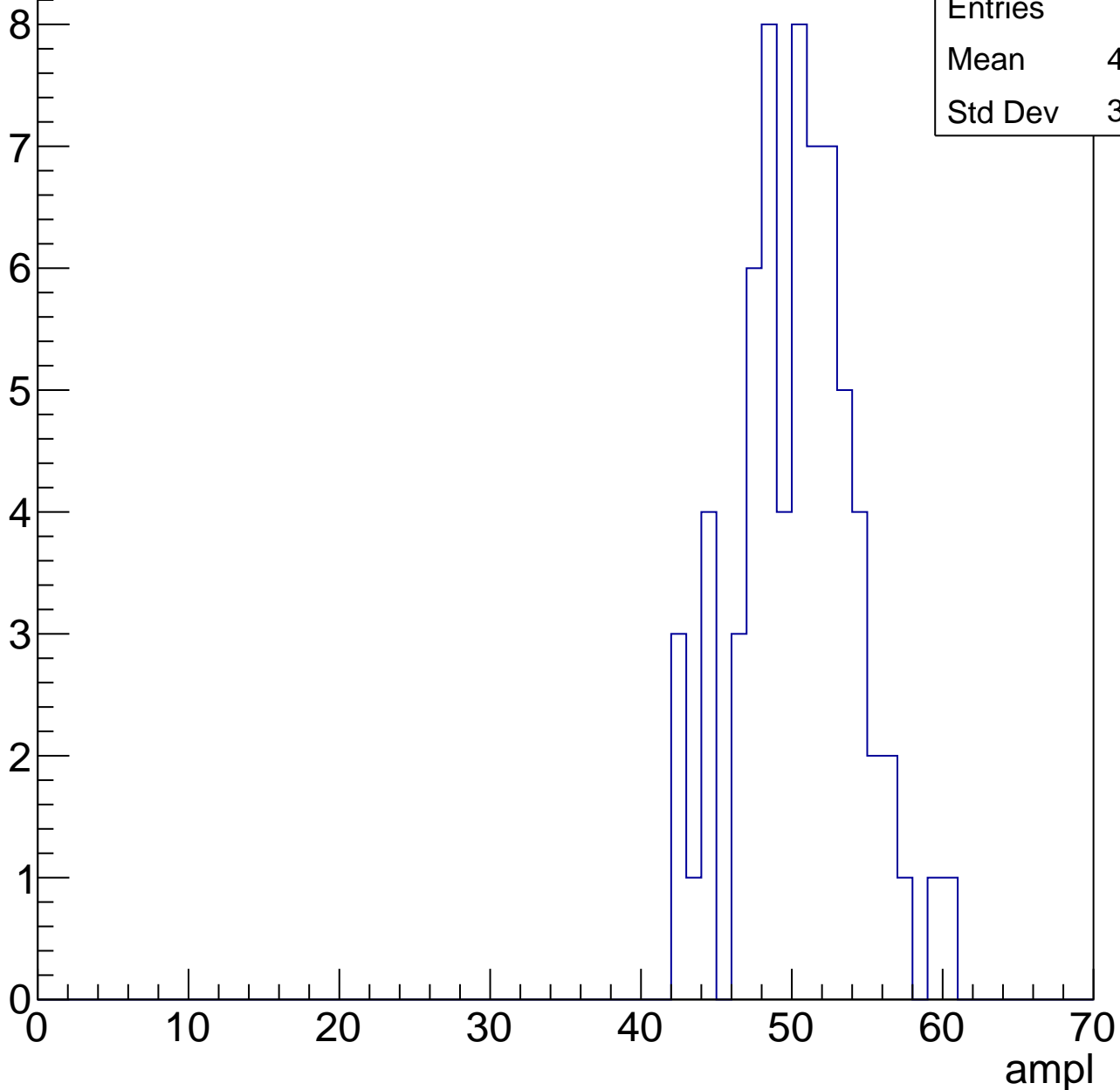


B1L103S, U10-ch12, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.93
Std Dev	3.899



B1L103S, U10-ch12, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	56.02
Std Dev	3.117

Entry

10

8

6

4

2

0

0

10

20

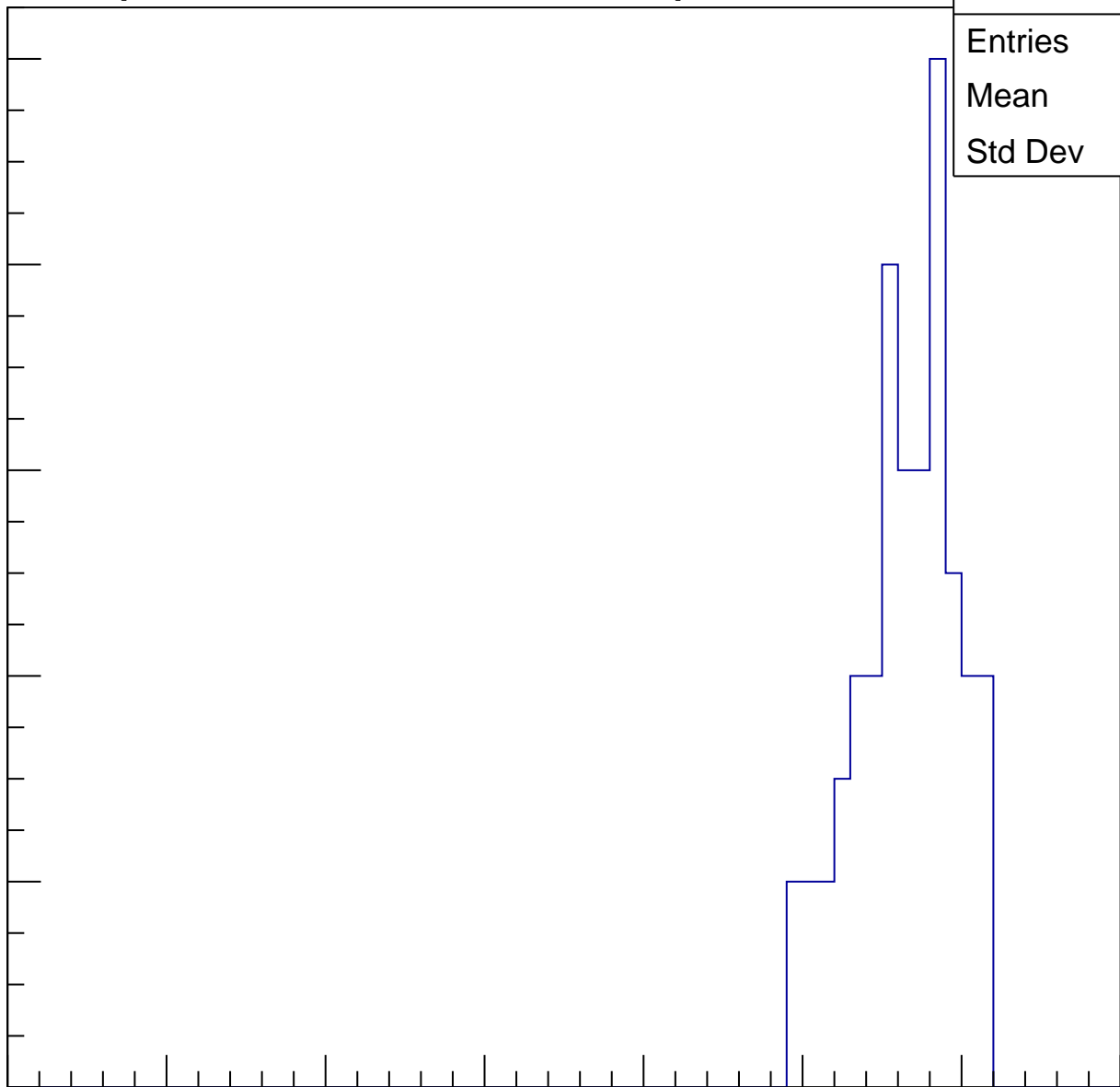
30

40

50

60

ampl

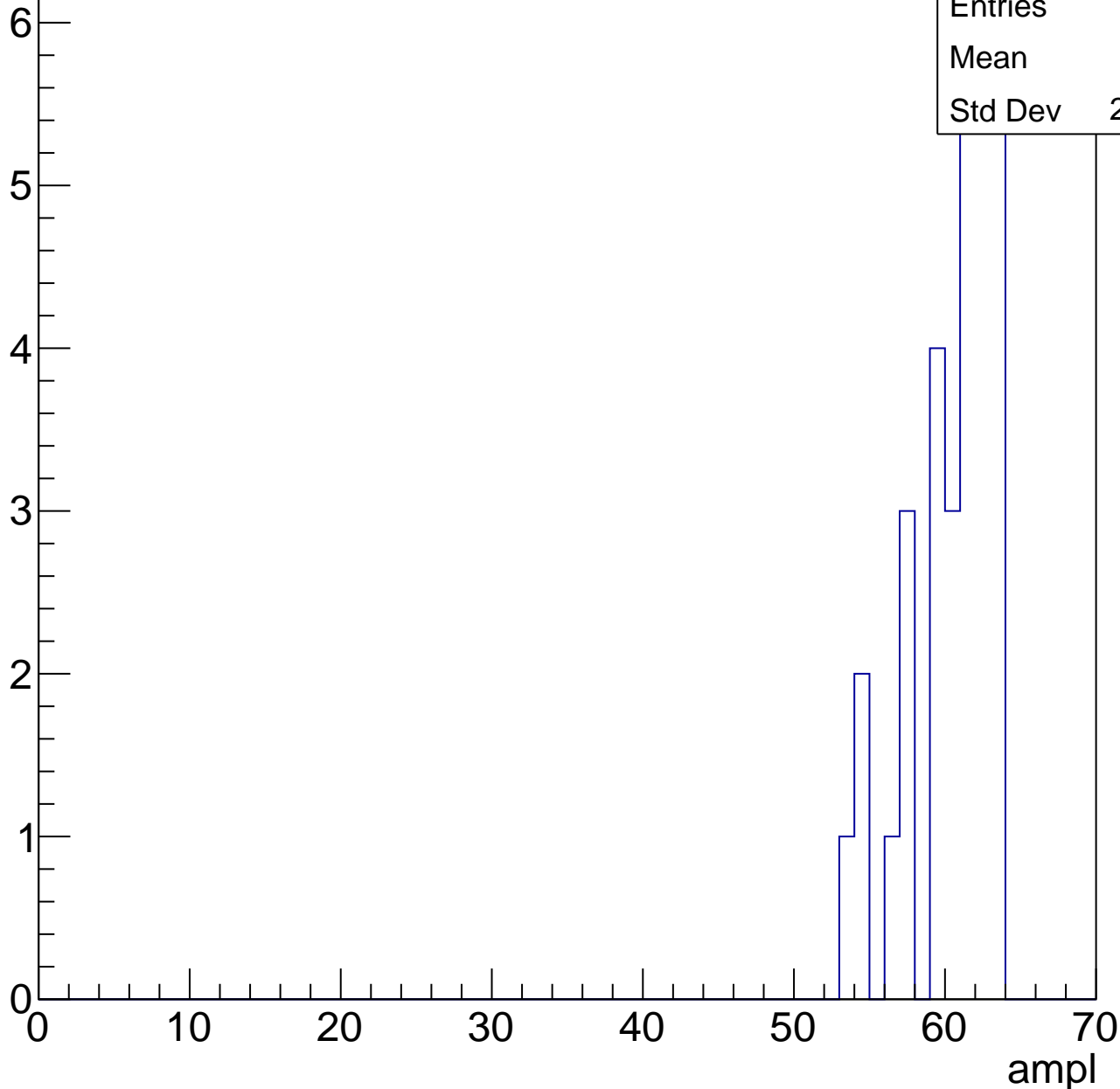


B1L103S, U10-ch12, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

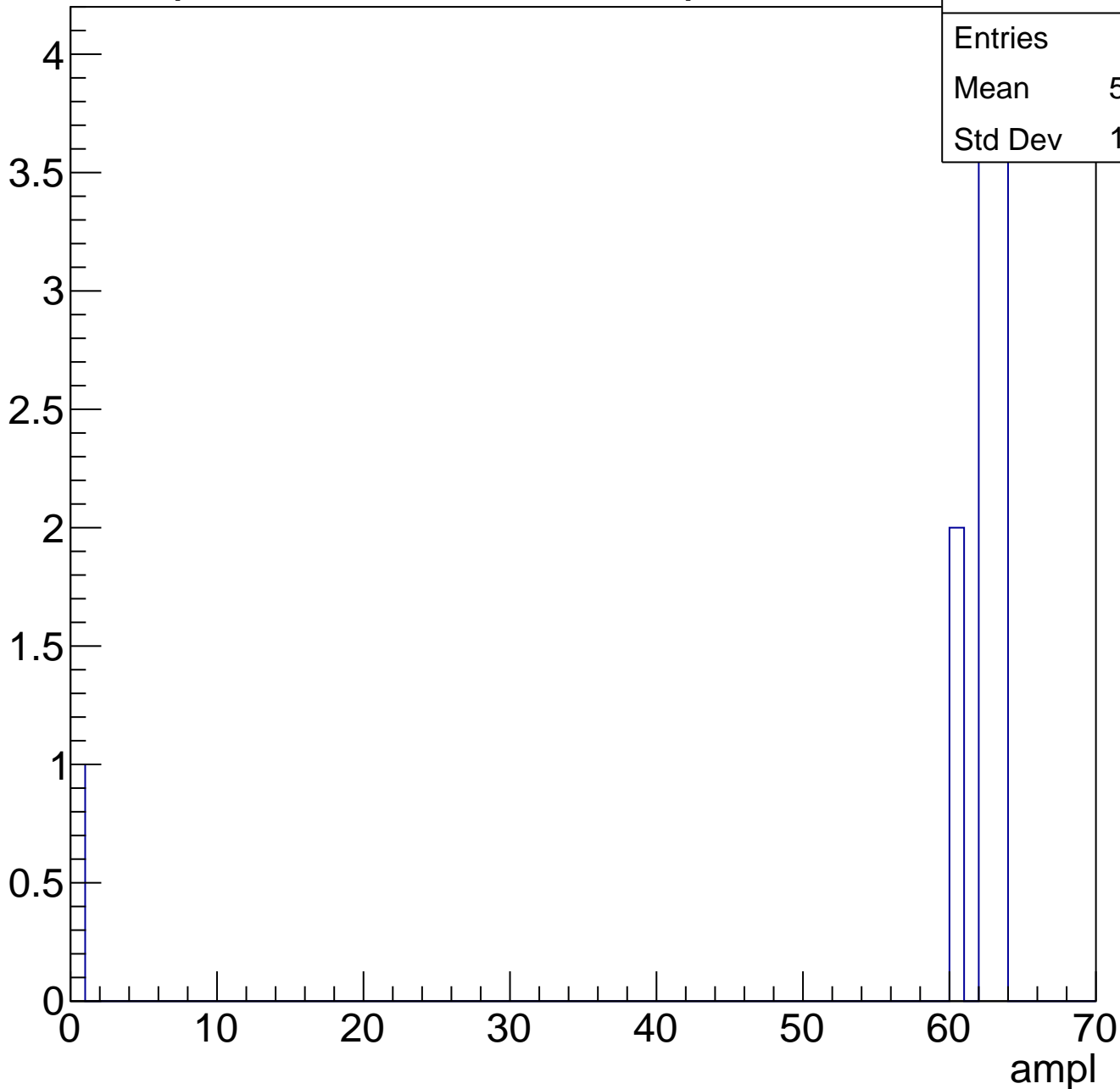
Entries	32
Mean	60
Std Dev	2.806



B1L103S, U10-ch12, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch12, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch13, adc0

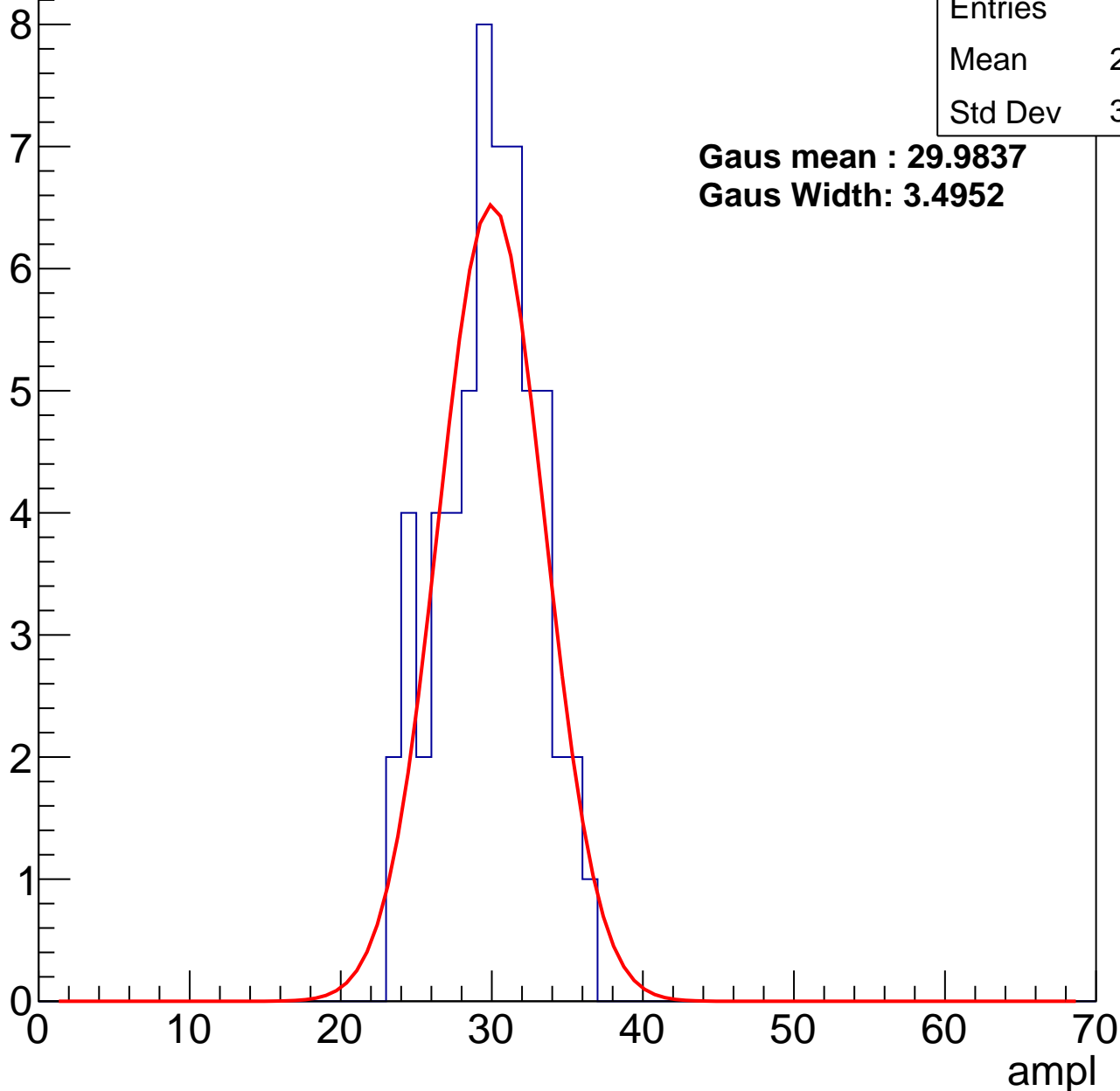
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	29.34
Std Dev	3.176

Gaus mean : 29.9837

Gaus Width: 3.4952



B1L103S, U10-ch13, adc1

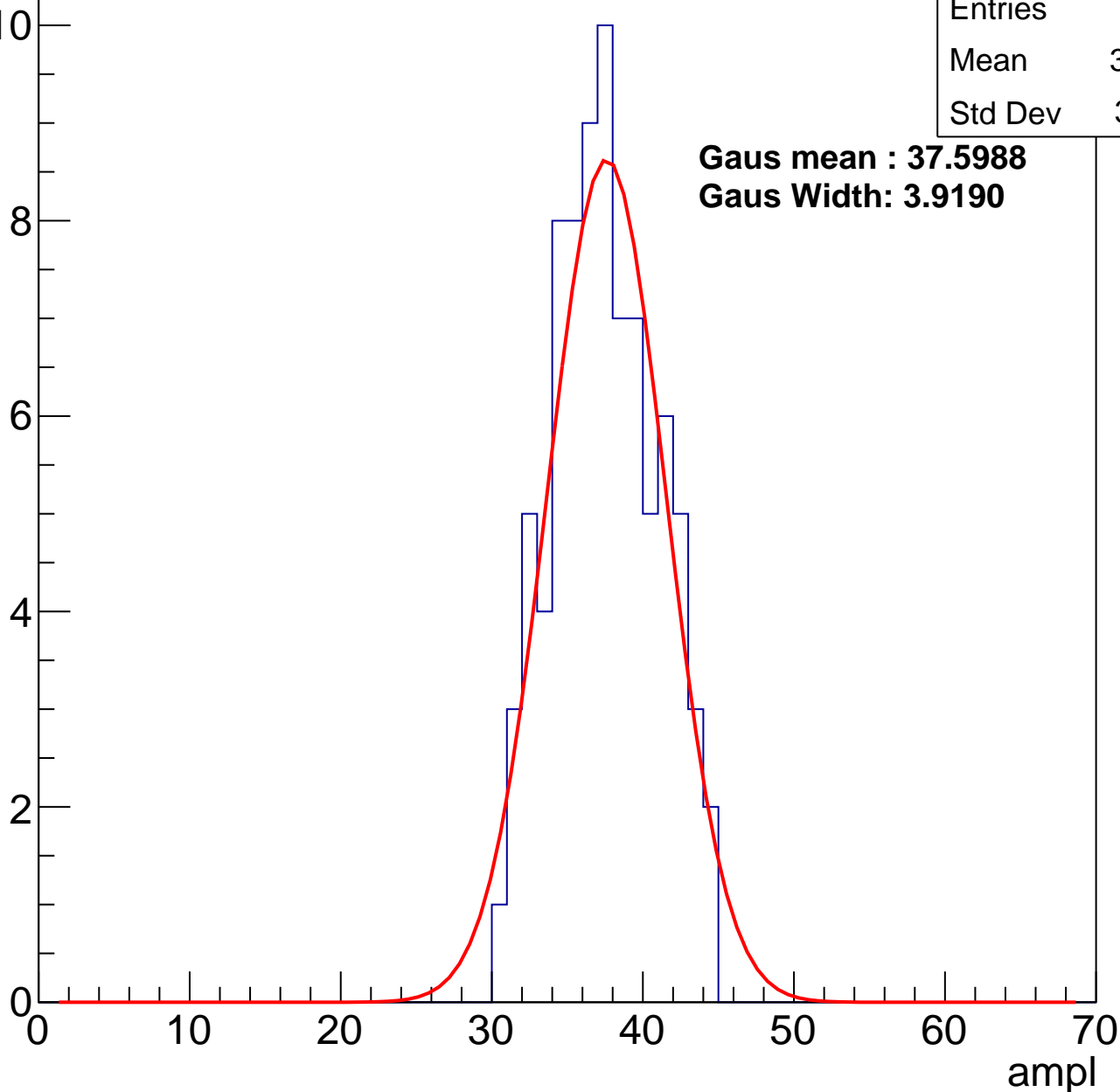
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	37.02
Std Dev	3.411

Gaus mean : 37.5988

Gaus Width: 3.9190



B1L103S, U10-ch13, adc2

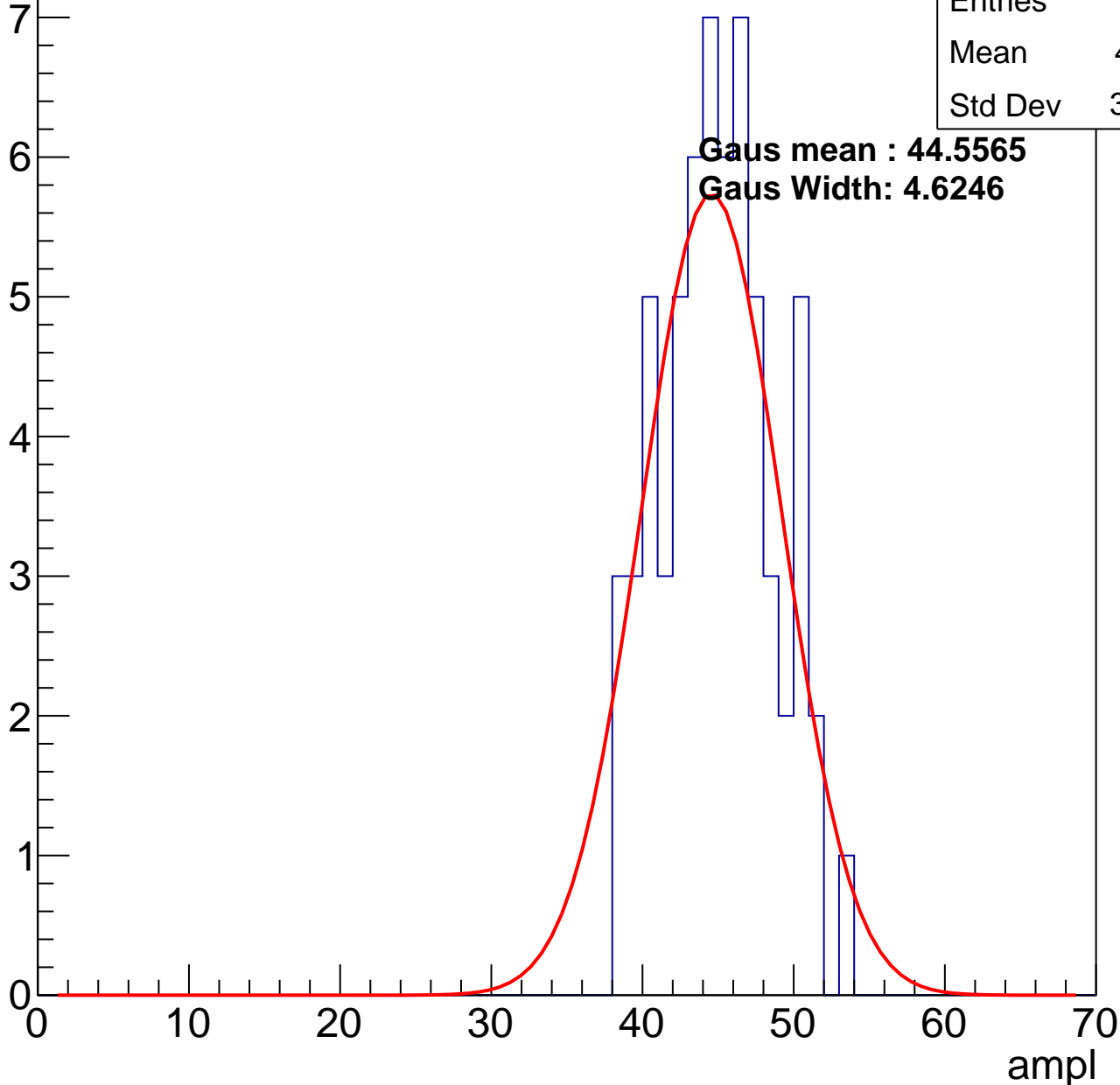
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.51
Std Dev	3.642

Gaus mean : 44.5565

Gaus Width: 4.6246

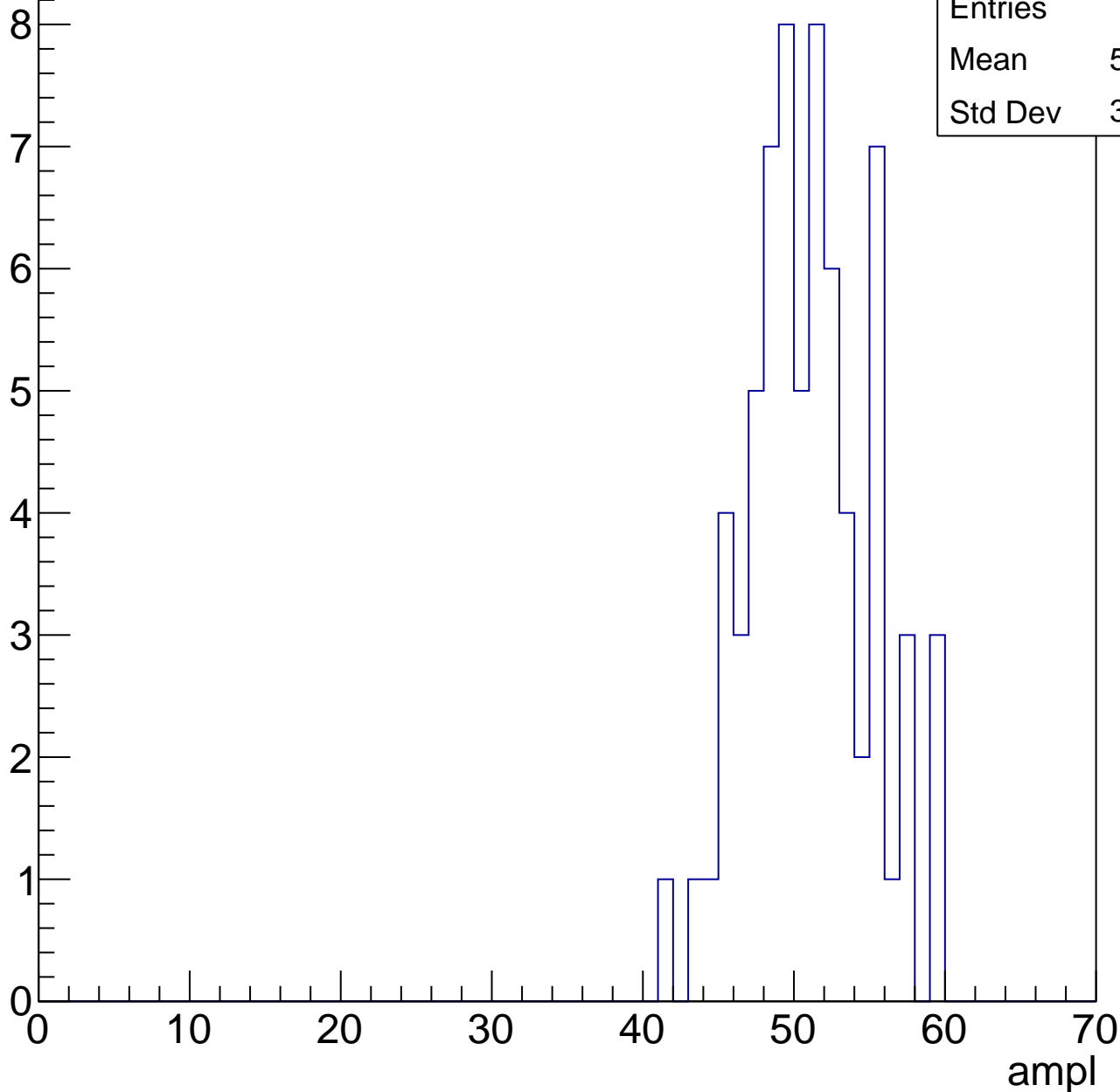


B1L103S, U10-ch13, adc3

calib_packv5_042523_0143.root, FC#7, port C2

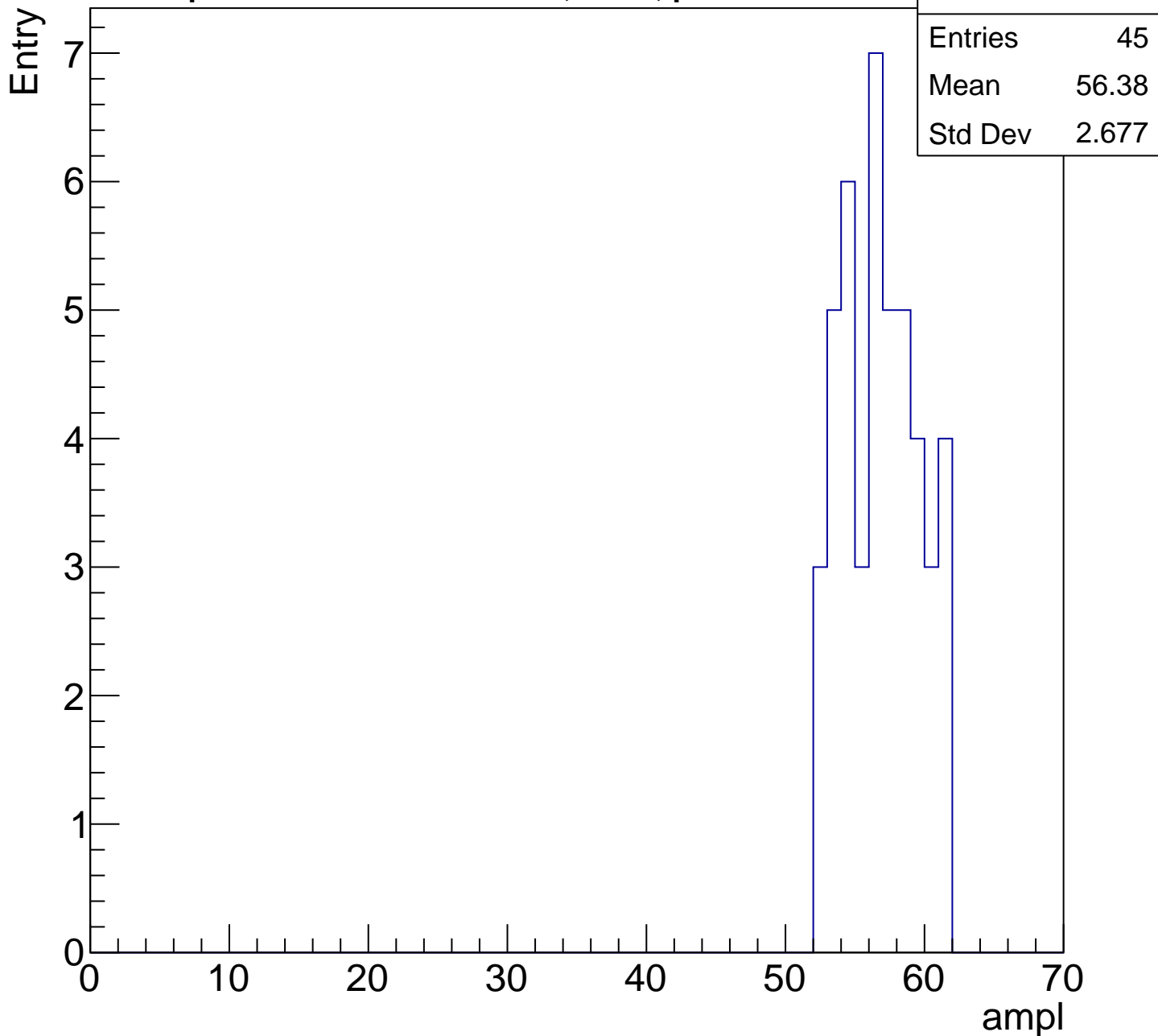
Entry

Entries	69
Mean	50.55
Std Dev	3.947



B1L103S, U10-ch13, adc4

calib_packv5_042523_0143.root, FC#7, port C2

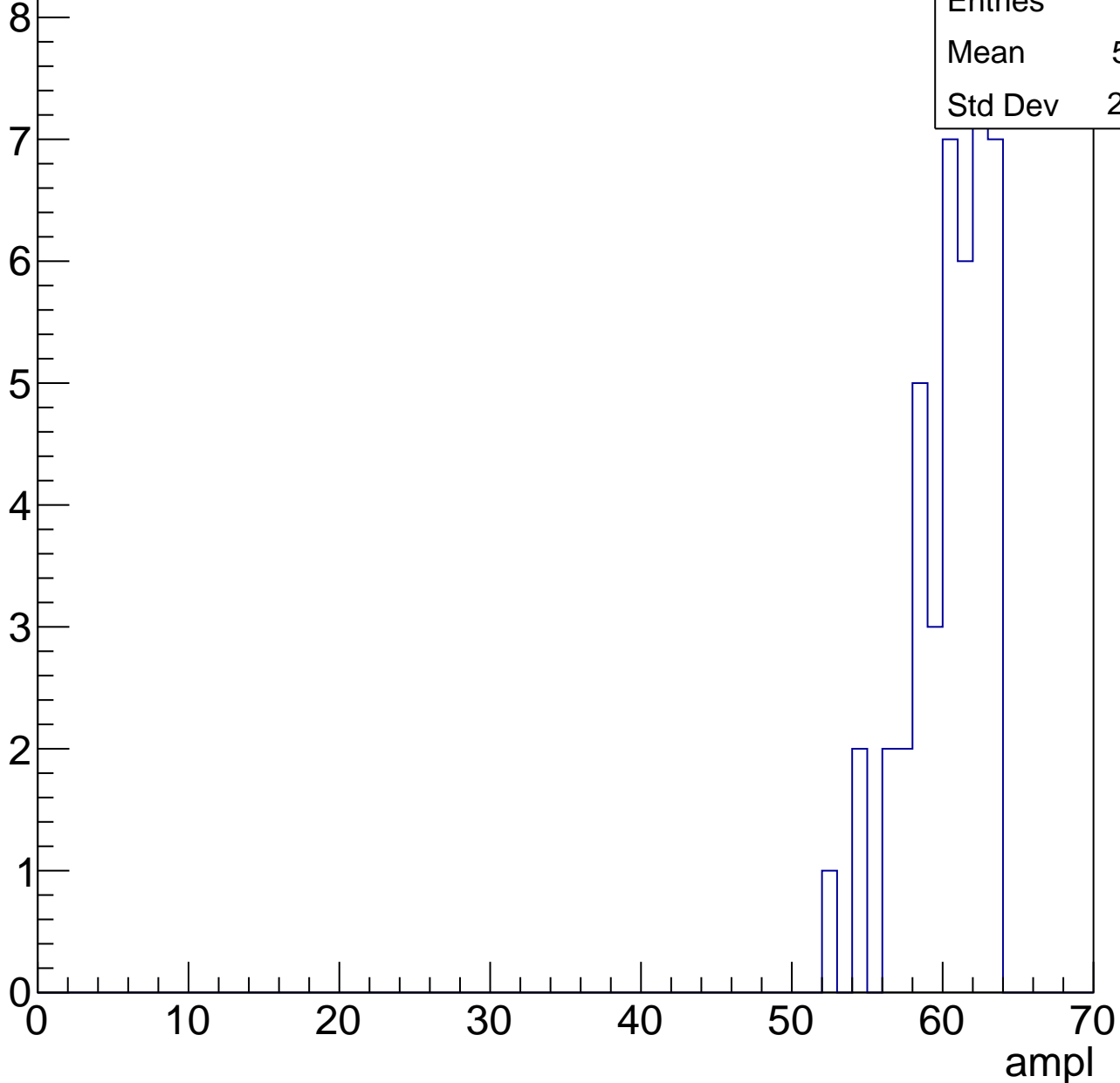


B1L103S, U10-ch13, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	59.91
Std Dev	2.683



B1L103S, U10-ch13, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch13, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch14, adc0

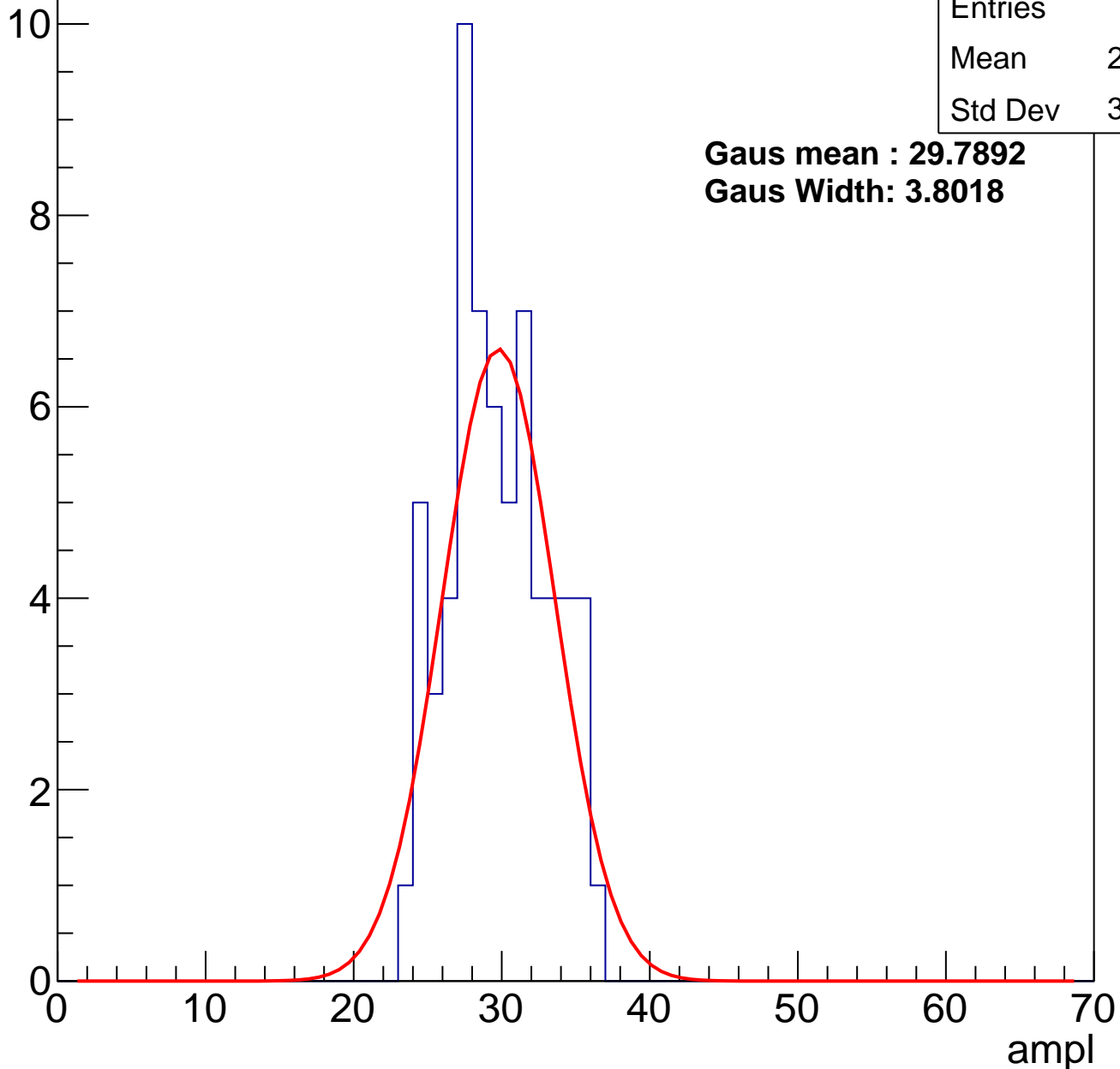
calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	29.25
Std Dev	3.314

Gaus mean : 29.7892

Gaus Width: 3.8018

Entry



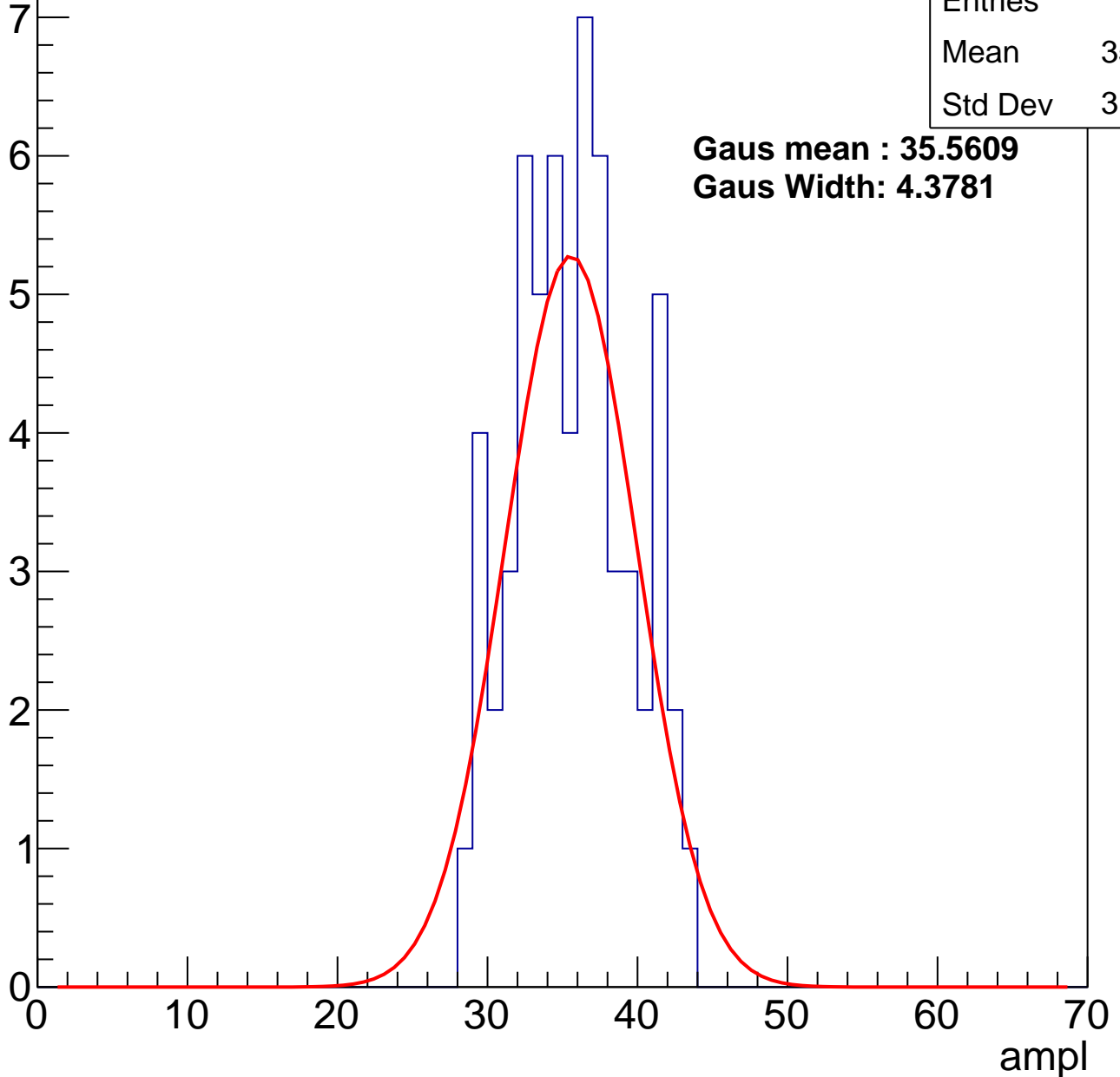
B1L103S, U10-ch14, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.25
Std Dev	3.797

Gaus mean : 35.5609
Gaus Width: 4.3781



B1L103S, U10-ch14, adc2

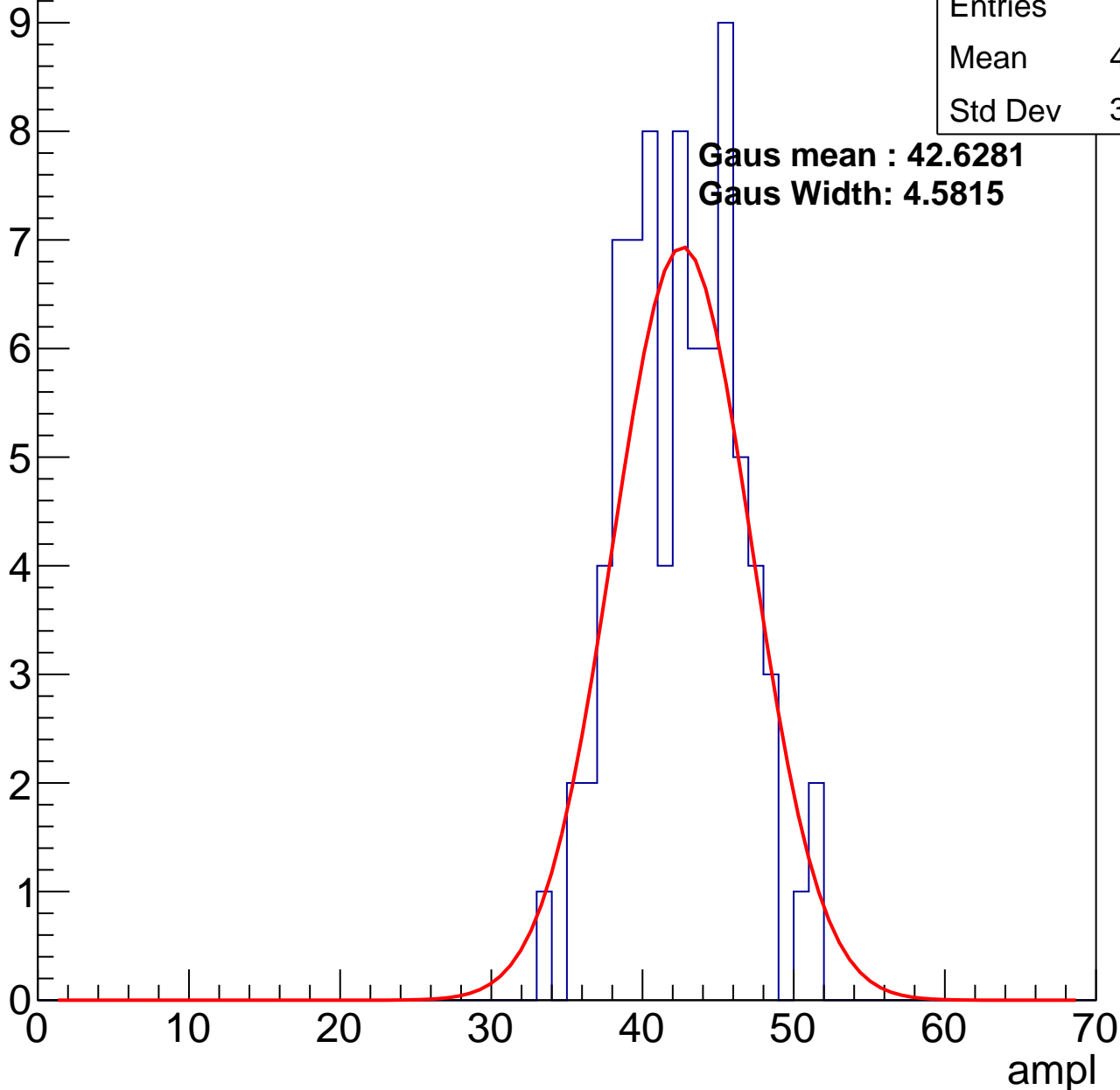
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	42.06
Std Dev	3.876

Gaus mean : 42.6281

Gaus Width: 4.5815

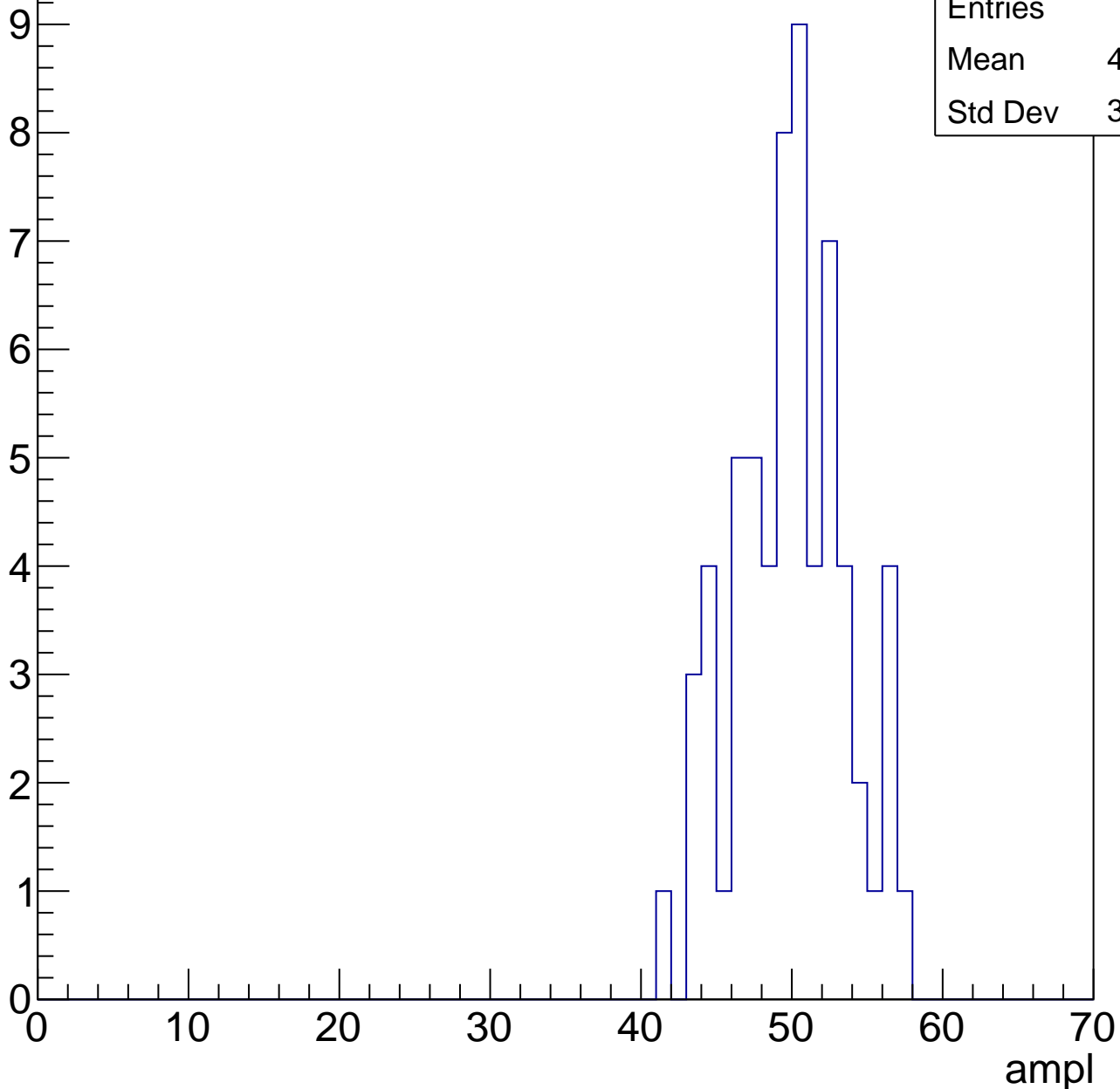


B1L103S, U10-ch14, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.43
Std Dev	3.659

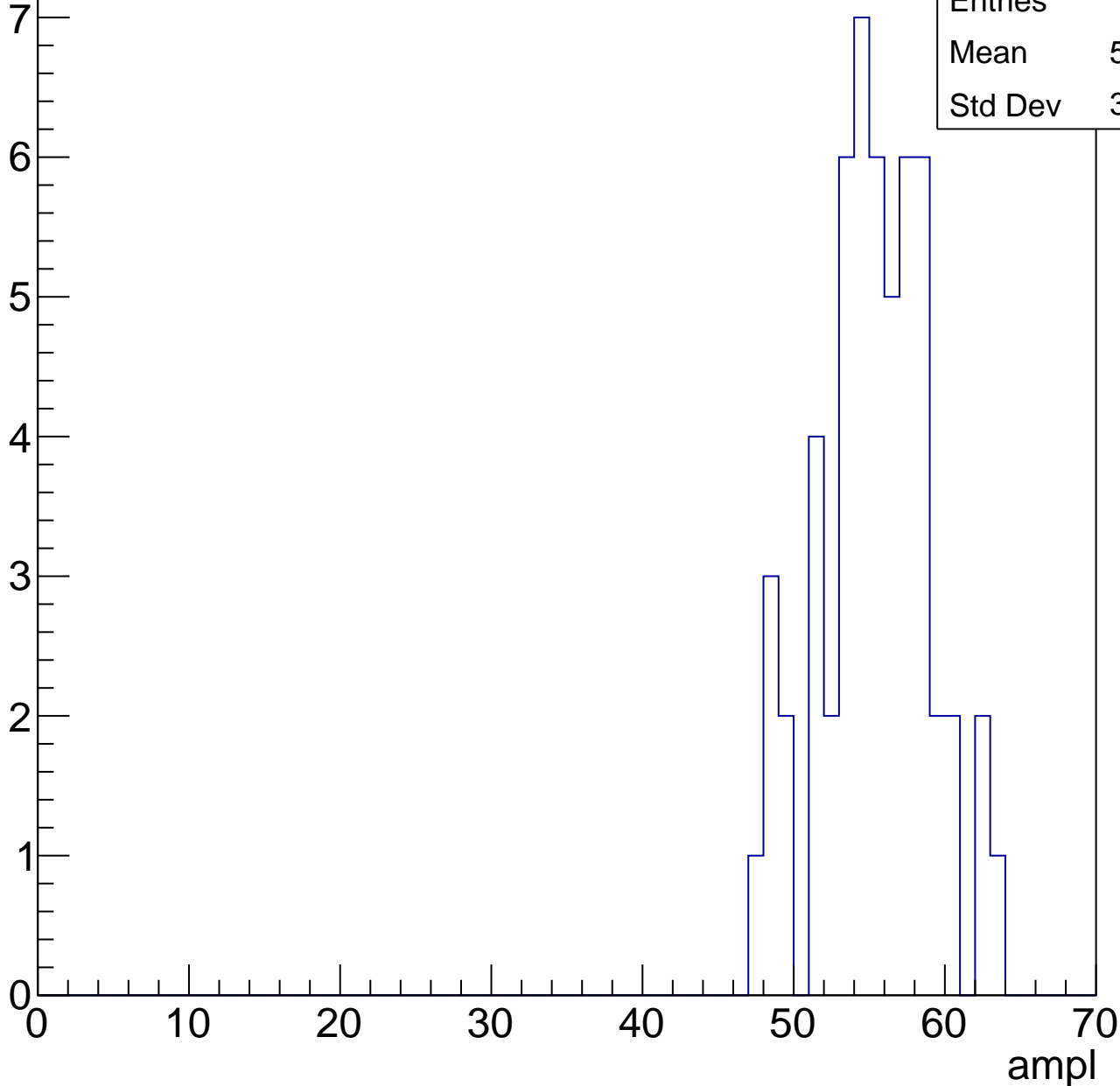


B1L103S, U10-ch14, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.87
Std Dev	3.623

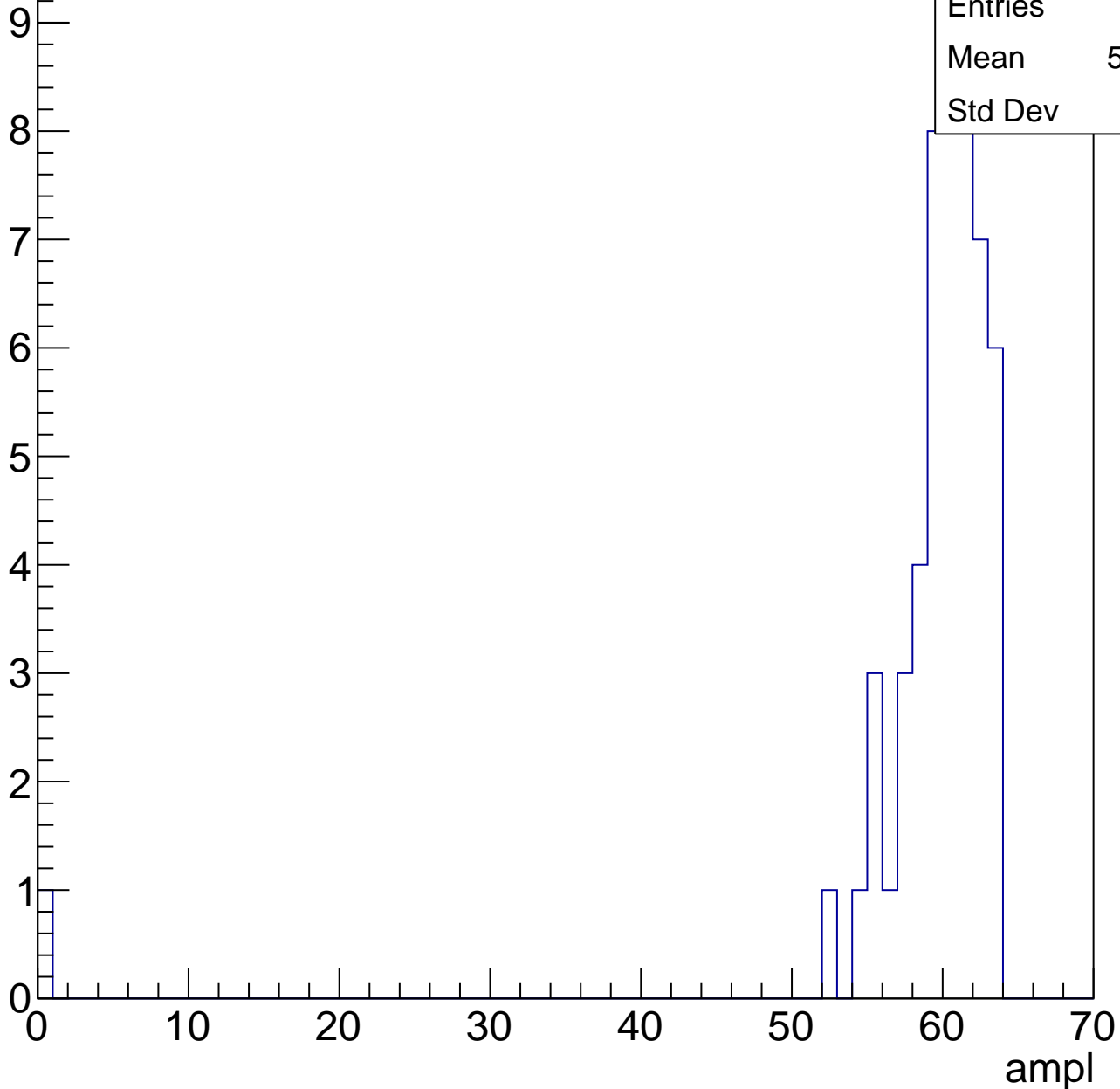


B1L103S, U10-ch14, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.52
Std Dev	8.57



B1L103S, U10-ch14, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch14, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch15, adc0

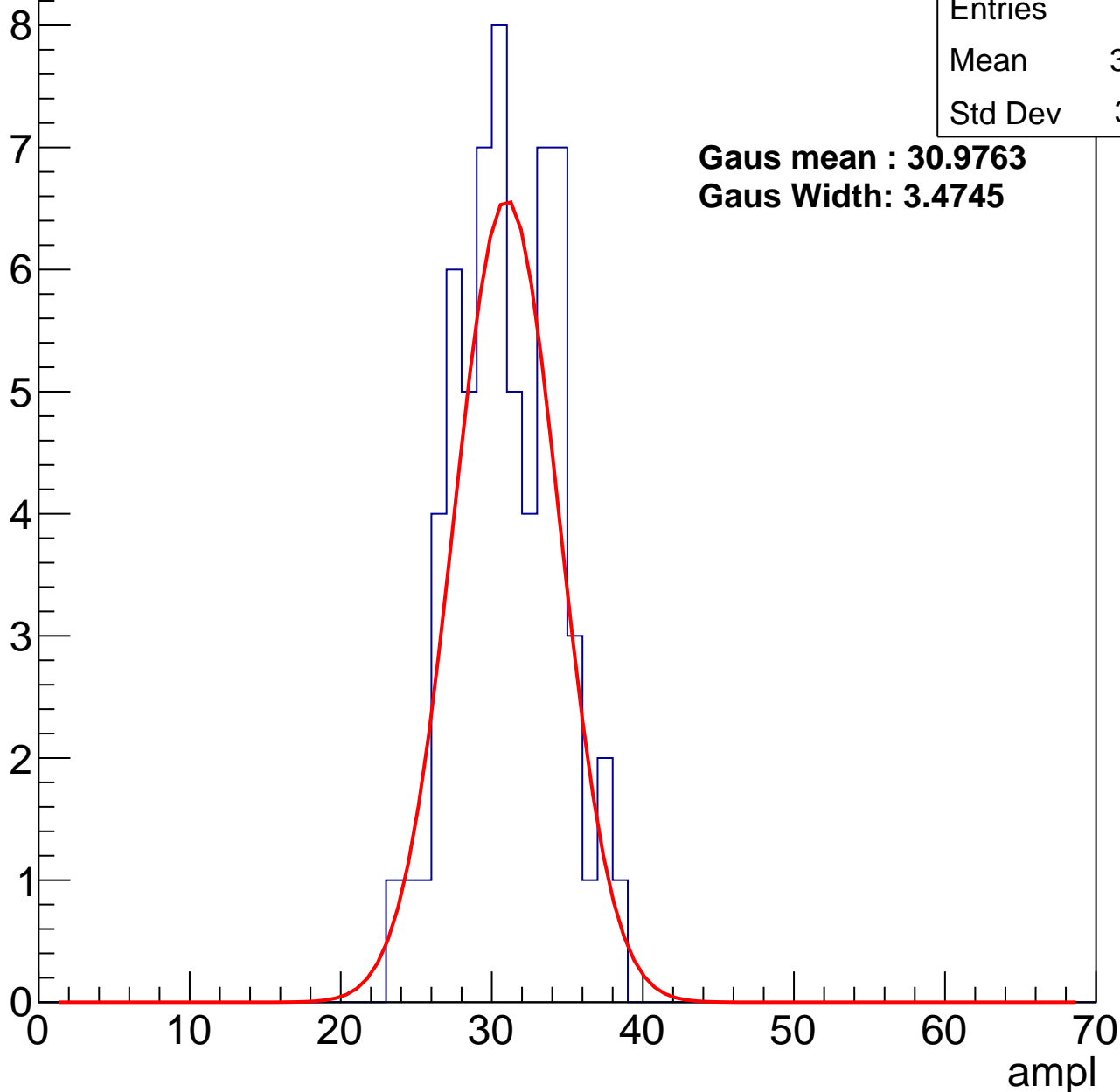
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	30.57
Std Dev	3.341

Gaus mean : 30.9763

Gaus Width: 3.4745



B1L103S, U10-ch15, adc1

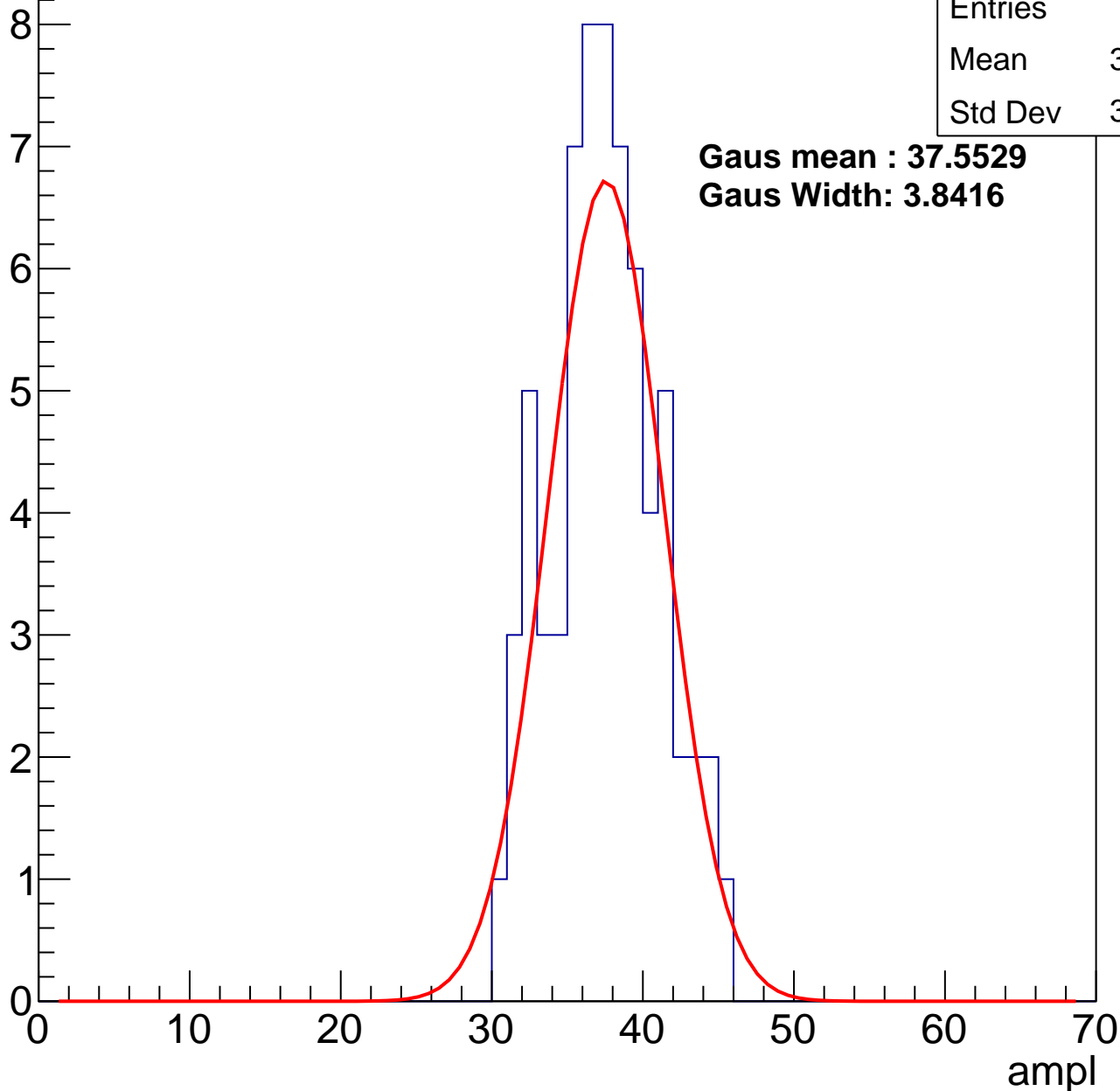
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37.03
Std Dev	3.507

Gaus mean : 37.5529

Gaus Width: 3.8416



B1L103S, U10-ch15, adc2

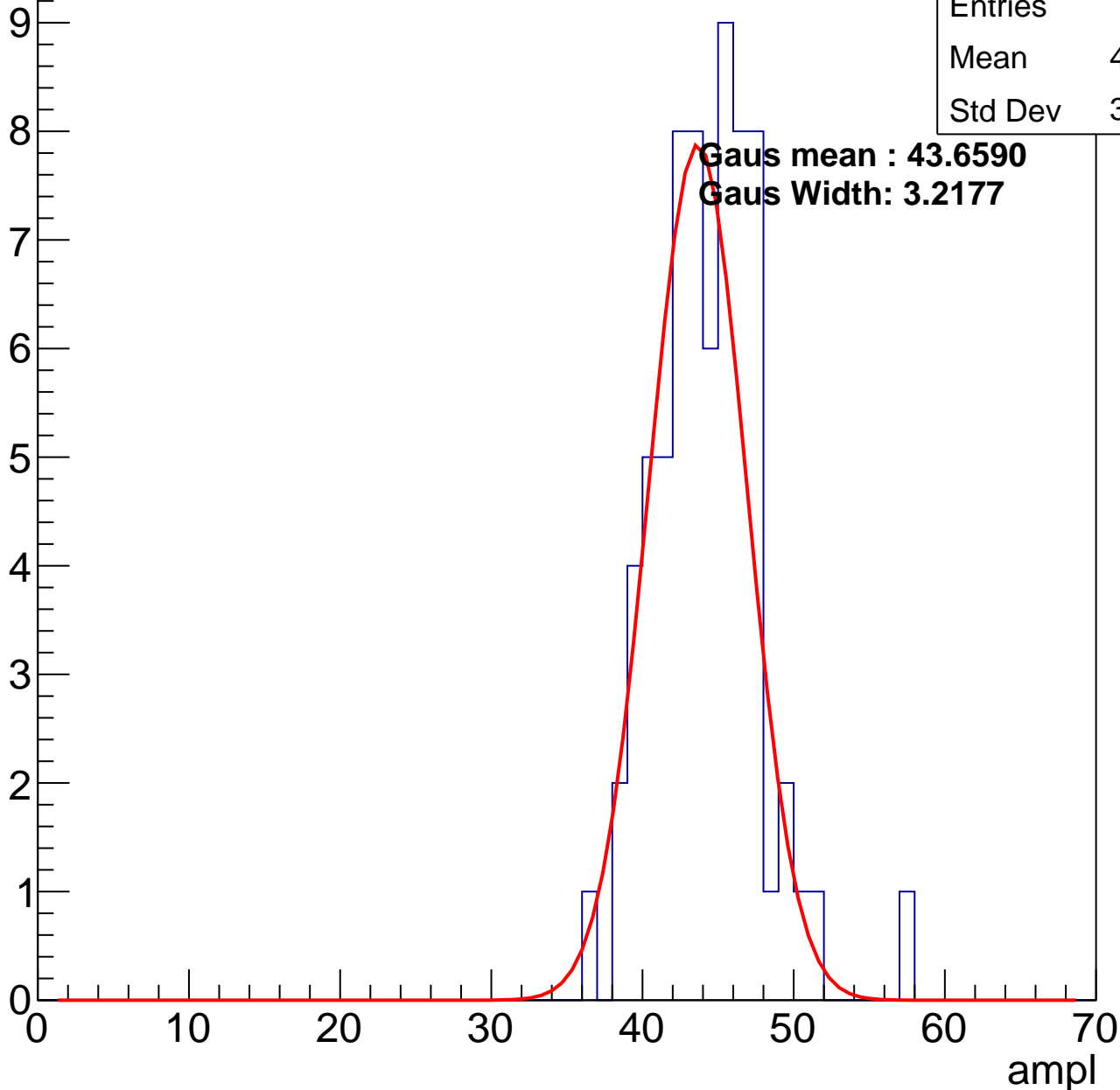
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.86
Std Dev	3.449

Gaus mean : 43.6590

Gaus Width: 3.2177

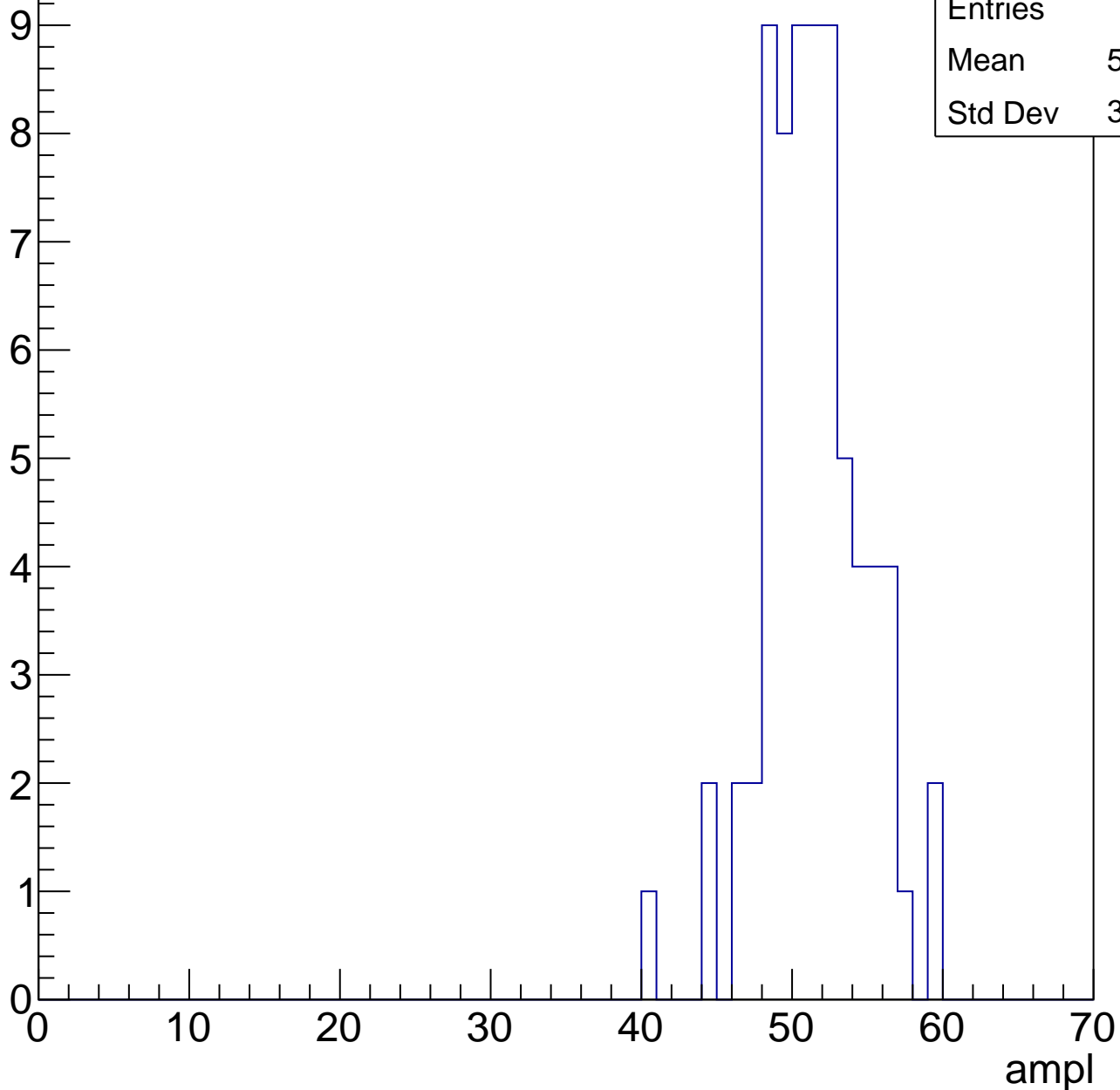


B1L103S, U10-ch15, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.92
Std Dev	3.389

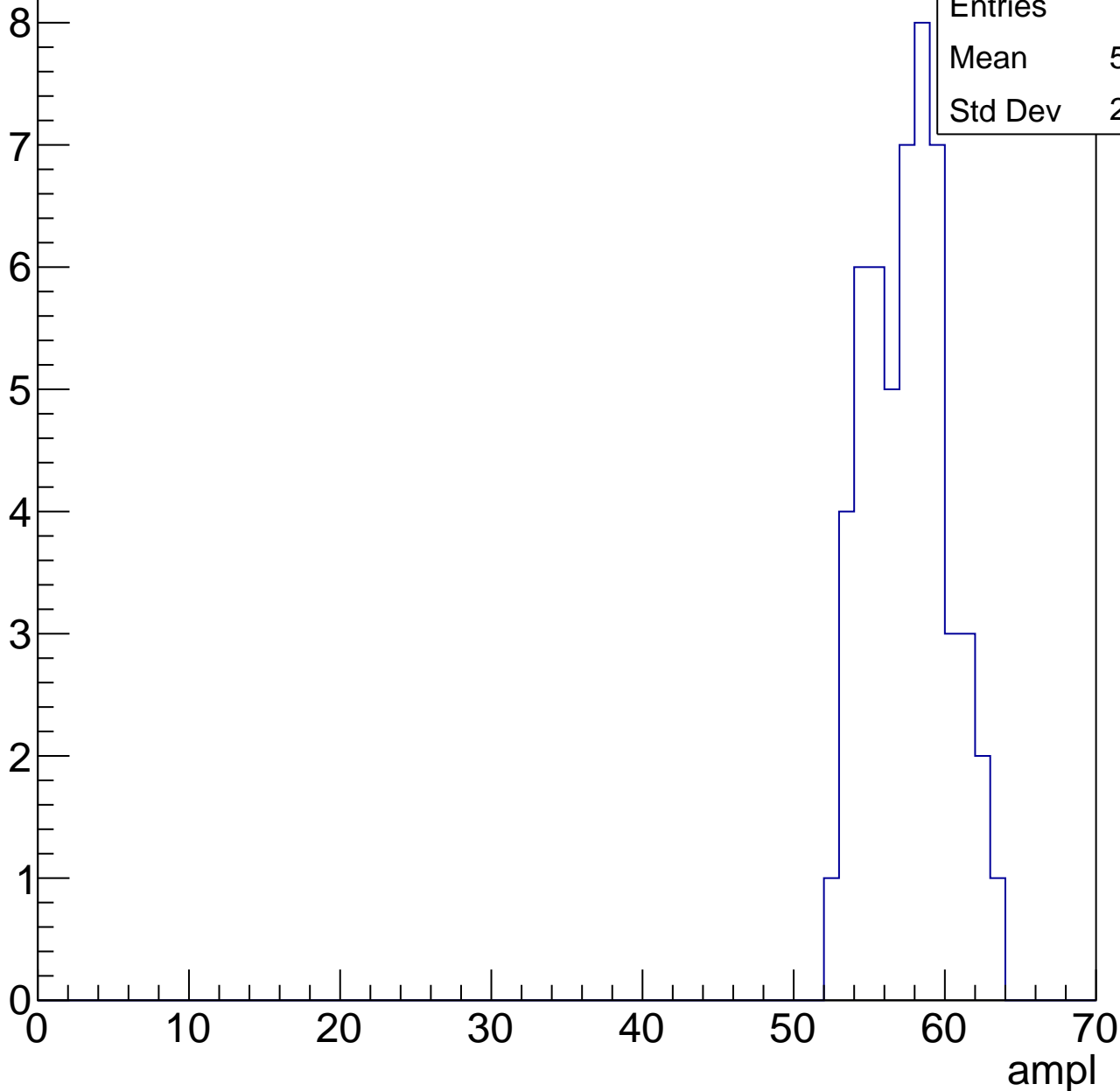


B1L103S, U10-ch15, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	57.06
Std Dev	2.638



B1L103S, U10-ch15, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries	31
Mean	58.52
Std Dev	10.91

ampl

0

10

20

30

40

50

60

70

B1L103S, U10-ch15, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch15, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch16, adc0

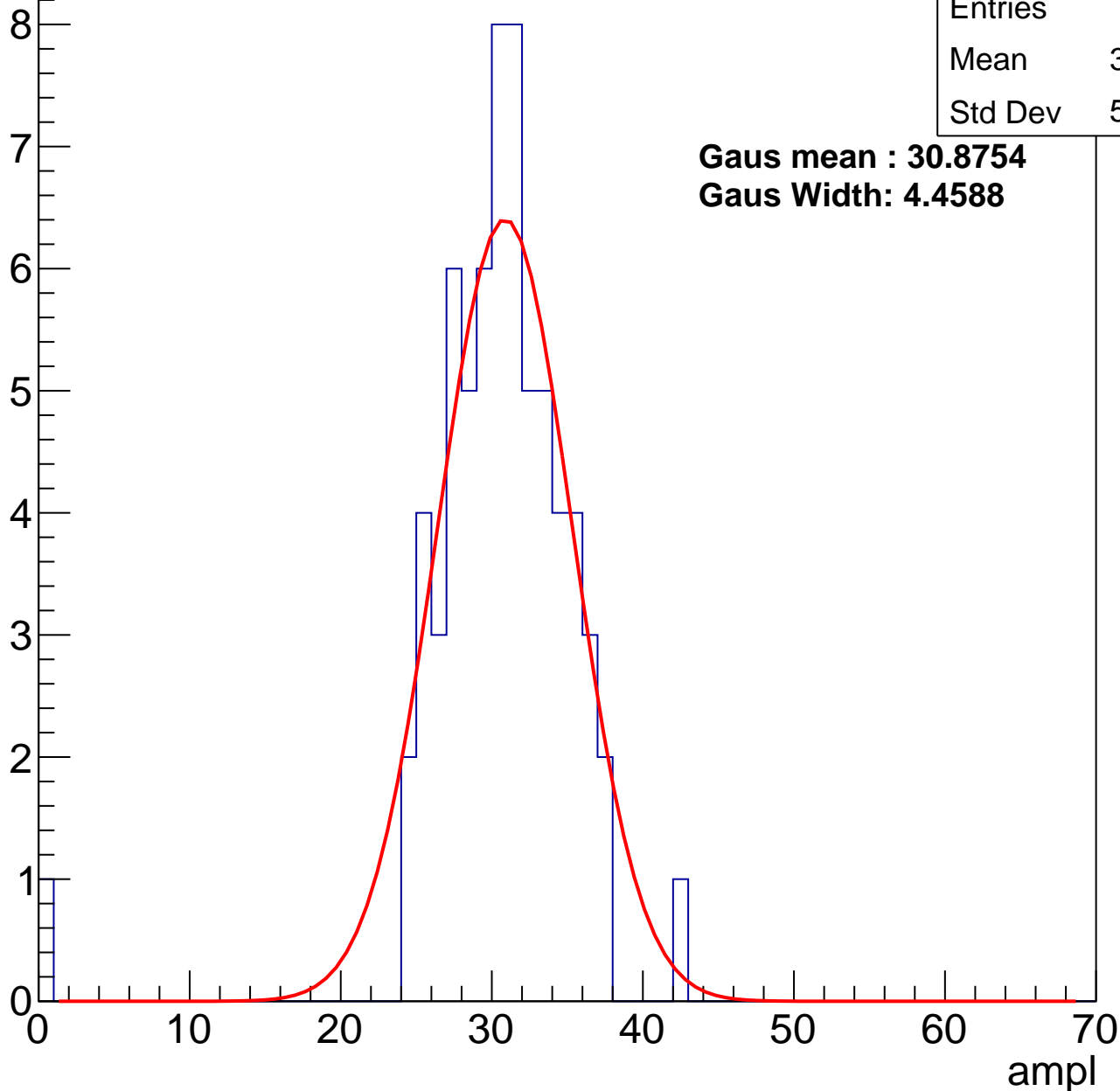
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	30.07
Std Dev	5.164

Gaus mean : 30.8754

Gaus Width: 4.4588

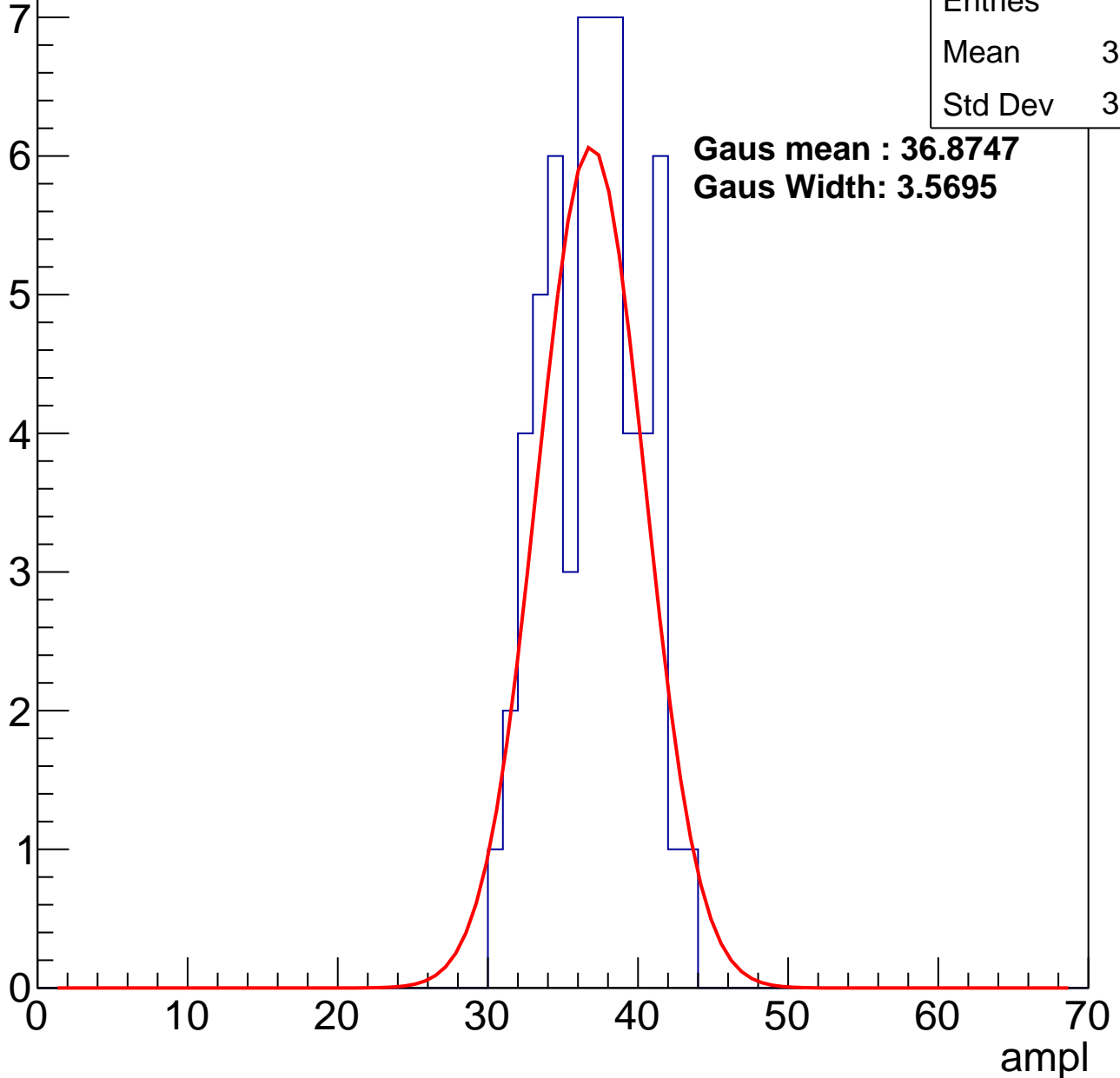


B1L103S, U10-ch16, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.52
Std Dev	3.153



B1L103S, U10-ch16, adc2

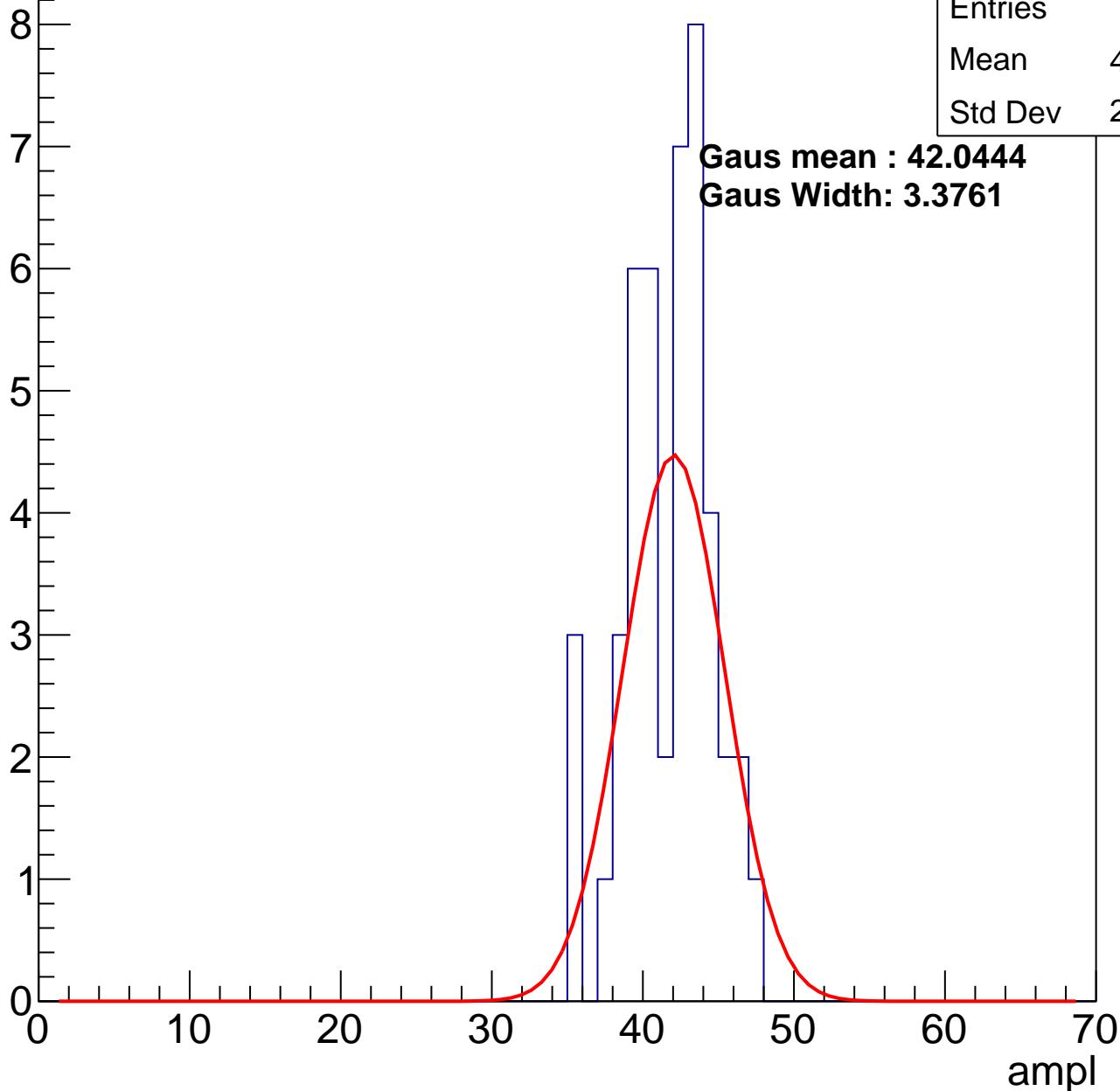
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	41.22
Std Dev	2.874

Gaus mean : 42.0444

Gaus Width: 3.3761

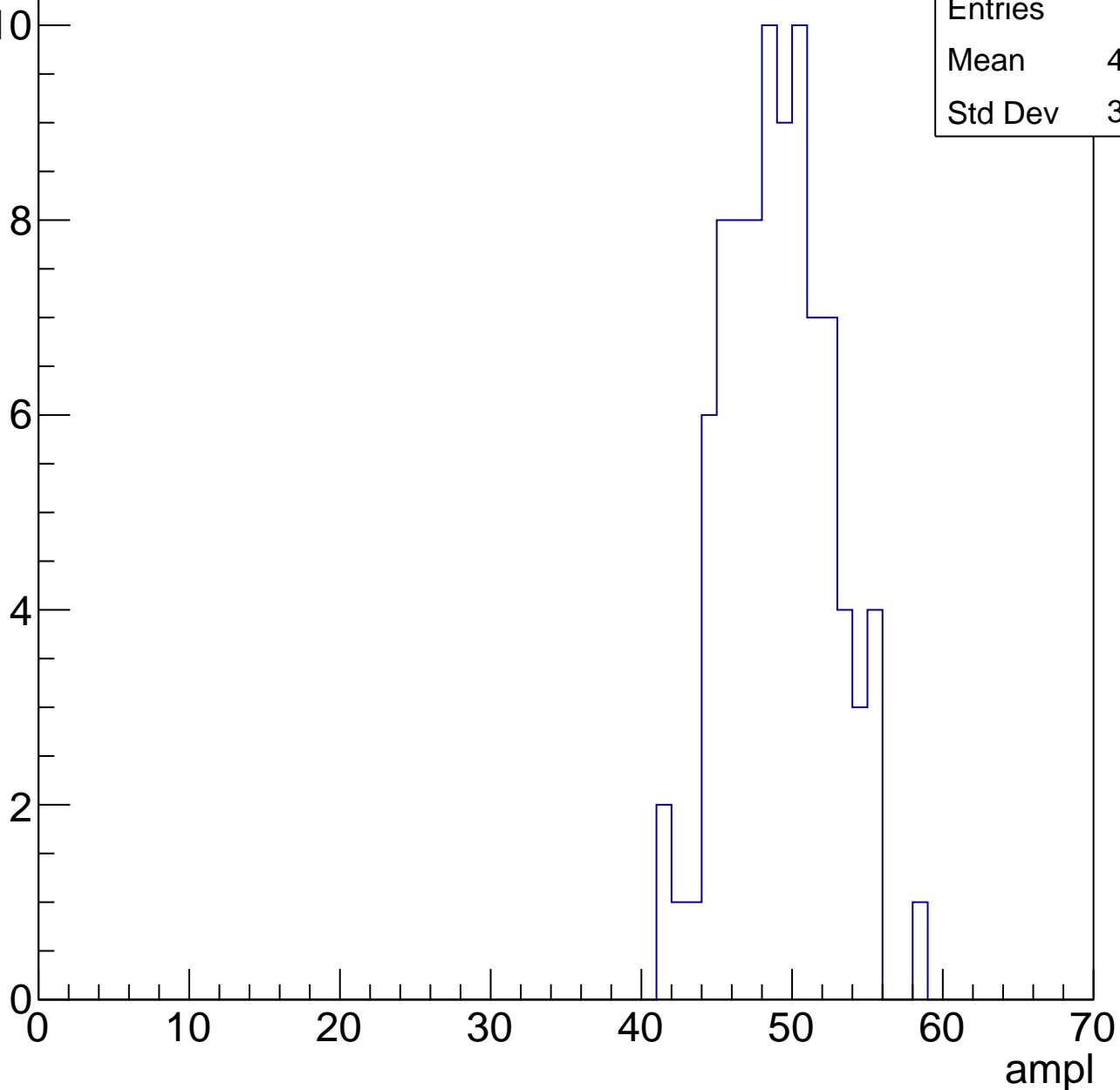


B1L103S, U10-ch16, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	48.64
Std Dev	3.449

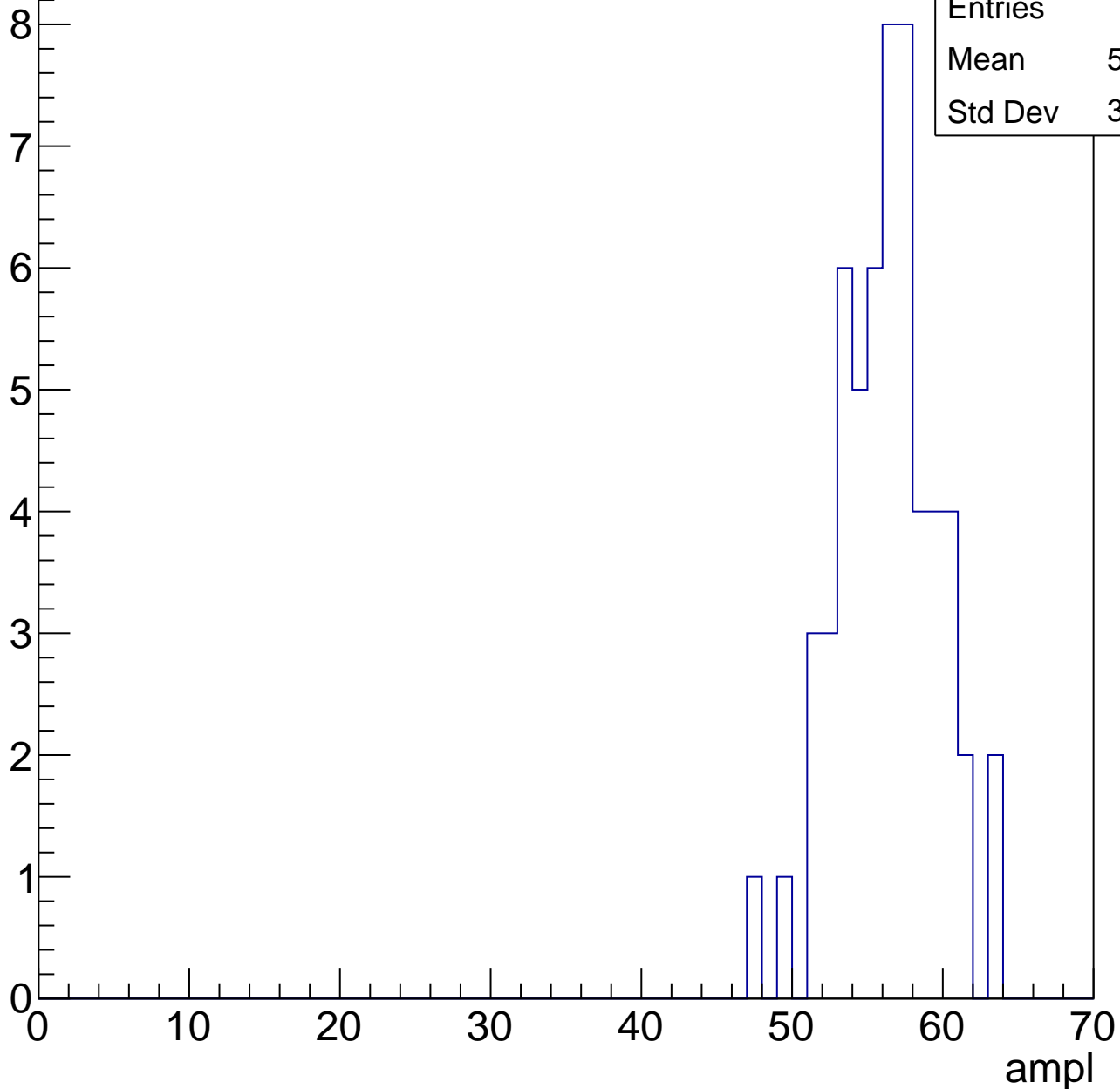


B1L103S, U10-ch16, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	55.84
Std Dev	3.254

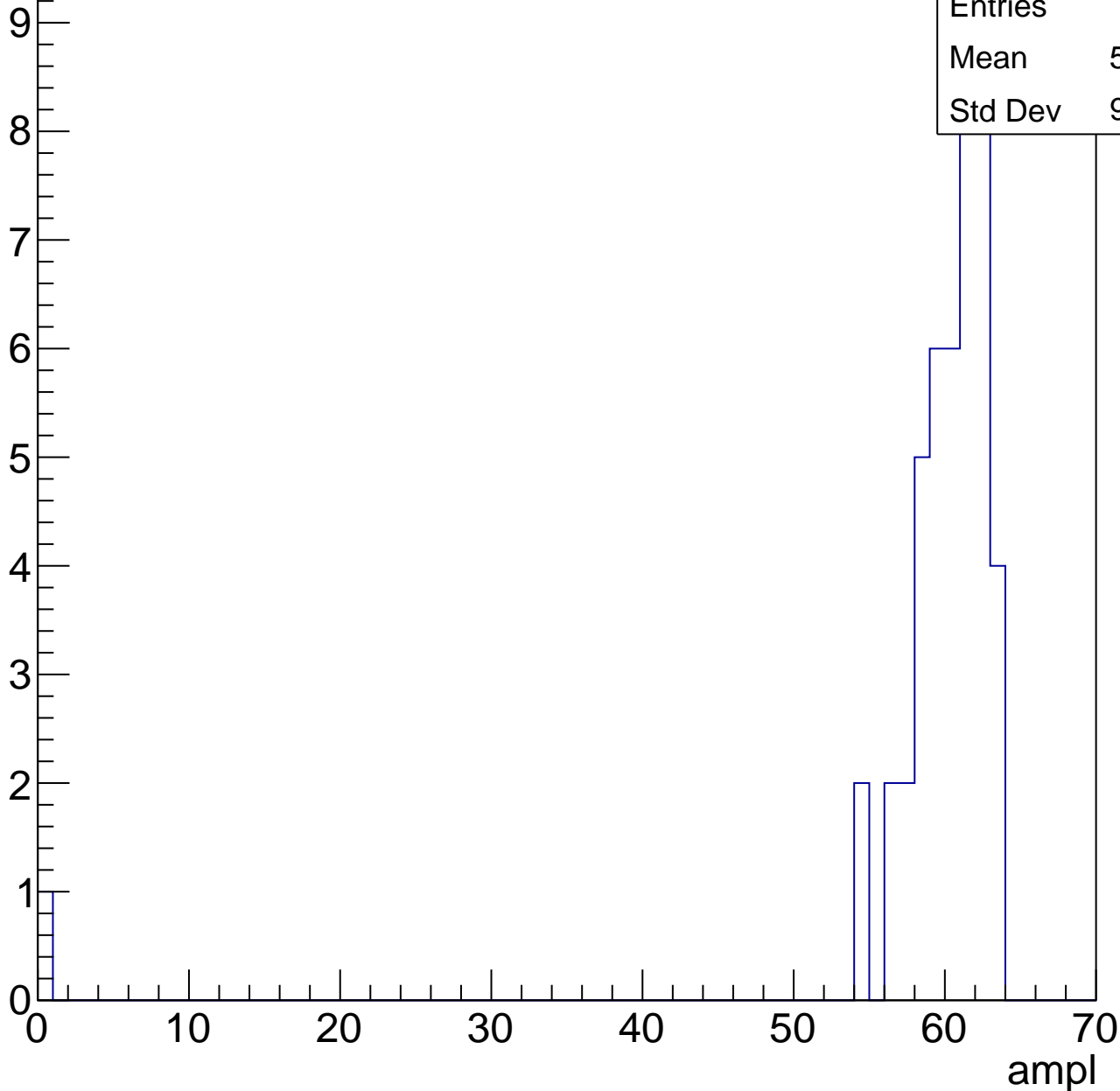


B1L103S, U10-ch16, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	58.58
Std Dev	9.113



B1L103S, U10-ch16, adc6

calib_packv5_042523_0143.root, FC#7, port C2

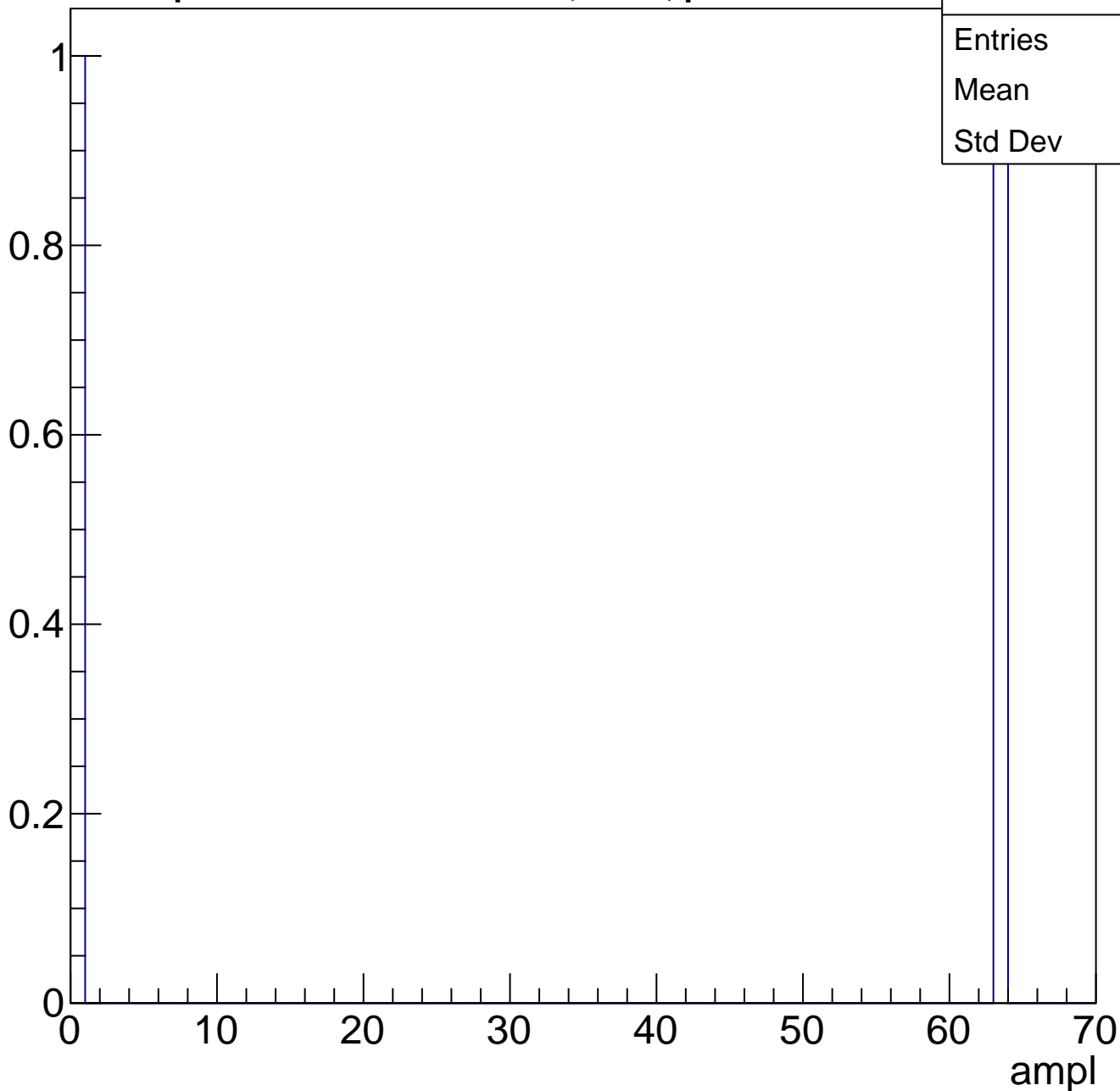
Entry



B1L103S, U10-ch16, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch17, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	30.36
Std Dev	3.588

Gaus mean : 30.7391

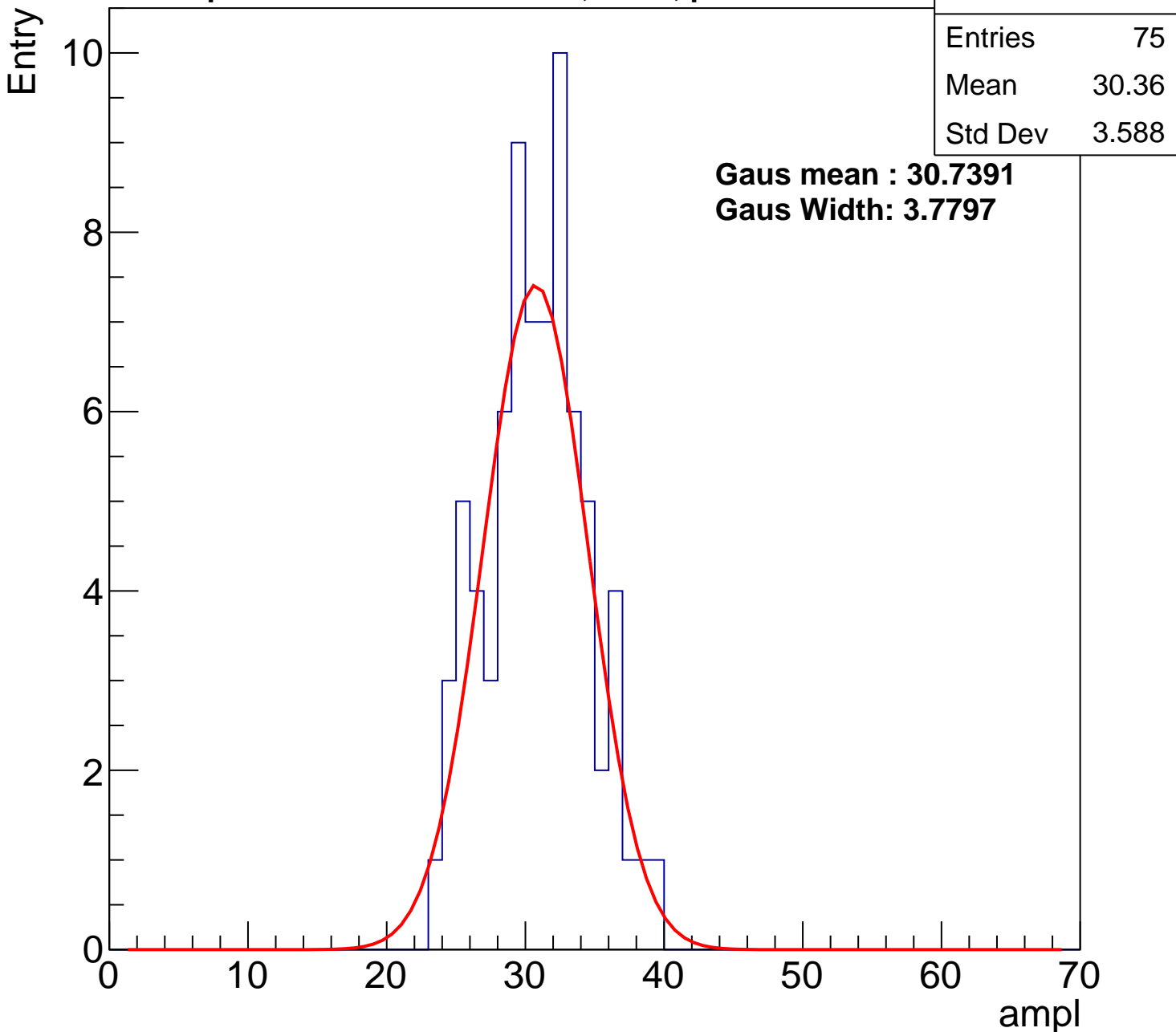
Gaus Width: 3.7797

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch17, adc1

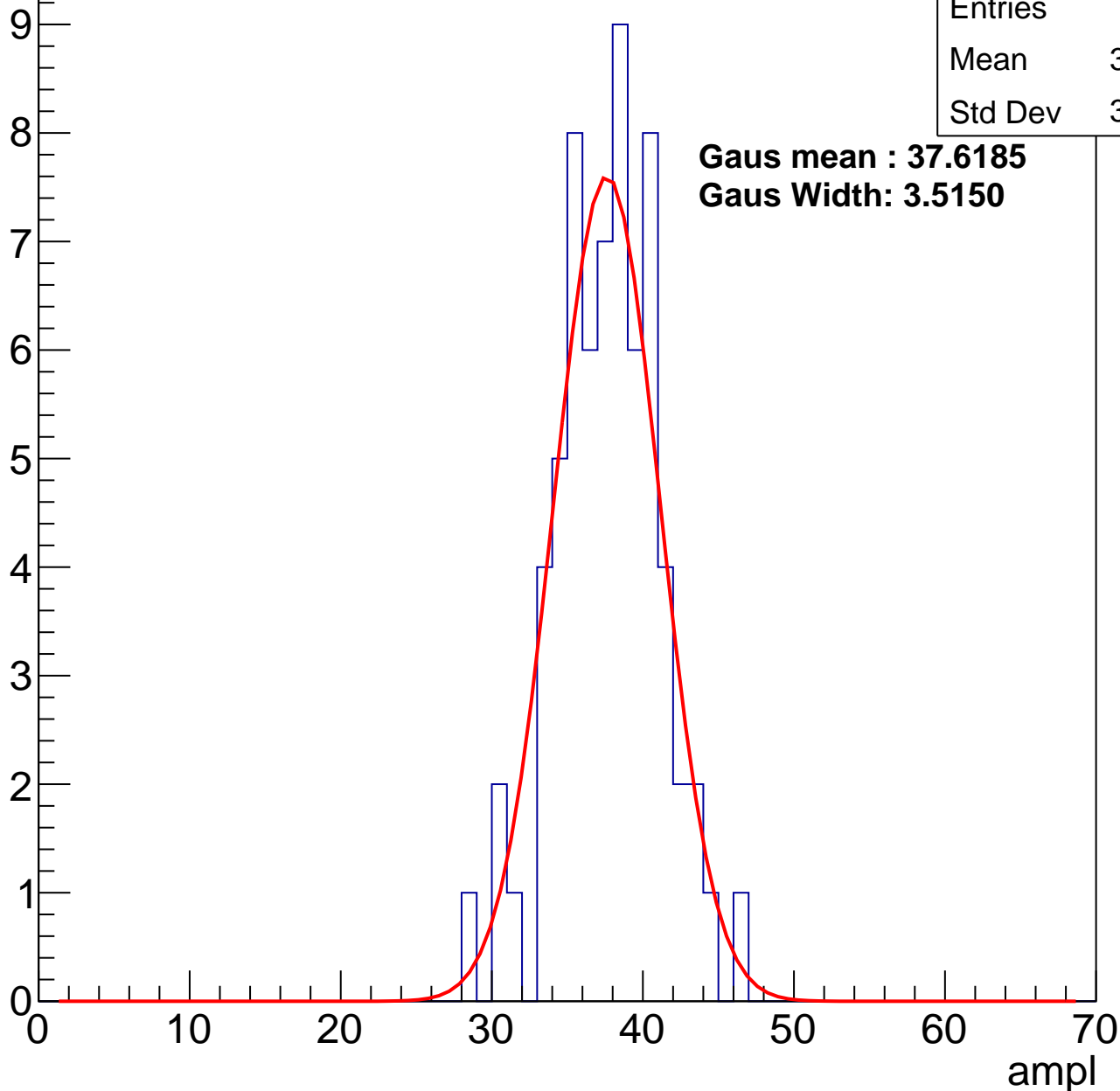
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37.25
Std Dev	3.409

Gaus mean : 37.6185

Gaus Width: 3.5150



B1L103S, U10-ch17, adc2

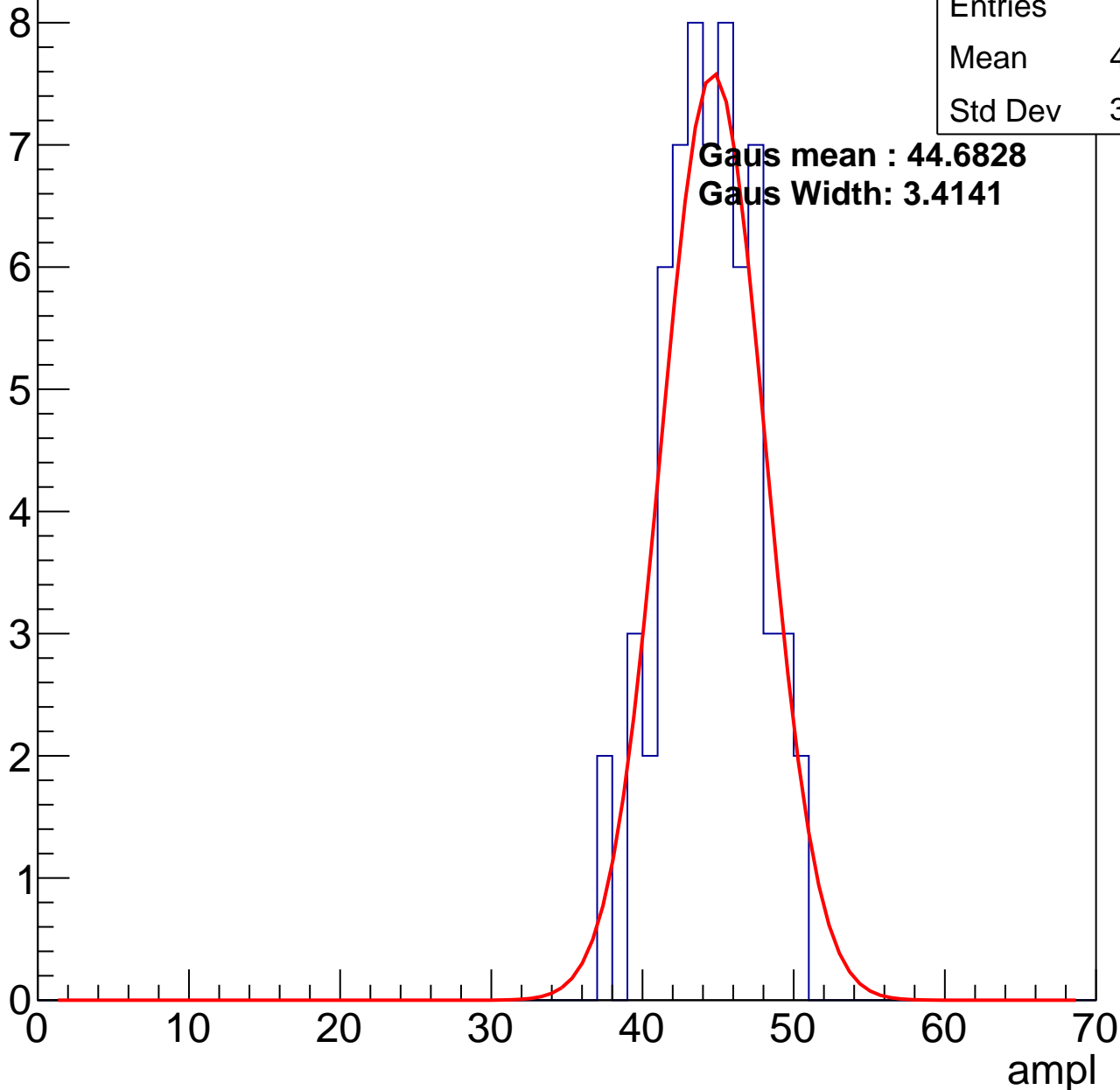
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	44.05
Std Dev	3.023

Gaus mean : 44.6828

Gaus Width: 3.4141



B1L103S, U10-ch17, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	83
Mean	51.39
Std Dev	3.706

Entry

10

8

6

4

2

0

0

10

20

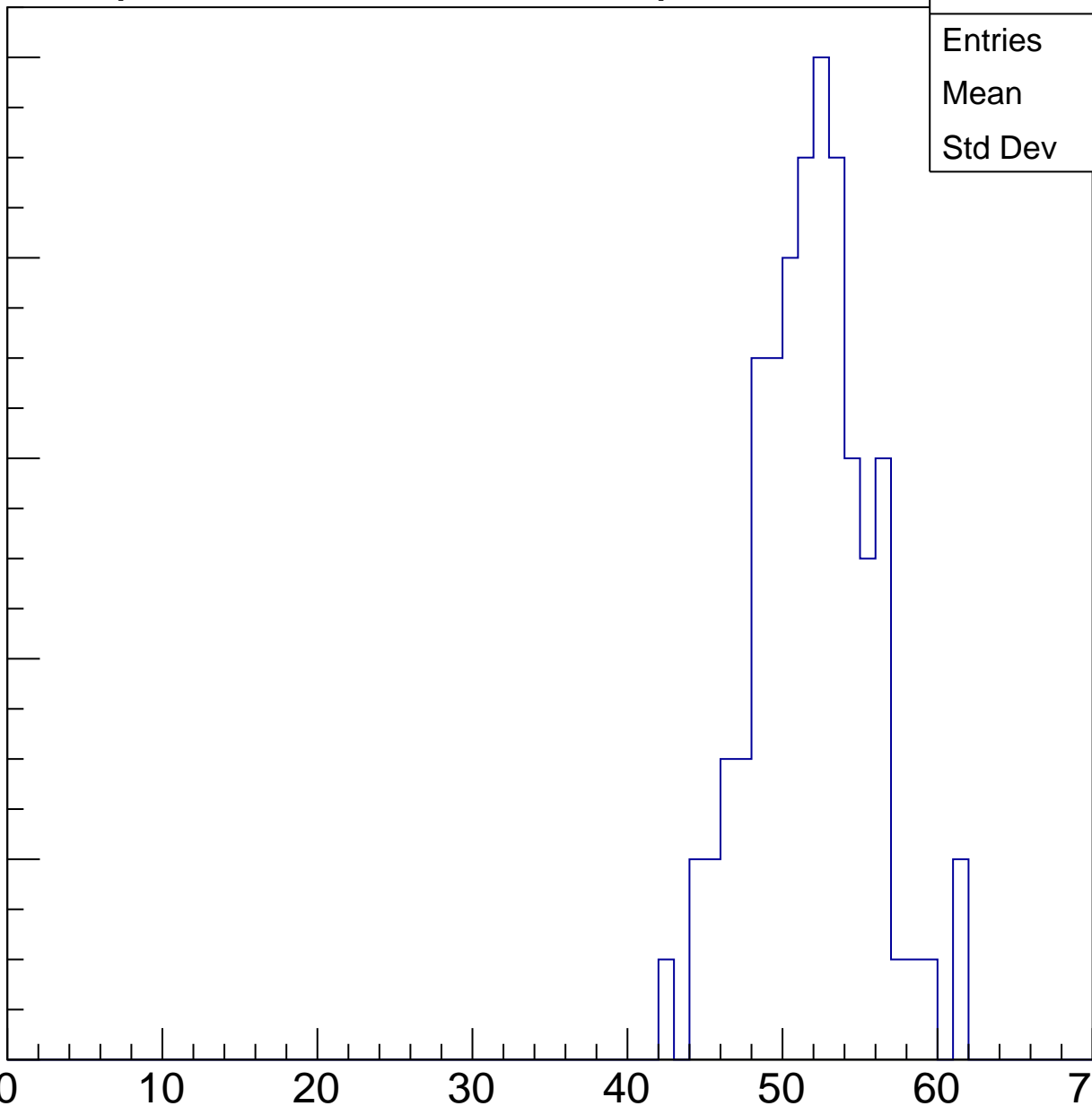
30

40

50

60

ampl

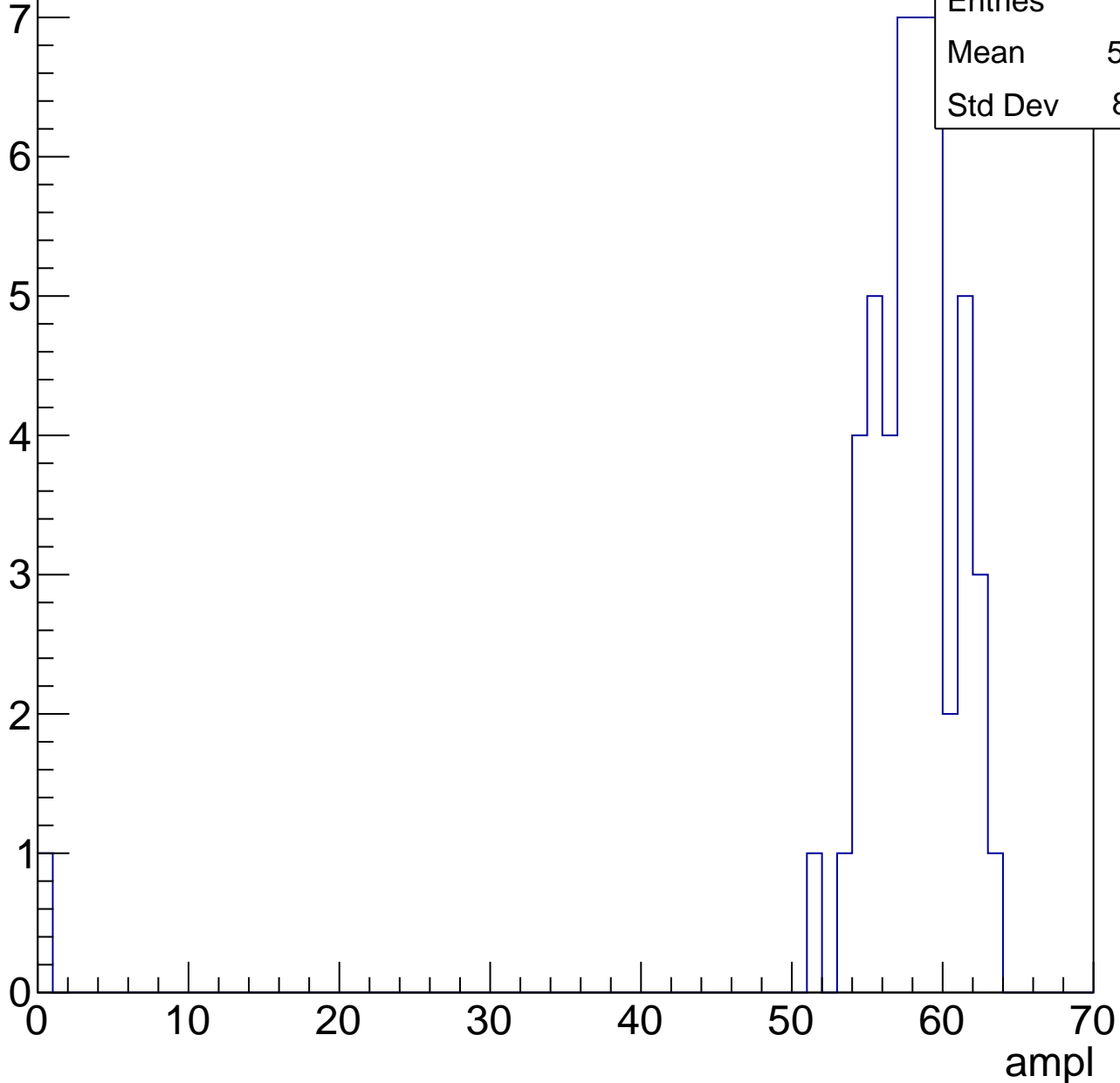


B1L103S, U10-ch17, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	56.48
Std Dev	8.651



B1L103S, U10-ch17, adc5

calib_packv5_042523_0143.root, FC#7, port C2

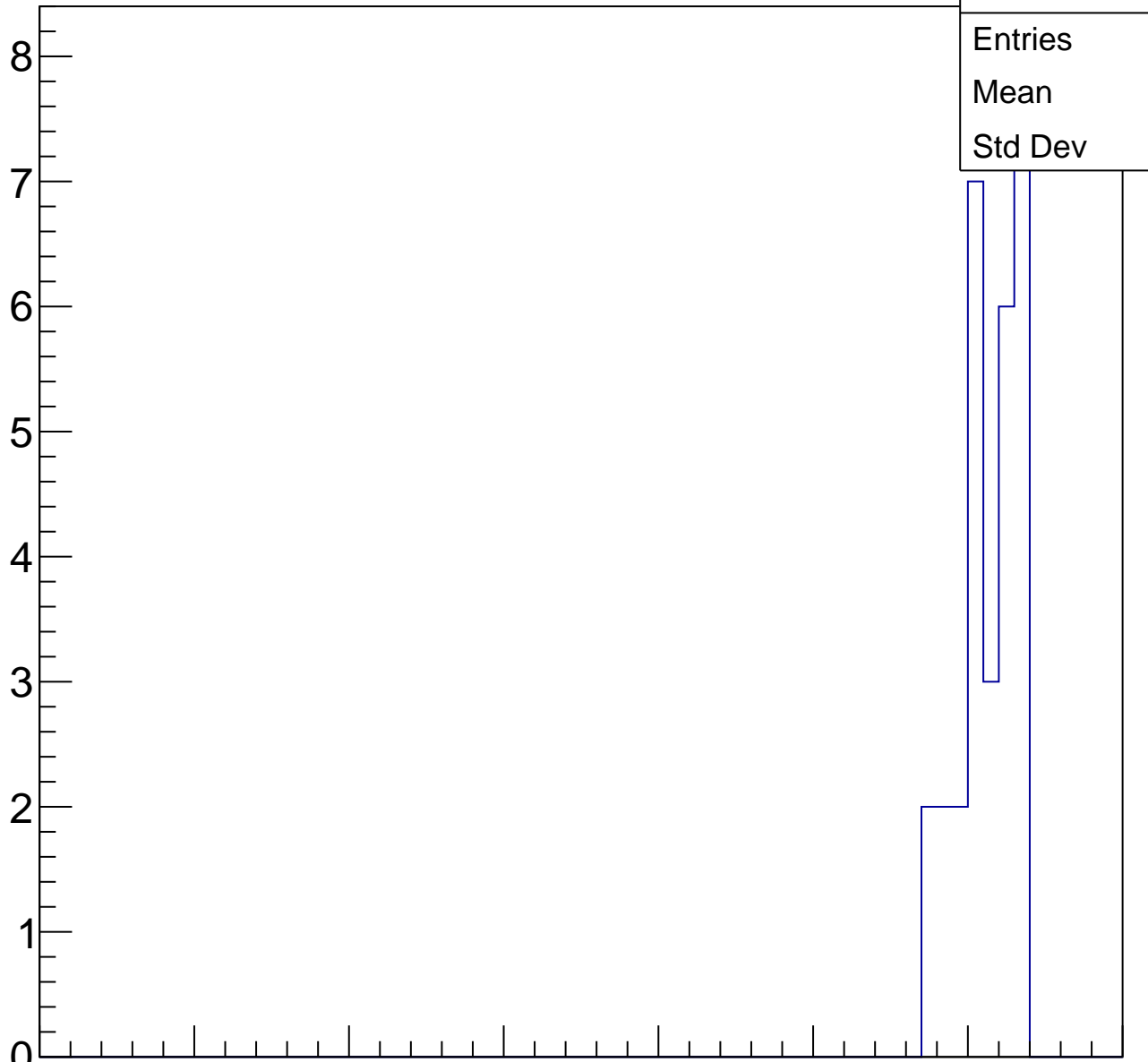
Entry

8
7
6
5
4
3
2
1
0

Entries	30
Mean	60.9
Std Dev	1.85

ampl

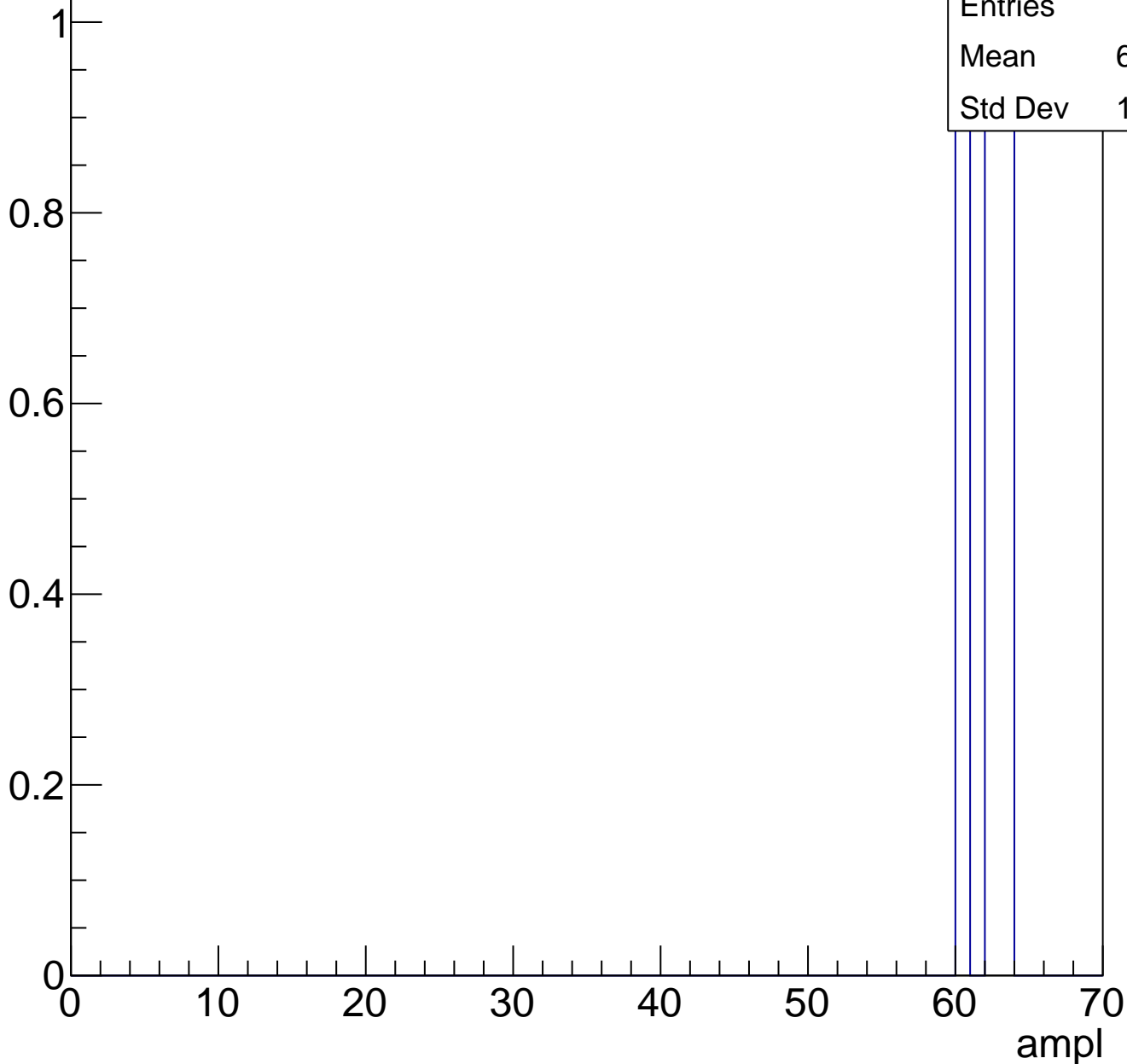
0 10 20 30 40 50 60 70



B1L103S, U10-ch17, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch17, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch18, adc0

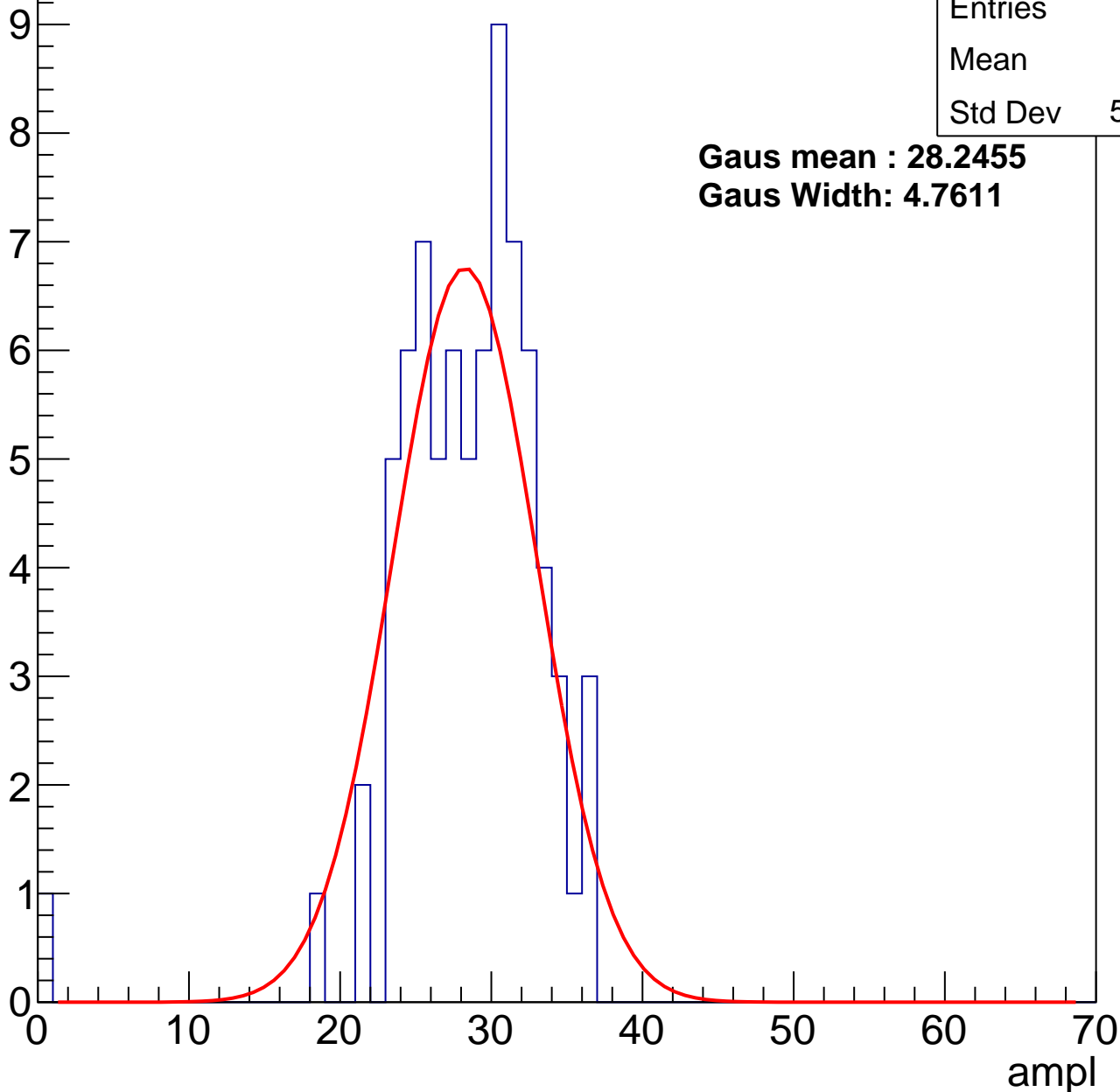
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	28
Std Dev	5.022

Gaus mean : 28.2455

Gaus Width: 4.7611



B1L103S, U10-ch18, adc1

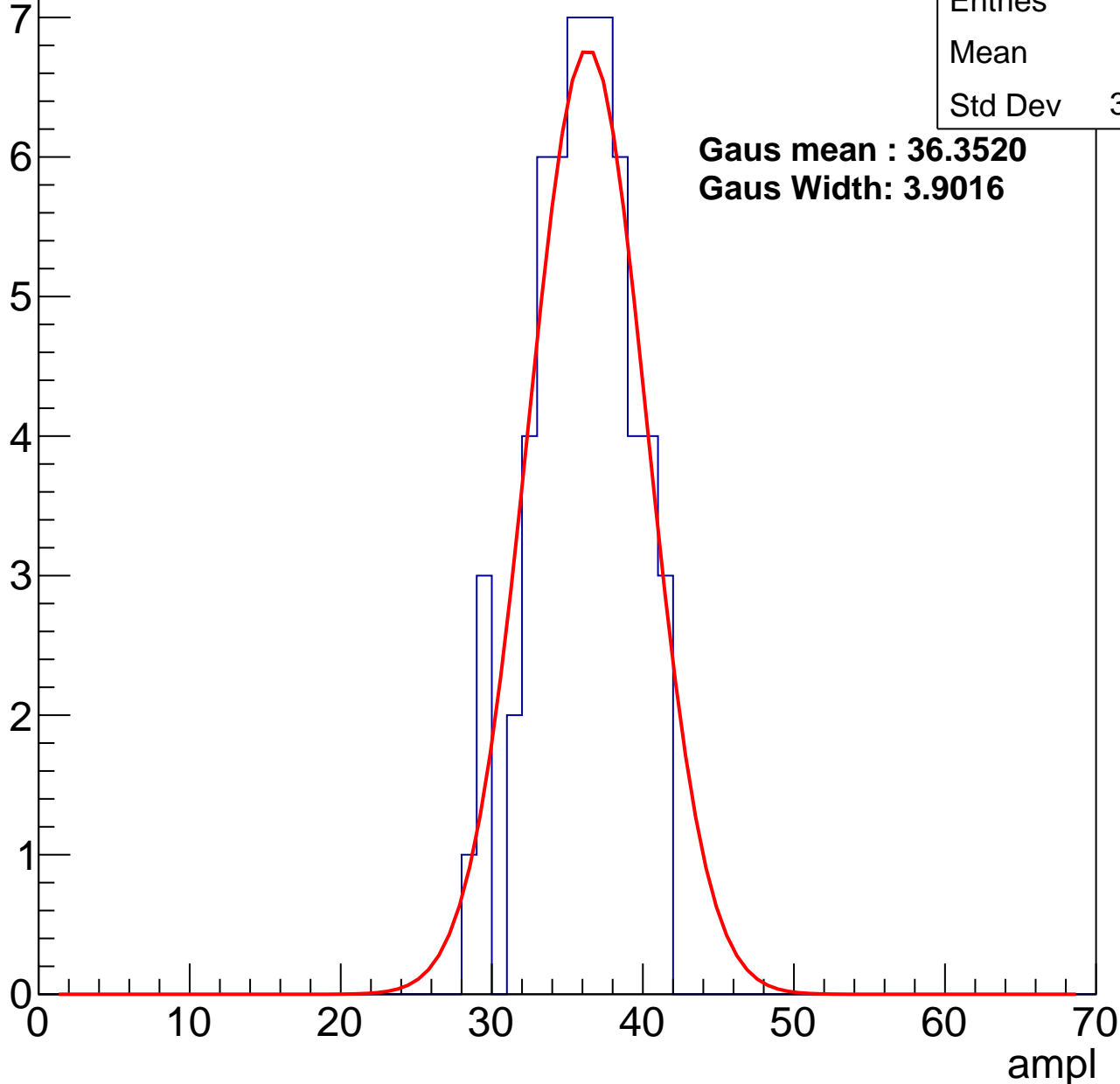
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.5
Std Dev	3.165

Gaus mean : 36.3520

Gaus Width: 3.9016



B1L103S, U10-ch18, adc2

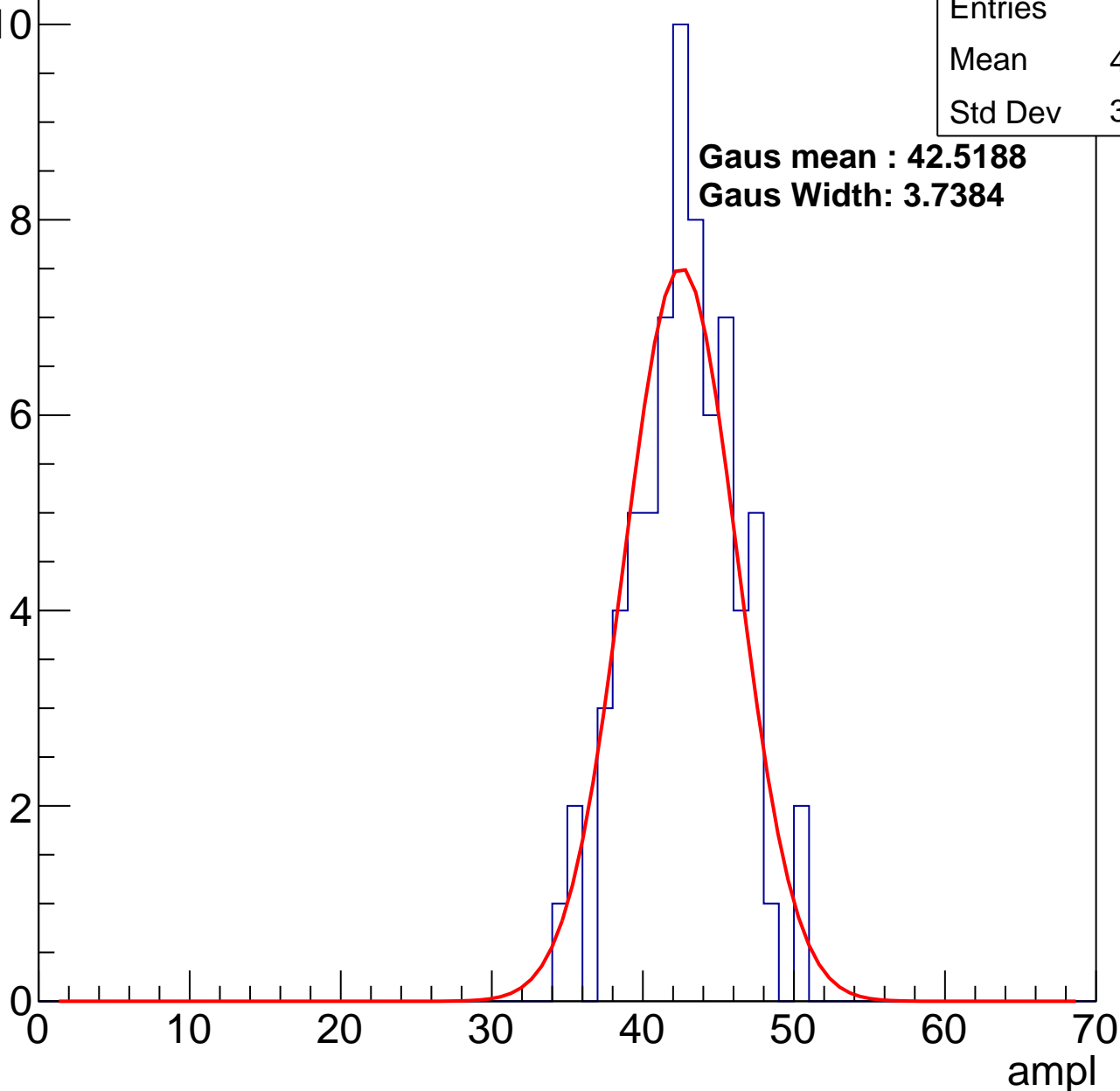
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	42.27
Std Dev	3.414

Gaus mean : 42.5188

Gaus Width: 3.7384

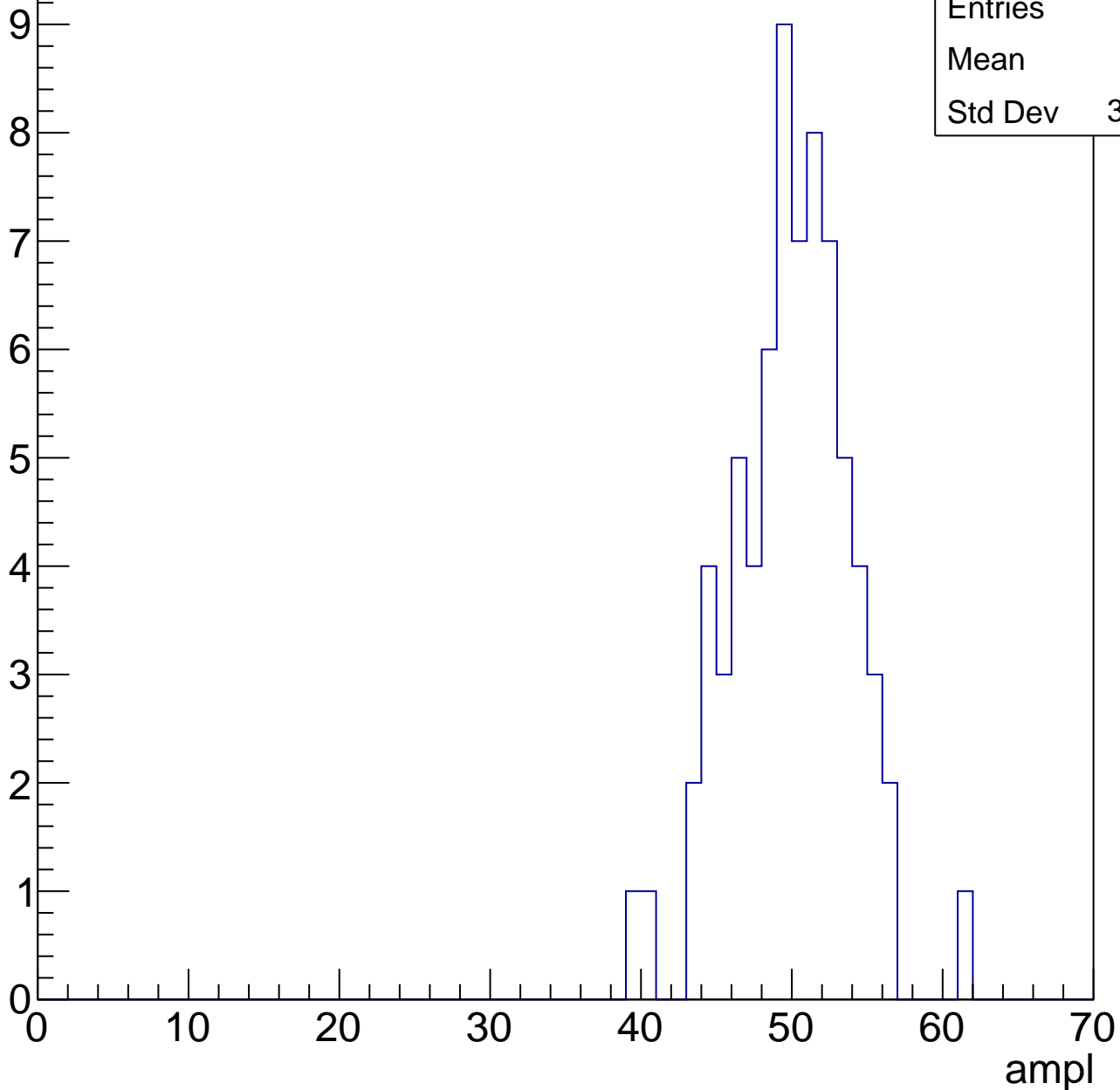


B1L103S, U10-ch18, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	49.5
Std Dev	3.877

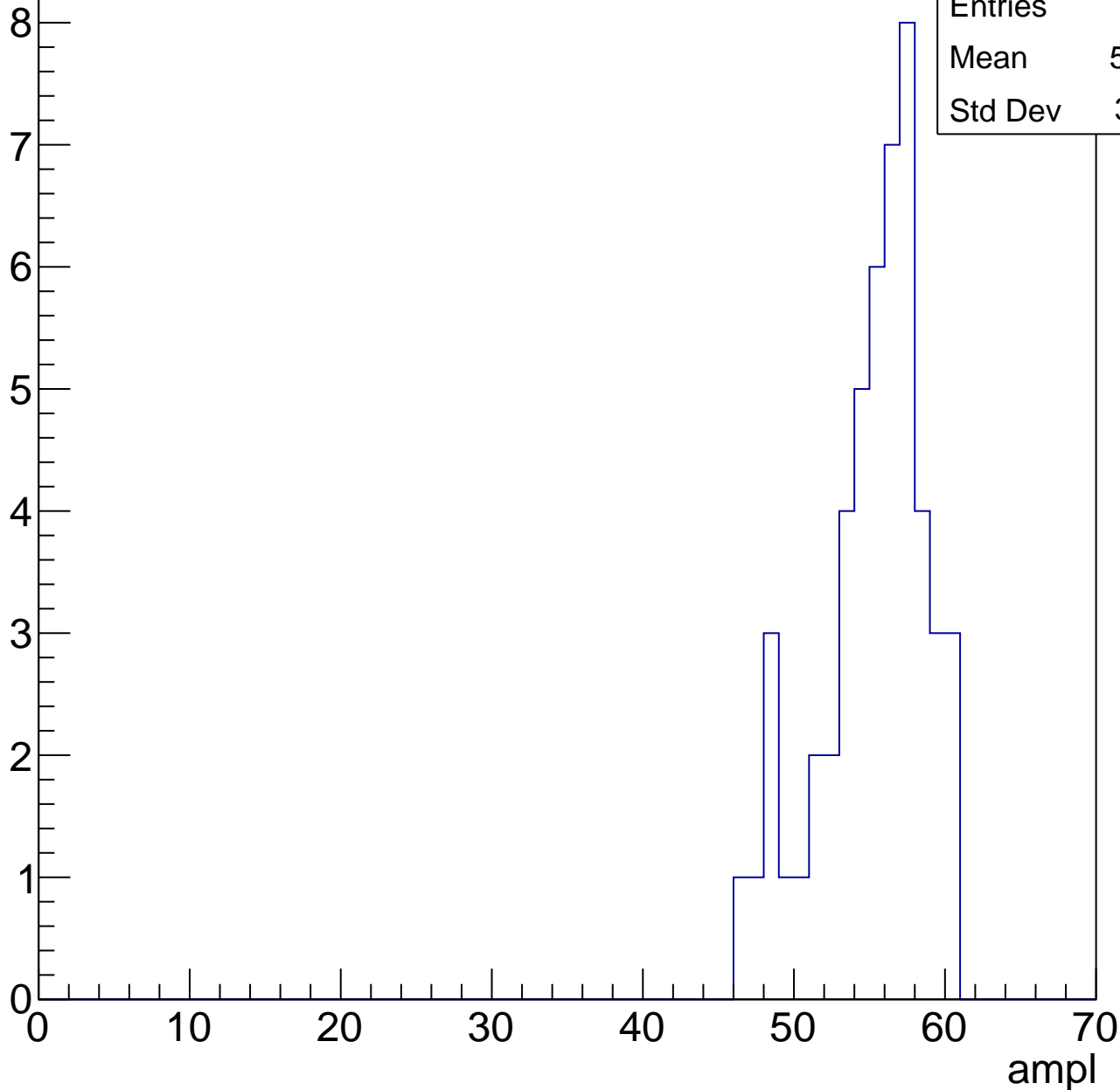


B1L103S, U10-ch18, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

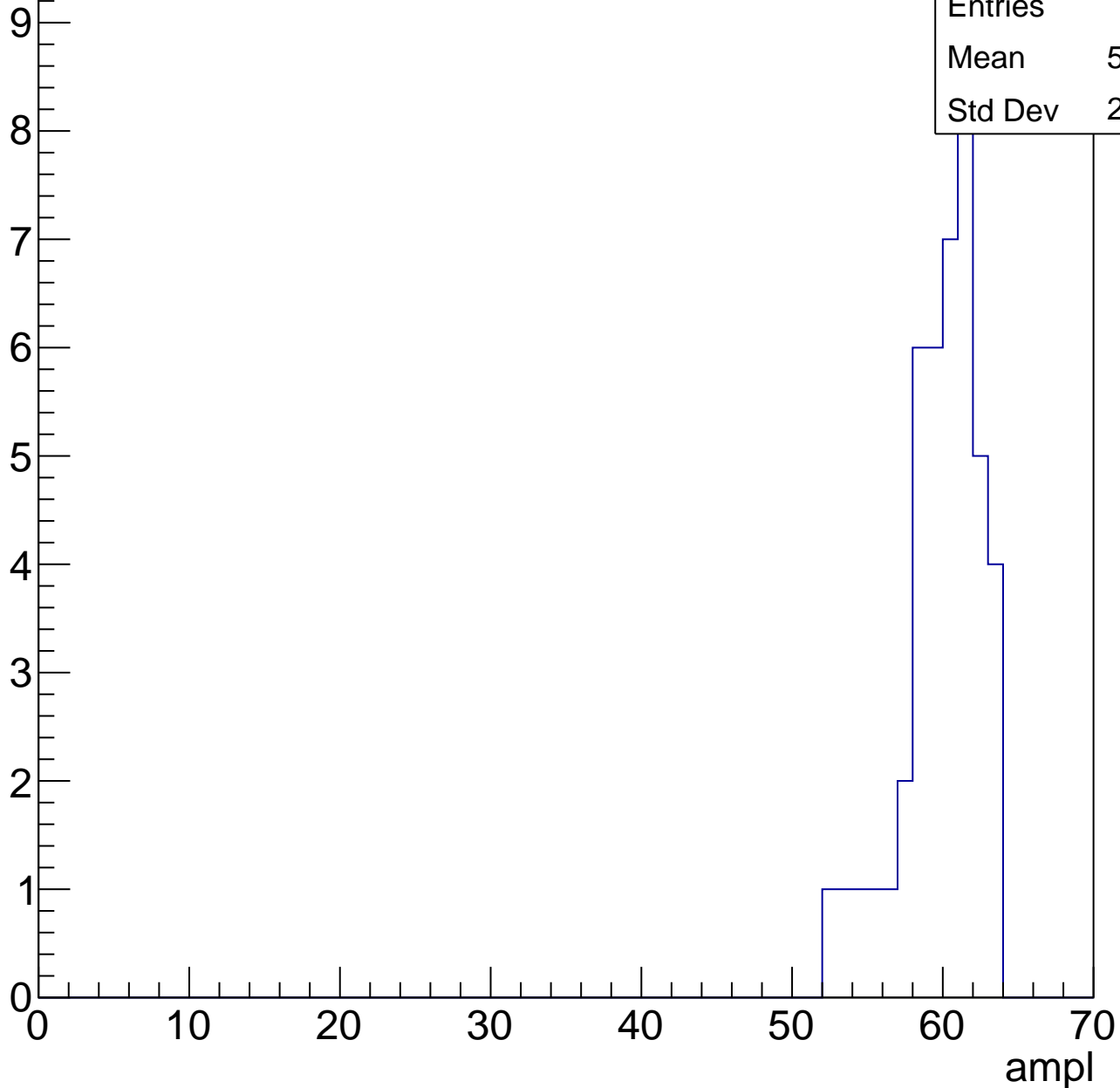
Entries	51
Mean	54.73
Std Dev	3.481



B1L103S, U10-ch18, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

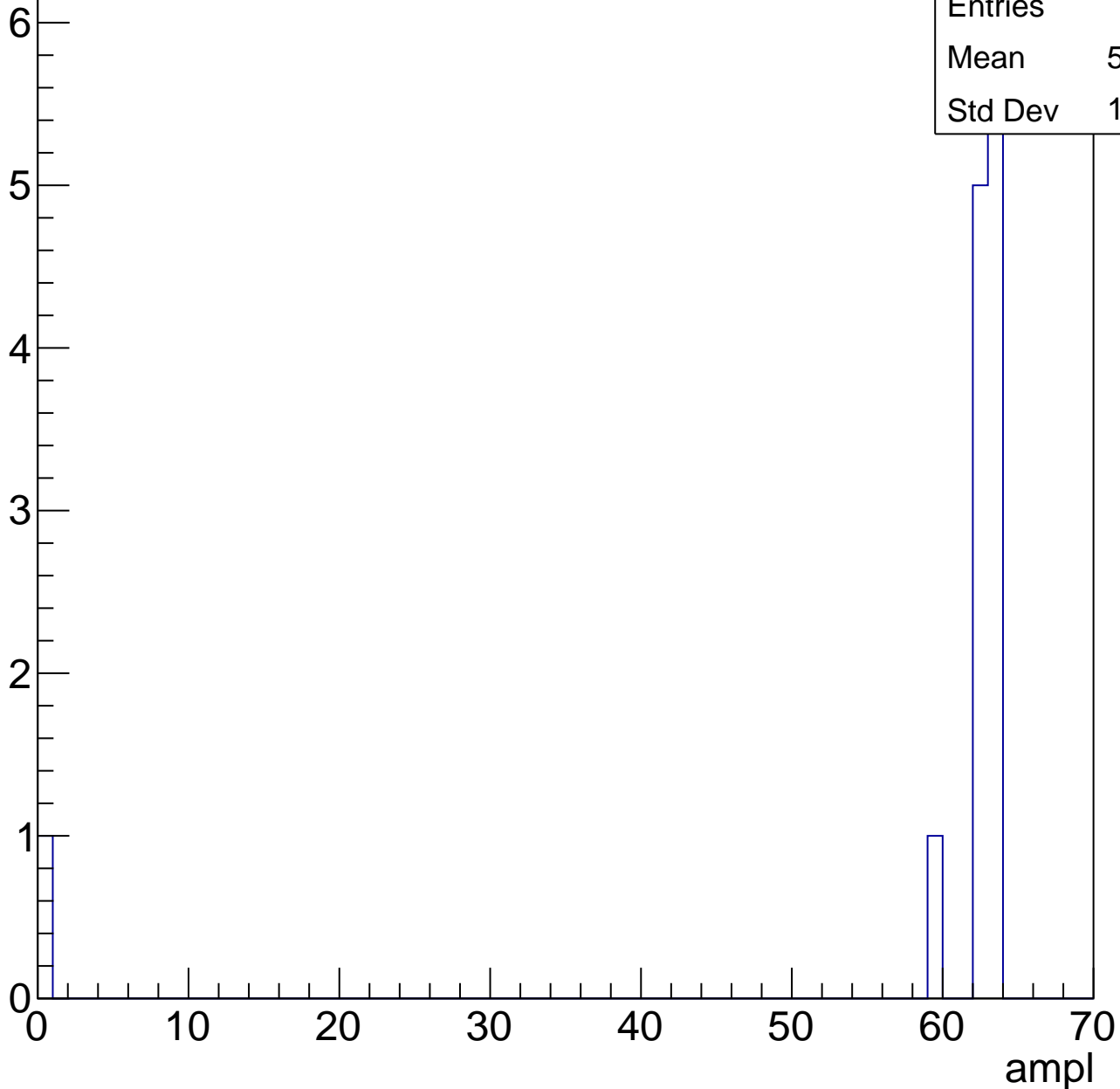


B1L103S, U10-ch18, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	57.46
Std Dev	16.62



B1L103S, U10-ch18, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch19, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	30.65
Std Dev	5.178

Gaus mean : 31.2594

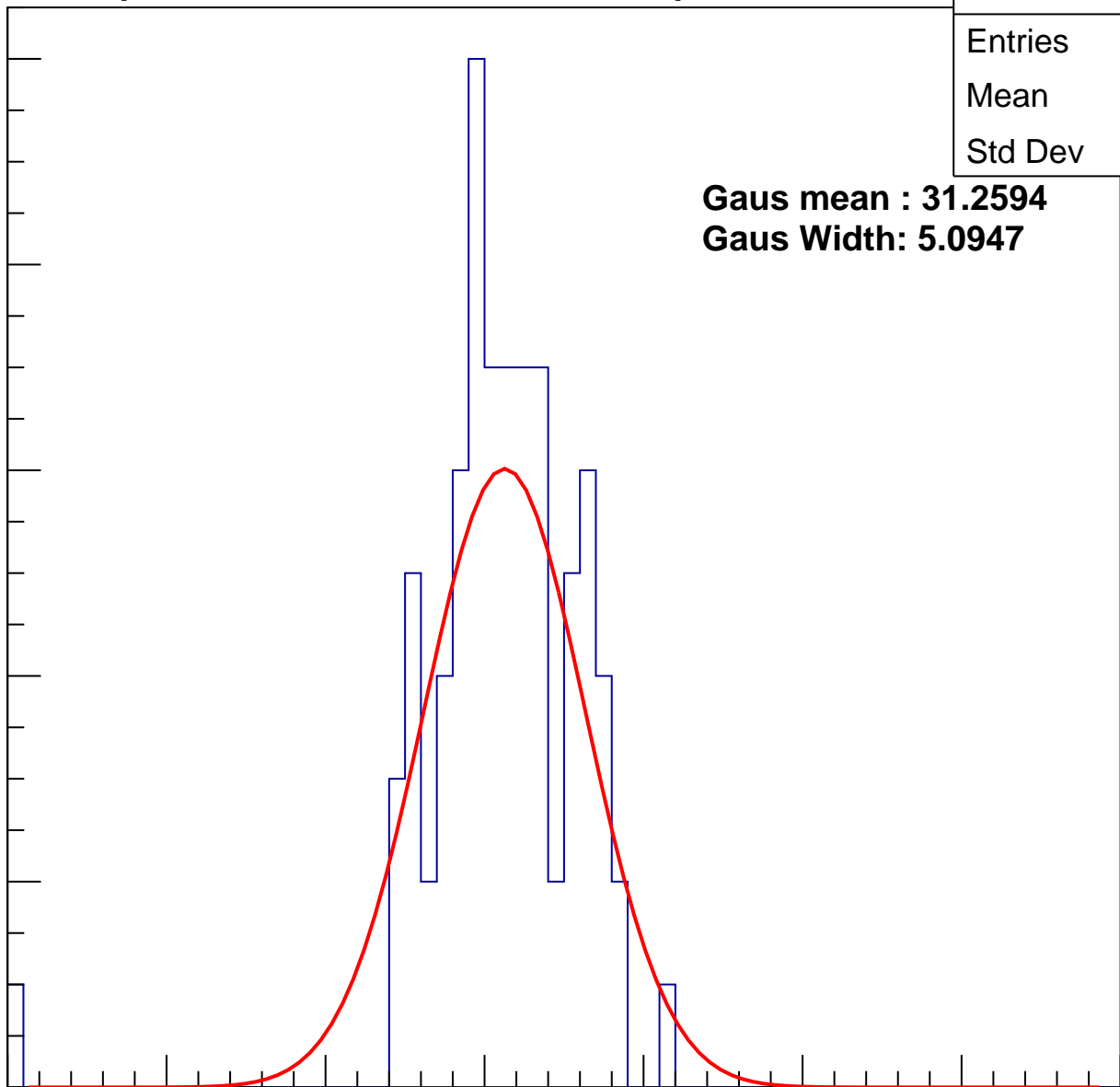
Gaus Width: 5.0947

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch19, adc1

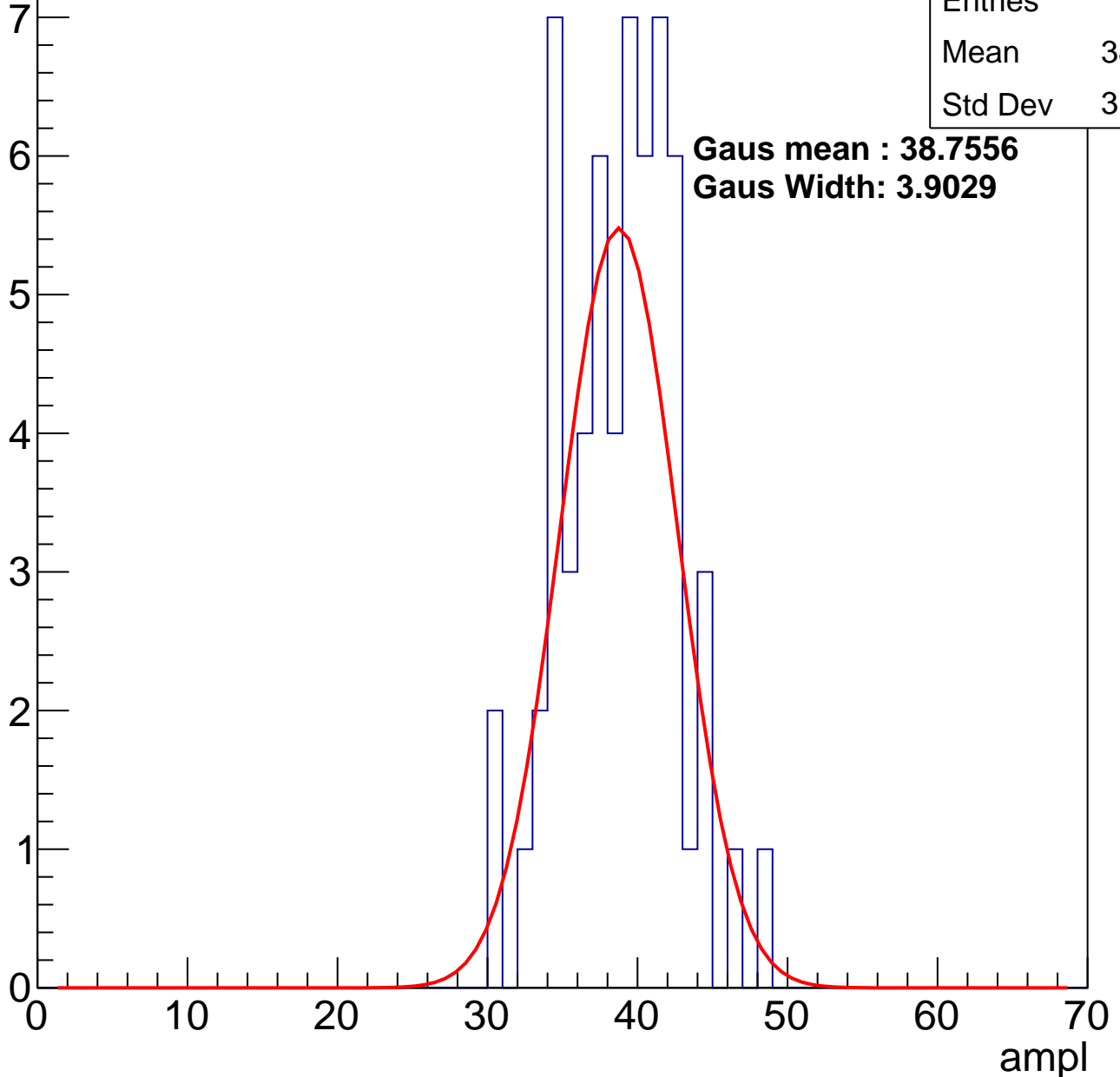
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	38.36
Std Dev	3.742

Gaus mean : 38.7556

Gaus Width: 3.9029



B1L103S, U10-ch19, adc2

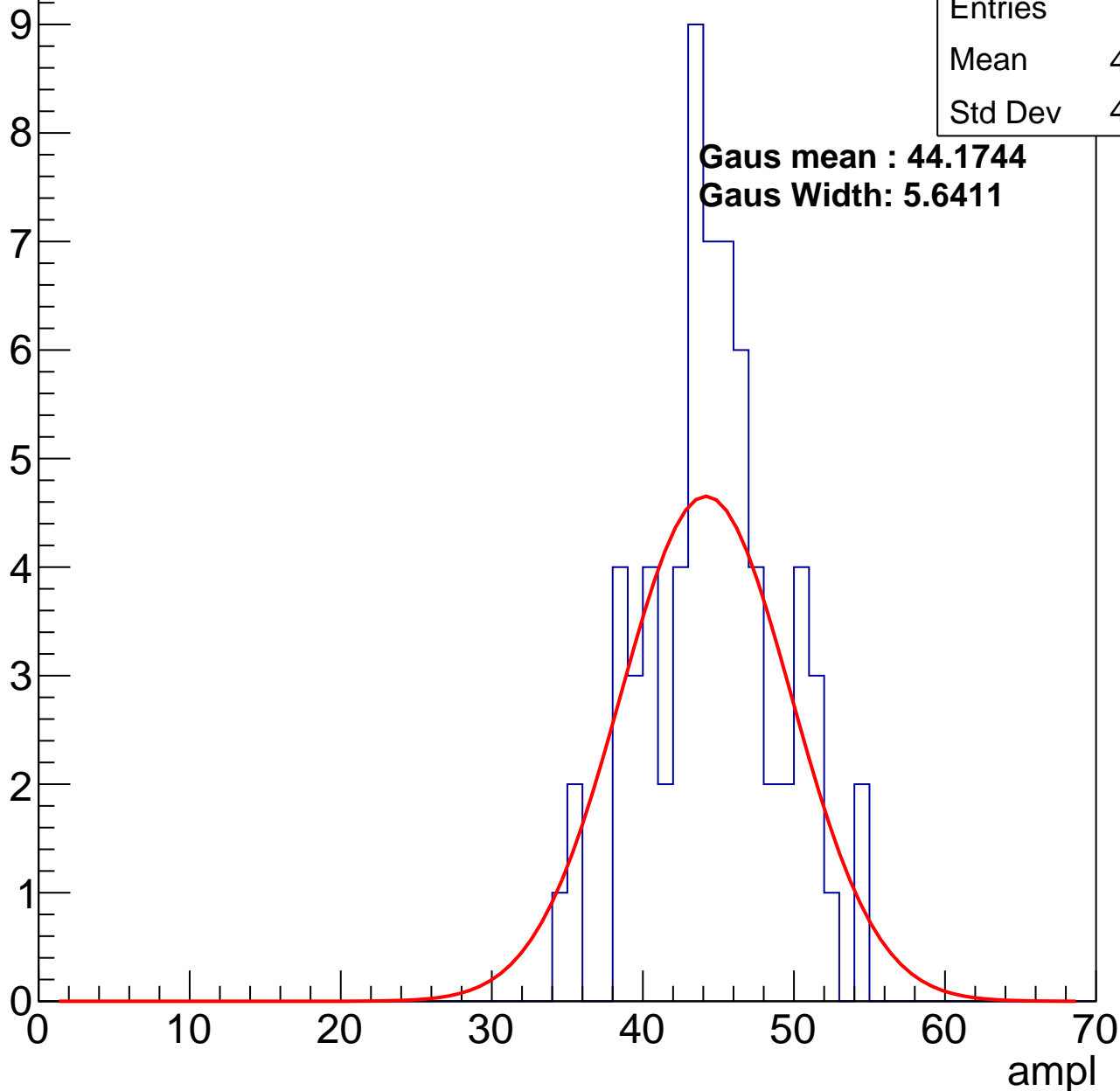
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	44.24
Std Dev	4.385

Gaus mean : 44.1744

Gaus Width: 5.6411

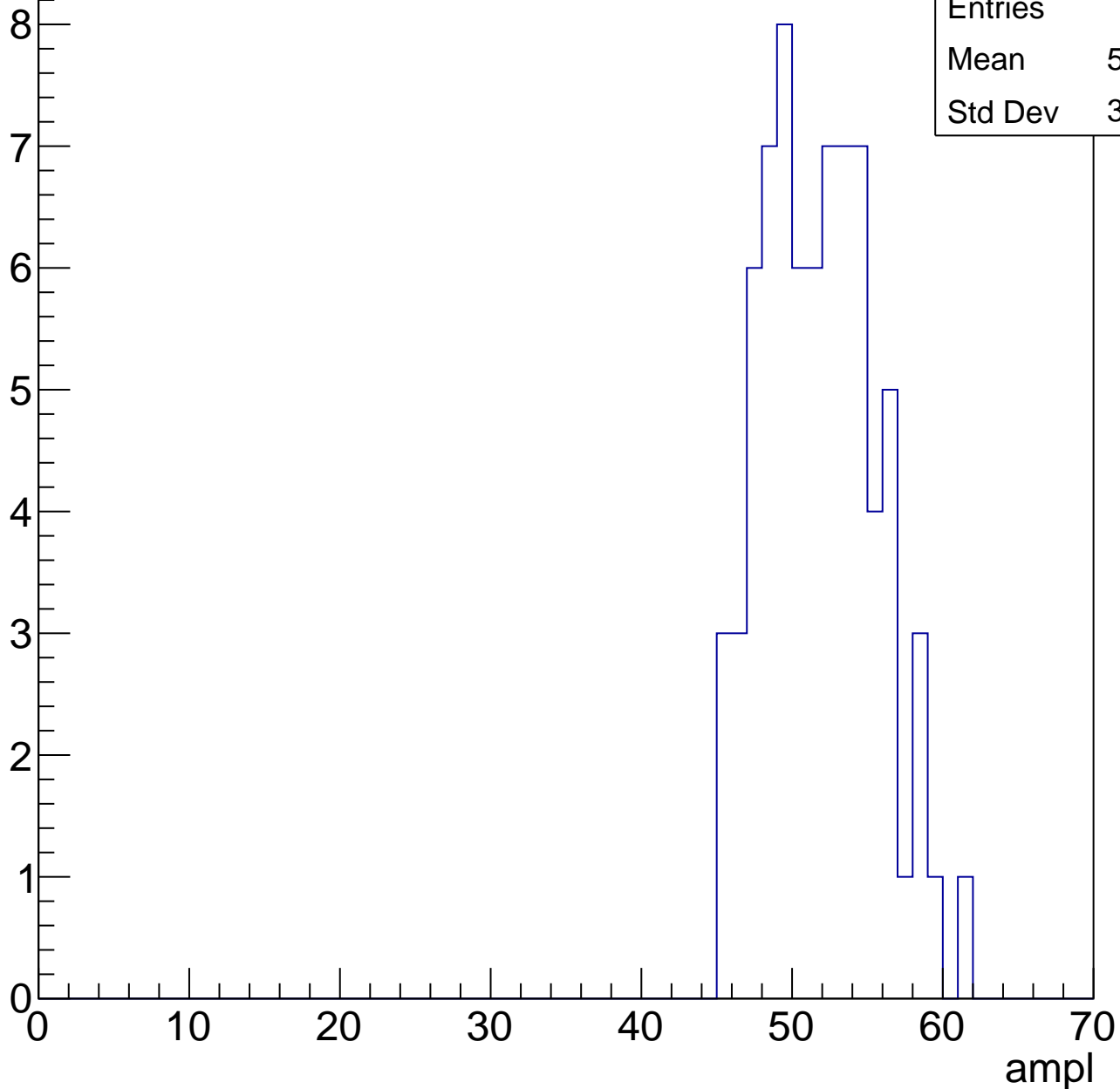


B1L103S, U10-ch19, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

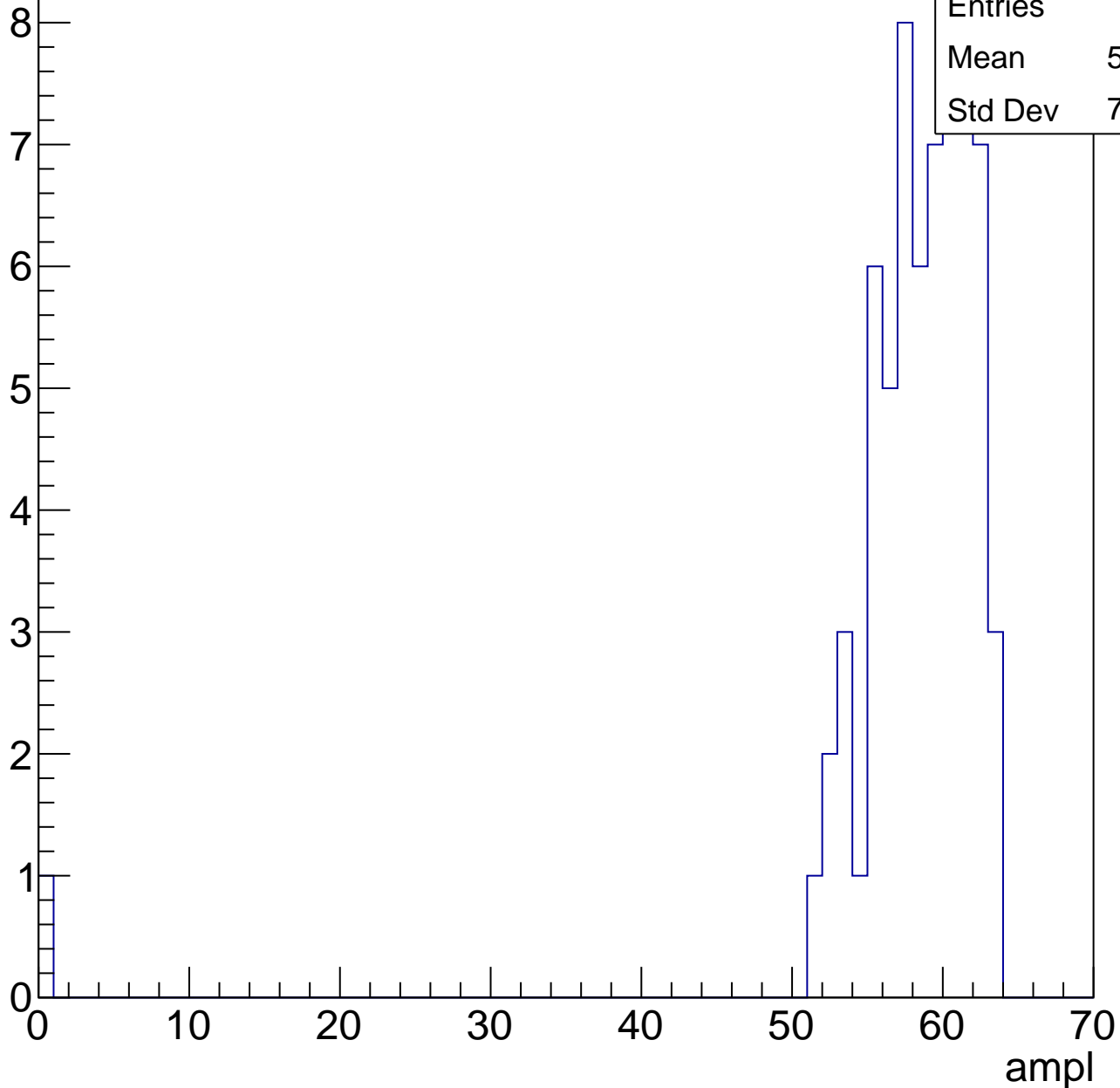
Entries	75
Mean	51.37
Std Dev	3.658



B1L103S, U10-ch19, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

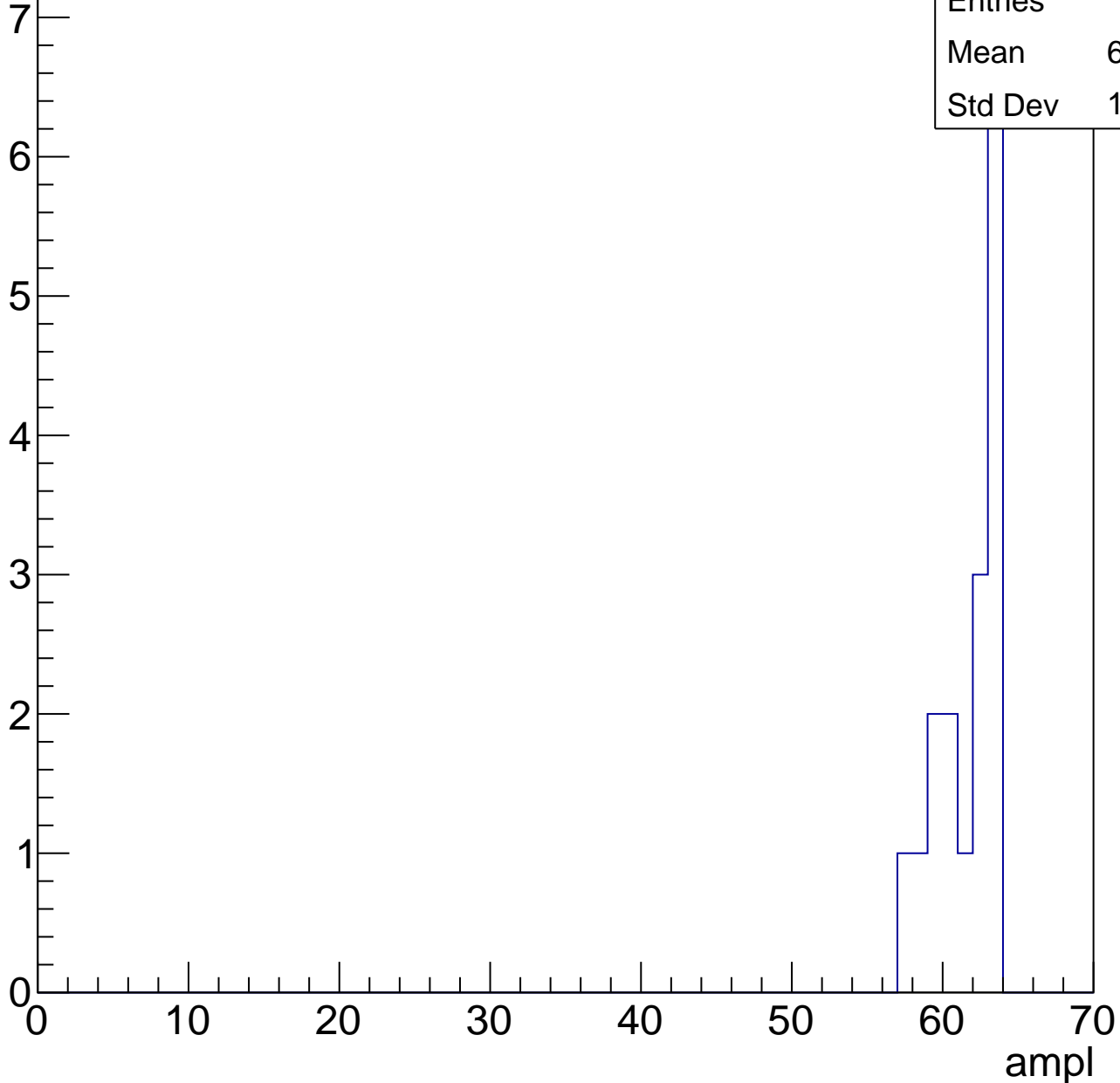


B1L103S, U10-ch19, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	17
Mean	61.24
Std Dev	1.956



B1L103S, U10-ch19, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

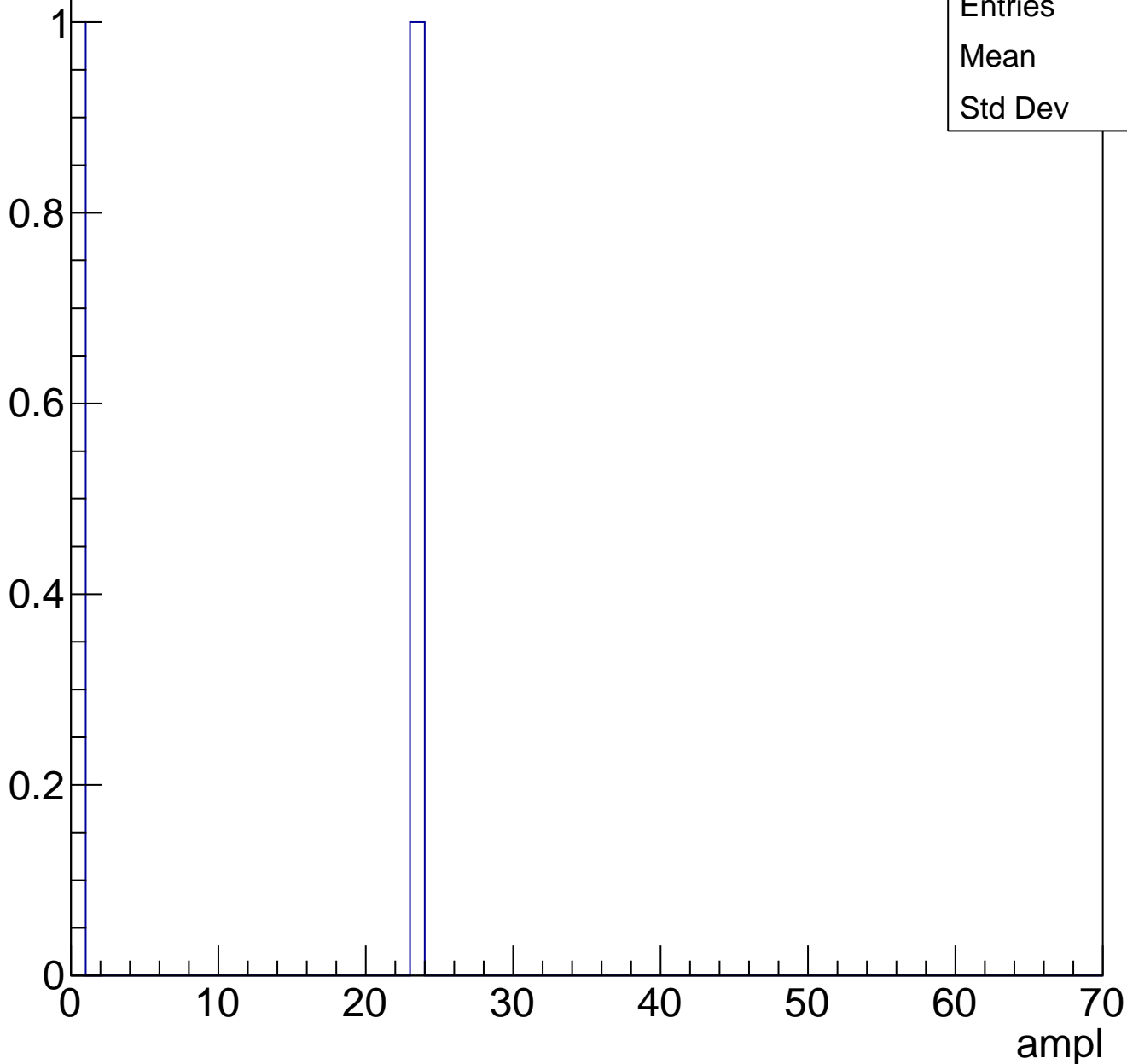


Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch19, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch20, adc0

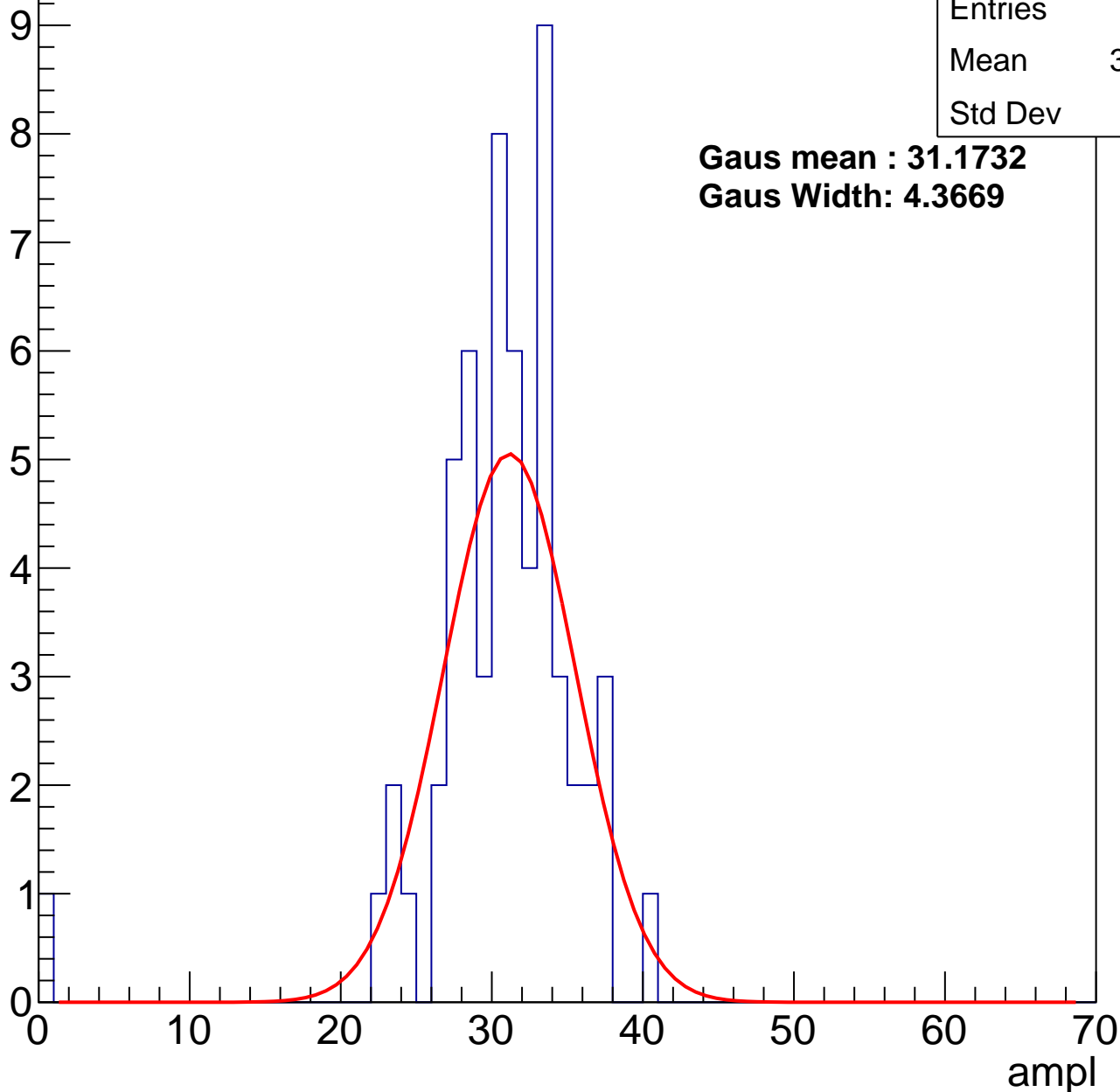
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	30.17
Std Dev	5.4

Gaus mean : 31.1732

Gaus Width: 4.3669



B1L103S, U10-ch20, adc1

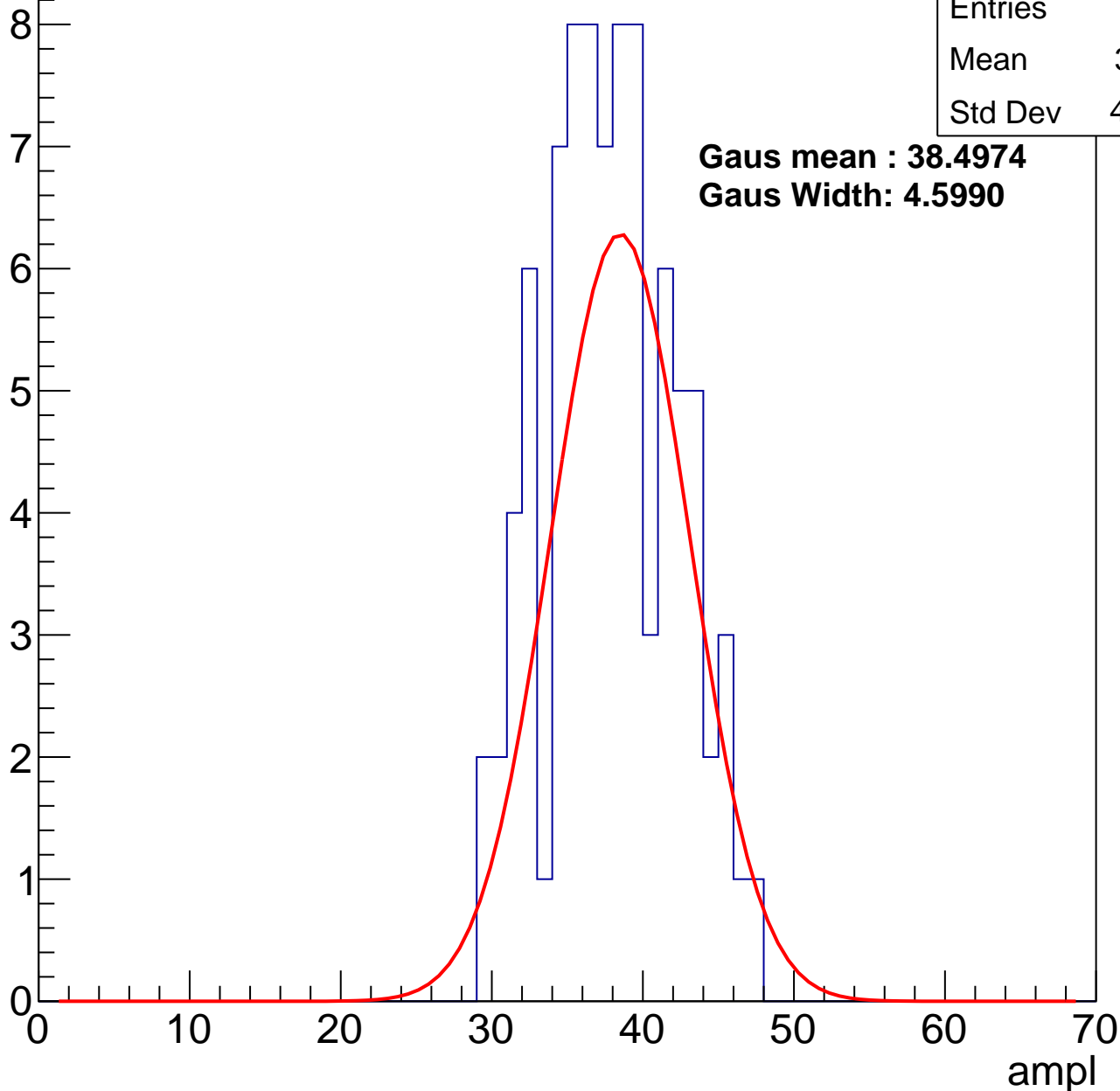
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	87
Mean	37.41
Std Dev	4.239

Gaus mean : 38.4974

Gaus Width: 4.5990



B1L103S, U10-ch20, adc2

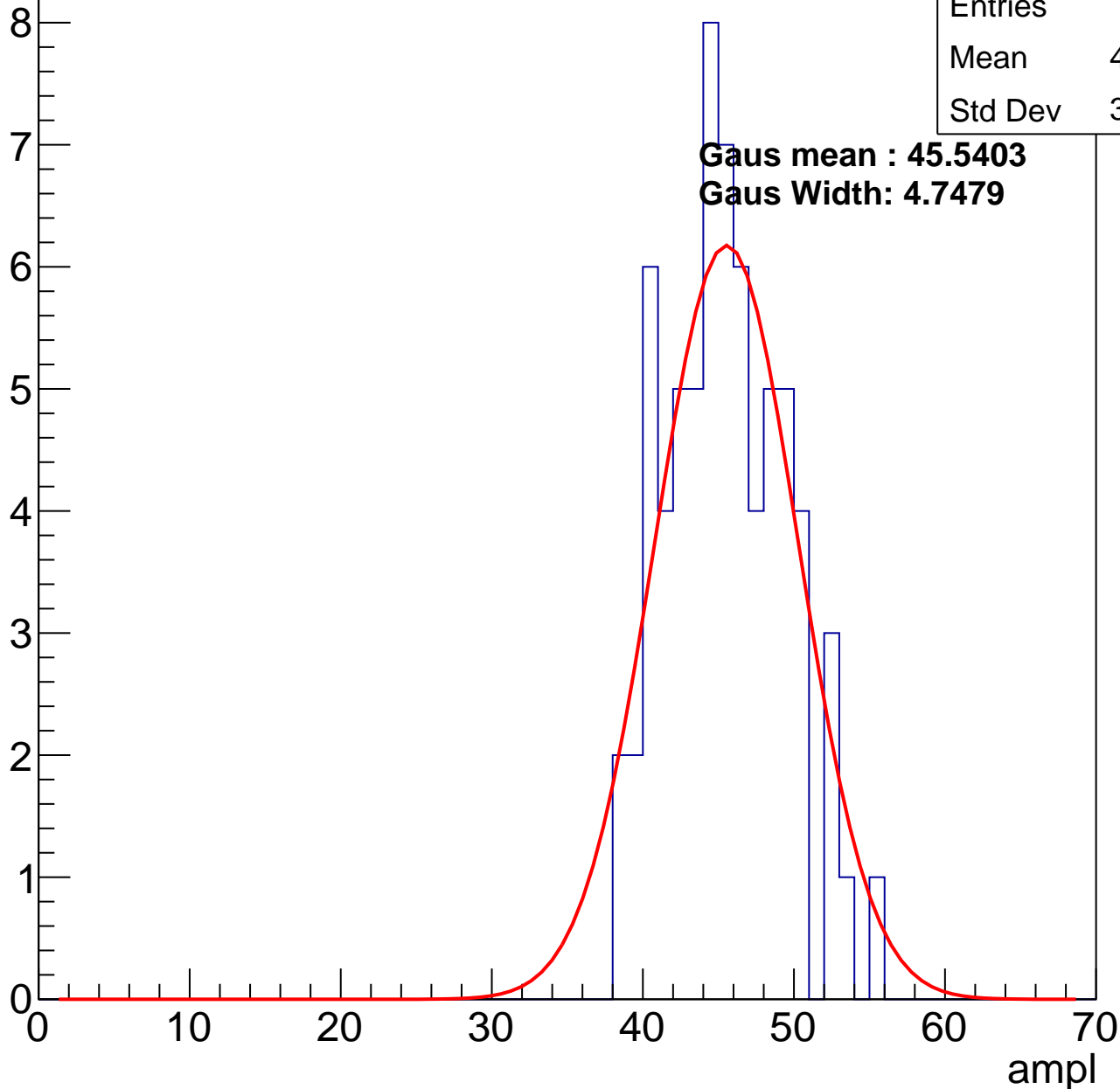
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	45.04
Std Dev	3.863

Gaus mean : 45.5403

Gaus Width: 4.7479

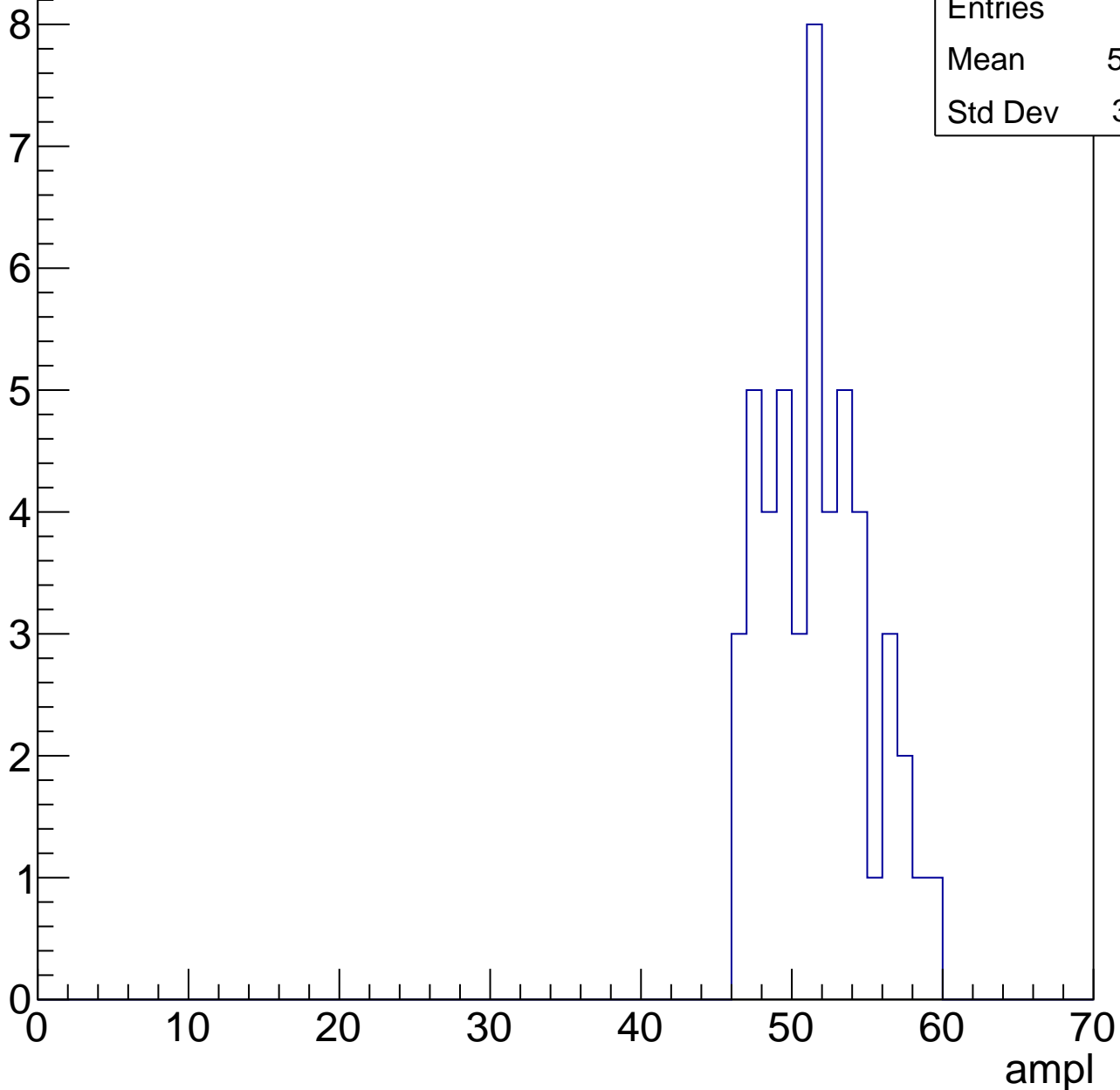


B1L103S, U10-ch20, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	51.24
Std Dev	3.341

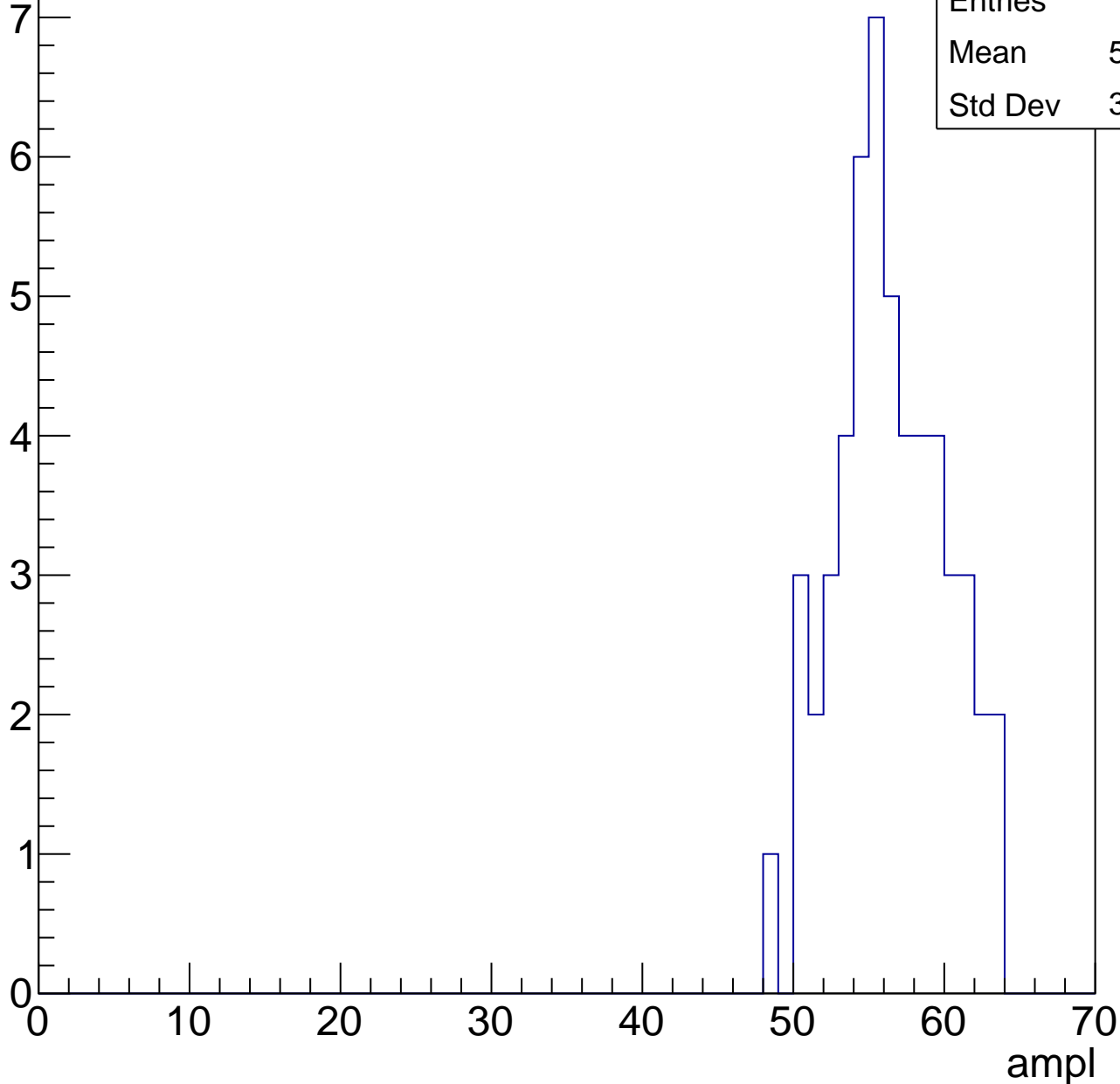


B1L103S, U10-ch20, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.96
Std Dev	3.597

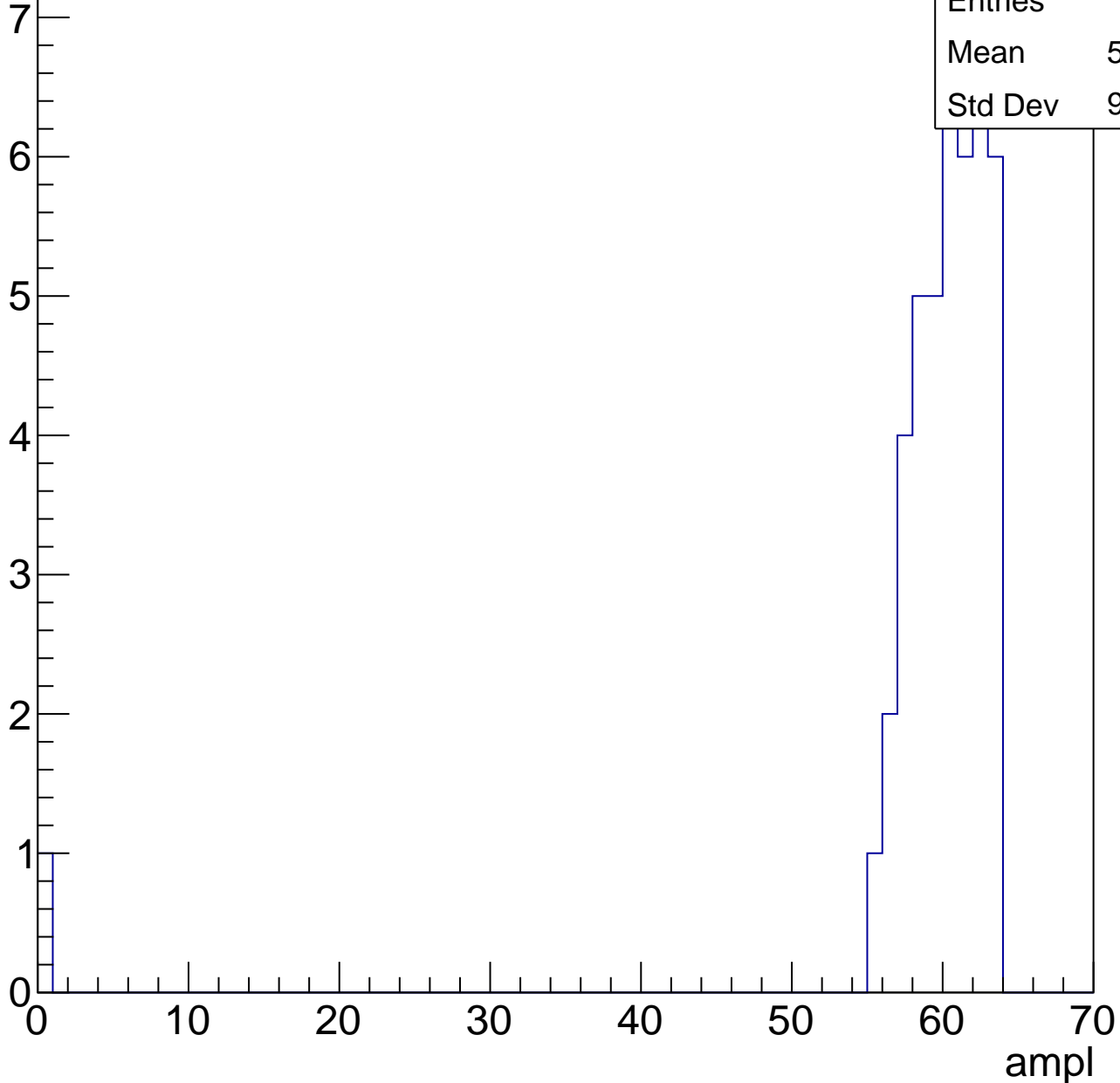


B1L103S, U10-ch20, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.59
Std Dev	9.193



B1L103S, U10-ch20, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	62.25
Std Dev	0.8292

B1L103S, U10-ch20, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U10-ch21, adc0

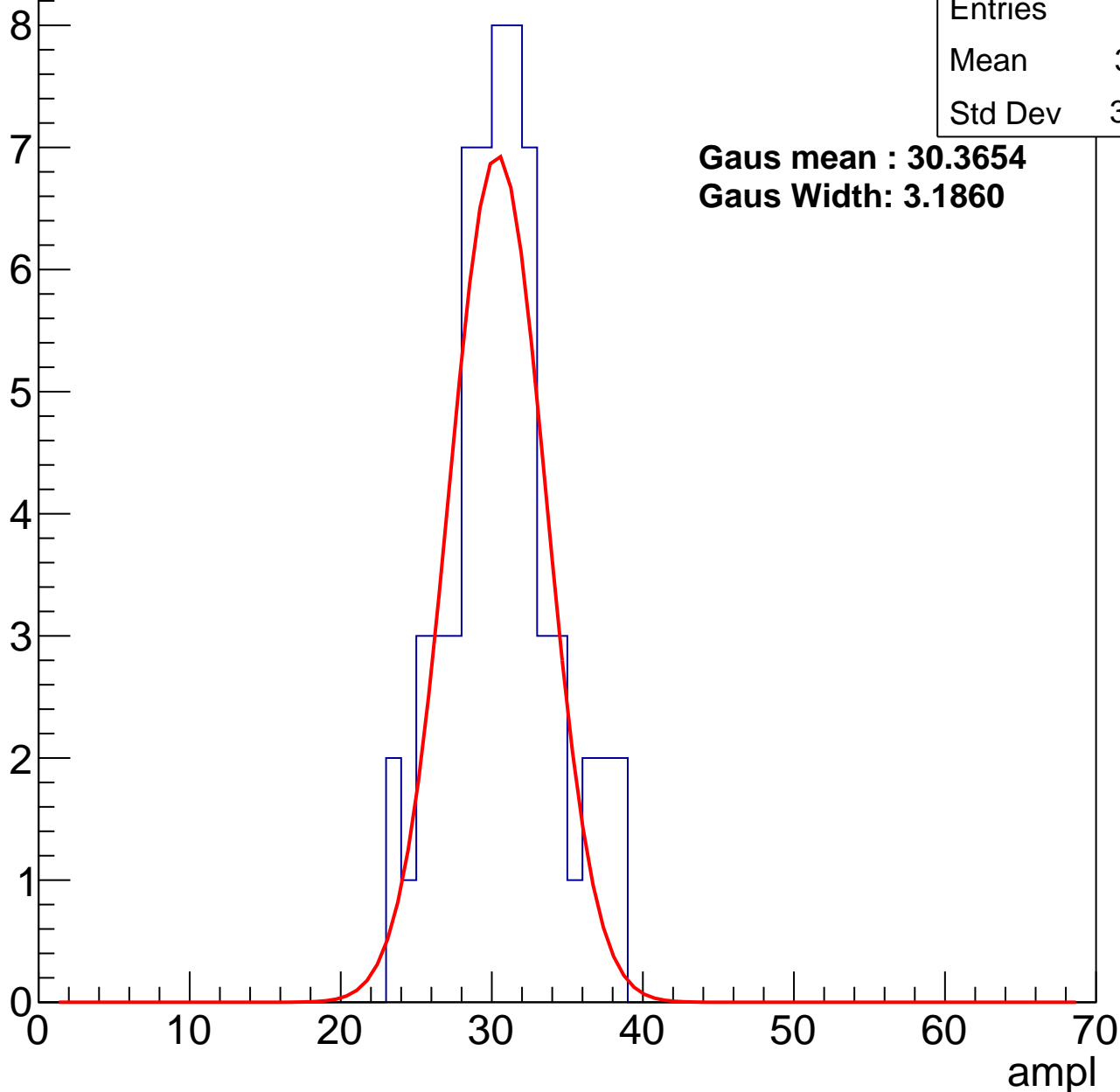
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	30.21
Std Dev	3.479

Gaus mean : 30.3654

Gaus Width: 3.1860



B1L103S, U10-ch21, adc1

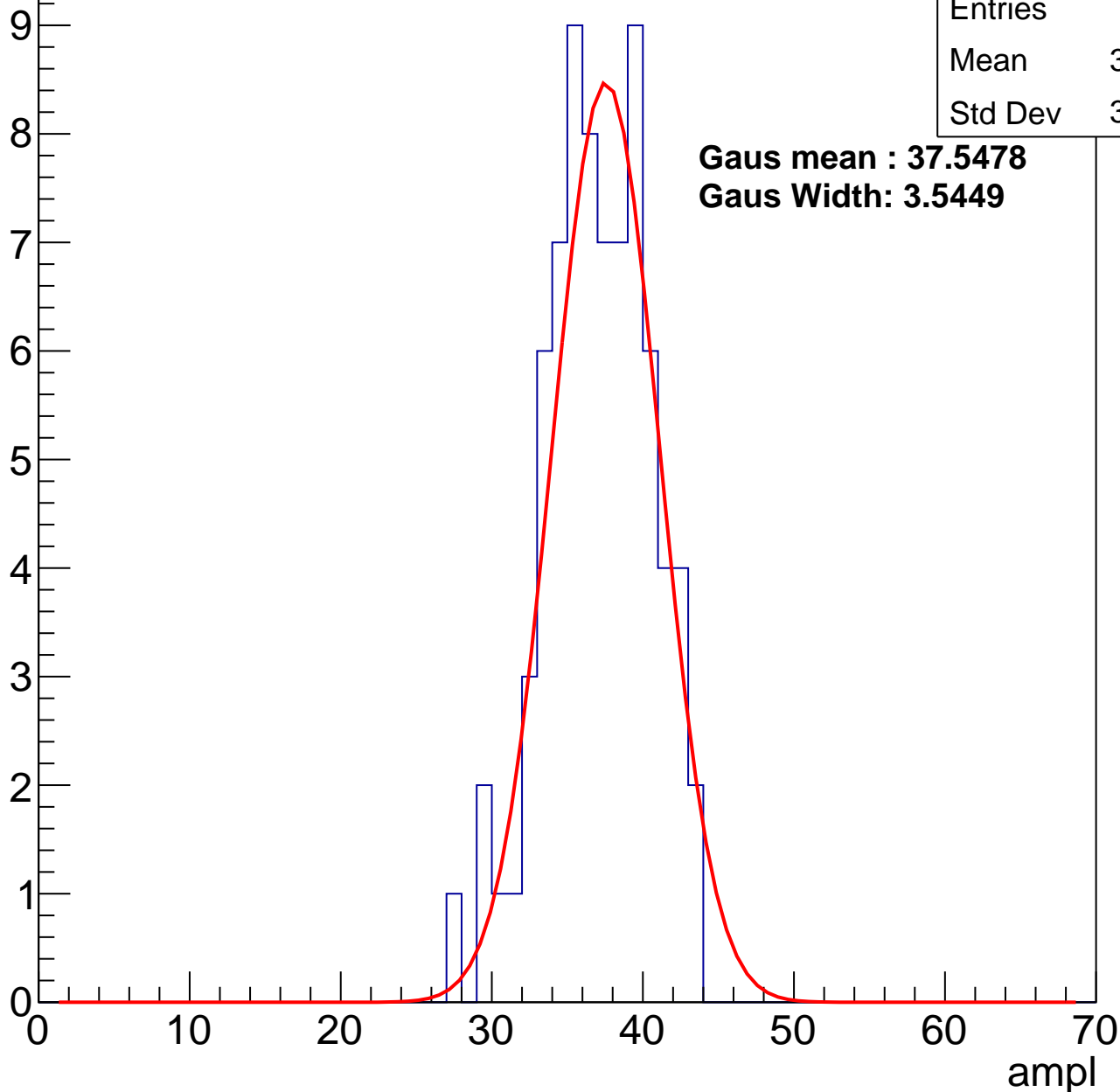
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.56
Std Dev	3.436

Gaus mean : 37.5478

Gaus Width: 3.5449



B1L103S, U10-ch21, adc2

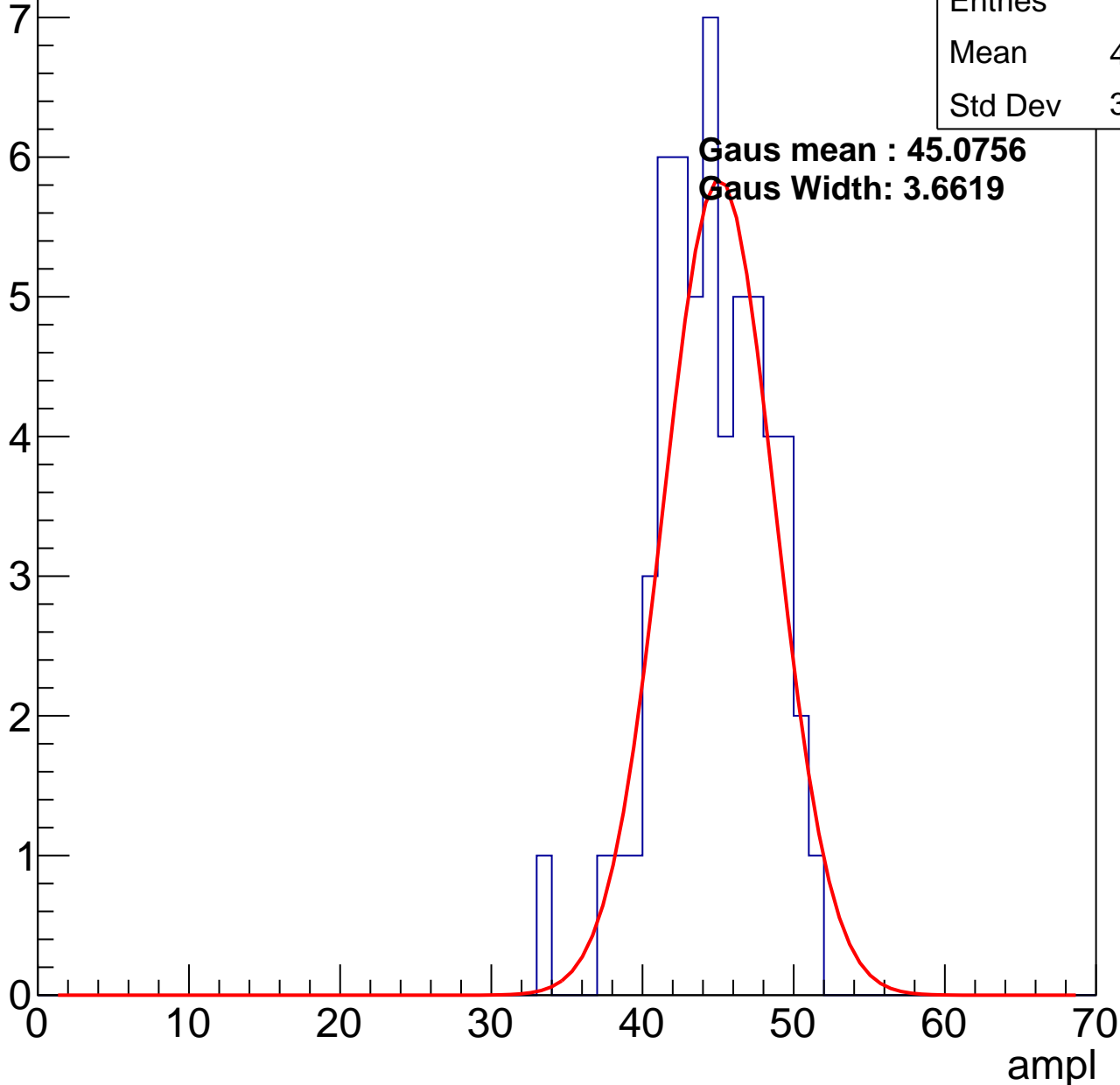
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	44.14
Std Dev	3.573

Gaus mean : 45.0756

Gaus Width: 3.6619

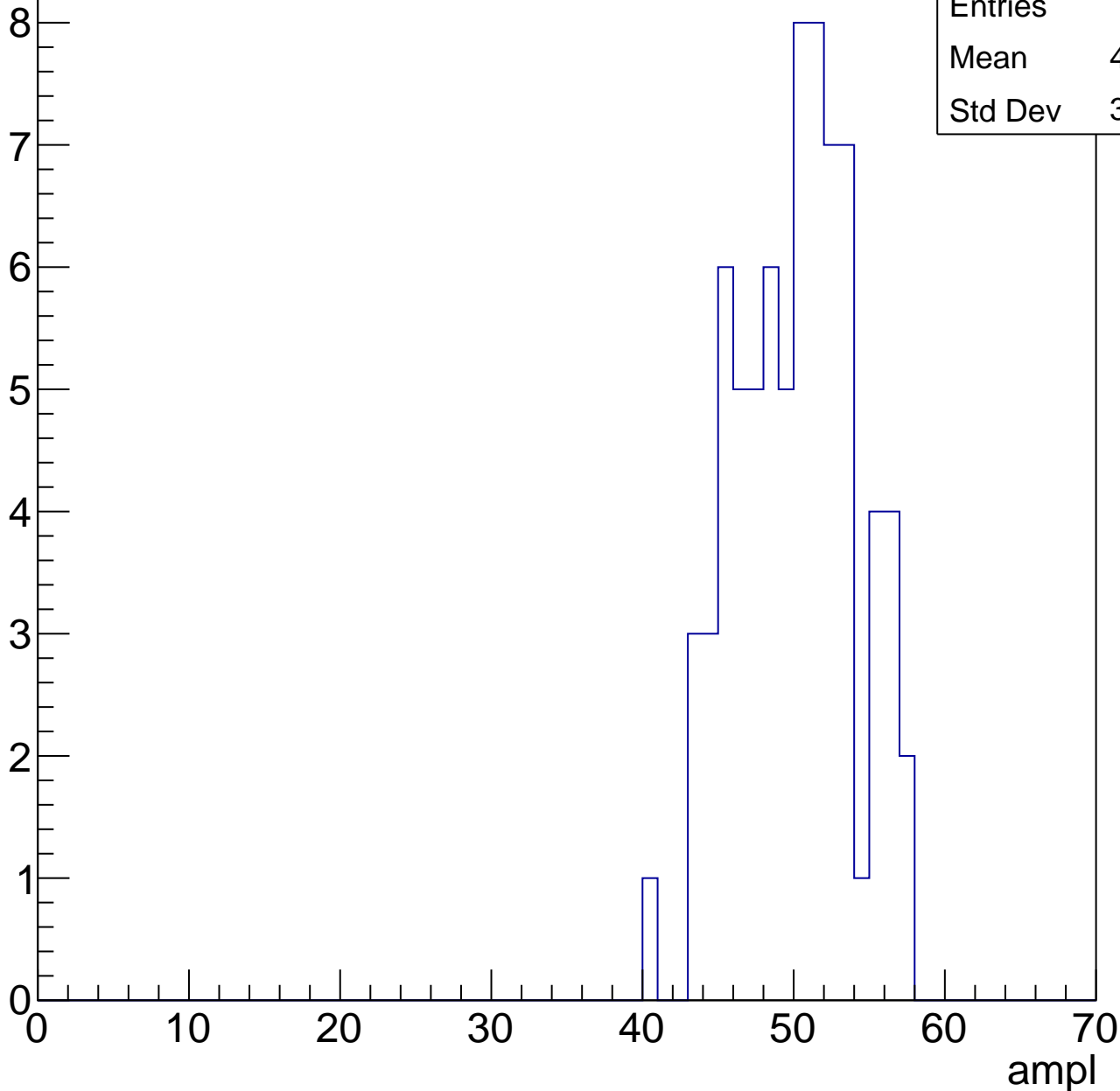


B1L103S, U10-ch21, adc3

calib_packv5_042523_0143.root, FC#7, port C2

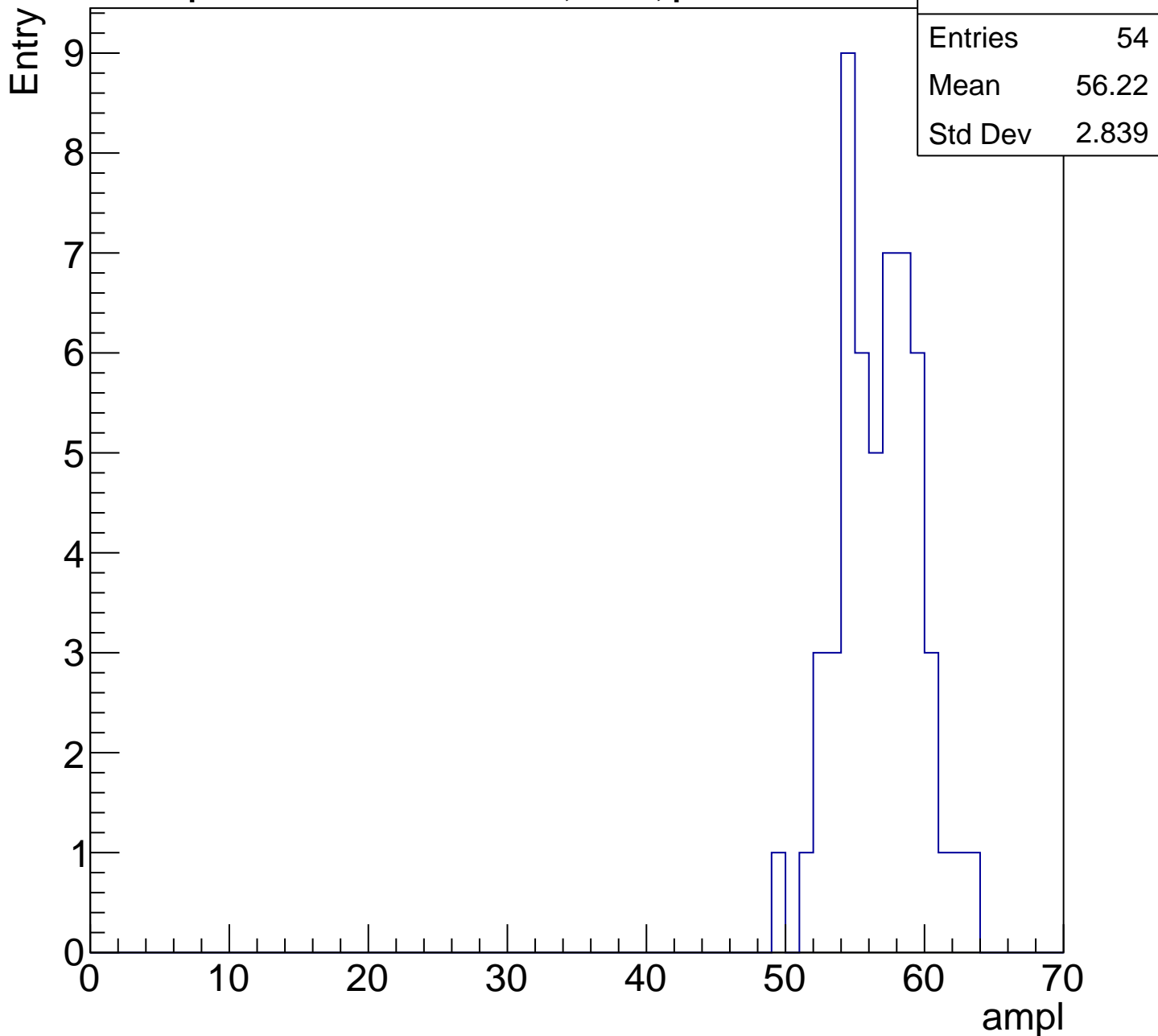
Entry

Entries	75
Mean	49.65
Std Dev	3.842



B1L103S, U10-ch21, adc4

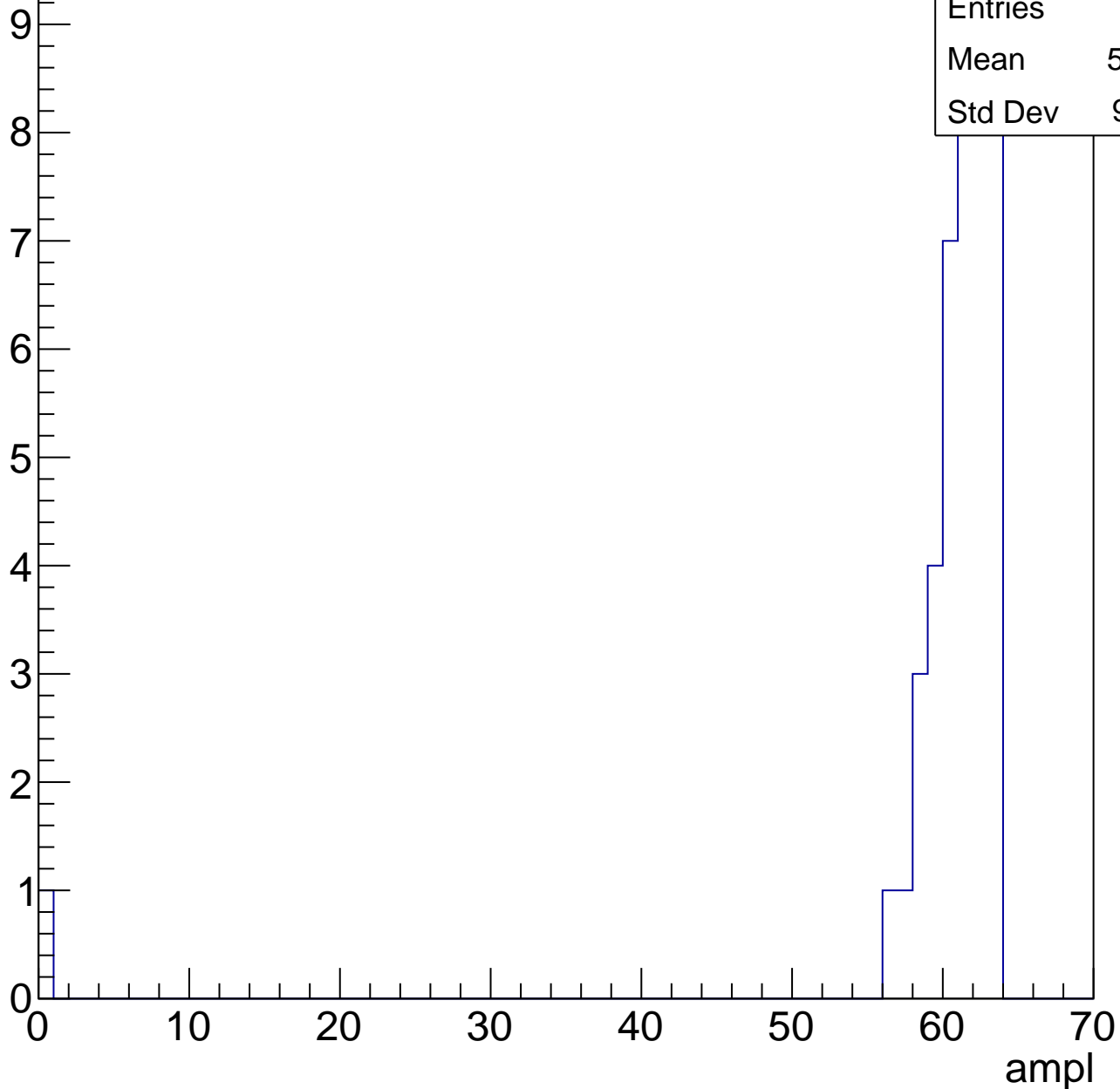
calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U10-ch21, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch21, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch21, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7
Std Dev	9.899

B1L103S, U10-ch22, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	66
Mean	29.86
Std Dev	3.089

Gaus mean : 31.3956

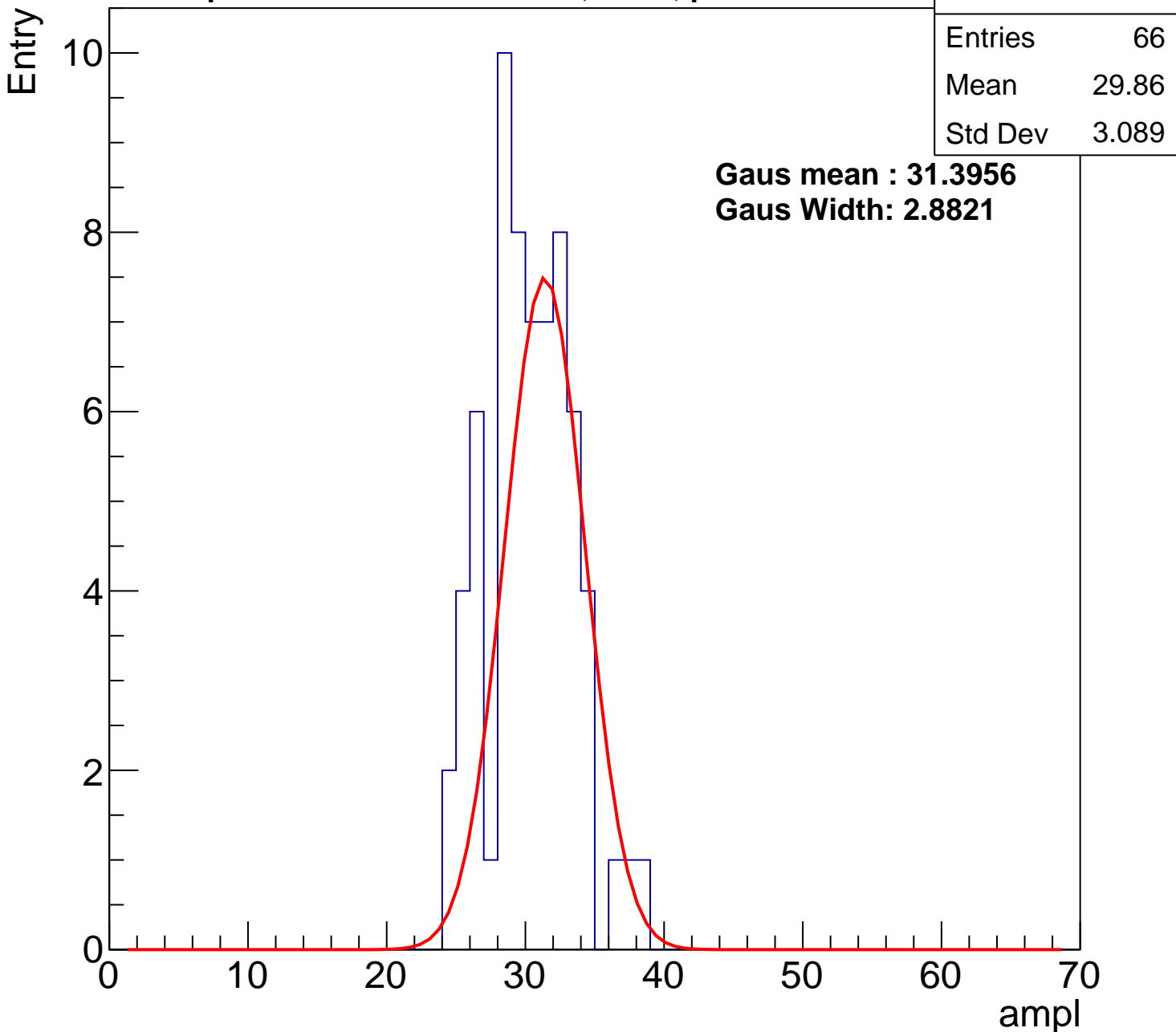
Gaus Width: 2.8821

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch22, adc1

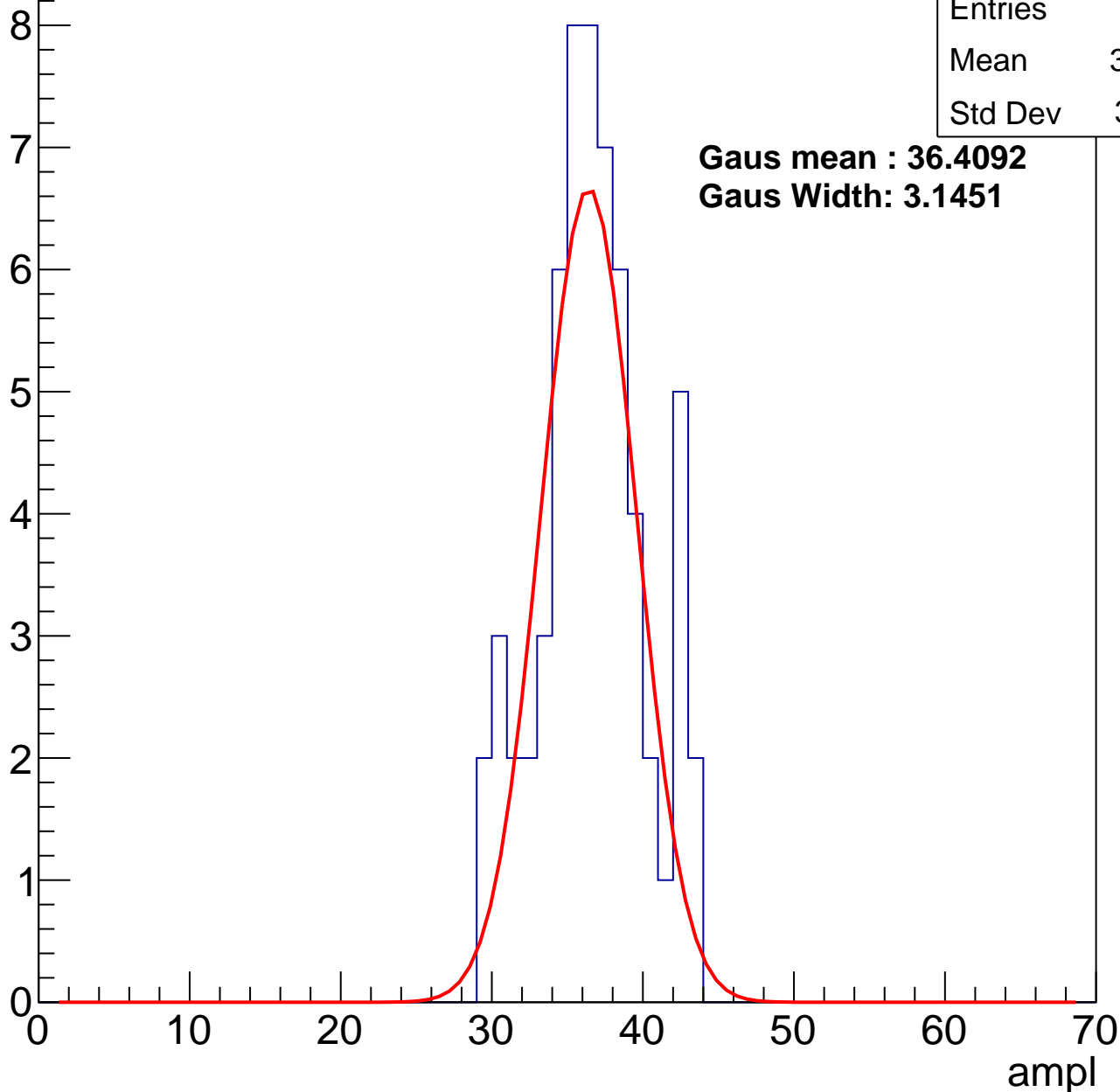
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	36.15
Std Dev	3.501

Gaus mean : 36.4092

Gaus Width: 3.1451



B1L103S, U10-ch22, adc2

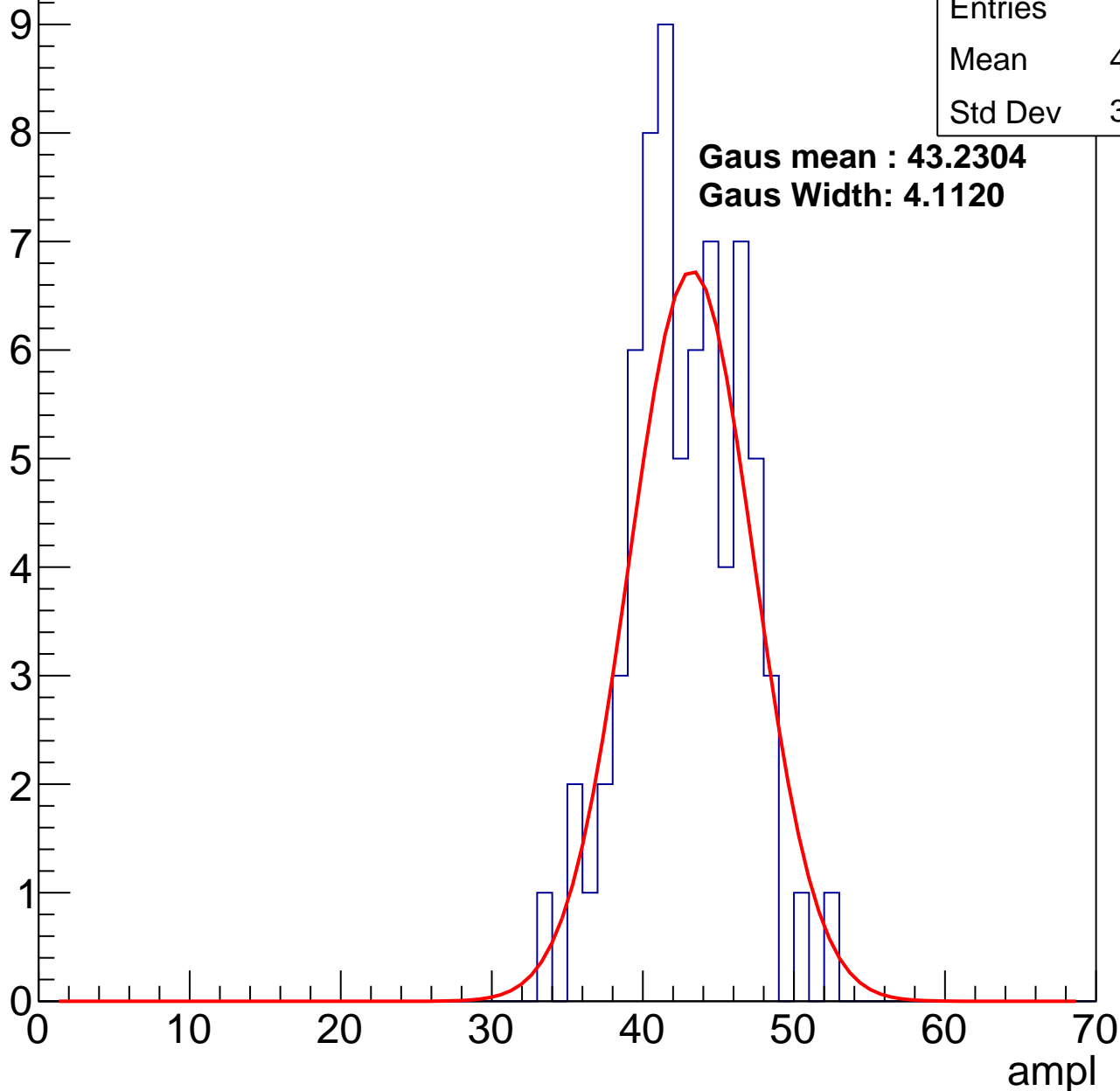
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.38
Std Dev	3.709

Gaus mean : 43.2304

Gaus Width: 4.1120

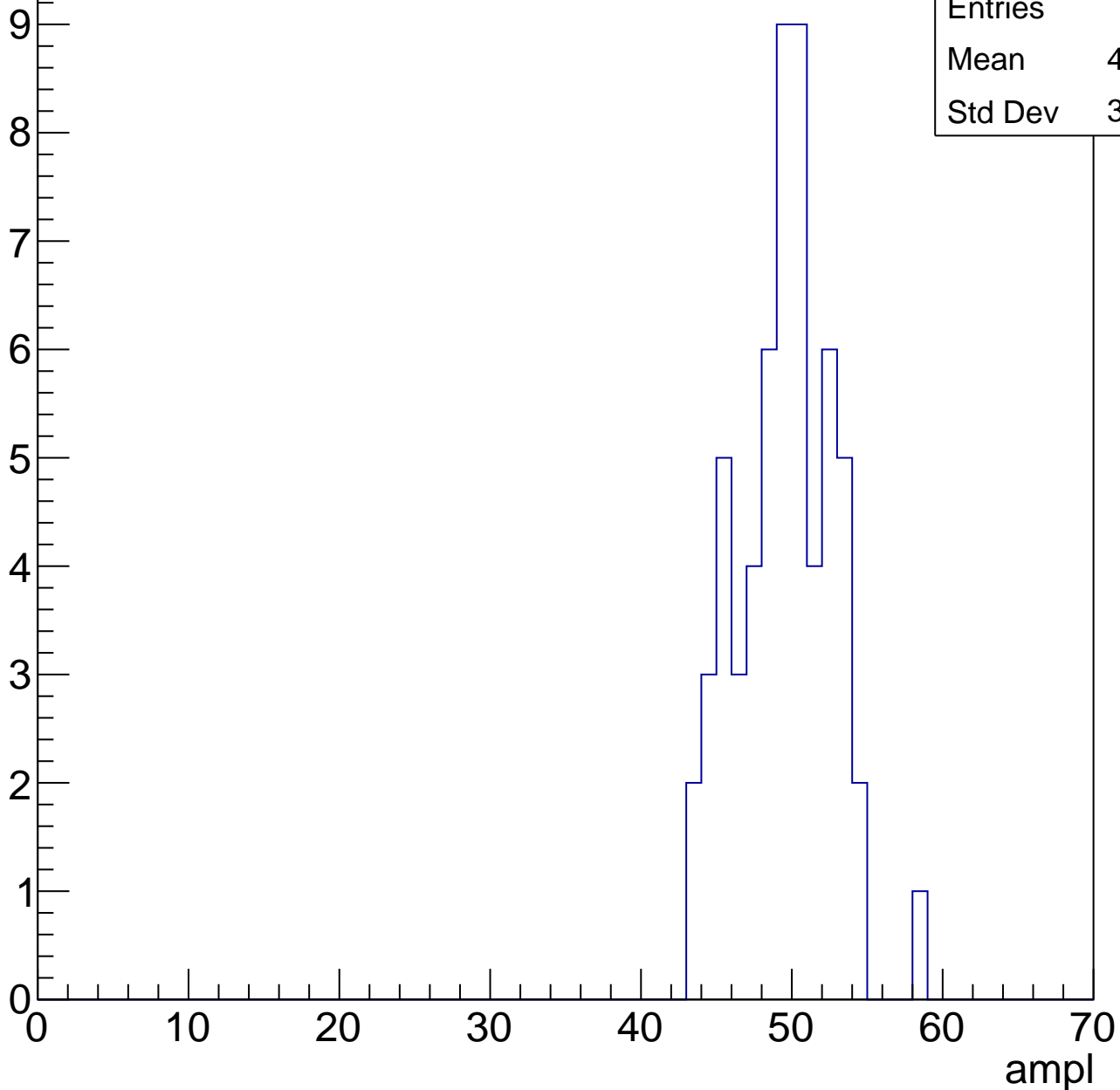


B1L103S, U10-ch22, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	49.07
Std Dev	3.097



B1L103S, U10-ch22, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	55.46
Std Dev	3.484

Entry

10

8

6

4

2

0

0

10

20

30

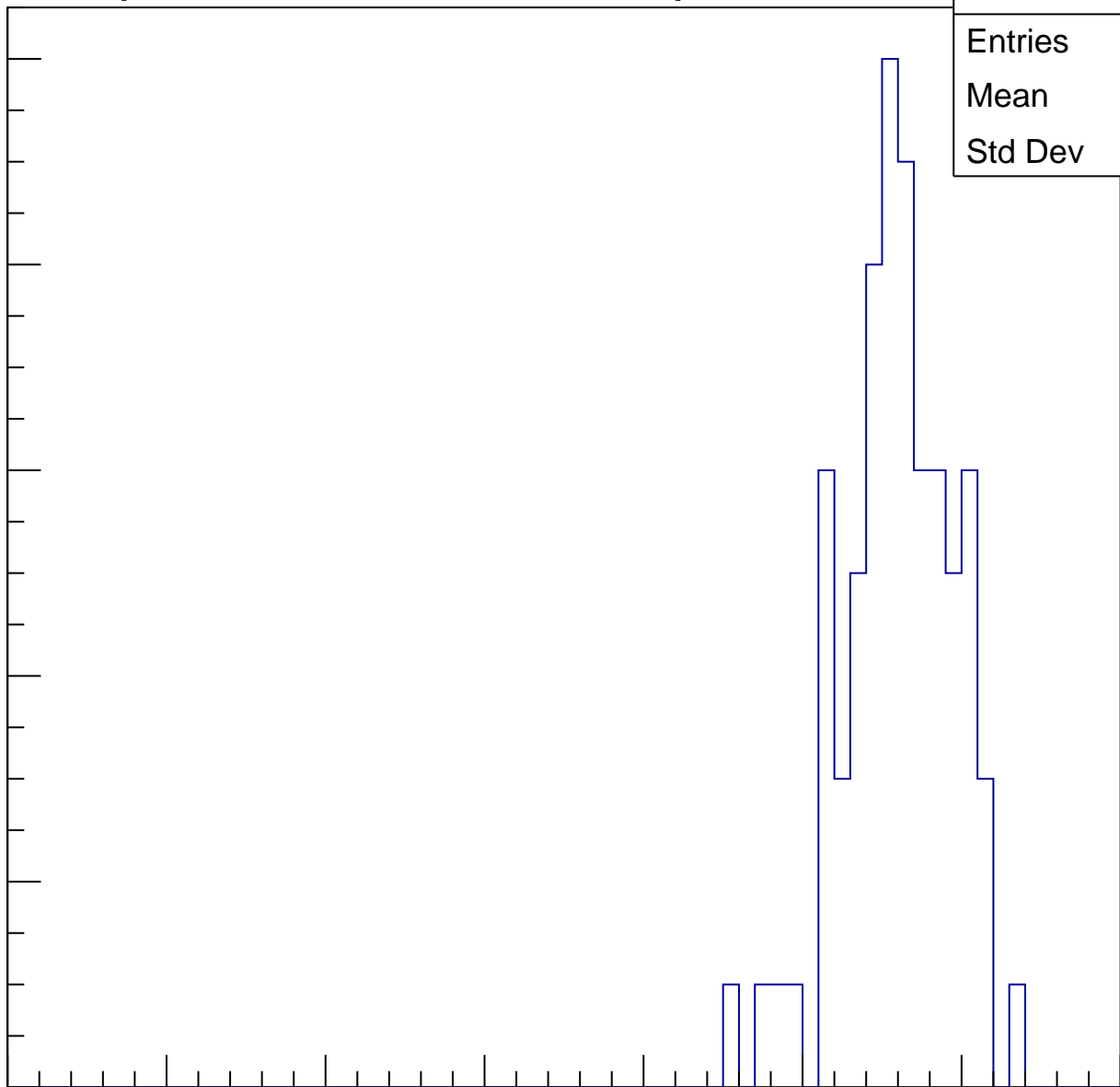
40

50

60

ampl

70

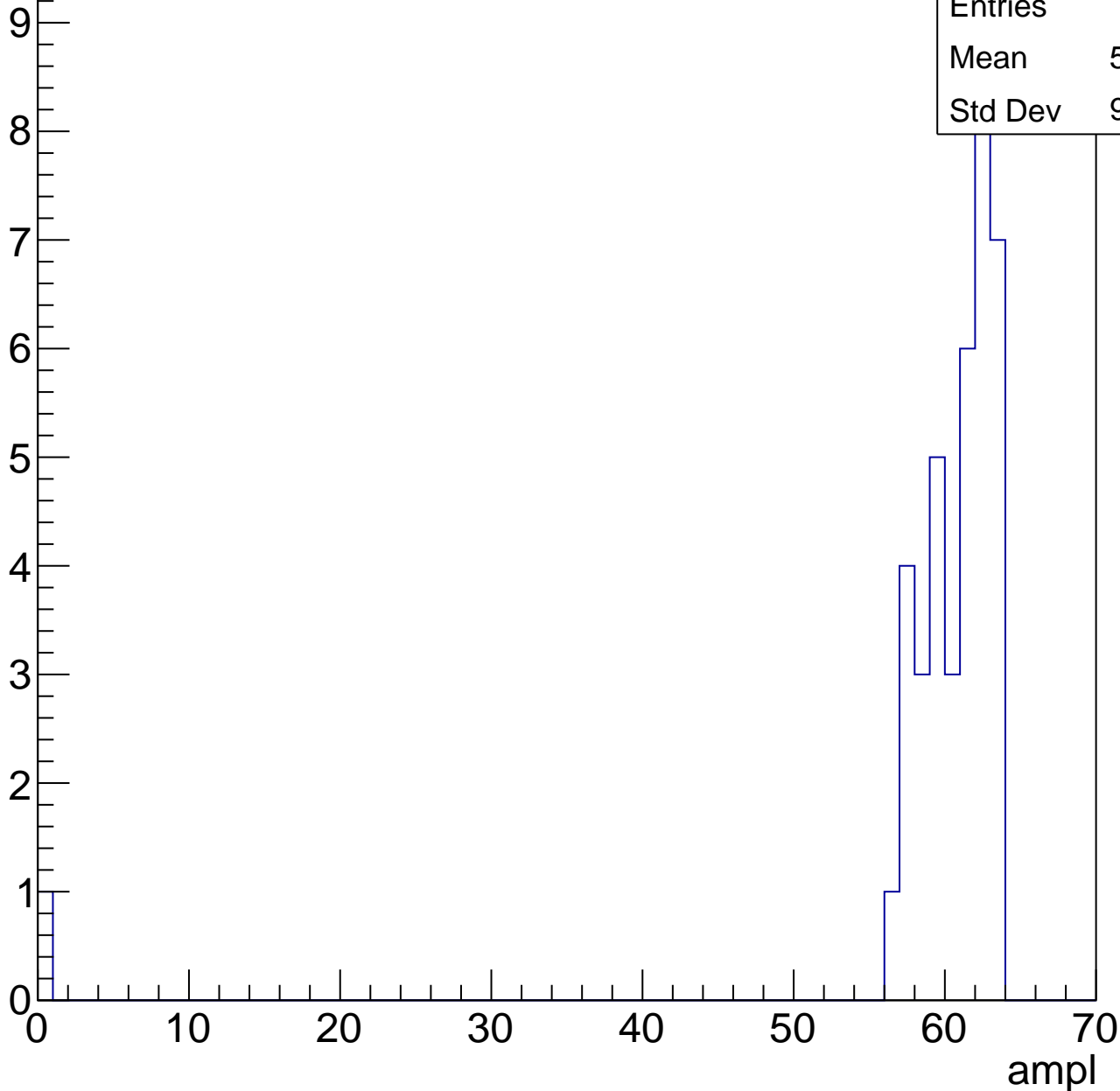


B1L103S, U10-ch22, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.92
Std Dev	9.778



B1L103S, U10-ch22, adc6

calib_packv5_042523_0143.root, FC#7, port C2

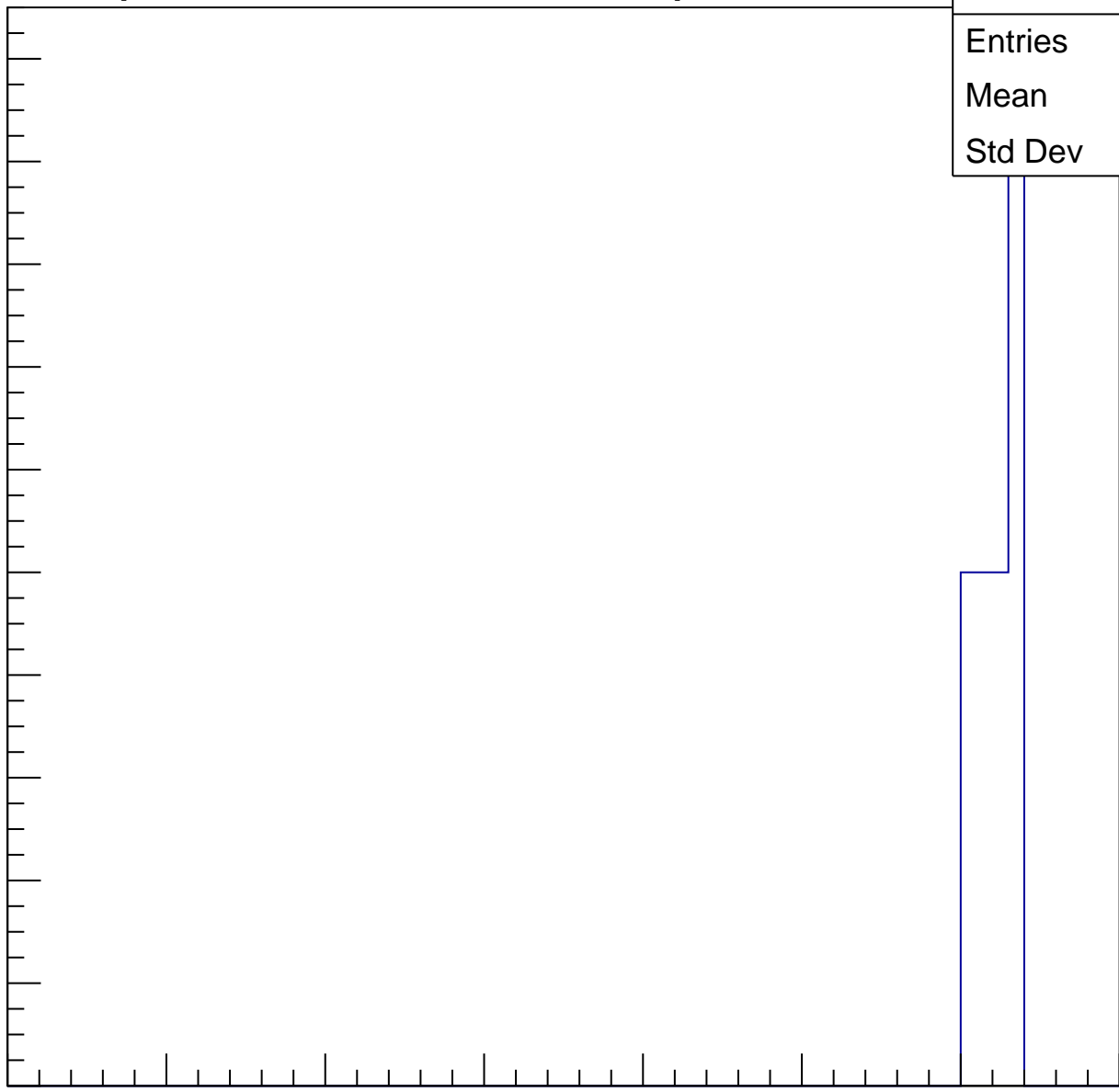
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch22, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch23, adc0

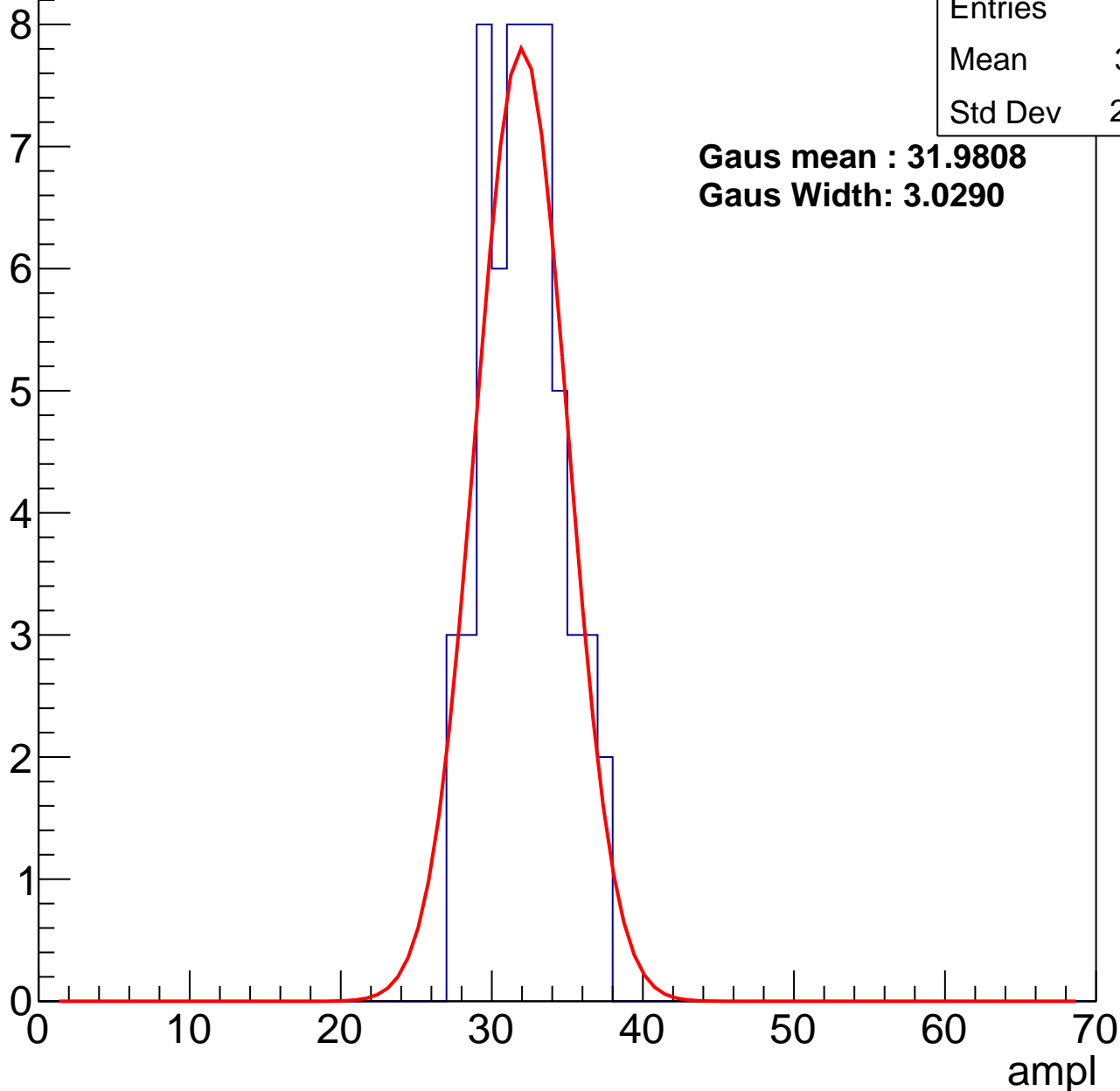
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	31.61
Std Dev	2.553

Gaus mean : 31.9808

Gaus Width: 3.0290



B1L103S, U10-ch23, adc1

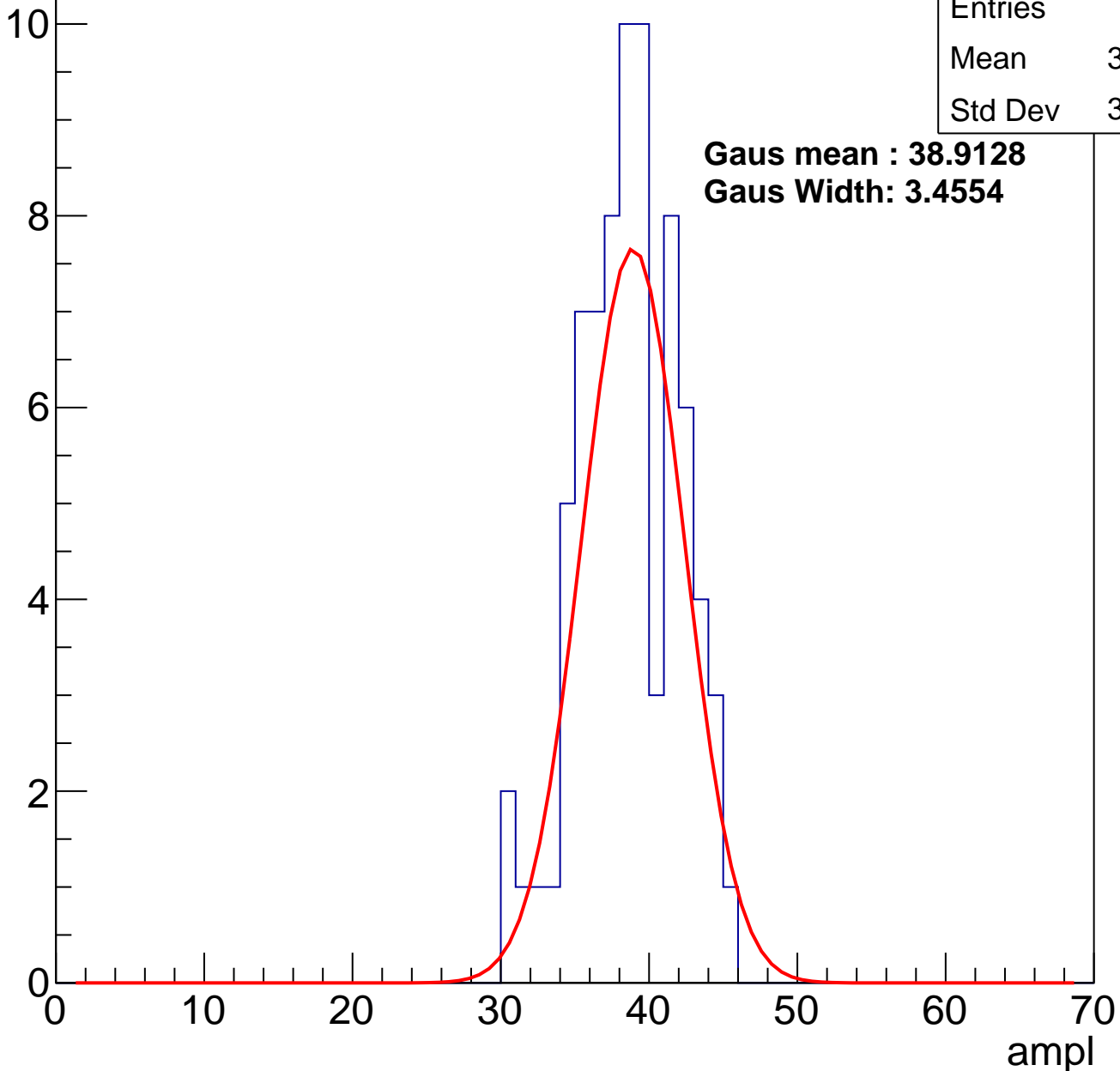
calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	38.16
Std Dev	3.346

Gaus mean : 38.9128

Gaus Width: 3.4554

Entry



B1L103S, U10-ch23, adc2

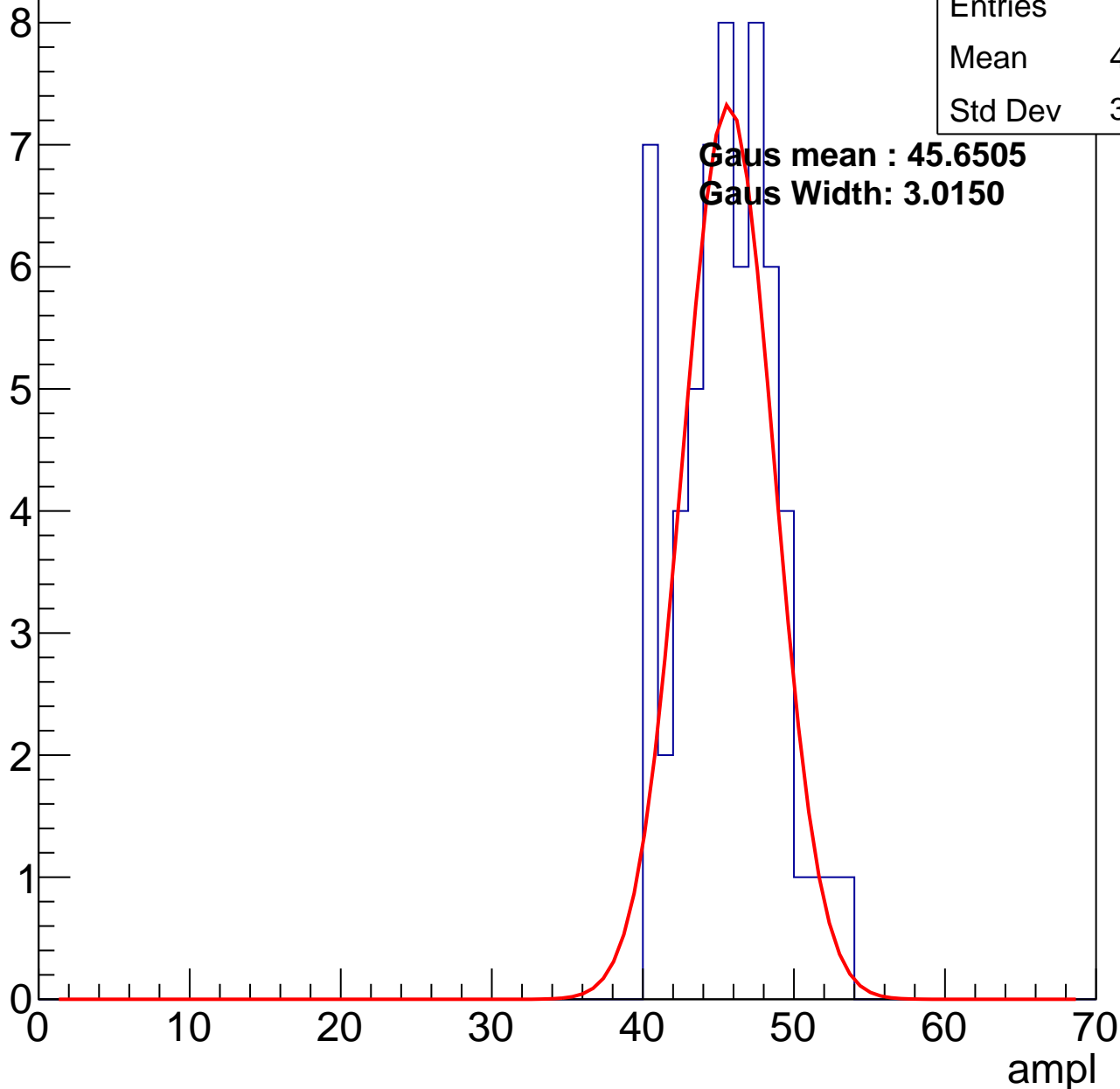
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	45.16
Std Dev	3.132

Gaus mean : 45.6505

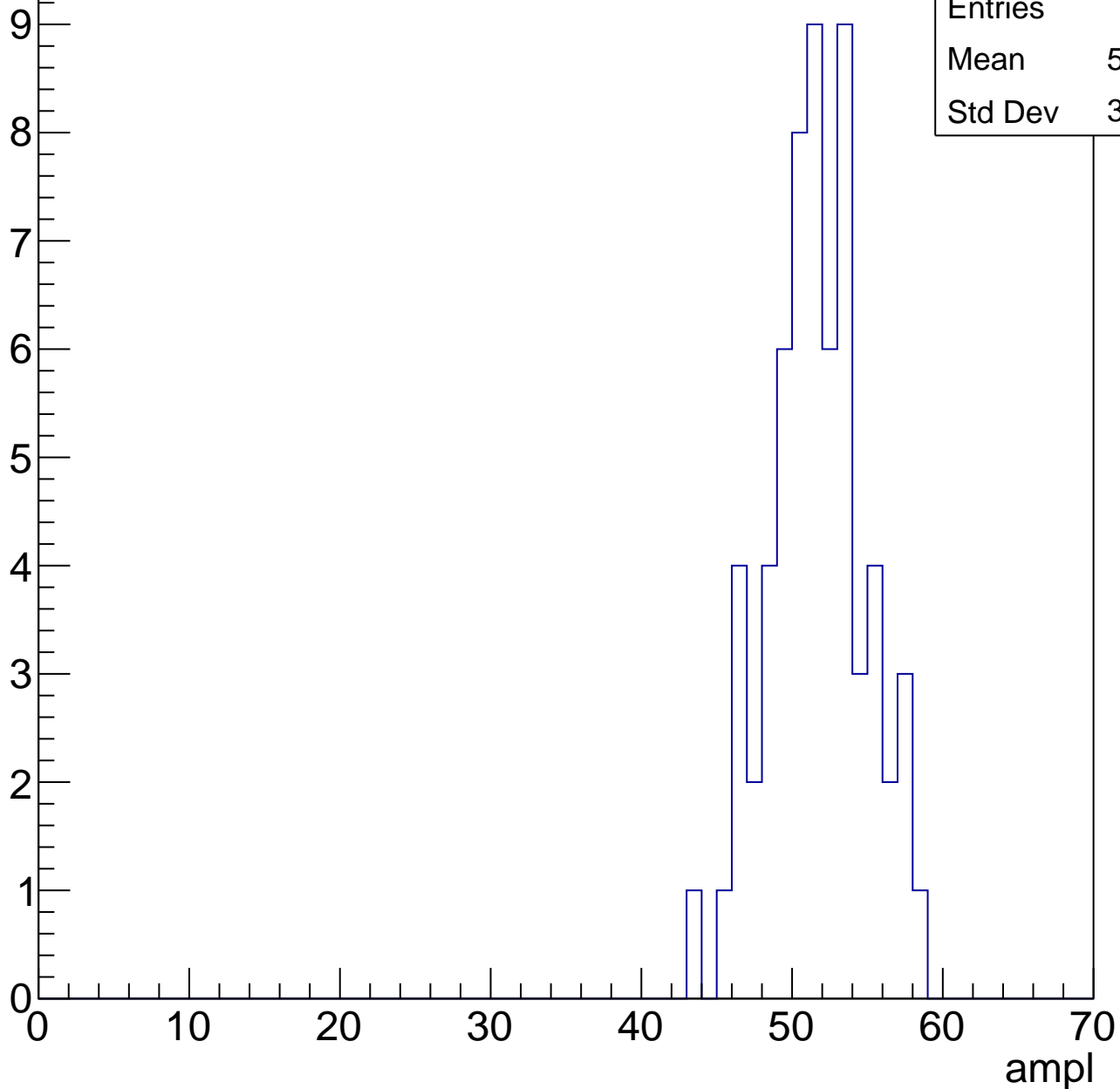
Gaus Width: 3.0150



B1L103S, U10-ch23, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

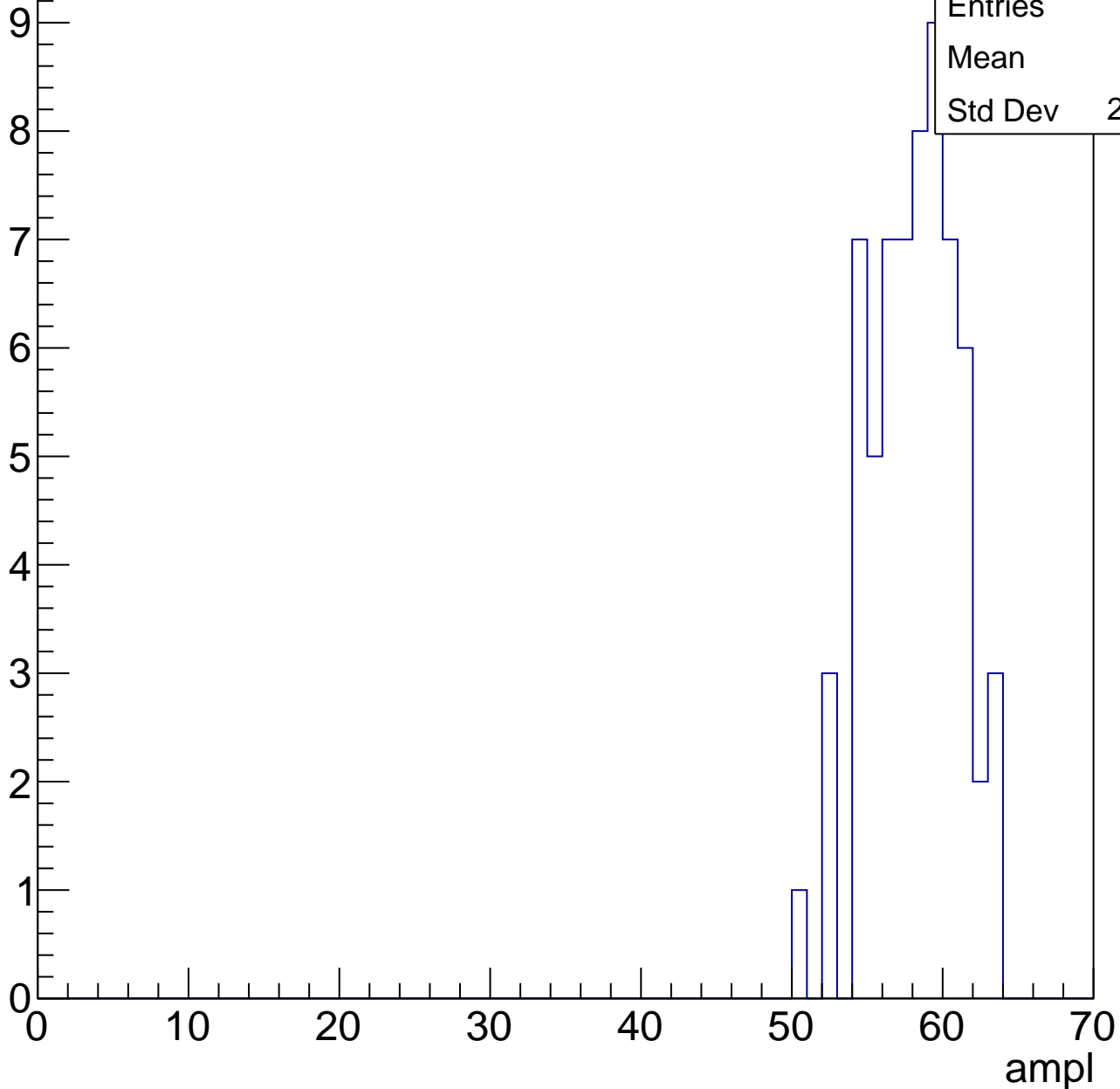


B1L103S, U10-ch23, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	57.6
Std Dev	2.913

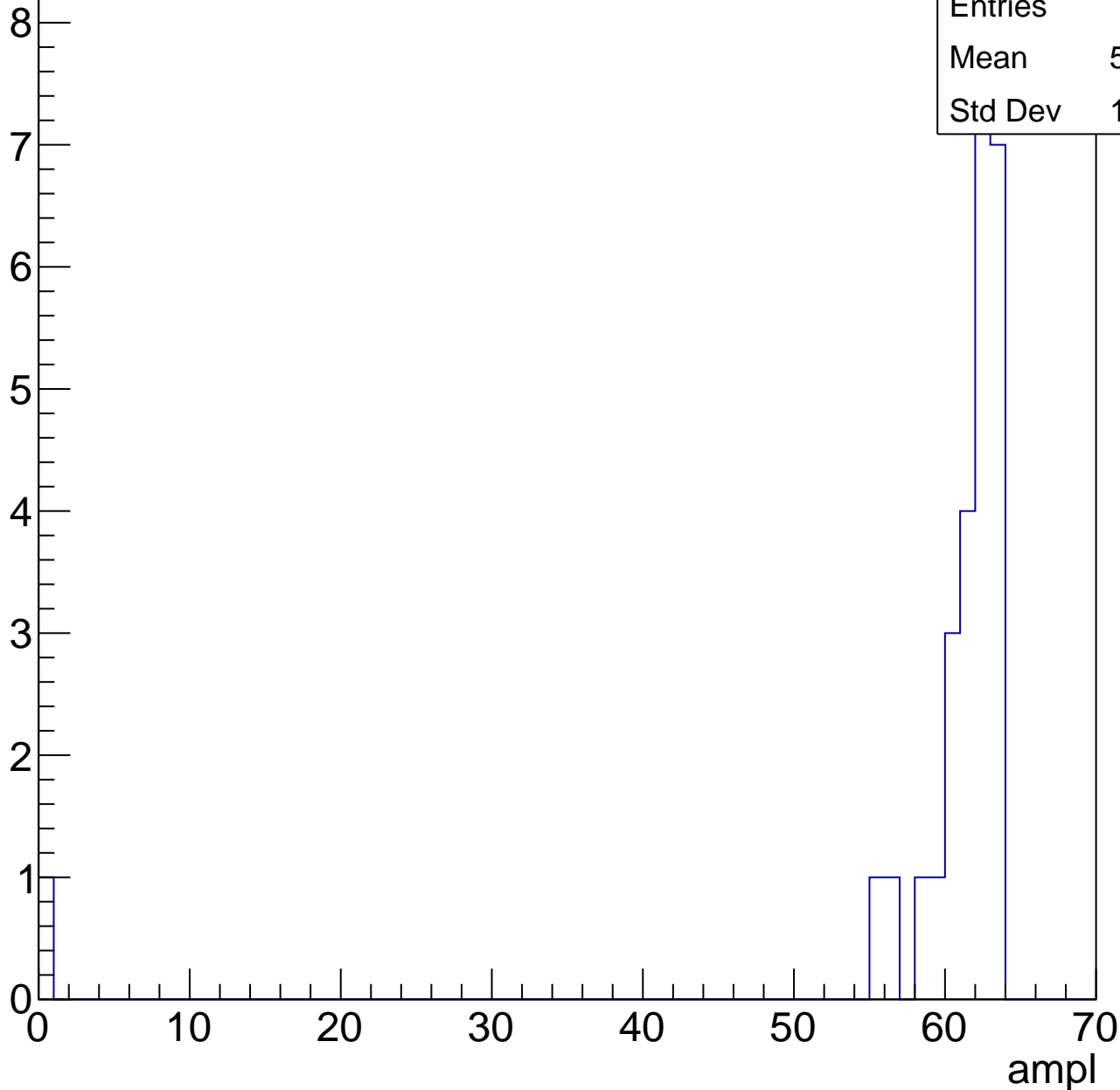


B1L103S, U10-ch23, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	58.85
Std Dev	11.72



B1L103S, U10-ch23, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

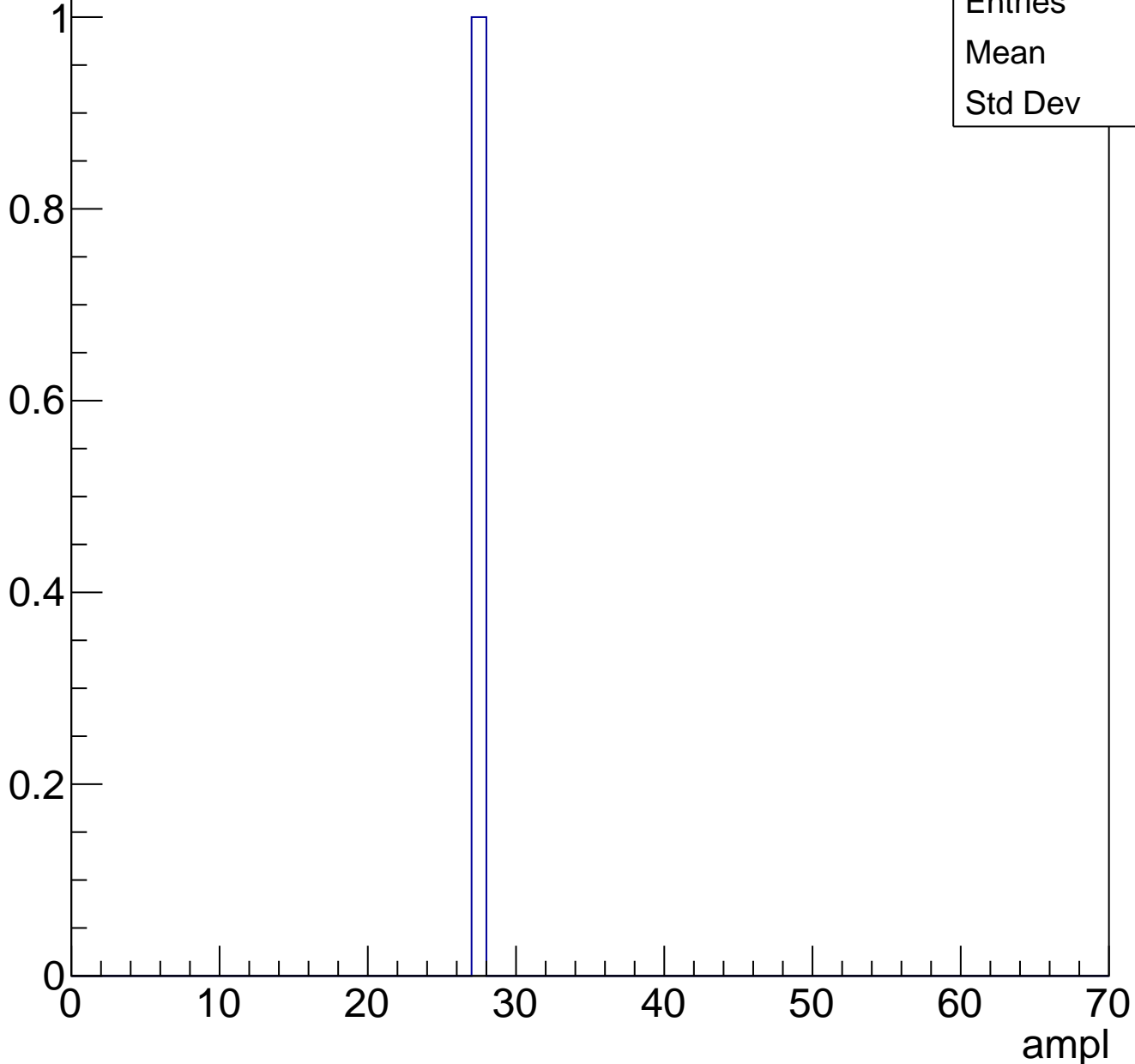


Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch23, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	27
Std Dev	0

B1L103S, U10-ch24, adc0

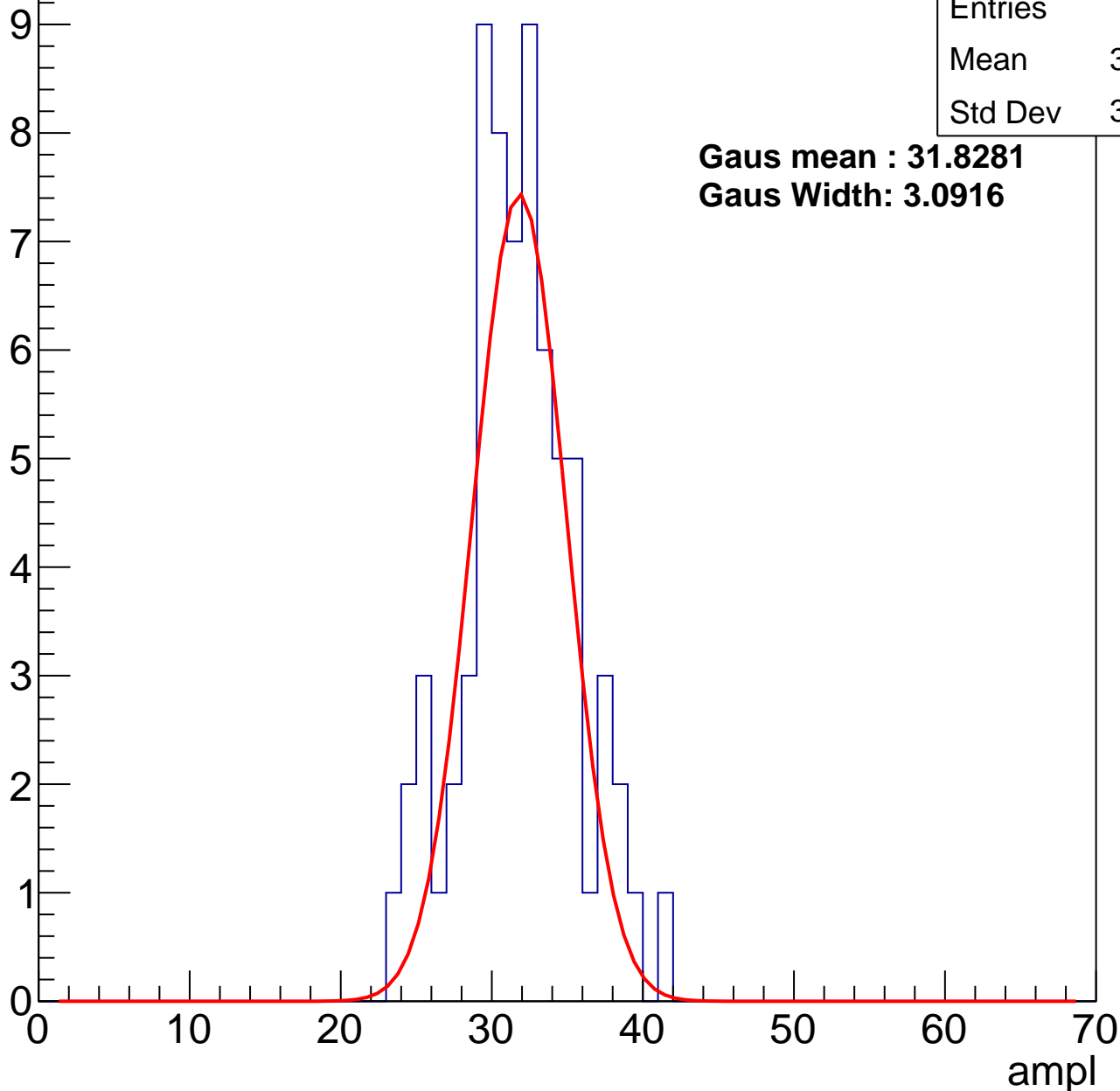
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	31.33
Std Dev	3.697

Gaus mean : 31.8281

Gaus Width: 3.0916



B1L103S, U10-ch24, adc1

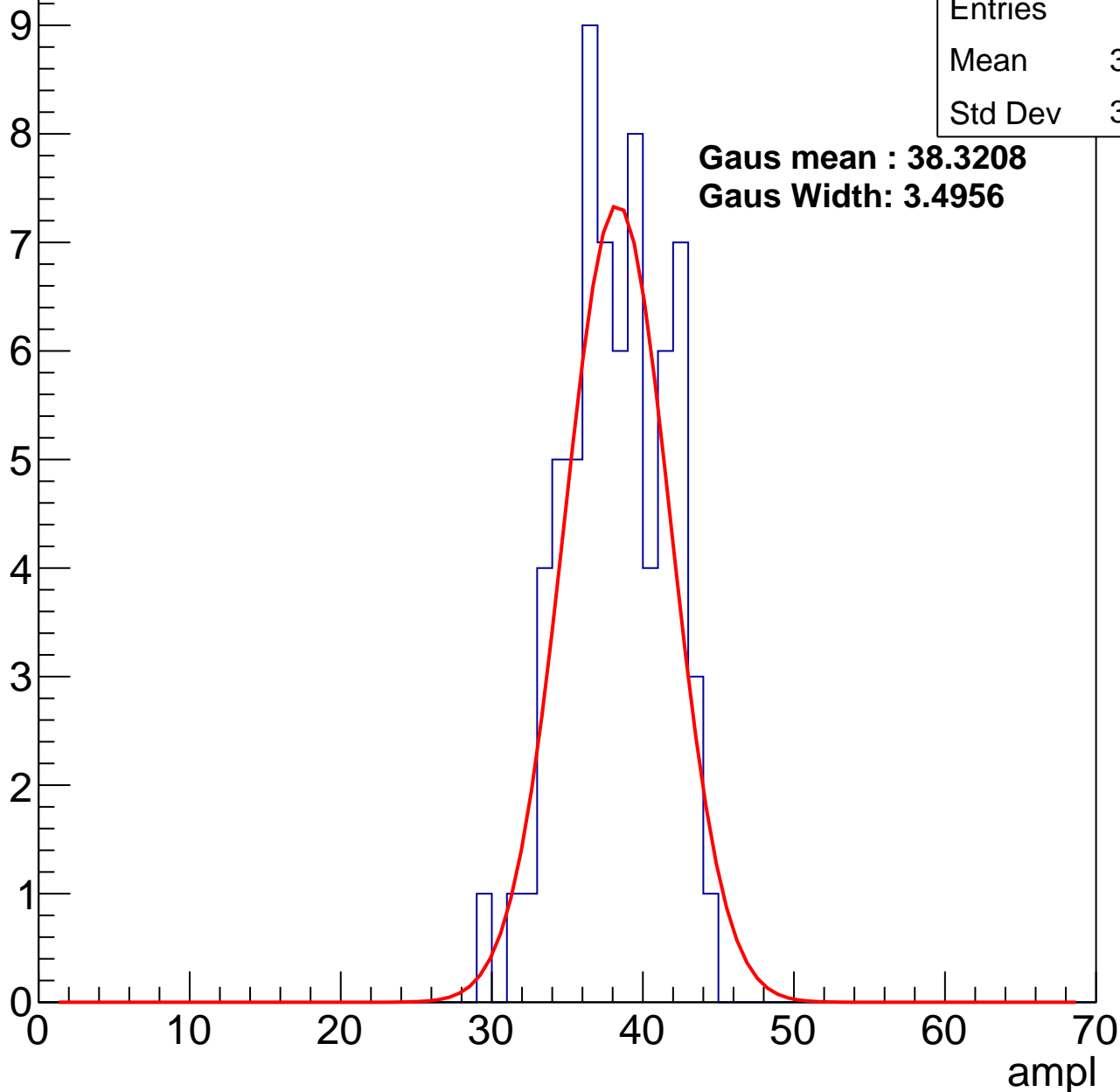
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37.72
Std Dev	3.276

Gaus mean : 38.3208

Gaus Width: 3.4956



B1L103S, U10-ch24, adc2

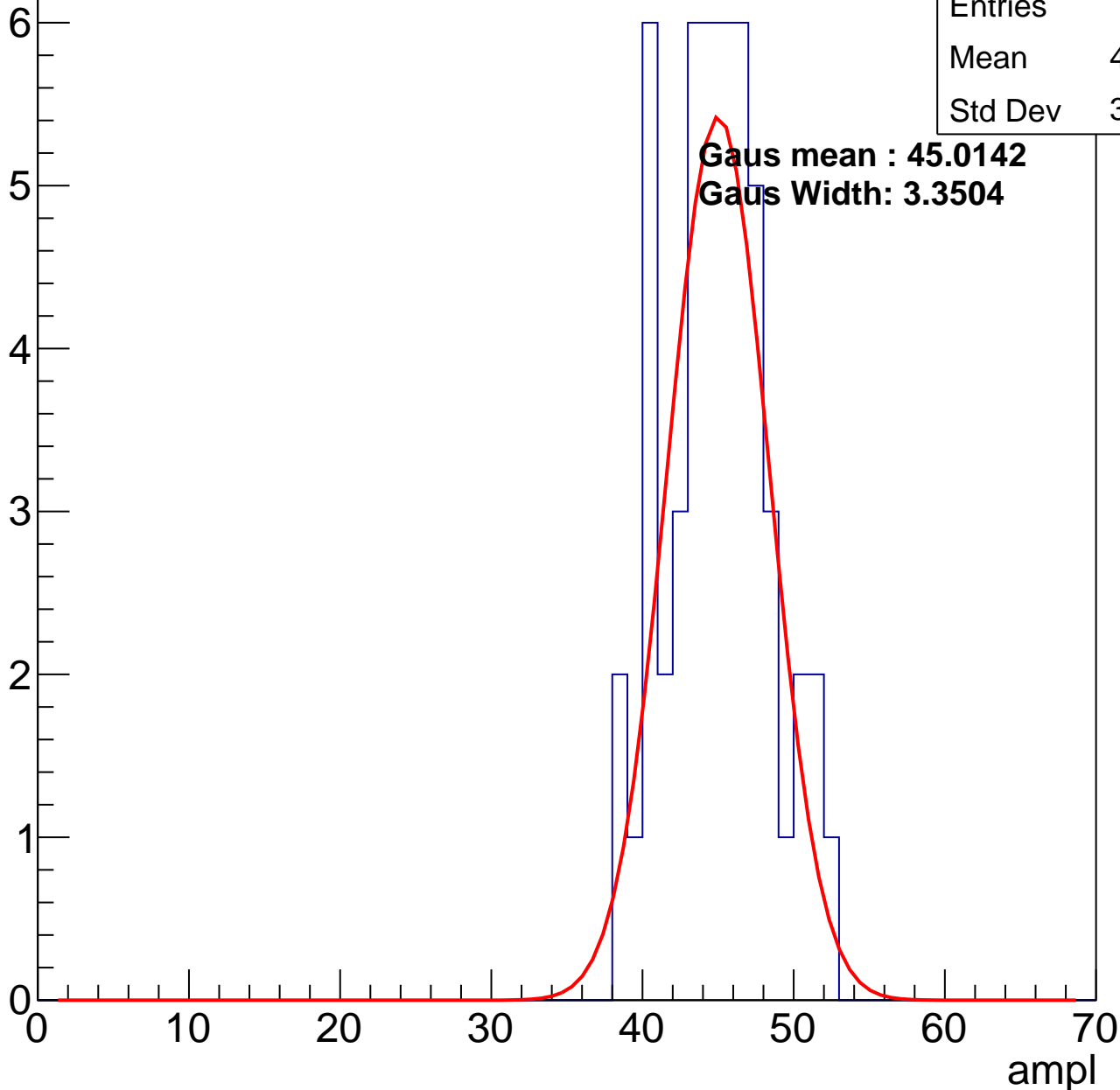
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	44.48
Std Dev	3.394

Gaus mean : 45.0142

Gaus Width: 3.3504

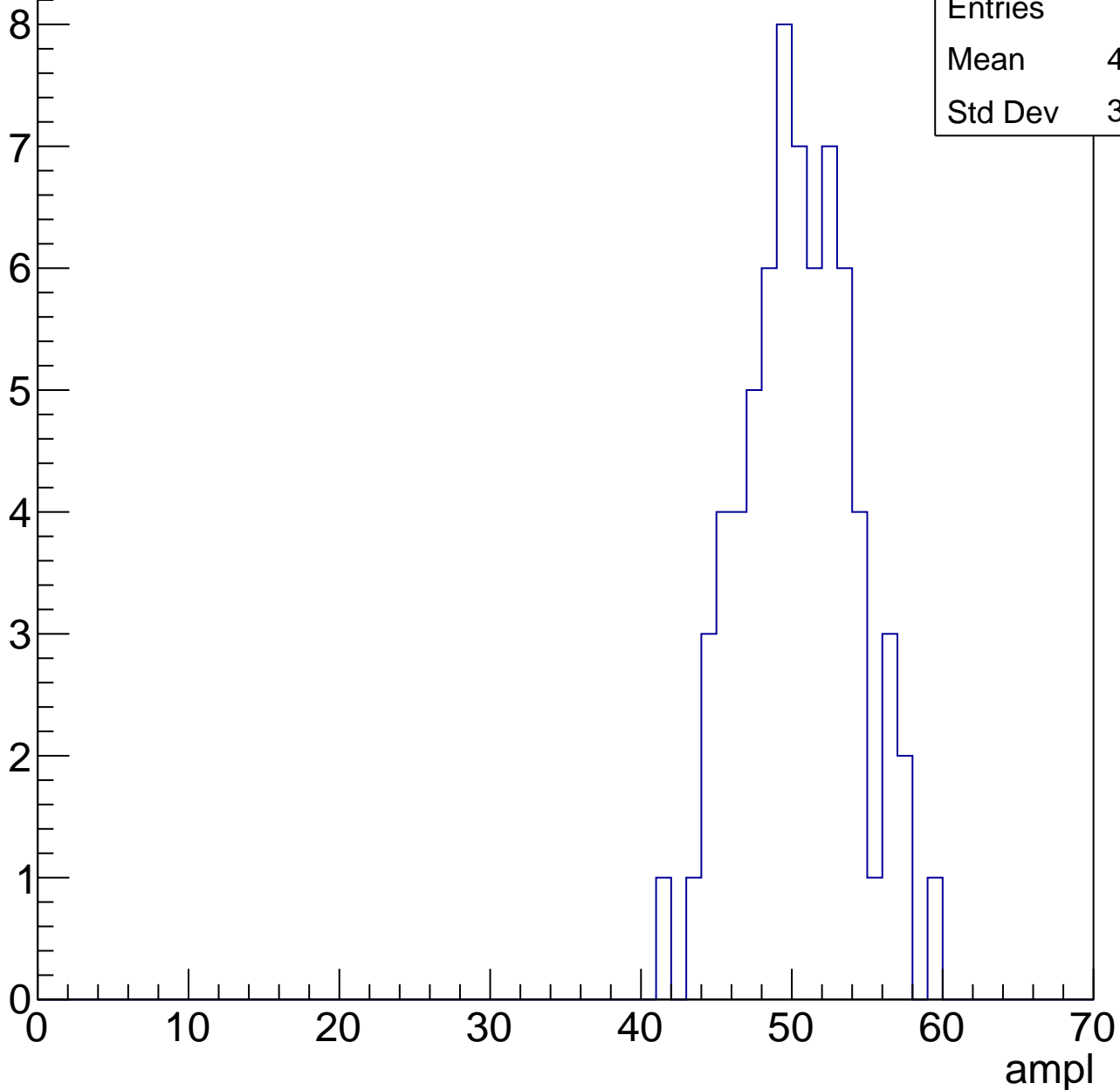


B1L103S, U10-ch24, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	49.93
Std Dev	3.696



B1L103S, U10-ch24, adc4

calib_packv5_042523_0143.root, FC#7, port C2

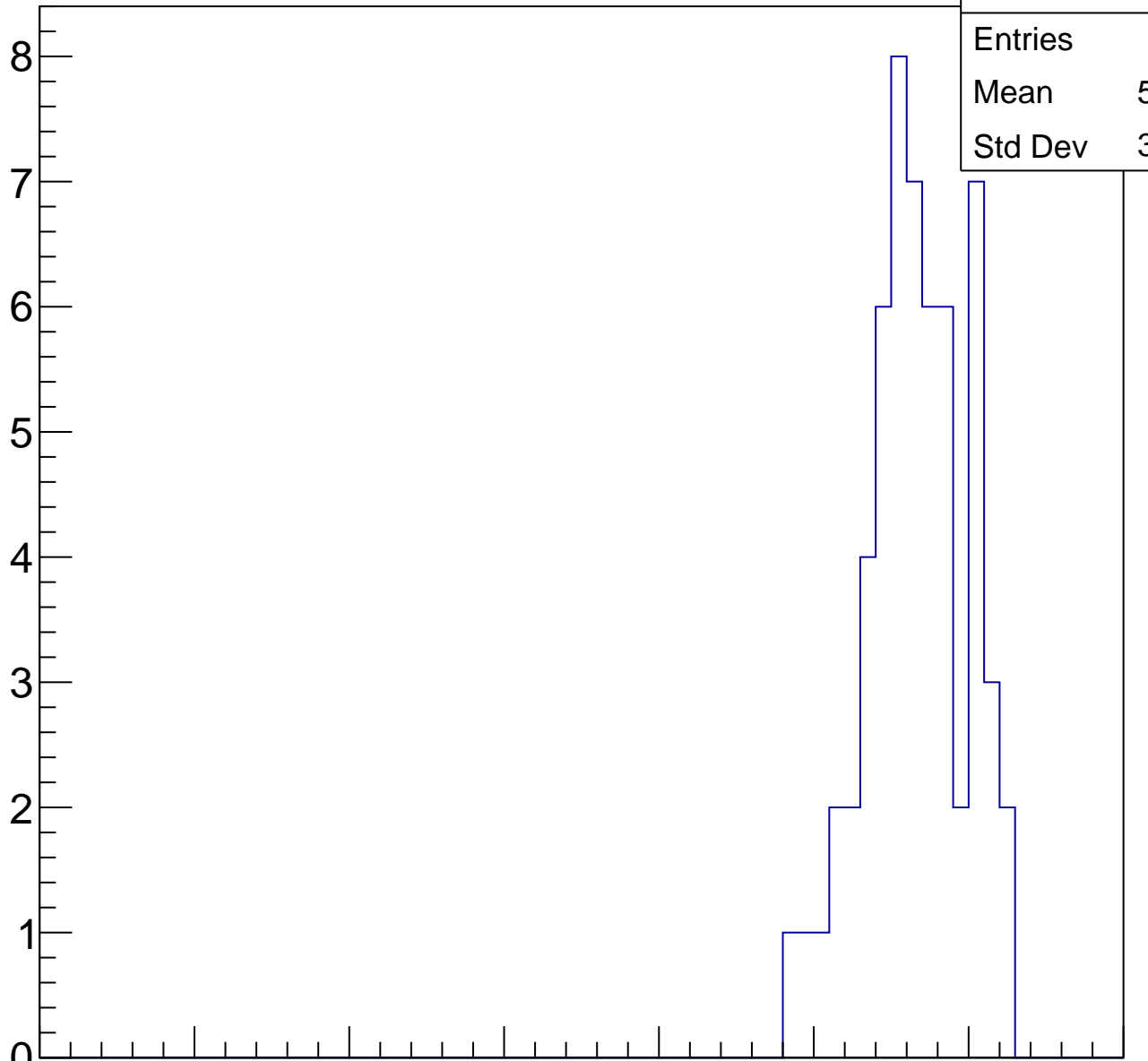
Entry

8
7
6
5
4
3
2
1
0

Entries	58
Mean	56.14
Std Dev	3.229

ampl

0 10 20 30 40 50 60 70

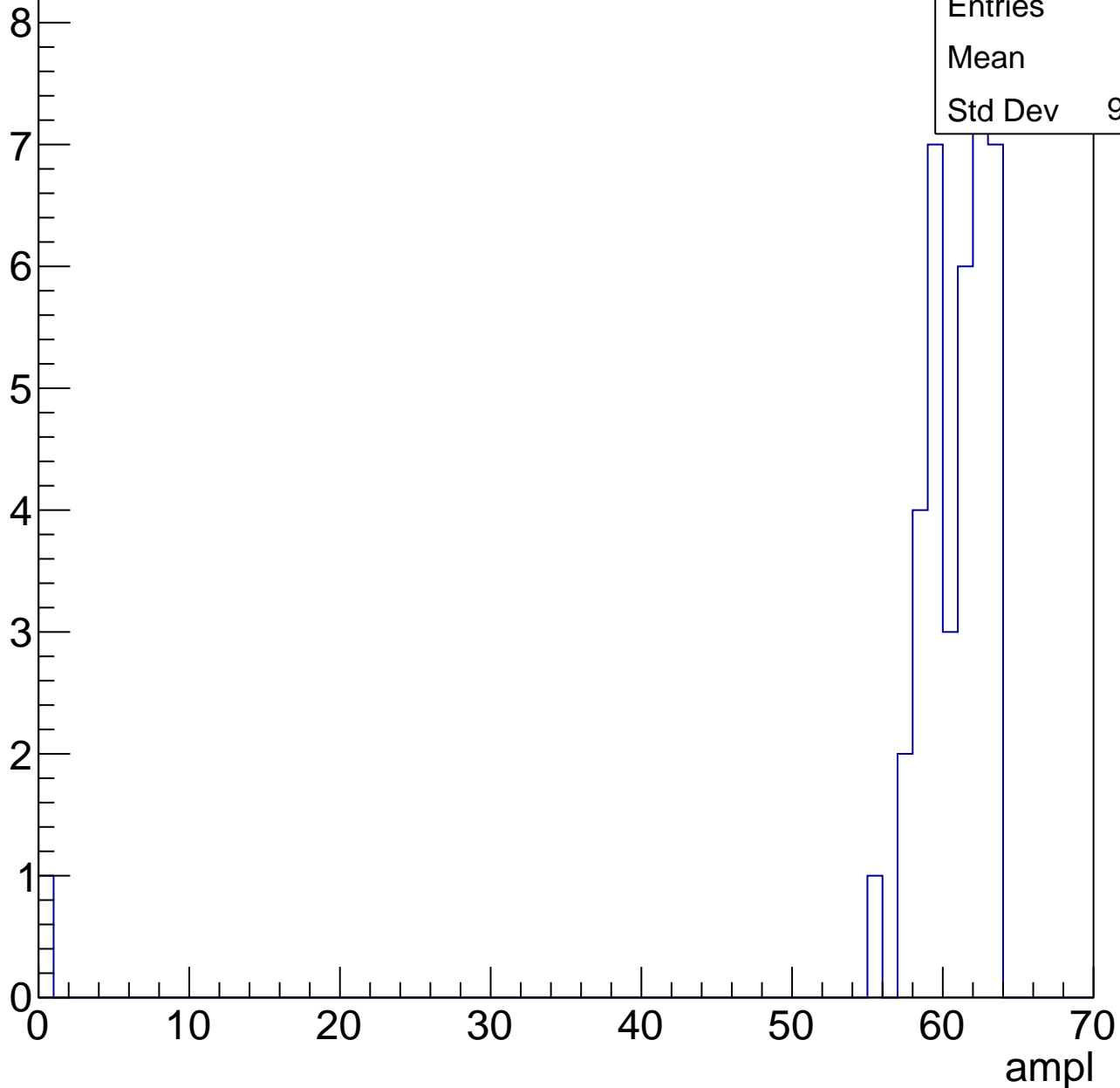


B1L103S, U10-ch24, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	58.9
Std Dev	9.766



B1L103S, U10-ch24, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch24, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch25, adc0

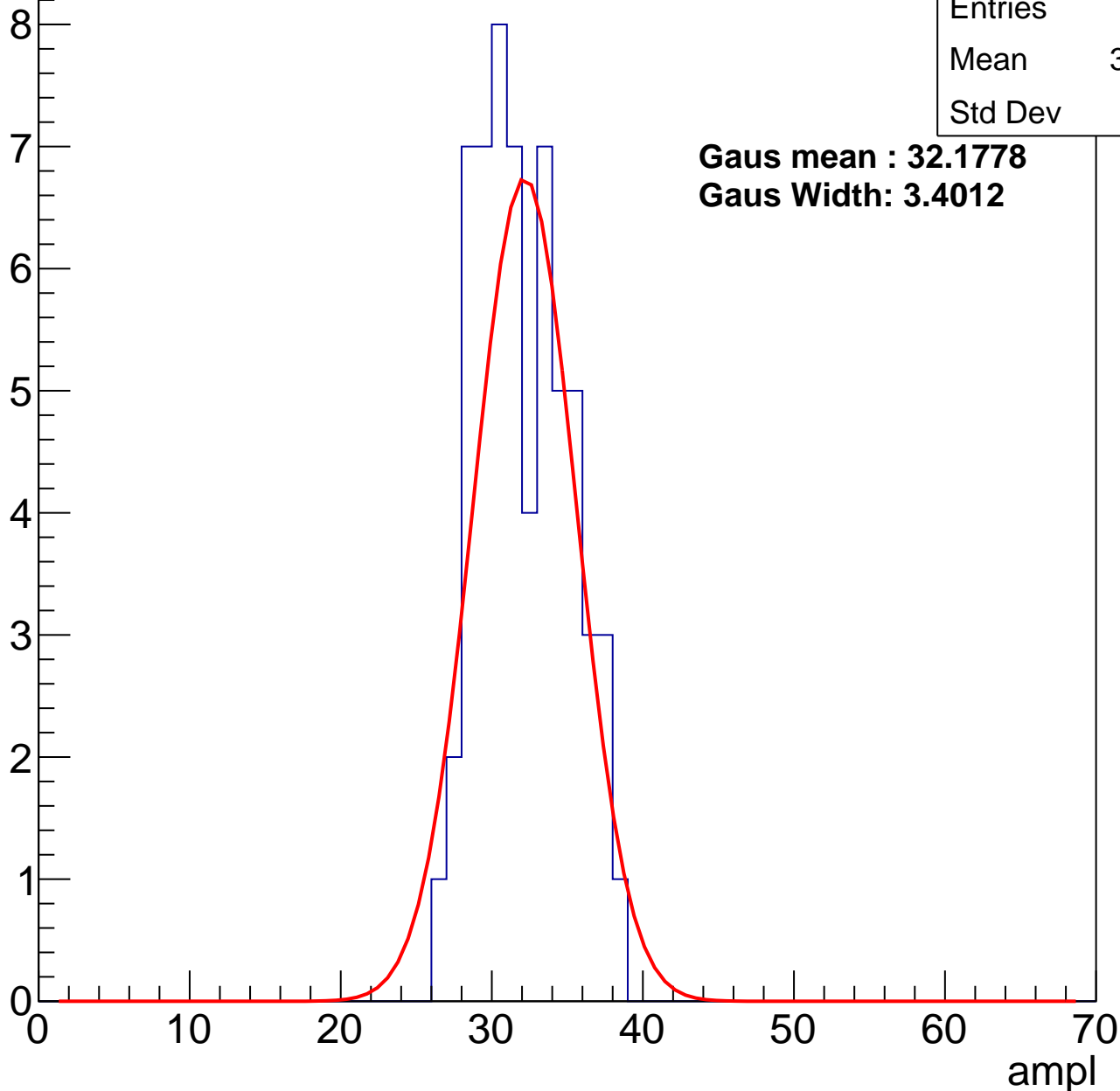
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	31.62
Std Dev	2.95

Gaus mean : 32.1778

Gaus Width: 3.4012



B1L103S, U10-ch25, adc1

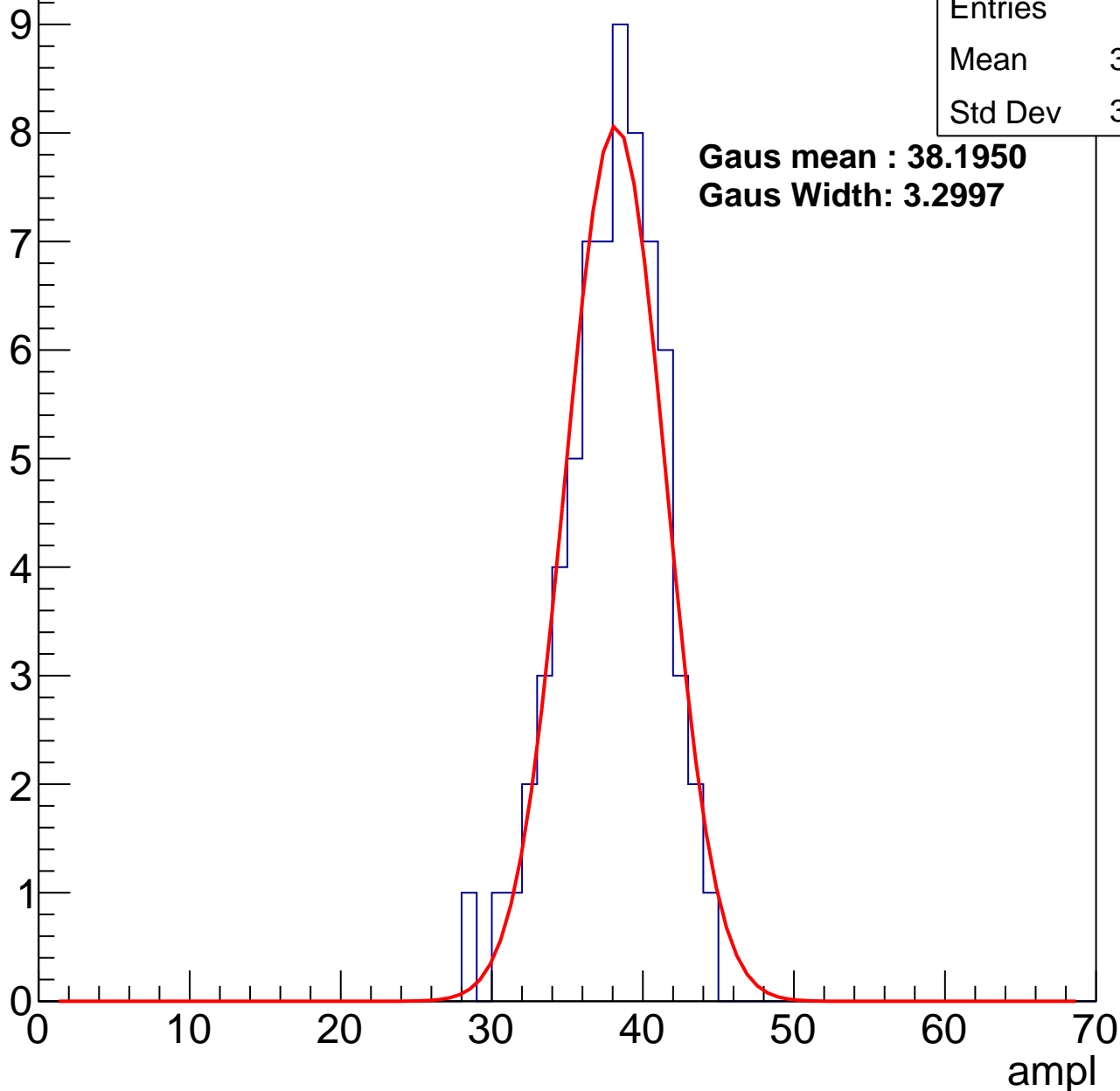
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	37.46
Std Dev	3.252

Gaus mean : 38.1950

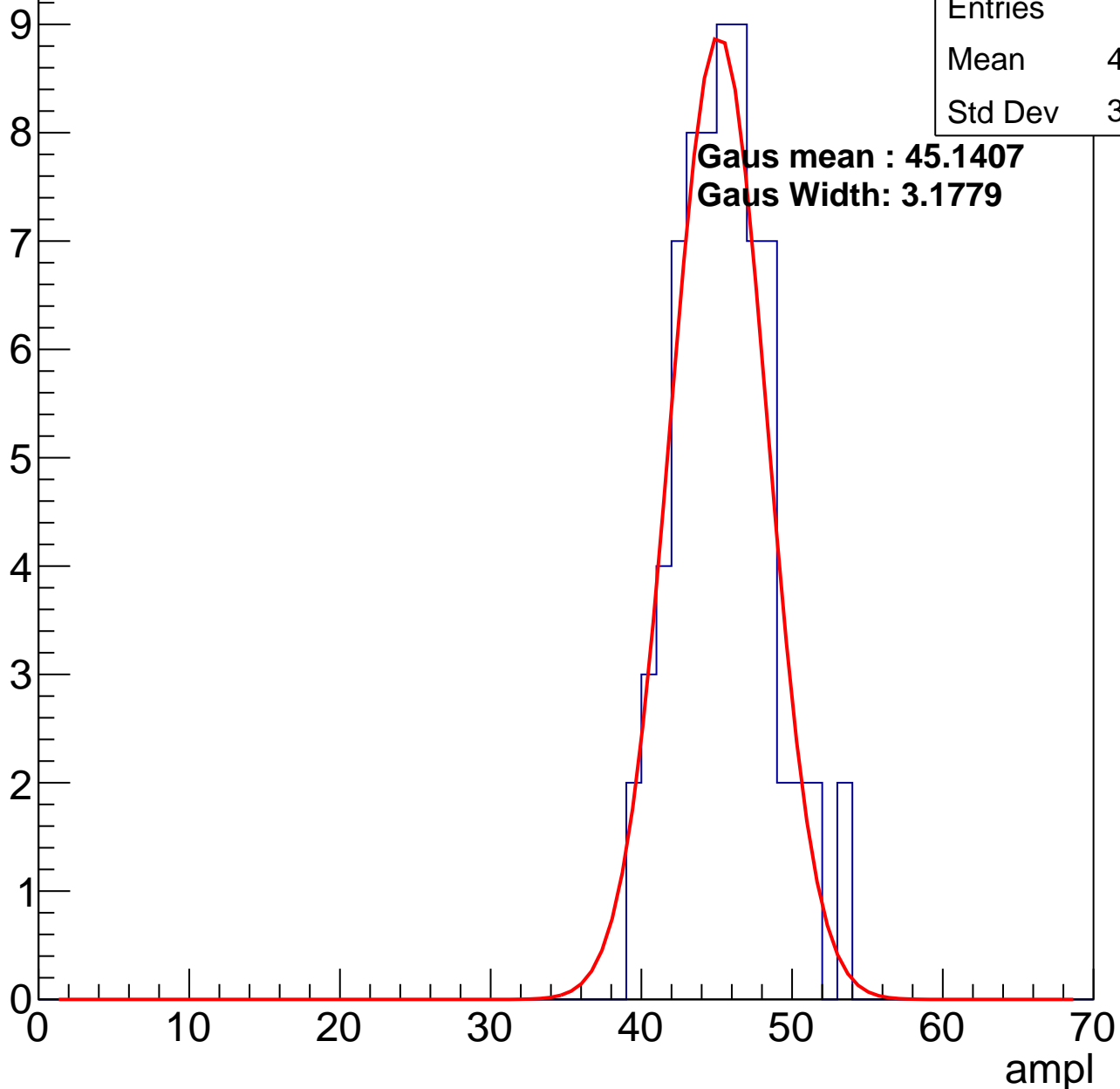
Gaus Width: 3.2997



B1L103S, U10-ch25, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	72
Mean	45.03
Std Dev	3.109

Gaus mean : 45.1407

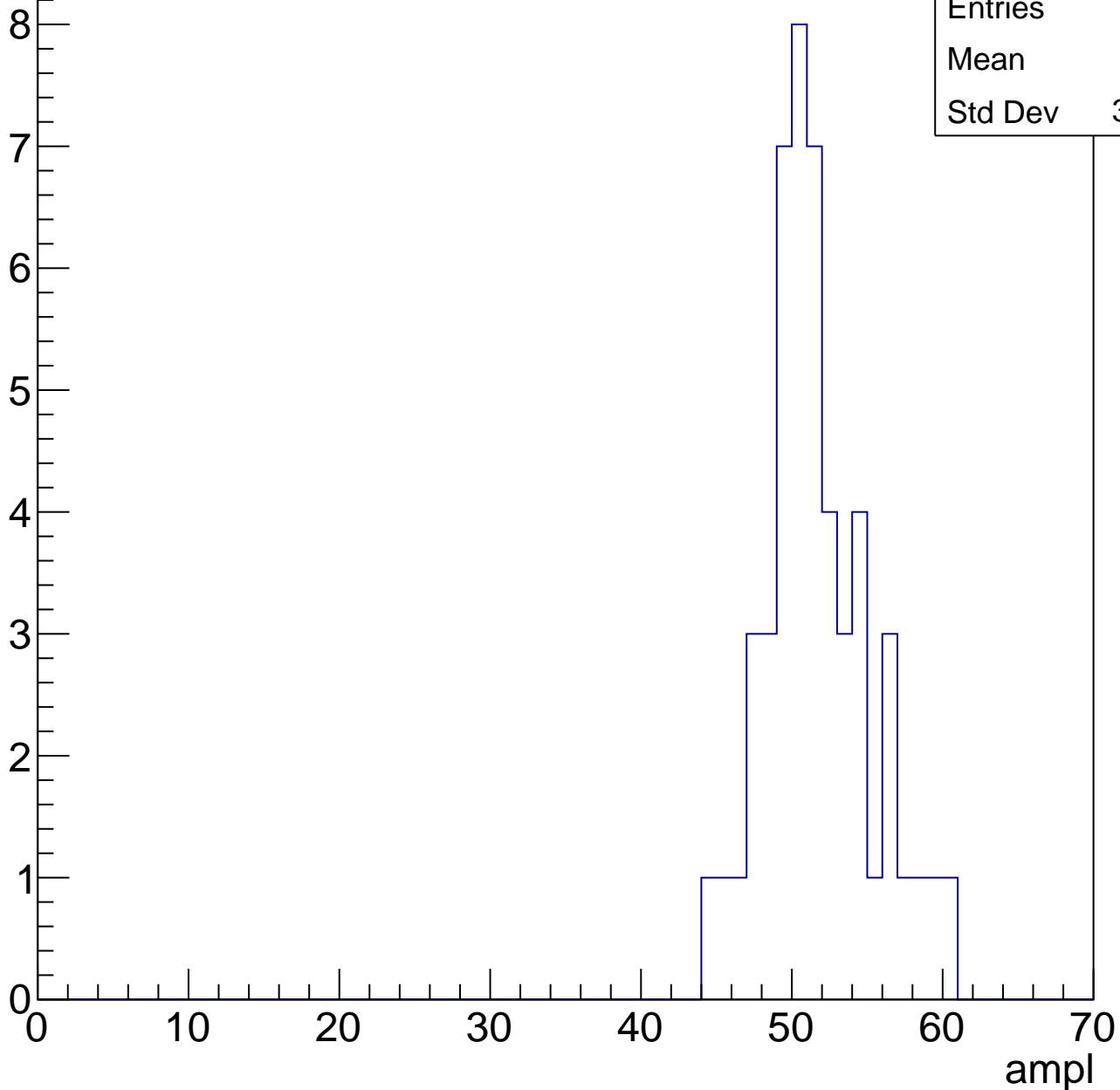
Gaus Width: 3.1779

B1L103S, U10-ch25, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	51.2
Std Dev	3.441

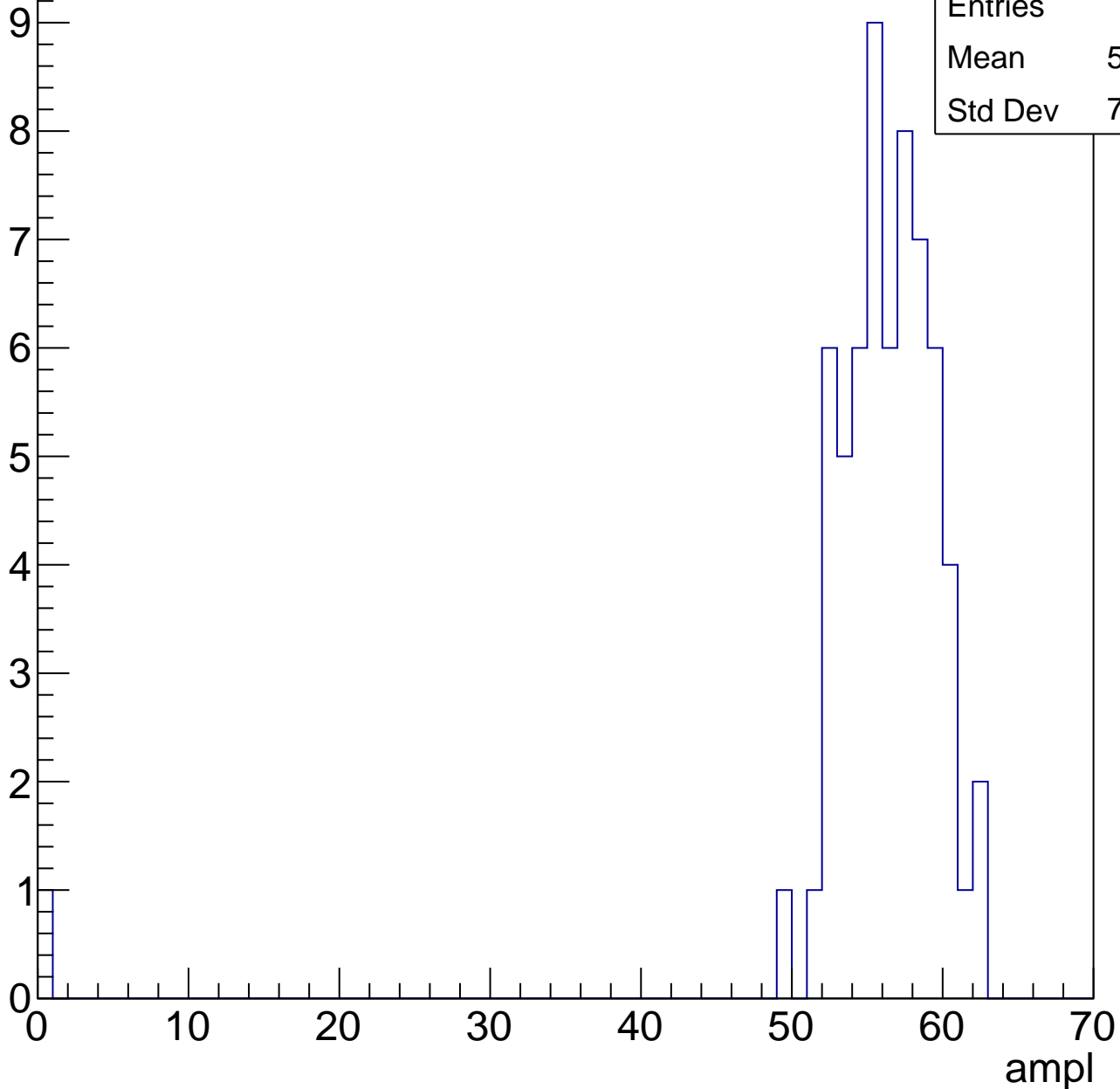


B1L103S, U10-ch25, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	55.13
Std Dev	7.546

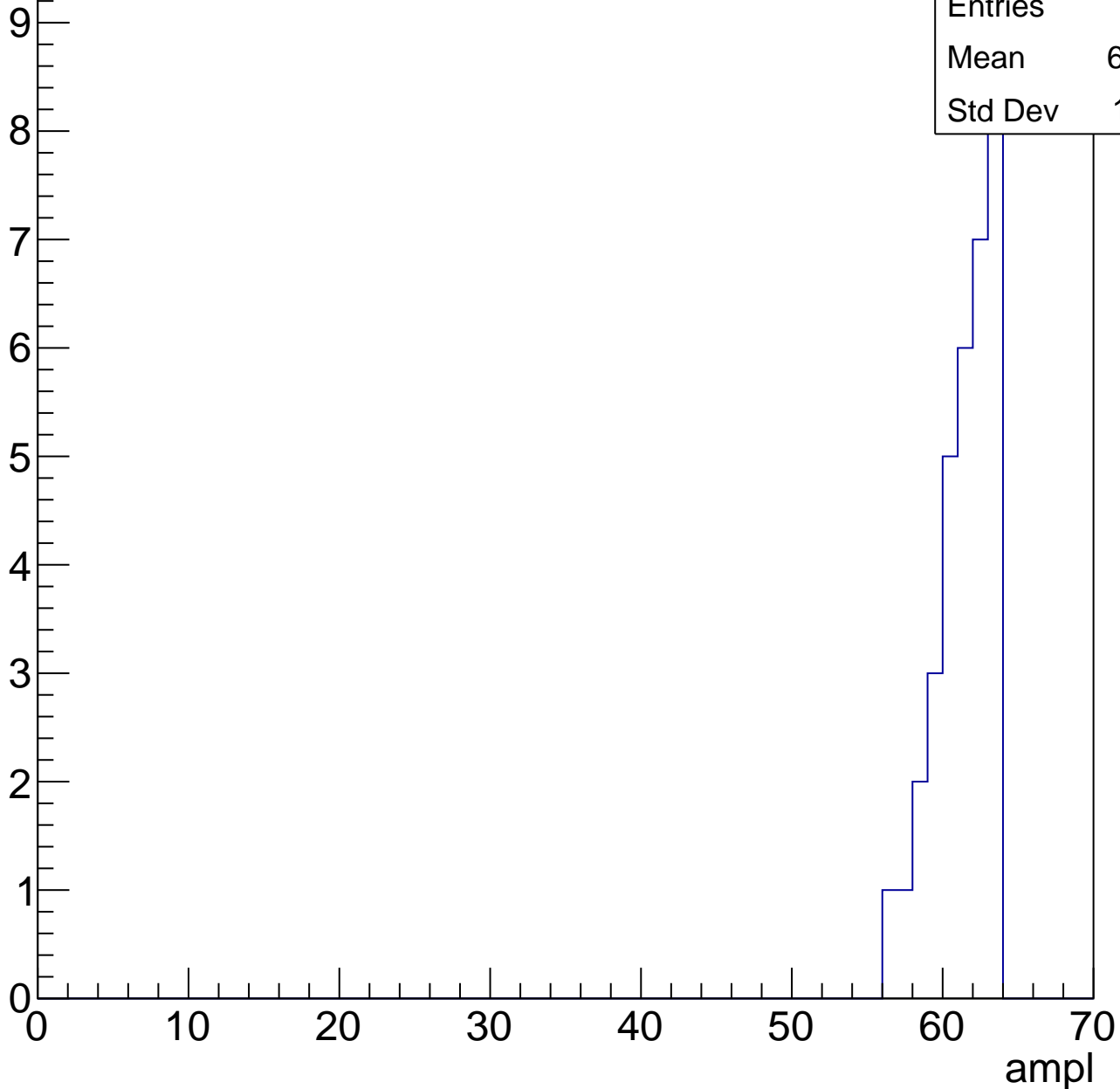


B1L103S, U10-ch25, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

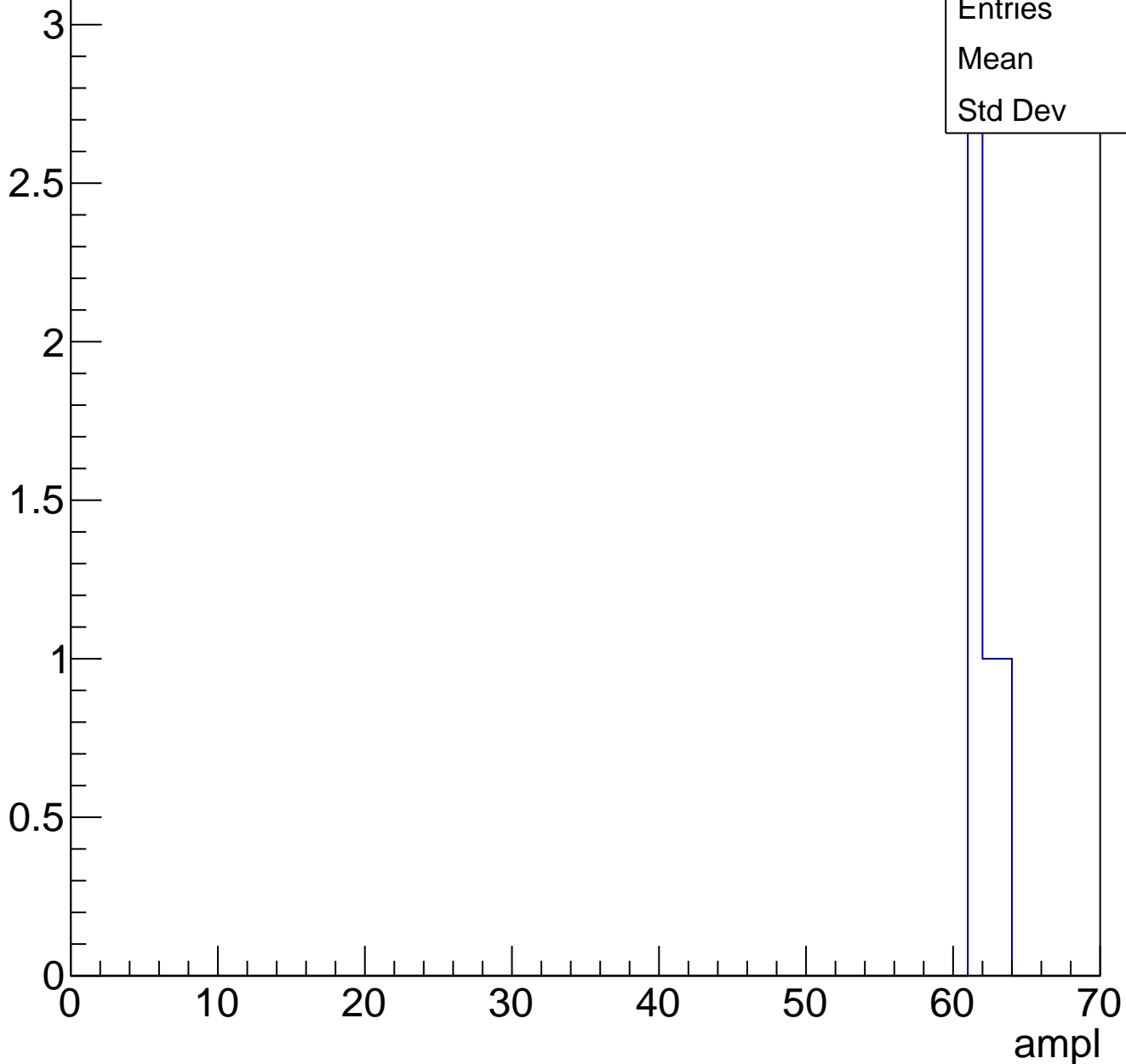
Entries	34
Mean	60.97
Std Dev	1.871



B1L103S, U10-ch25, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	61.6
Std Dev	0.8

B1L103S, U10-ch25, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch26, adc0

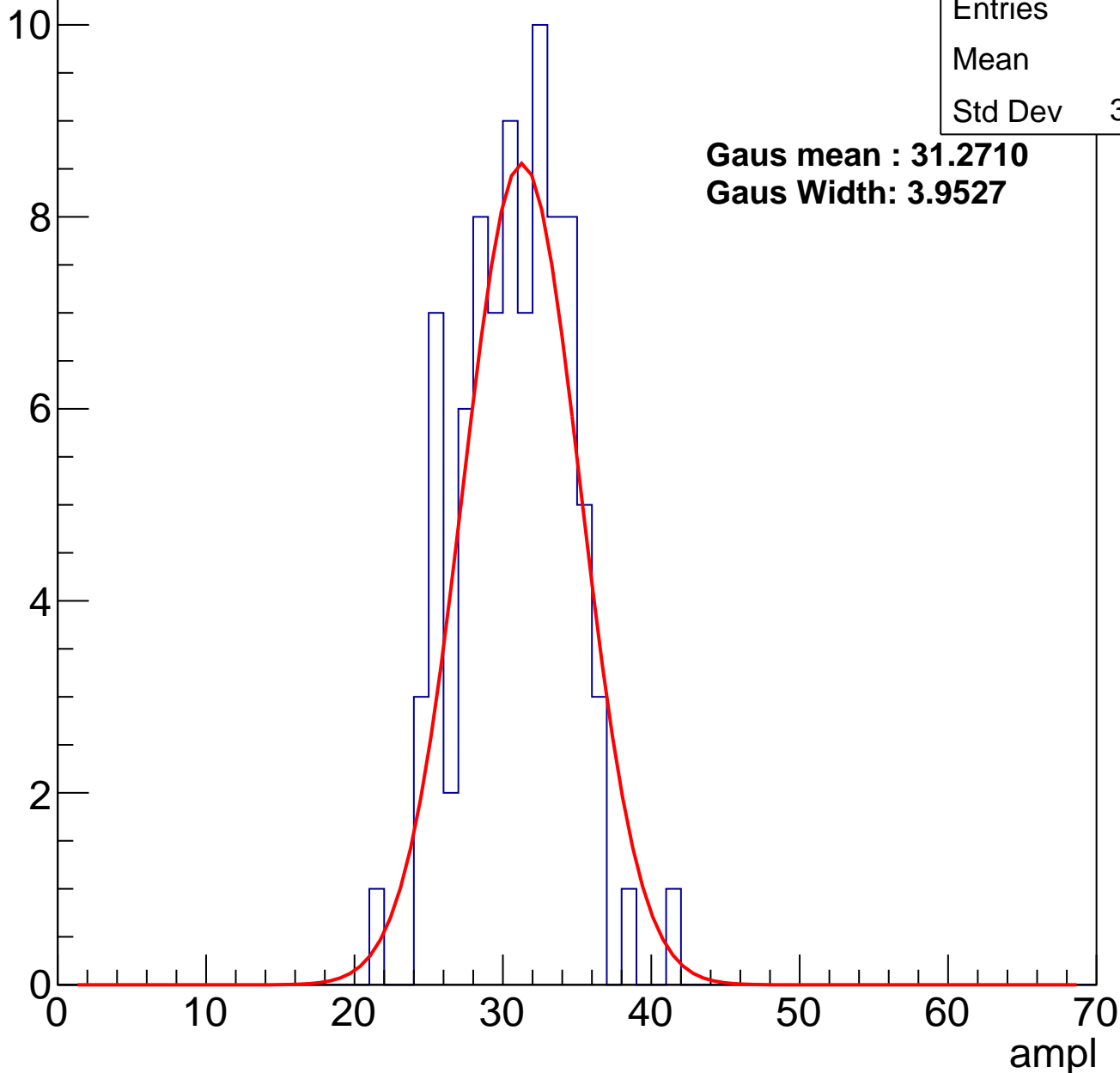
calib_packv5_042523_0143.root, FC#7, port C2

Entries	86
Mean	30.4
Std Dev	3.642

Gaus mean : 31.2710

Gaus Width: 3.9527

Entry



B1L103S, U10-ch26, adc1

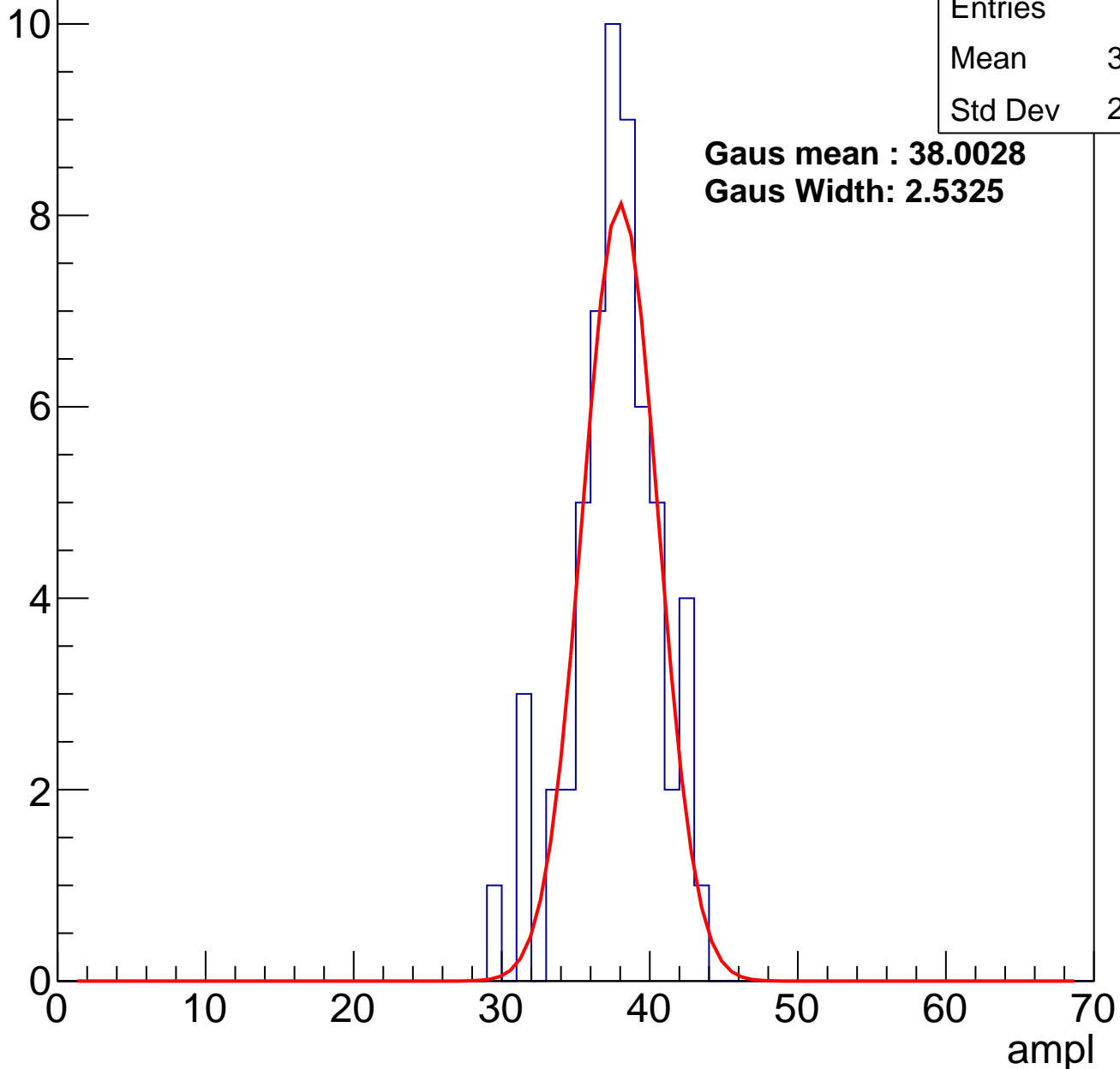
calib_packv5_042523_0143.root, FC#7, port C2

Entries	57
Mean	37.23
Std Dev	2.938

Gaus mean : 38.0028

Gaus Width: 2.5325

Entry



B1L103S, U10-ch26, adc2

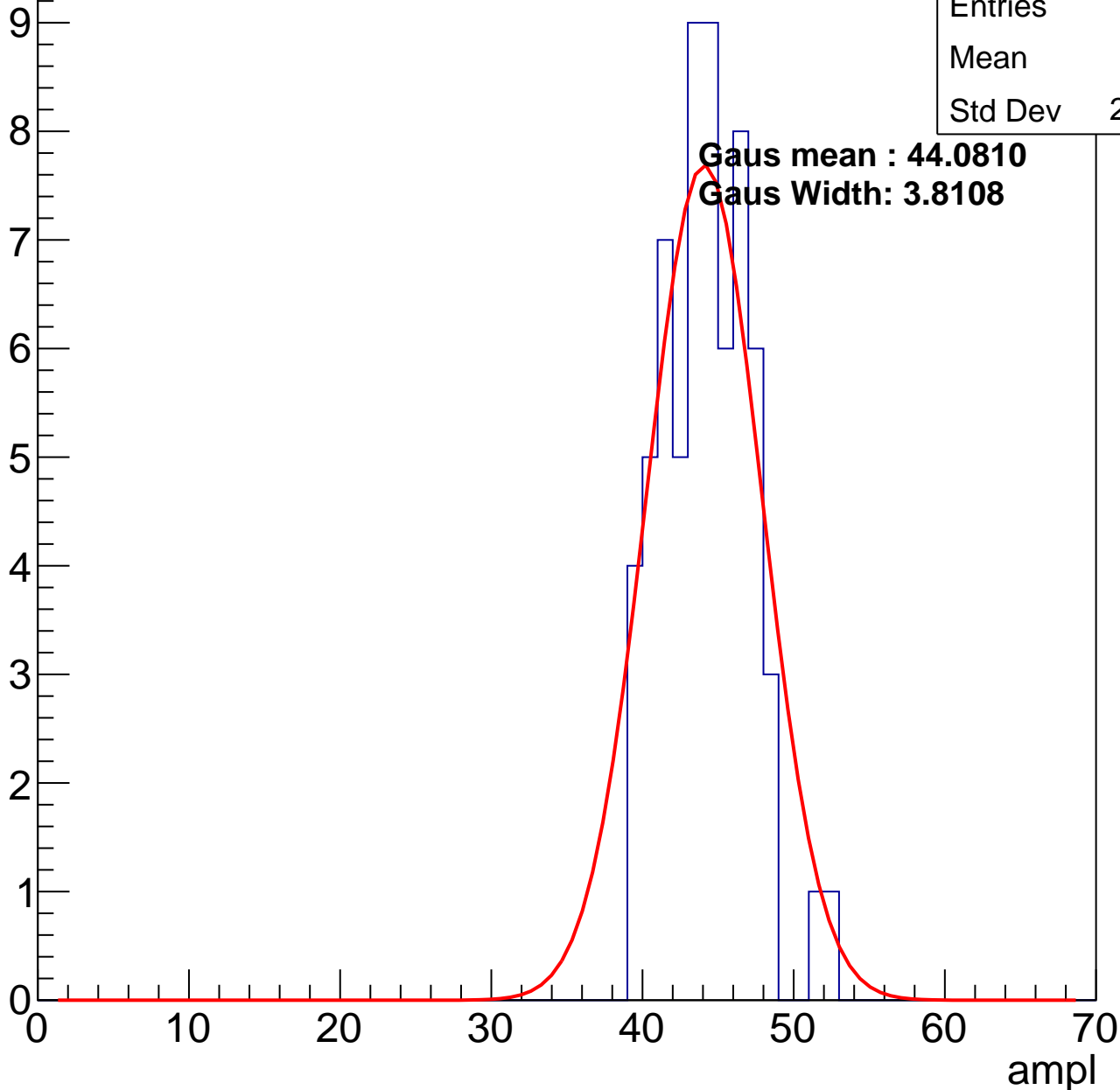
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.8
Std Dev	2.857

Gaus mean : 44.0810

Gaus Width: 3.8108



B1L103S, U10-ch26, adc3

calib_packv5_042523_0143.root, FC#7, port C2

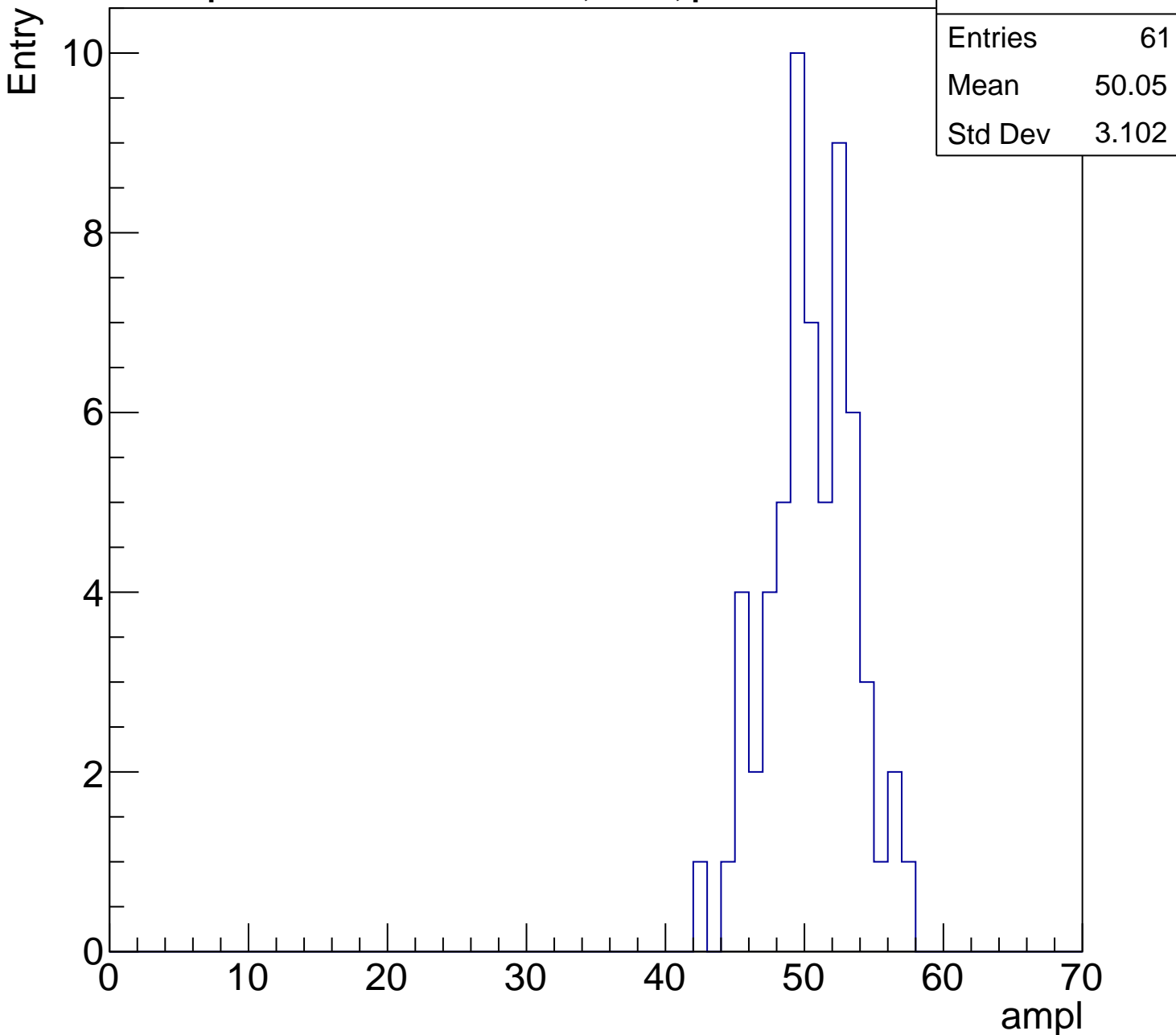
Entries	61
Mean	50.05
Std Dev	3.102

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

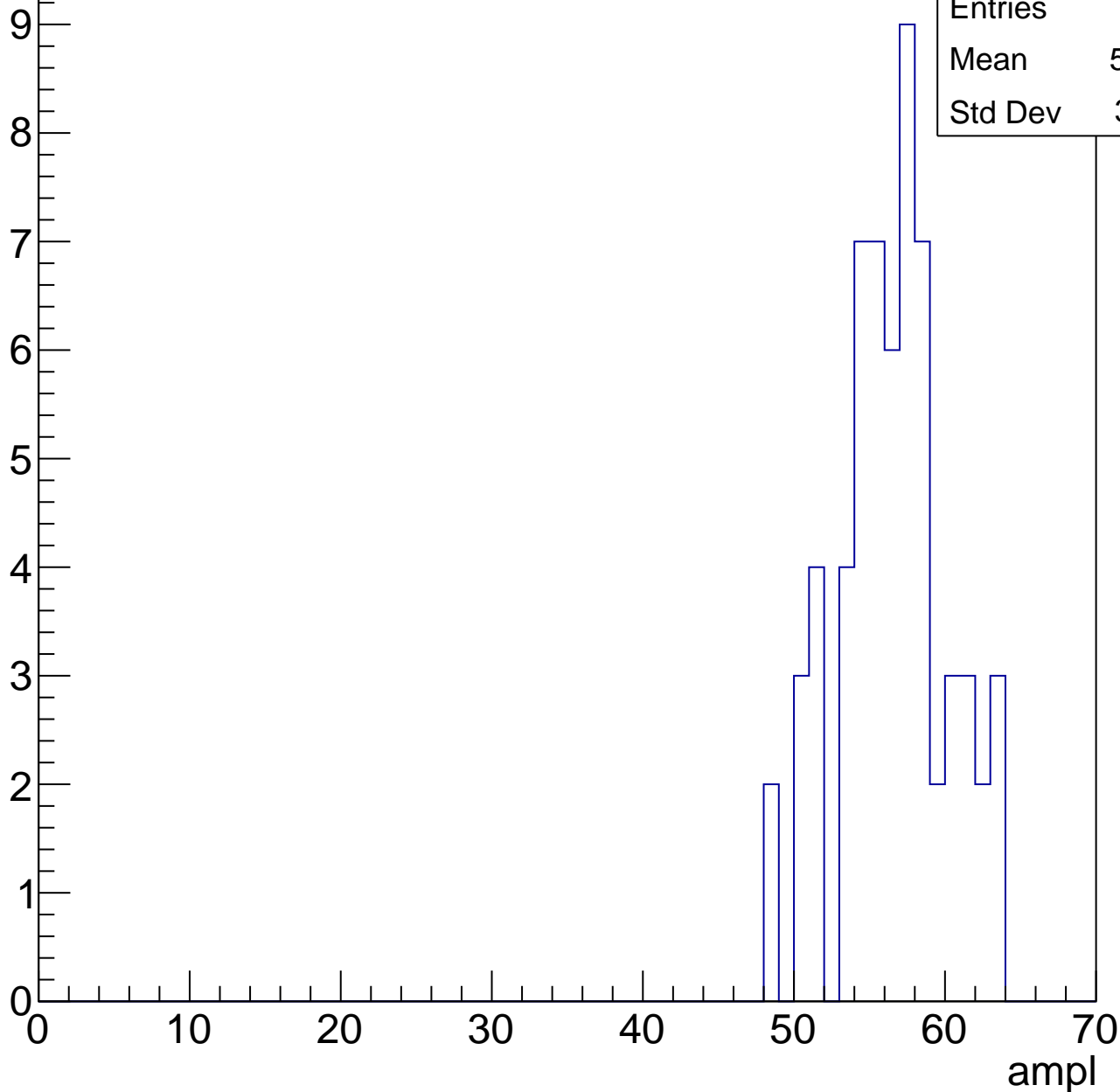


B1L103S, U10-ch26, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

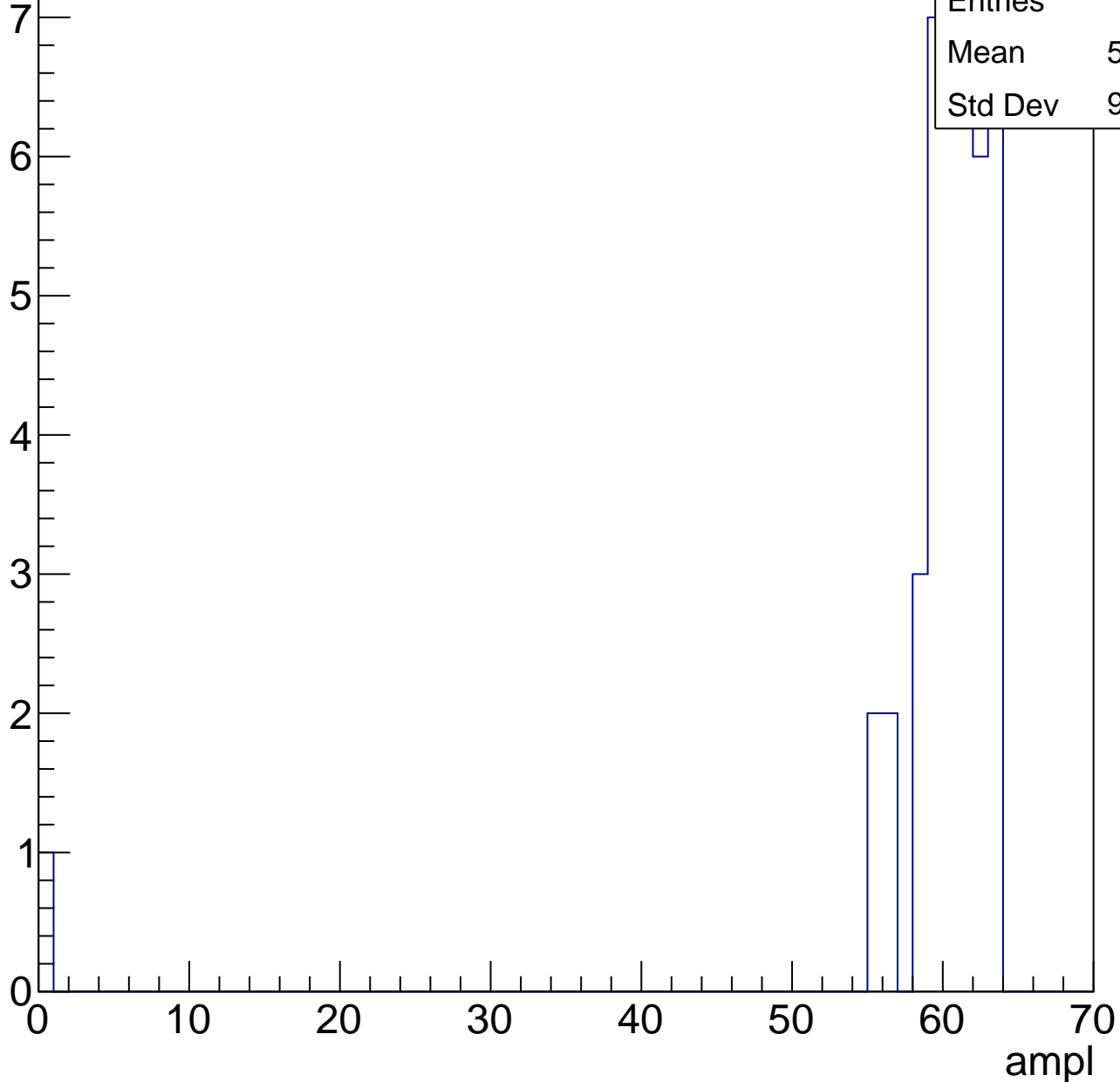
Entries	62
Mean	56.03
Std Dev	3.601



B1L103S, U10-ch26, adc5

calib_packv5_042523_0143.root, FC#7, port C2

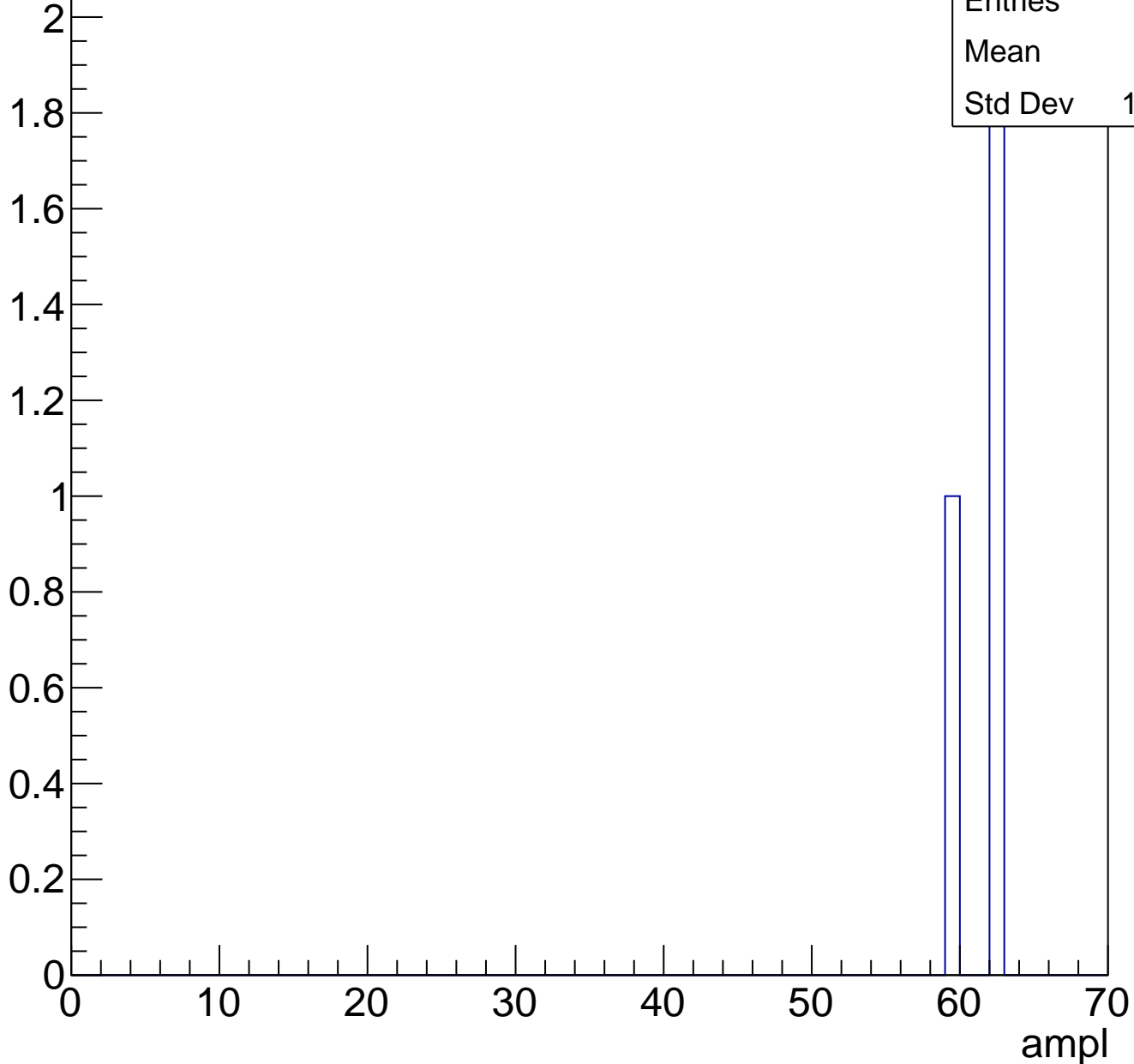
Entry



B1L103S, U10-ch26, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch26, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch27, adc0

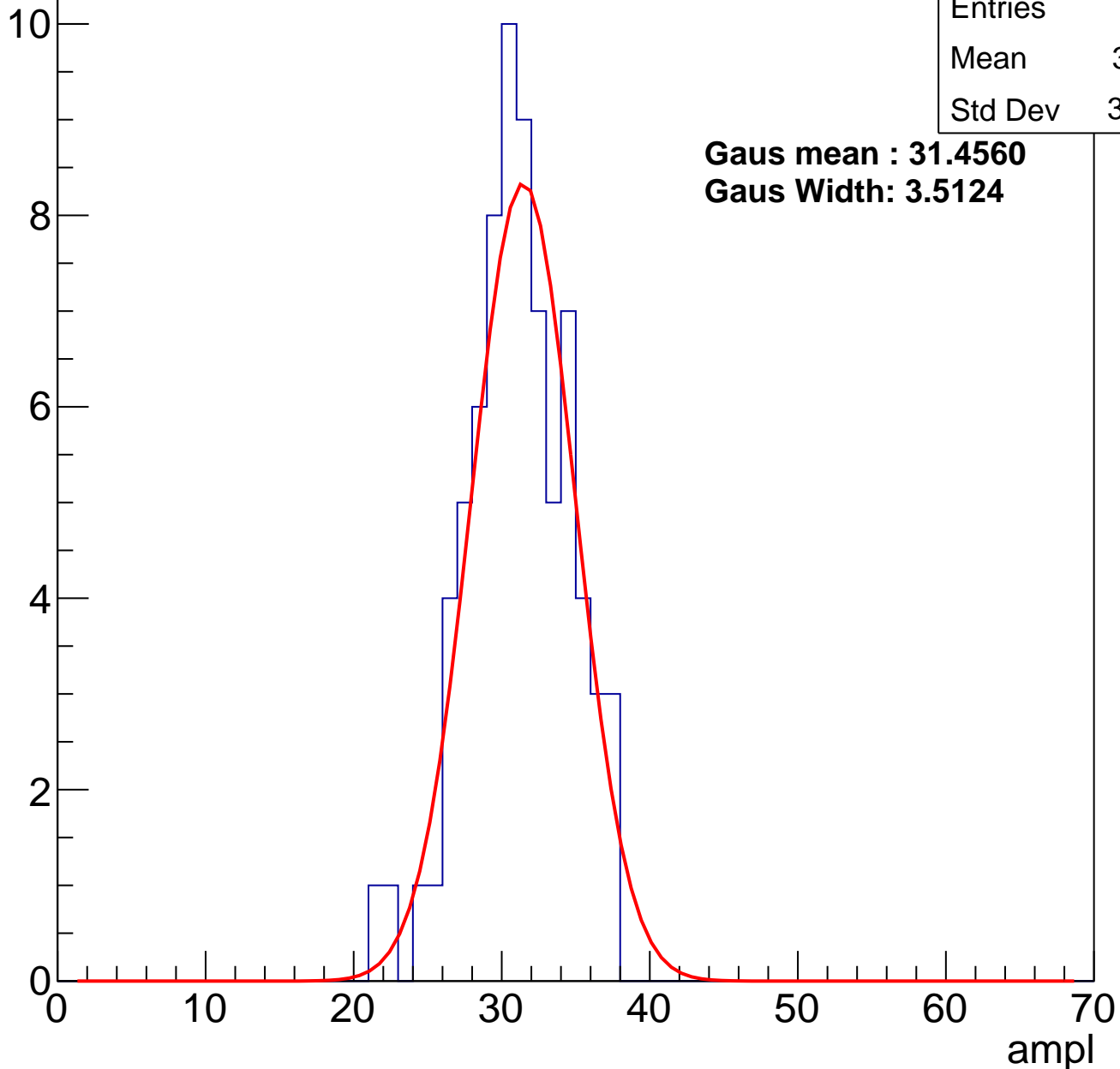
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	30.61
Std Dev	3.402

Gaus mean : 31.4560

Gaus Width: 3.5124

Entry



B1L103S, U10-ch27, adc1

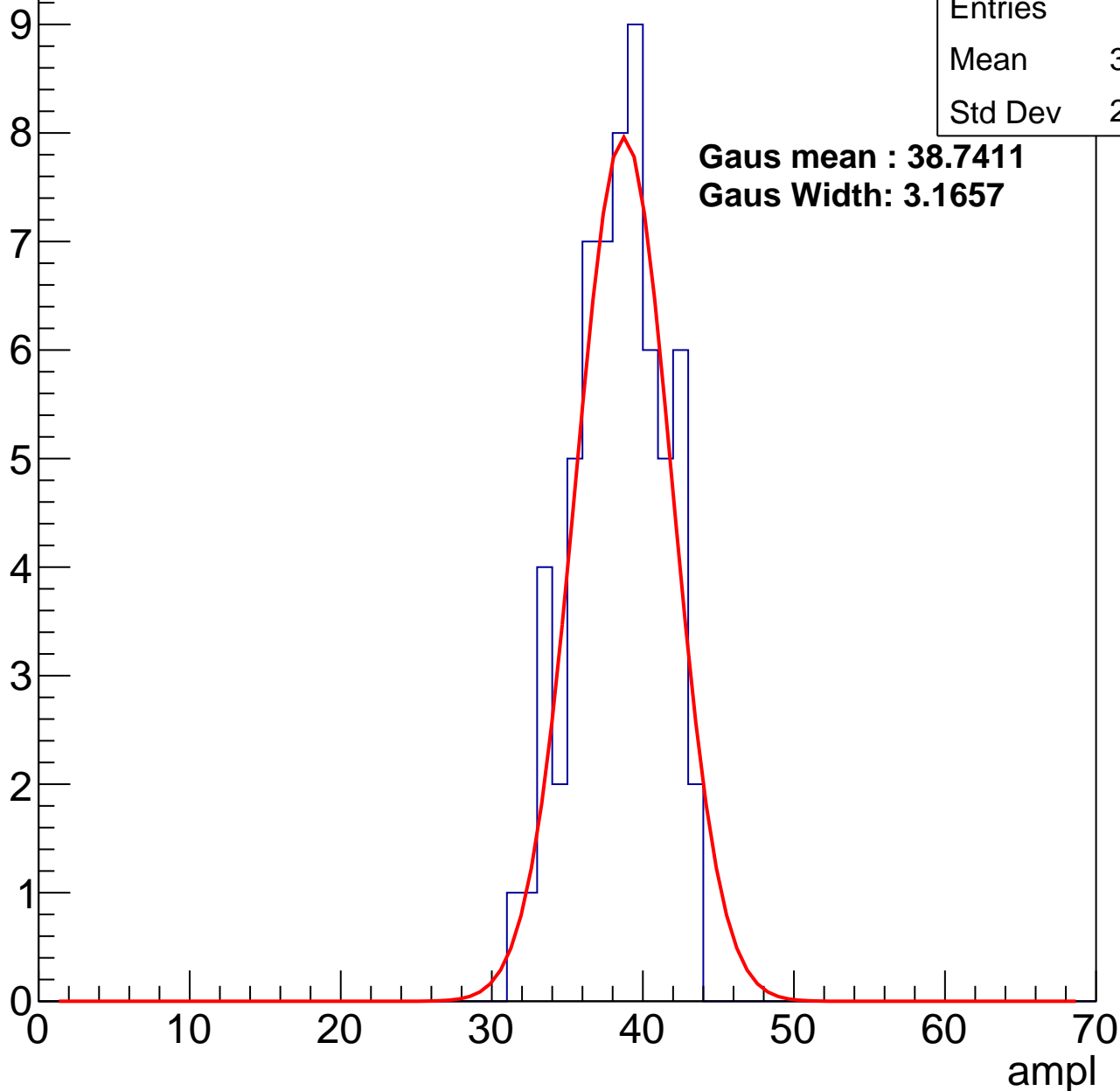
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	37.89
Std Dev	2.874

Gaus mean : 38.7411

Gaus Width: 3.1657



B1L103S, U10-ch27, adc2

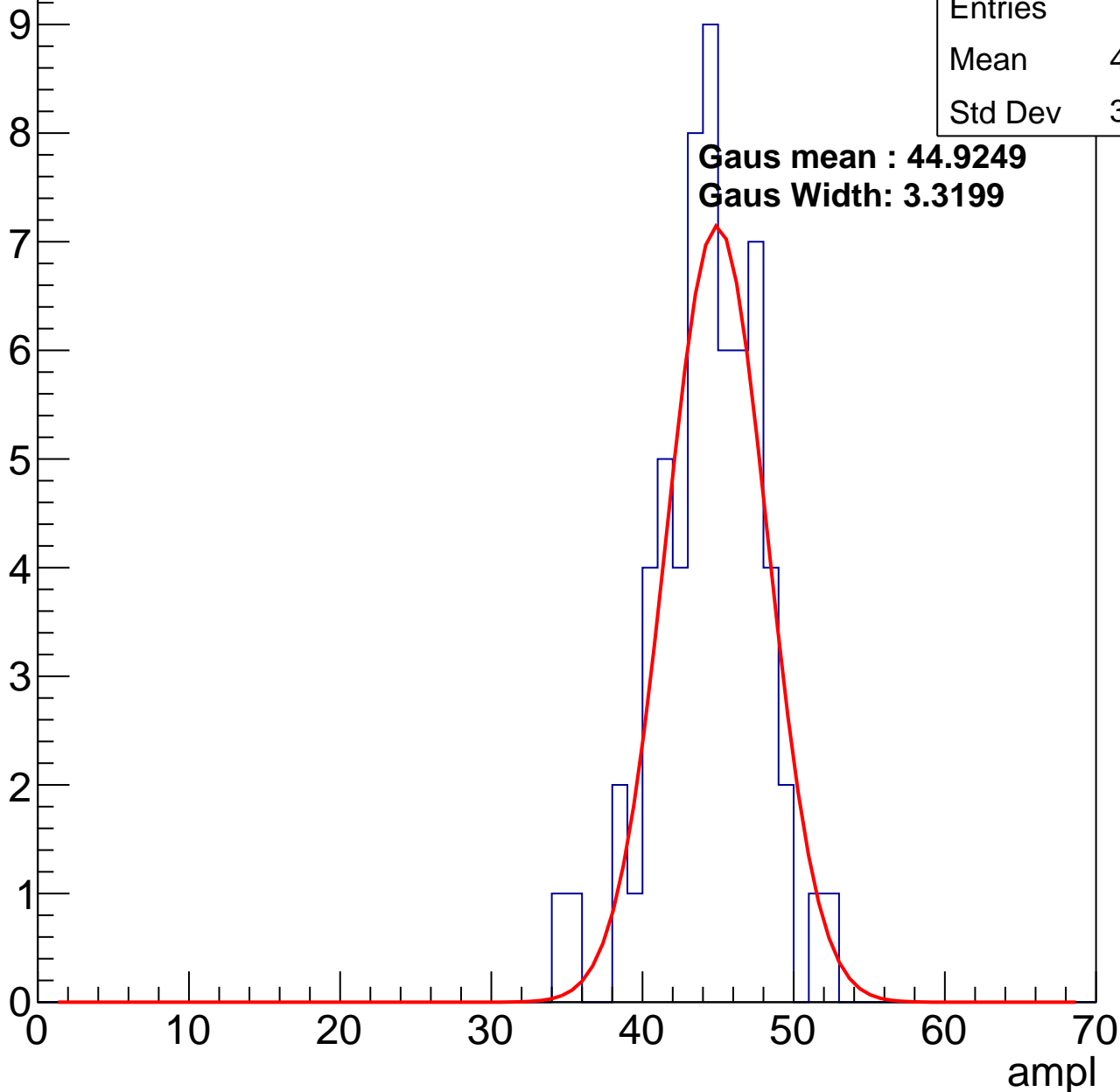
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	43.95
Std Dev	3.433

Gaus mean : 44.9249

Gaus Width: 3.3199



B1L103S, U10-ch27, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	50.43
Std Dev	3.173

Entry

10

8

6

4

2

0

0

10

20

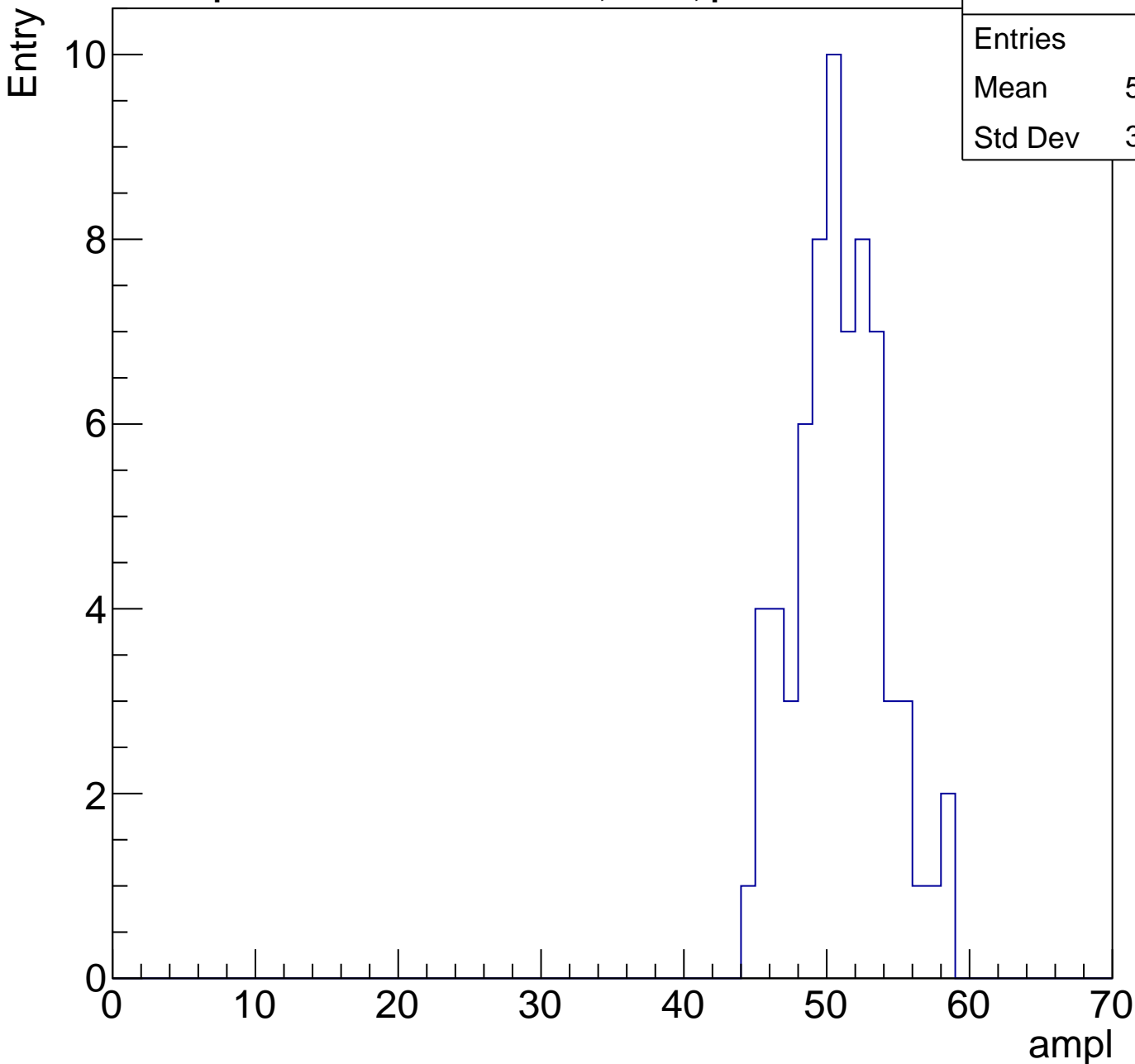
30

40

50

60

ampl

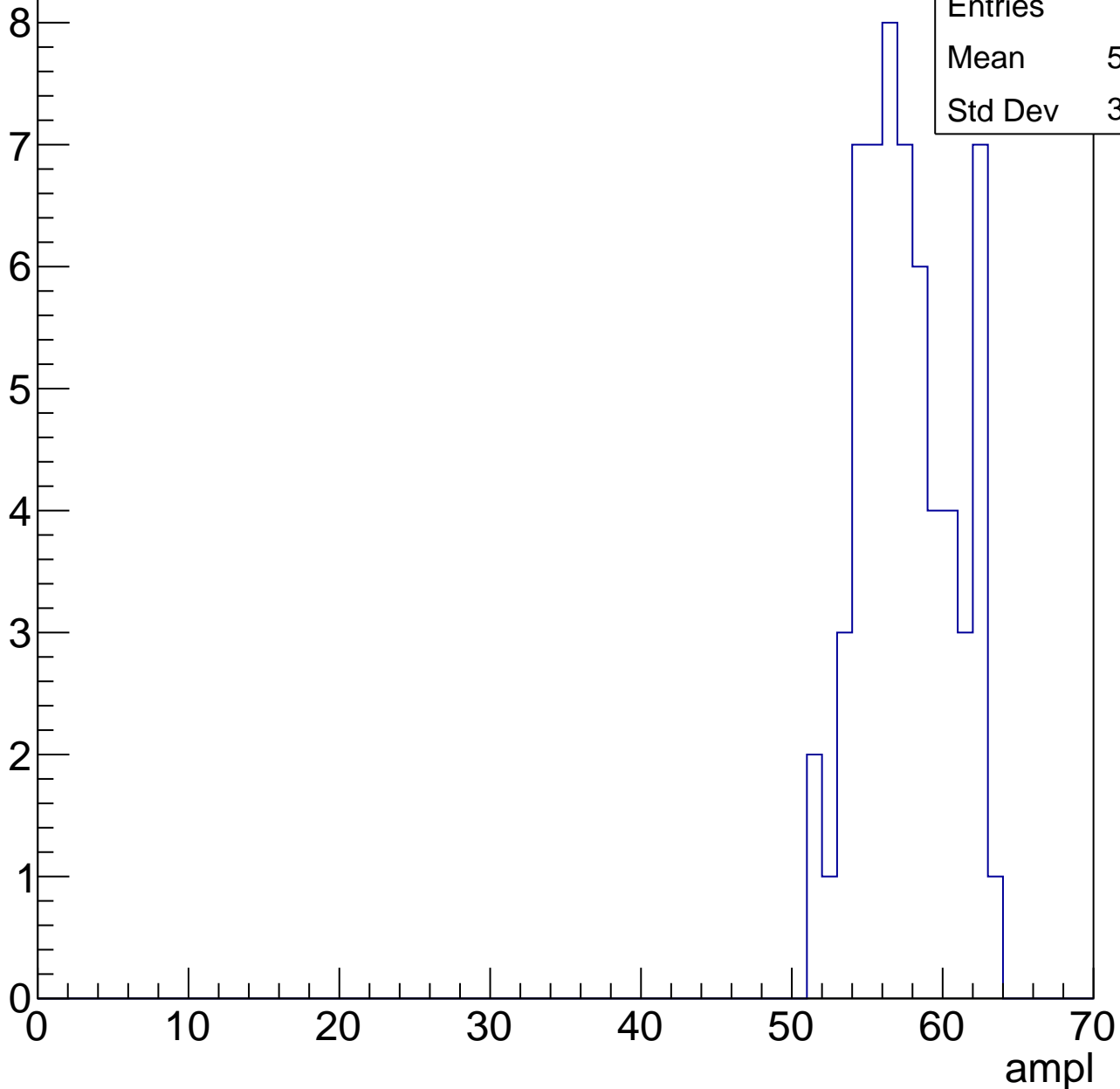


B1L103S, U10-ch27, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	57.12
Std Dev	3.056

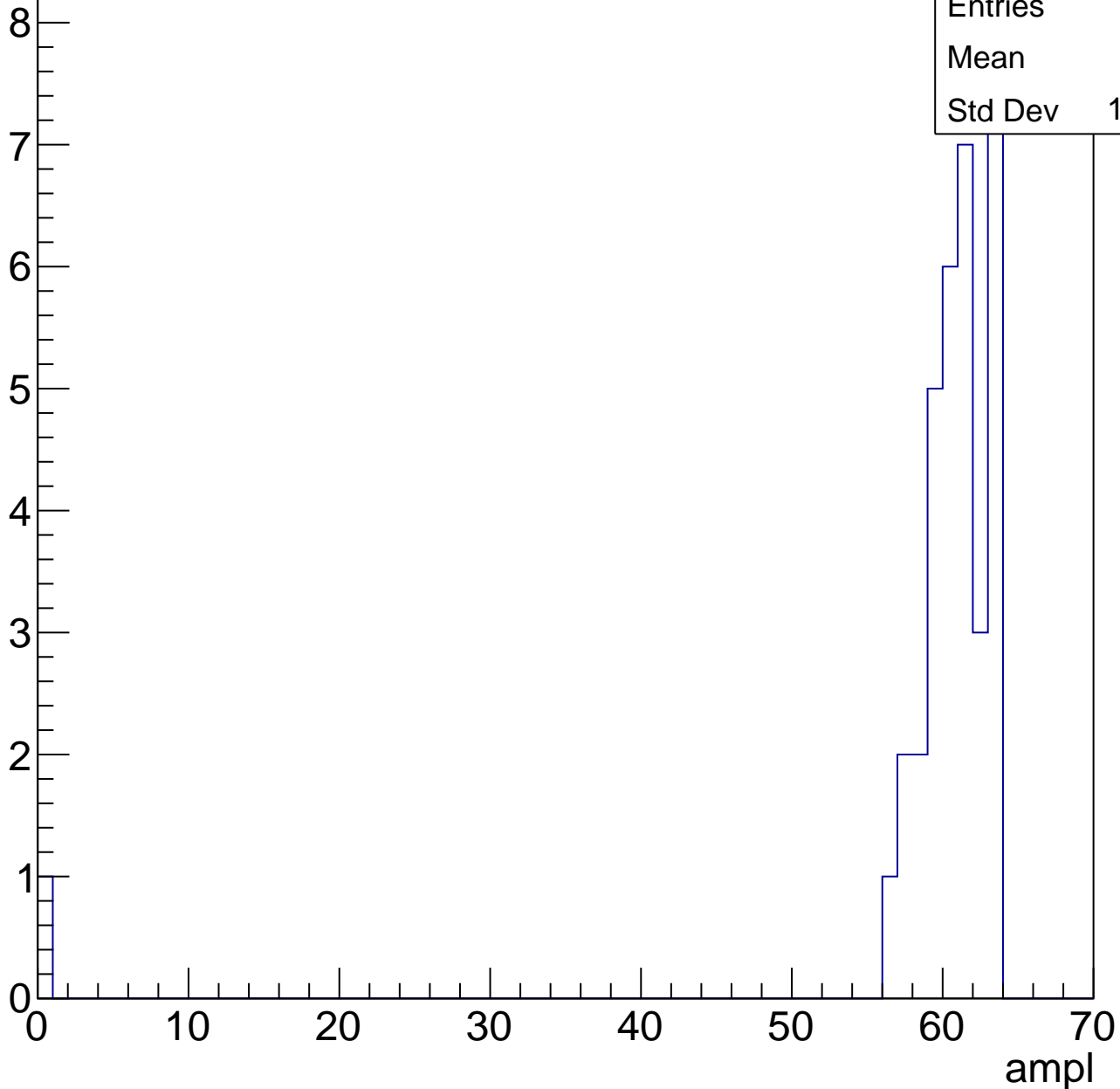


B1L103S, U10-ch27, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

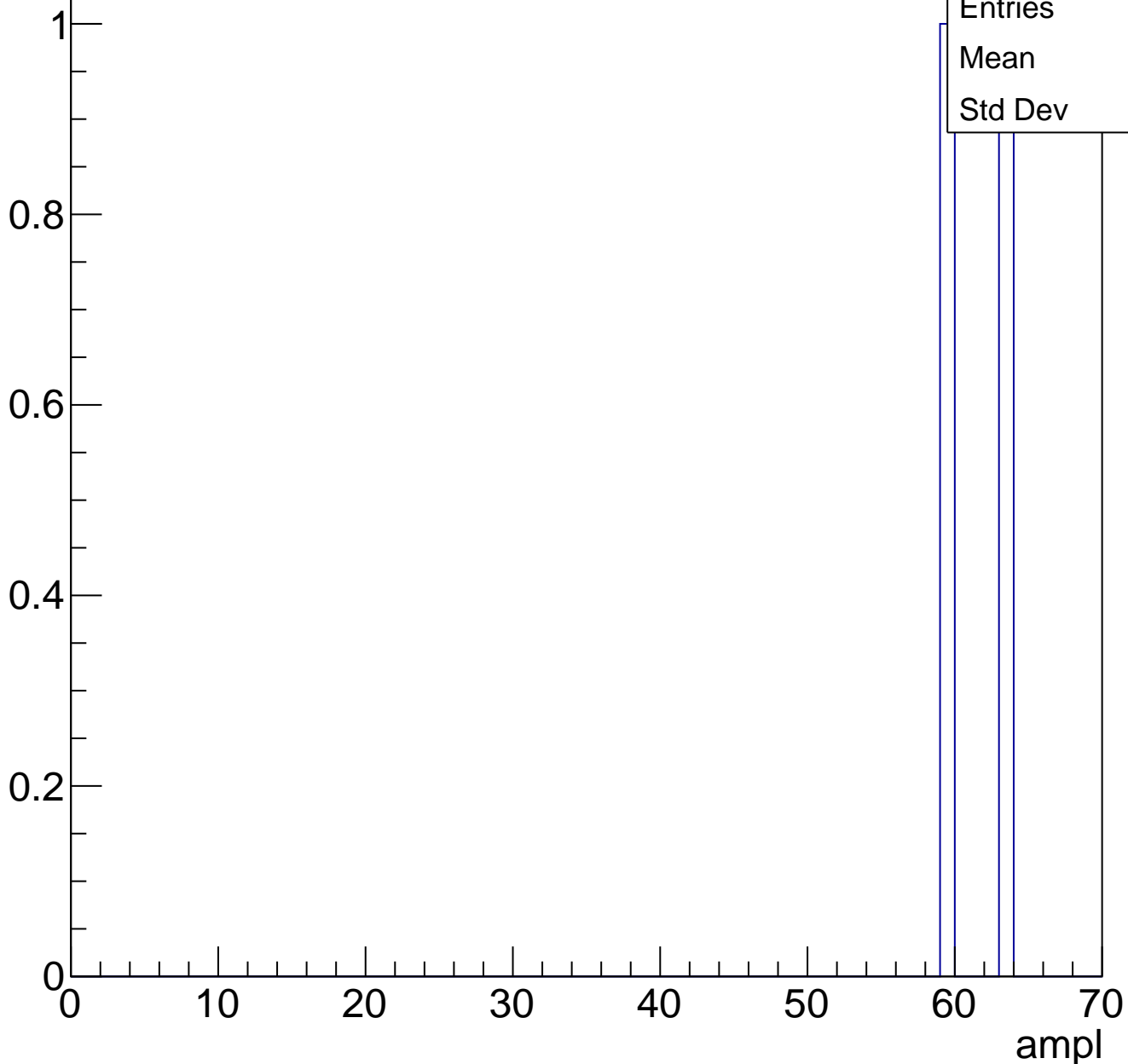
Entries	35
Mean	58.8
Std Dev	10.26



B1L103S, U10-ch27, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch27, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch28, adc0

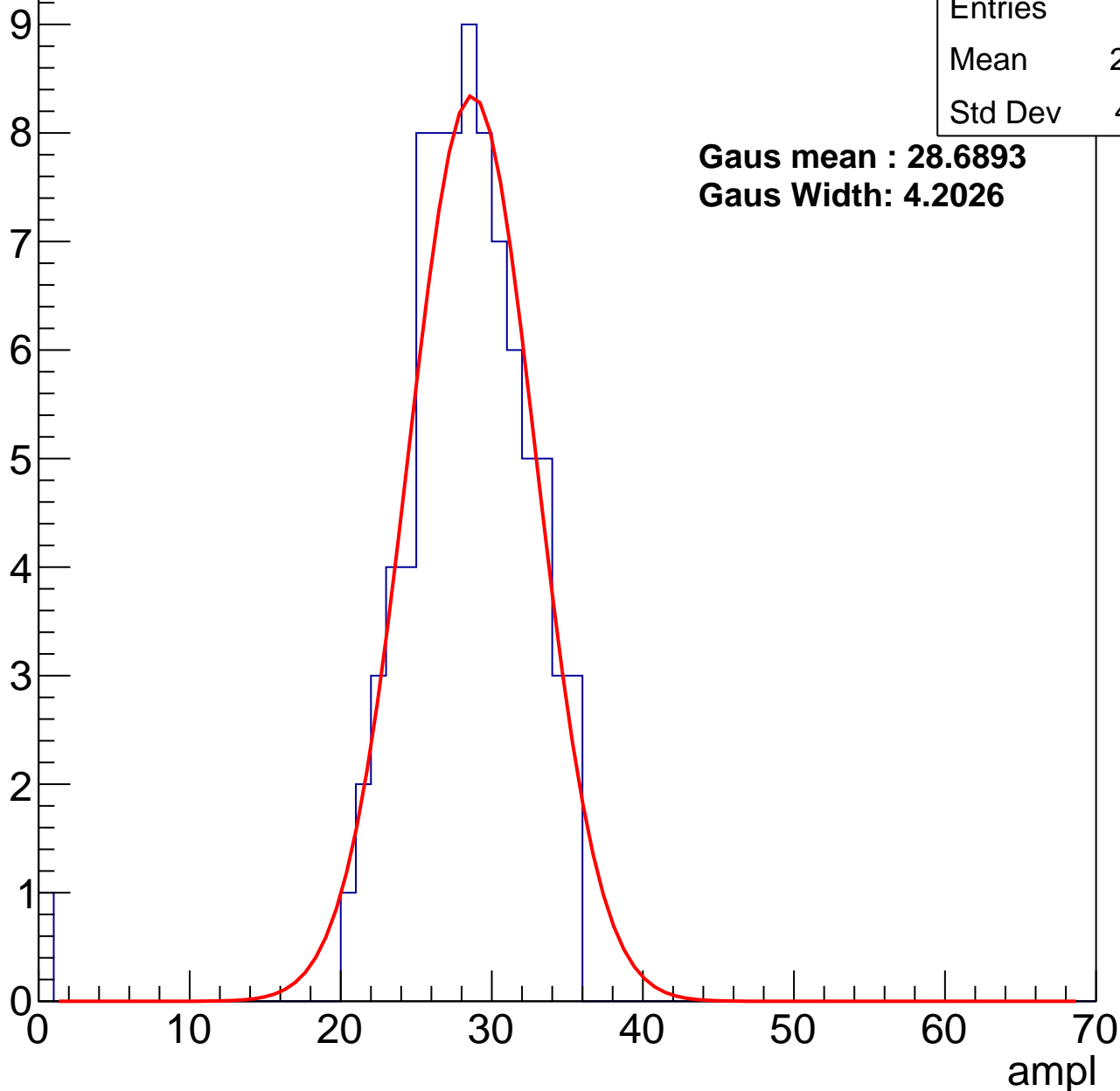
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	27.67
Std Dev	4.691

Gaus mean : 28.6893

Gaus Width: 4.2026



B1L103S, U10-ch28, adc1

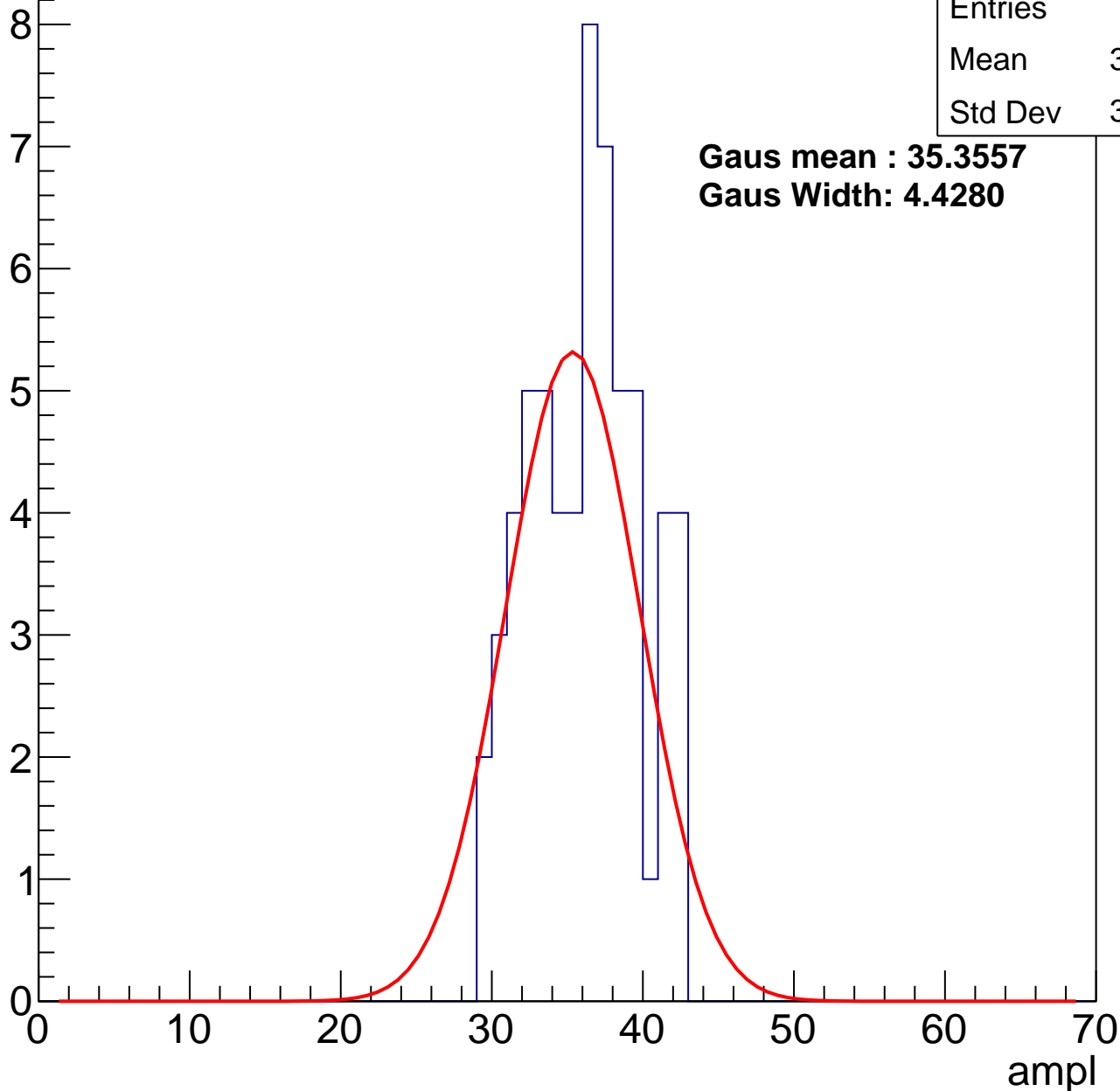
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	35.69
Std Dev	3.569

Gaus mean : 35.3557

Gaus Width: 4.4280



B1L103S, U10-ch28, adc2

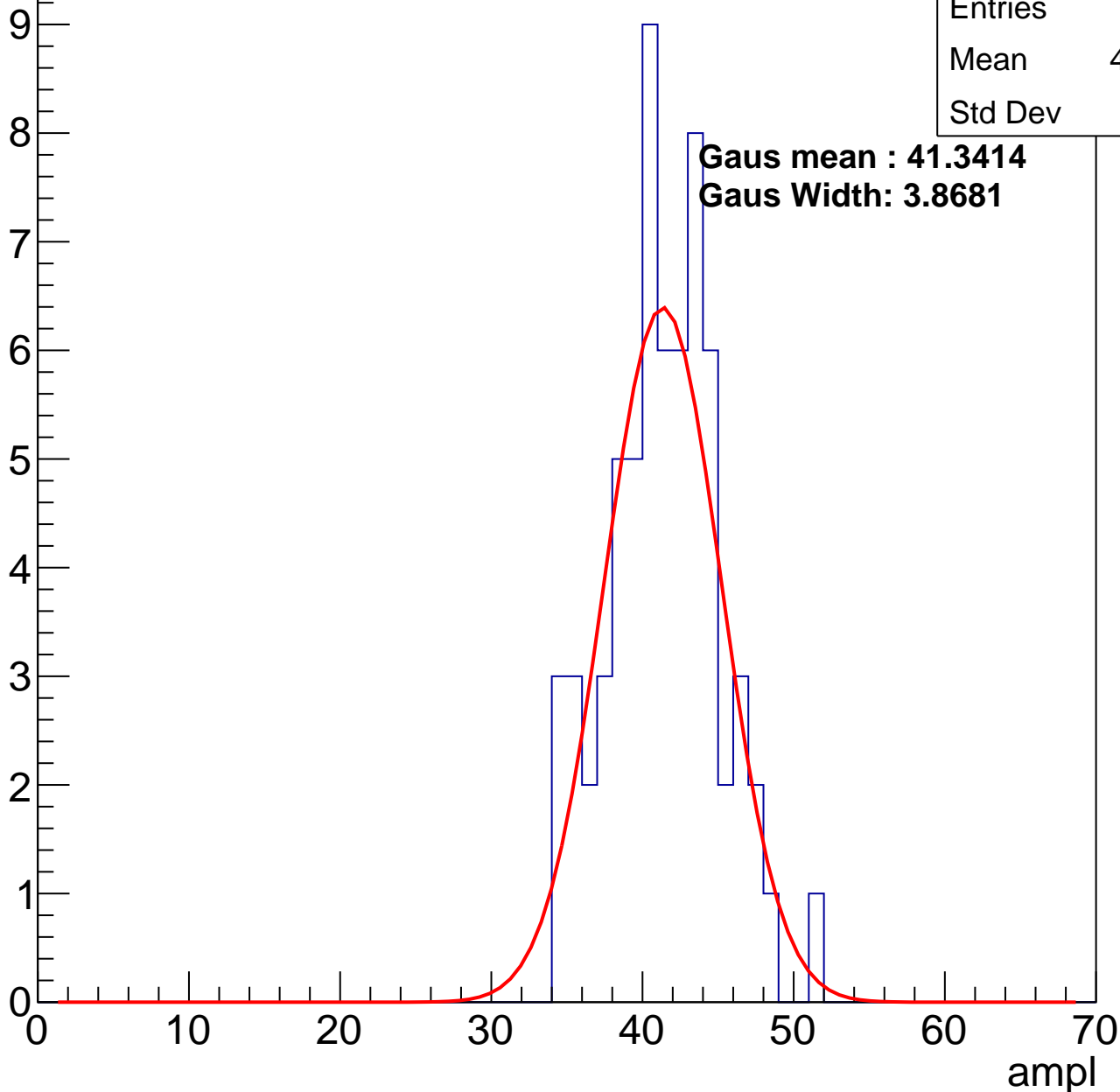
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.95
Std Dev	3.61

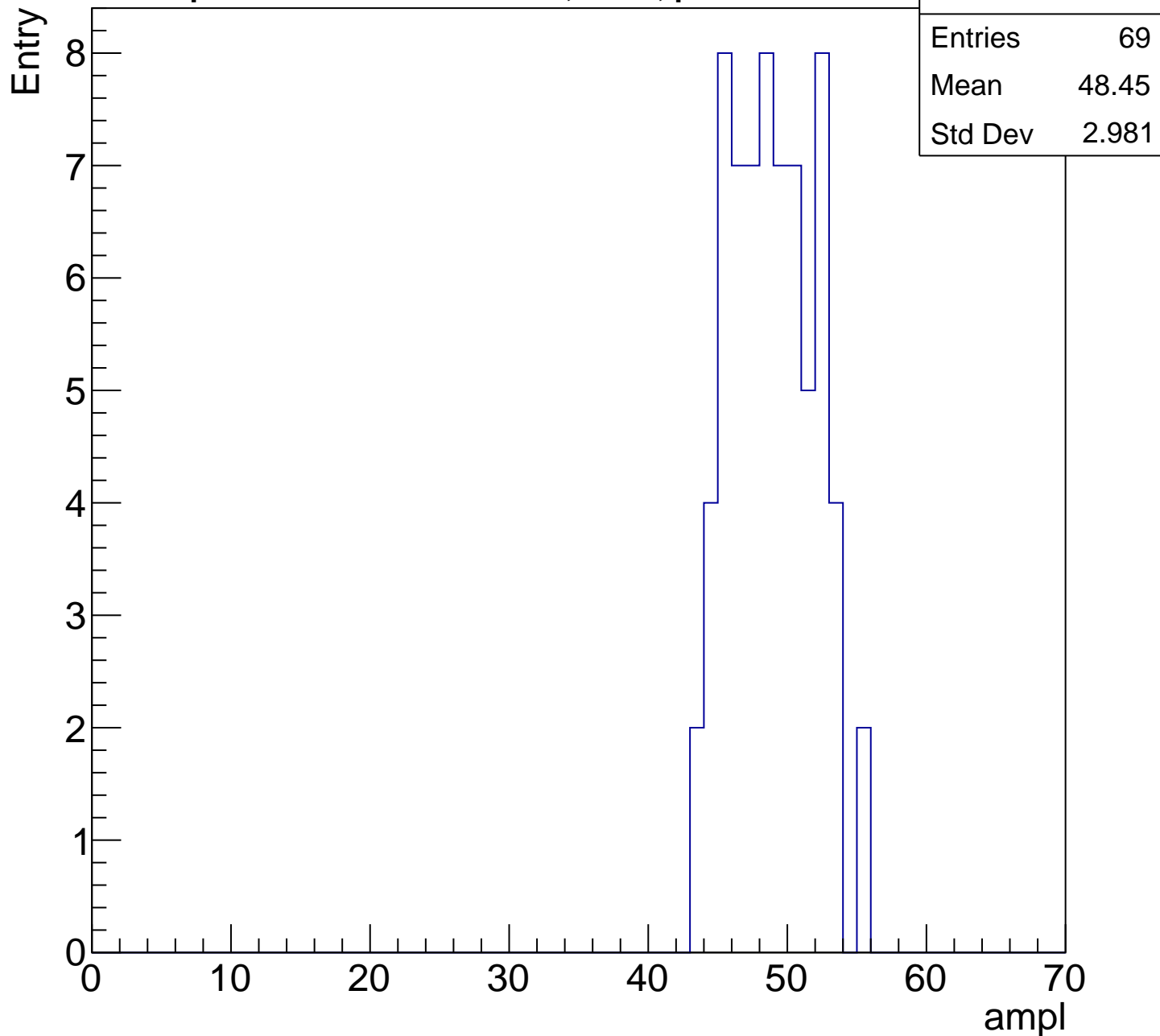
Gaus mean : 41.3414

Gaus Width: 3.8681



B1L103S, U10-ch28, adc3

calib_packv5_042523_0143.root, FC#7, port C2

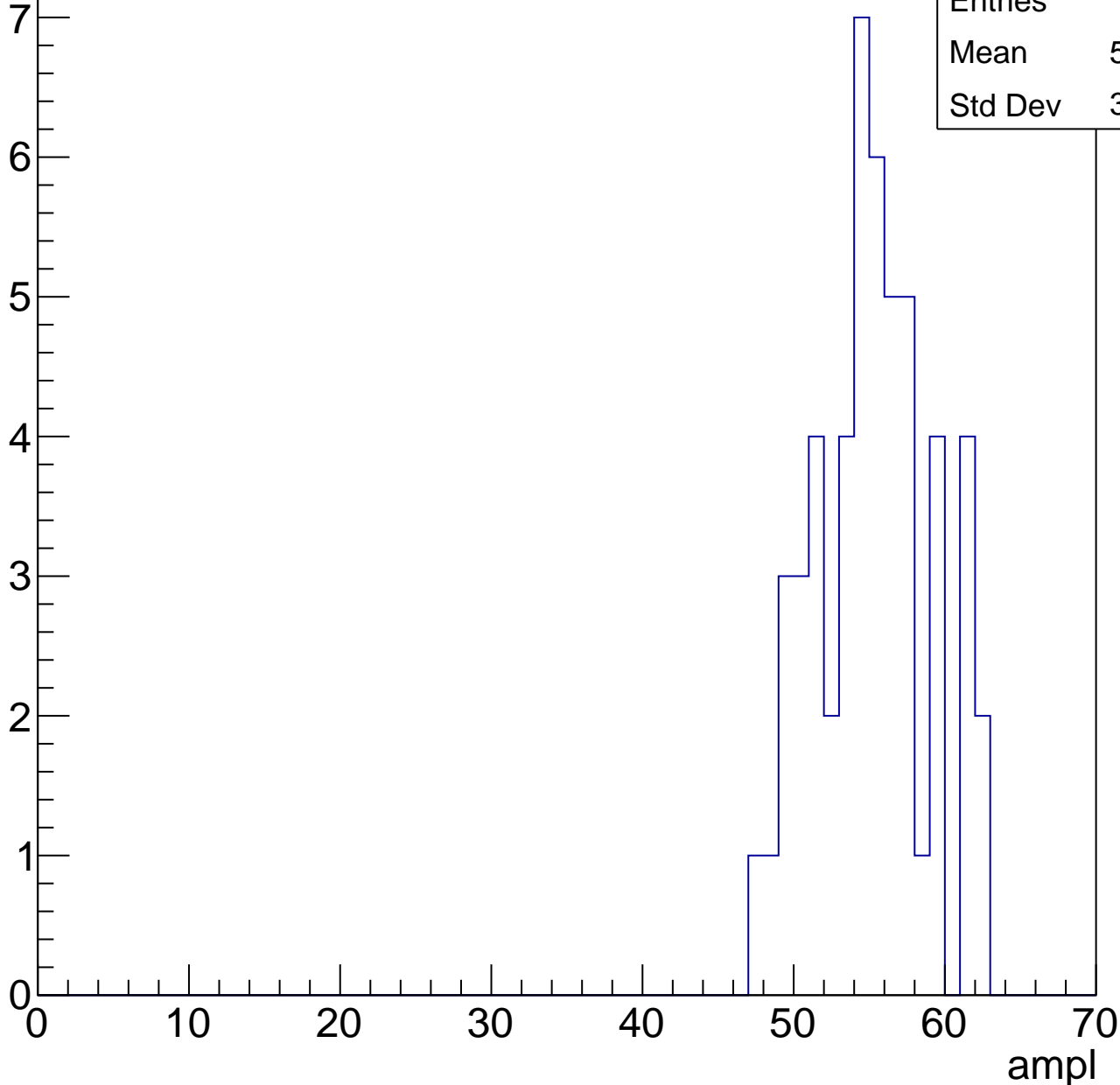


B1L103S, U10-ch28, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

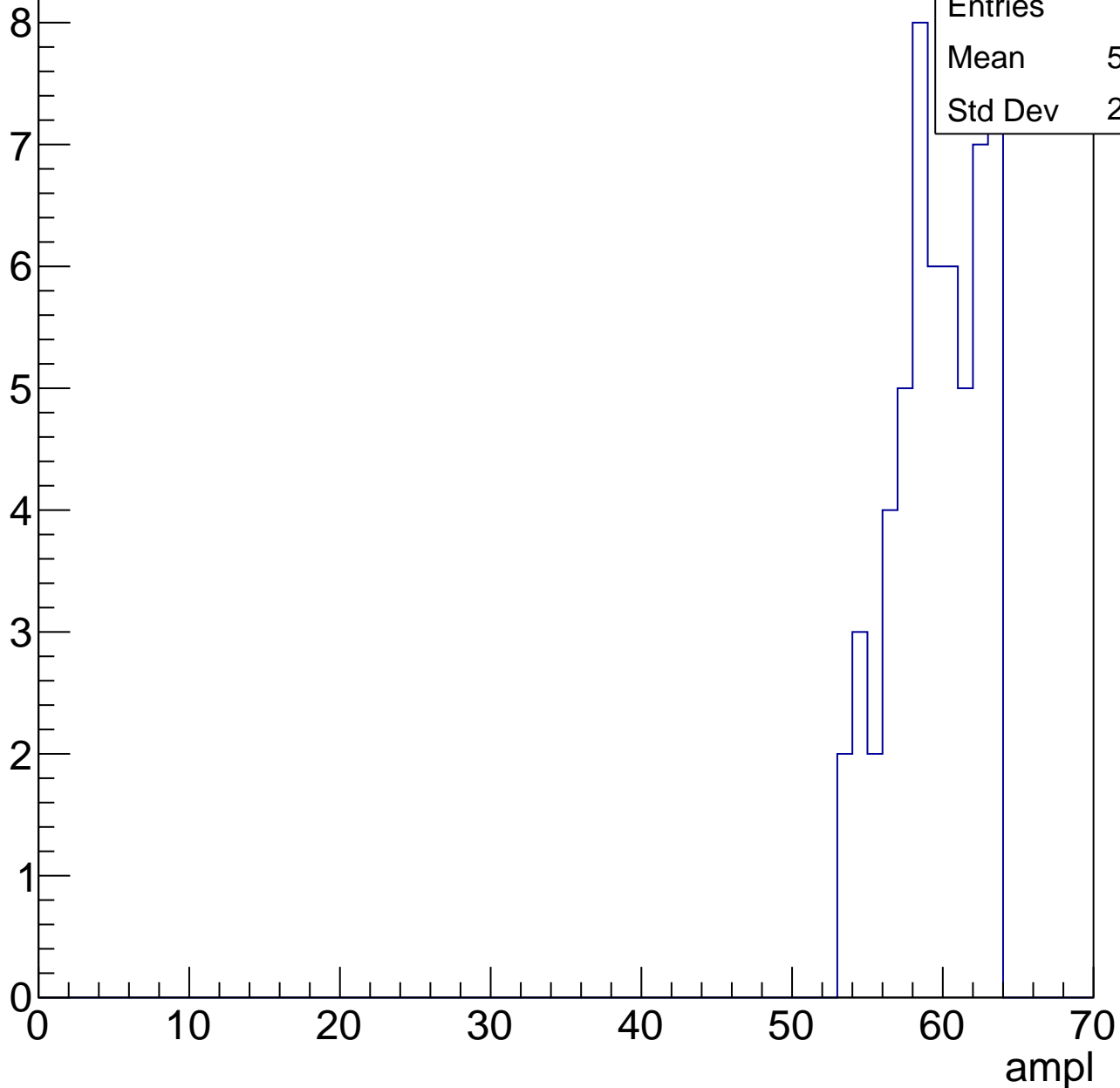
Entries	52
Mean	54.75
Std Dev	3.767



B1L103S, U10-ch28, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

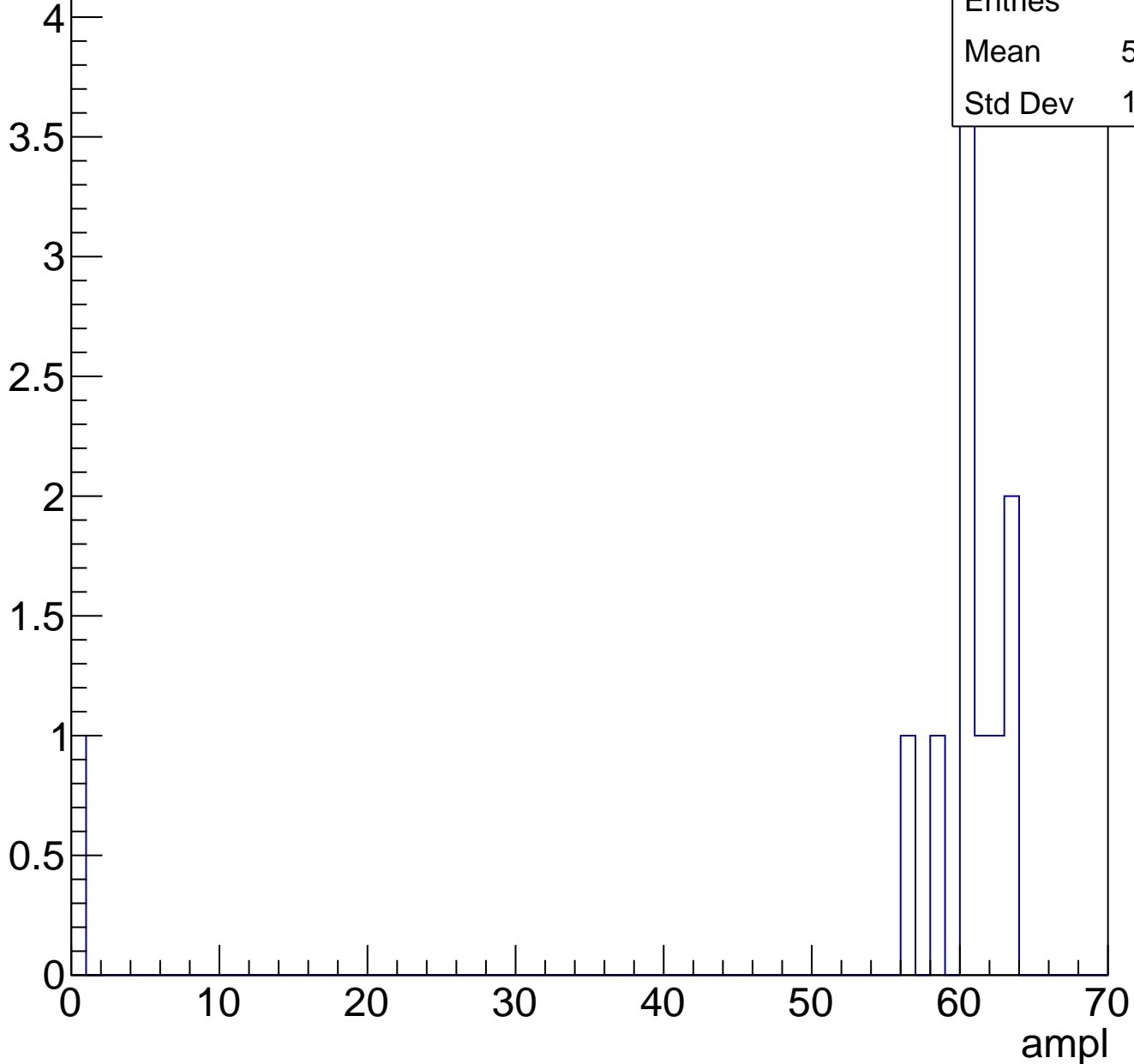


Entries	56
Mean	59.07
Std Dev	2.865

B1L103S, U10-ch28, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	11
Mean	54.82
Std Dev	17.45

B1L103S, U10-ch28, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch29, adc0

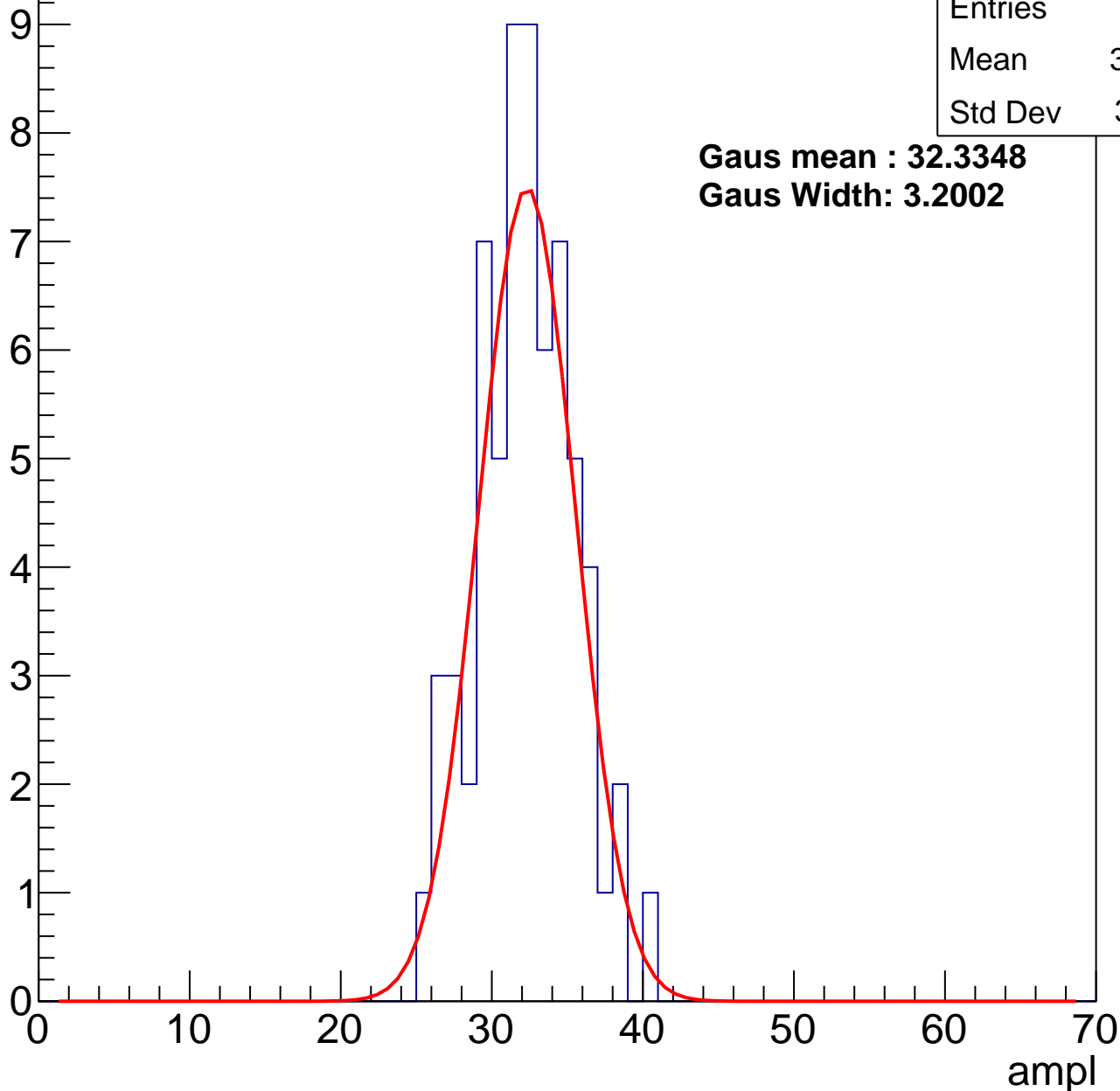
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	31.82
Std Dev	3.181

Gaus mean : 32.3348

Gaus Width: 3.2002



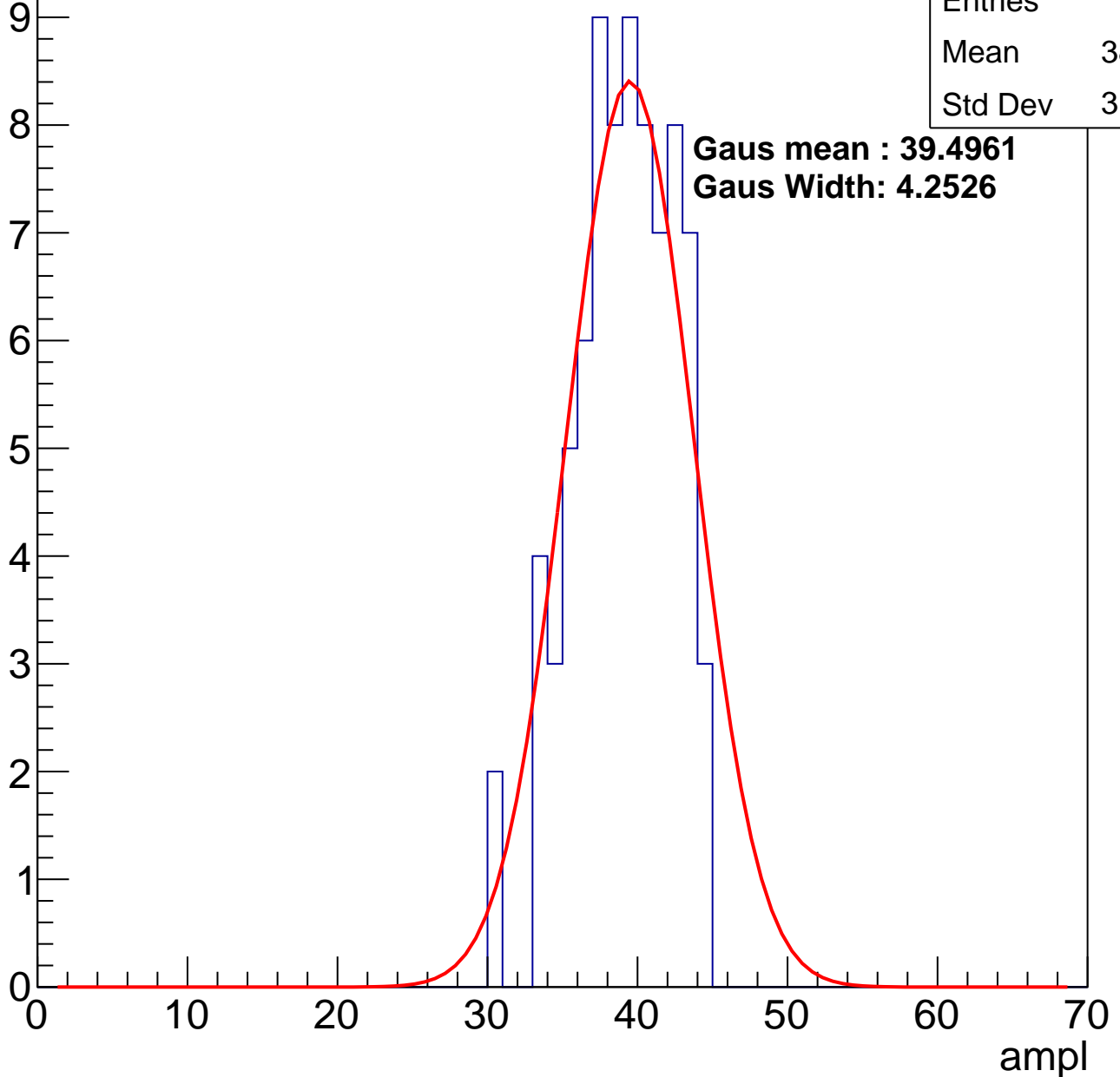
B1L103S, U10-ch29, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	38.59
Std Dev	3.263

Gaus mean : 39.4961
Gaus Width: 4.2526



B1L103S, U10-ch29, adc2

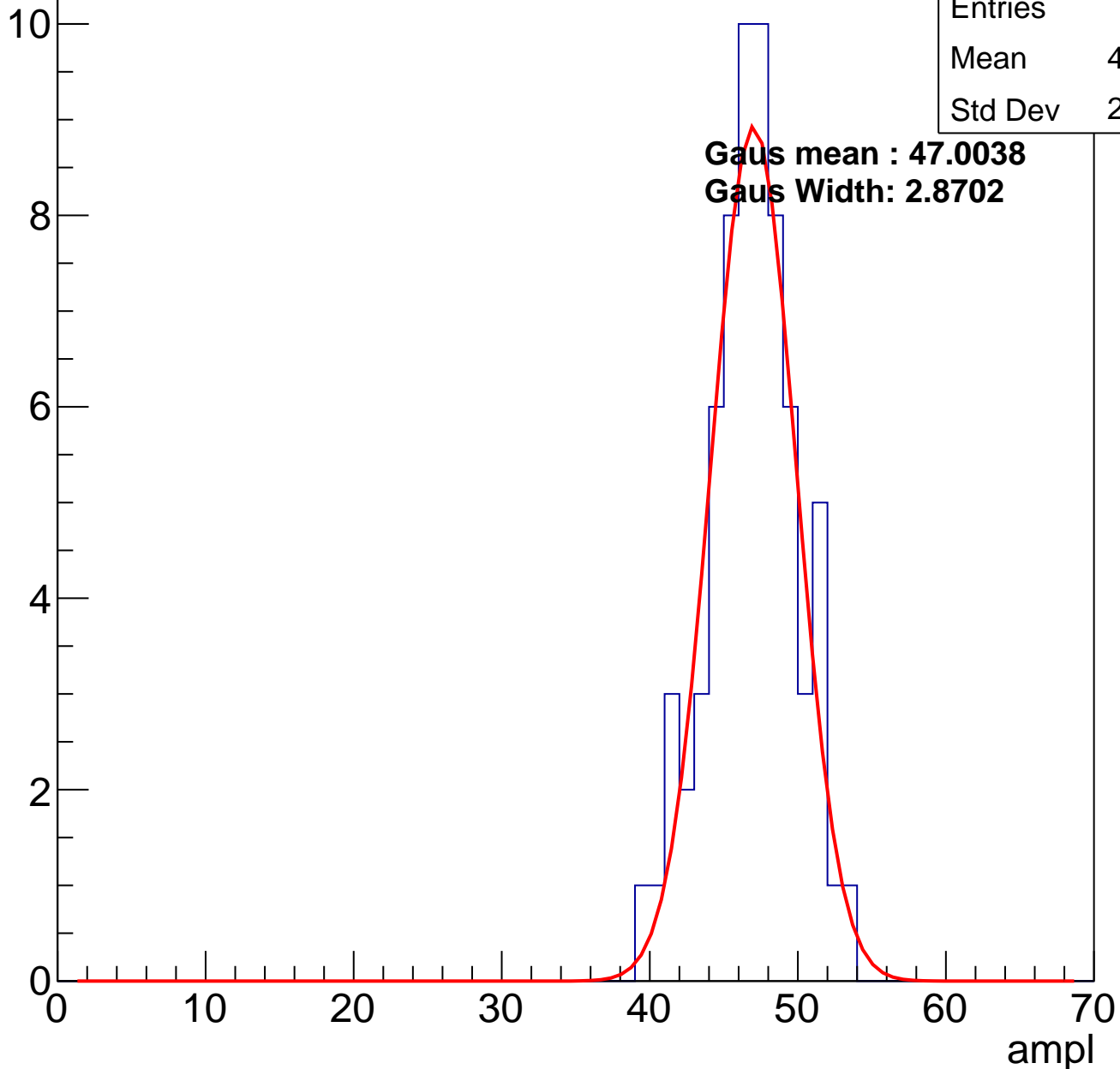
calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	46.43
Std Dev	2.952

Gaus mean : 47.0038

Gaus Width: 2.8702

Entry

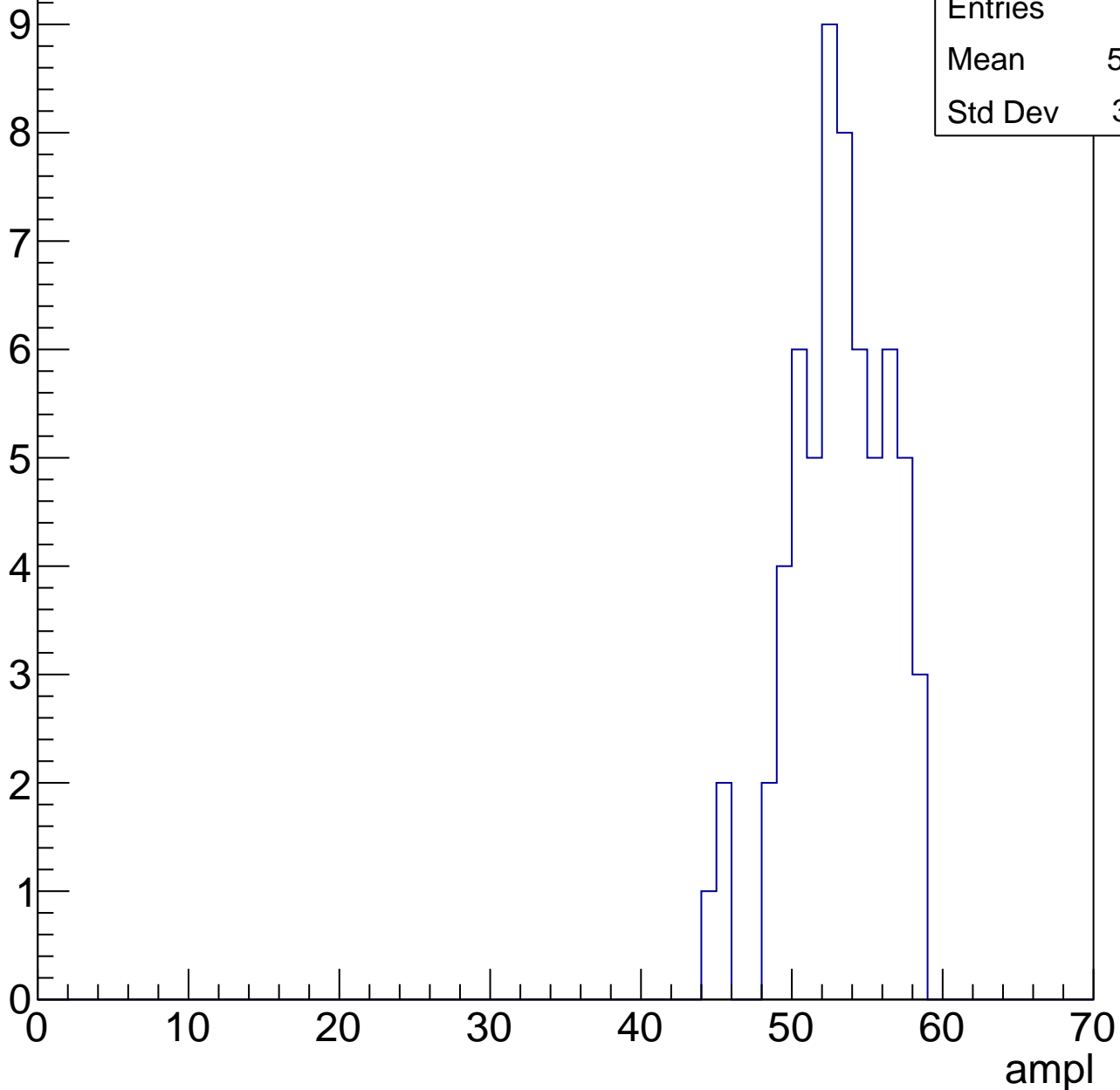


B1L103S, U10-ch29, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	52.69
Std Dev	3.201

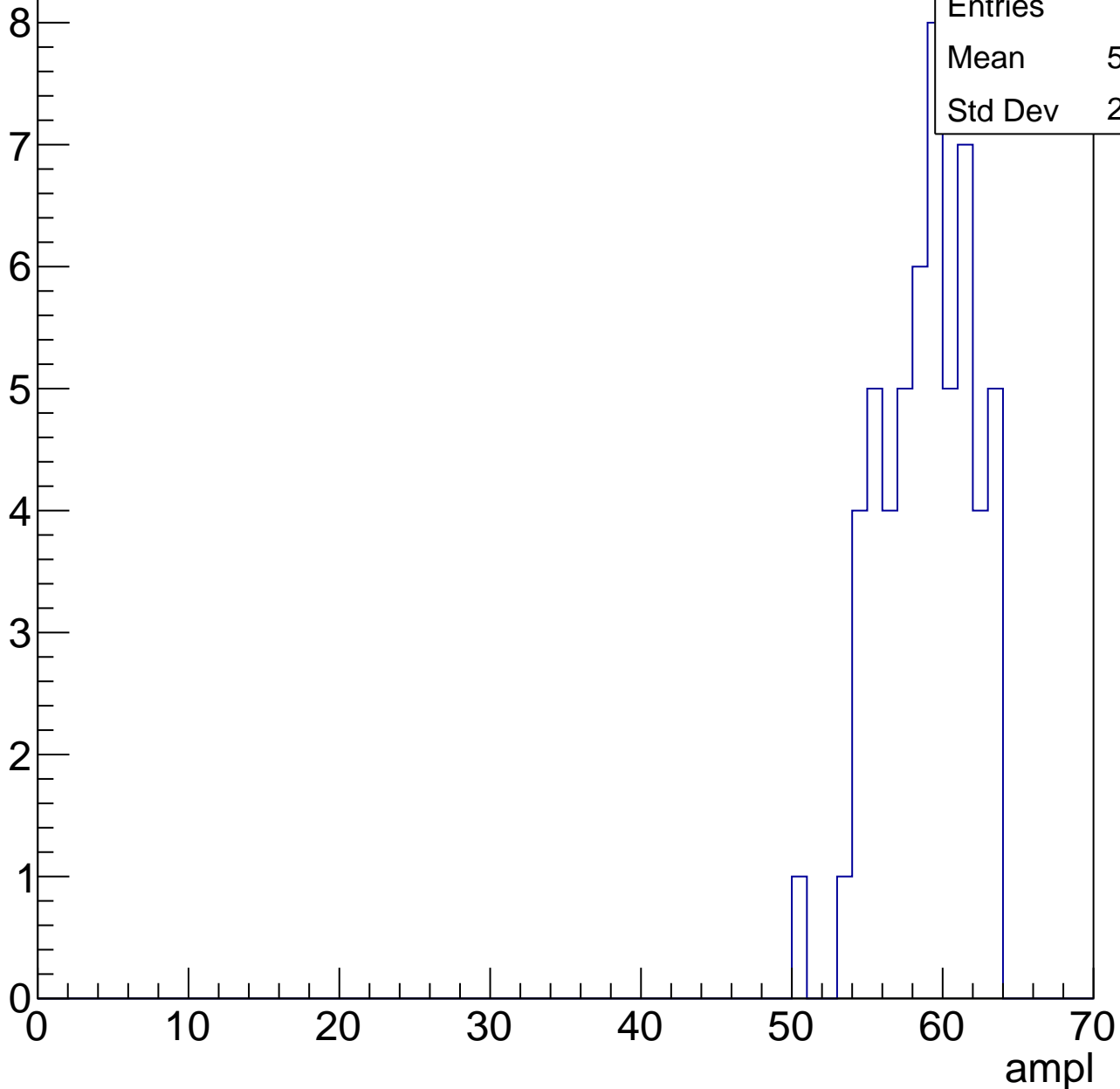


B1L103S, U10-ch29, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	58.42
Std Dev	2.983

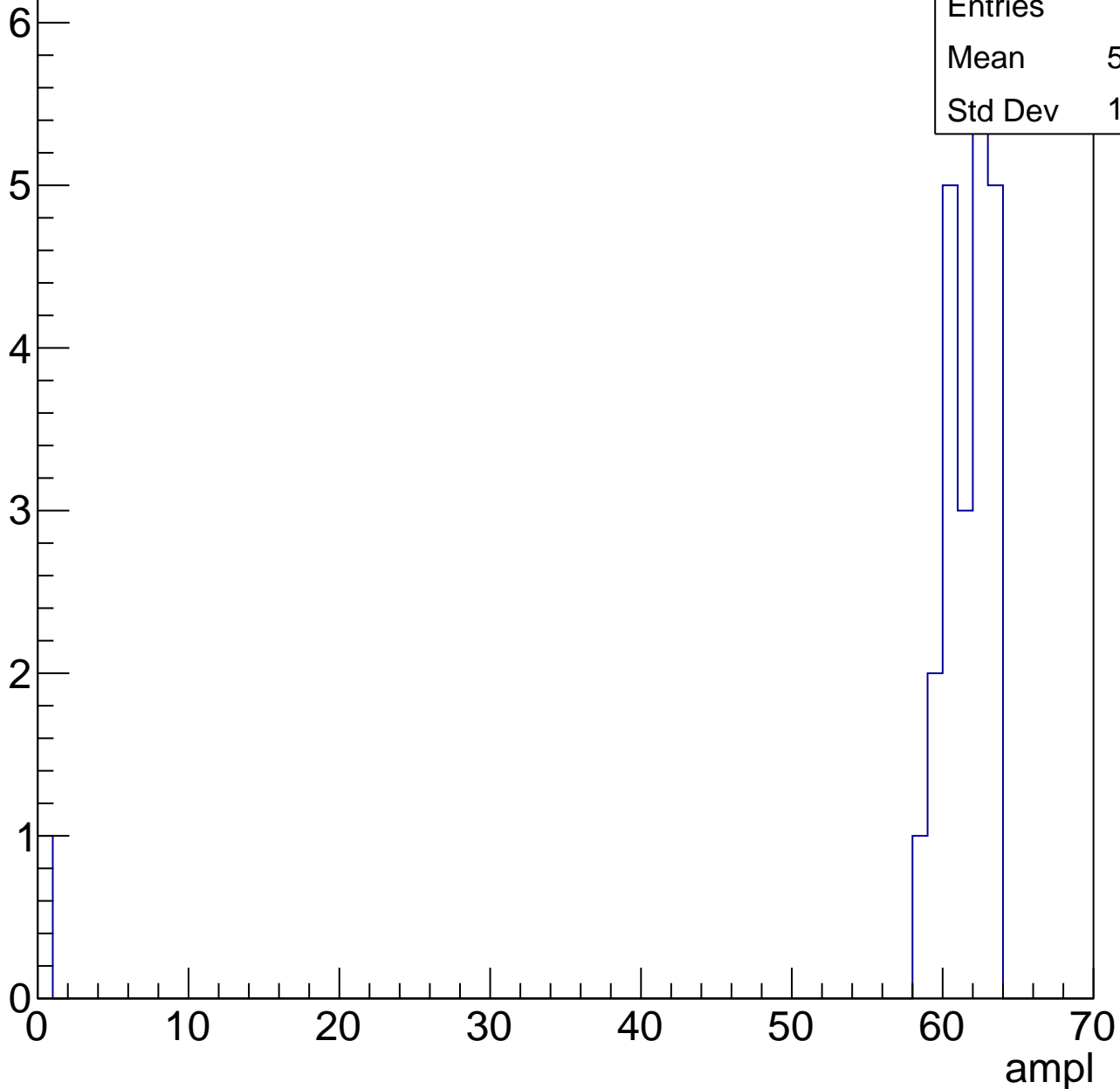


B1L103S, U10-ch29, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	23
Mean	58.52
Std Dev	12.56



B1L103S, U10-ch29, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch29, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch30, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	30.04
Std Dev	4.979

Gaus mean : 30.9458

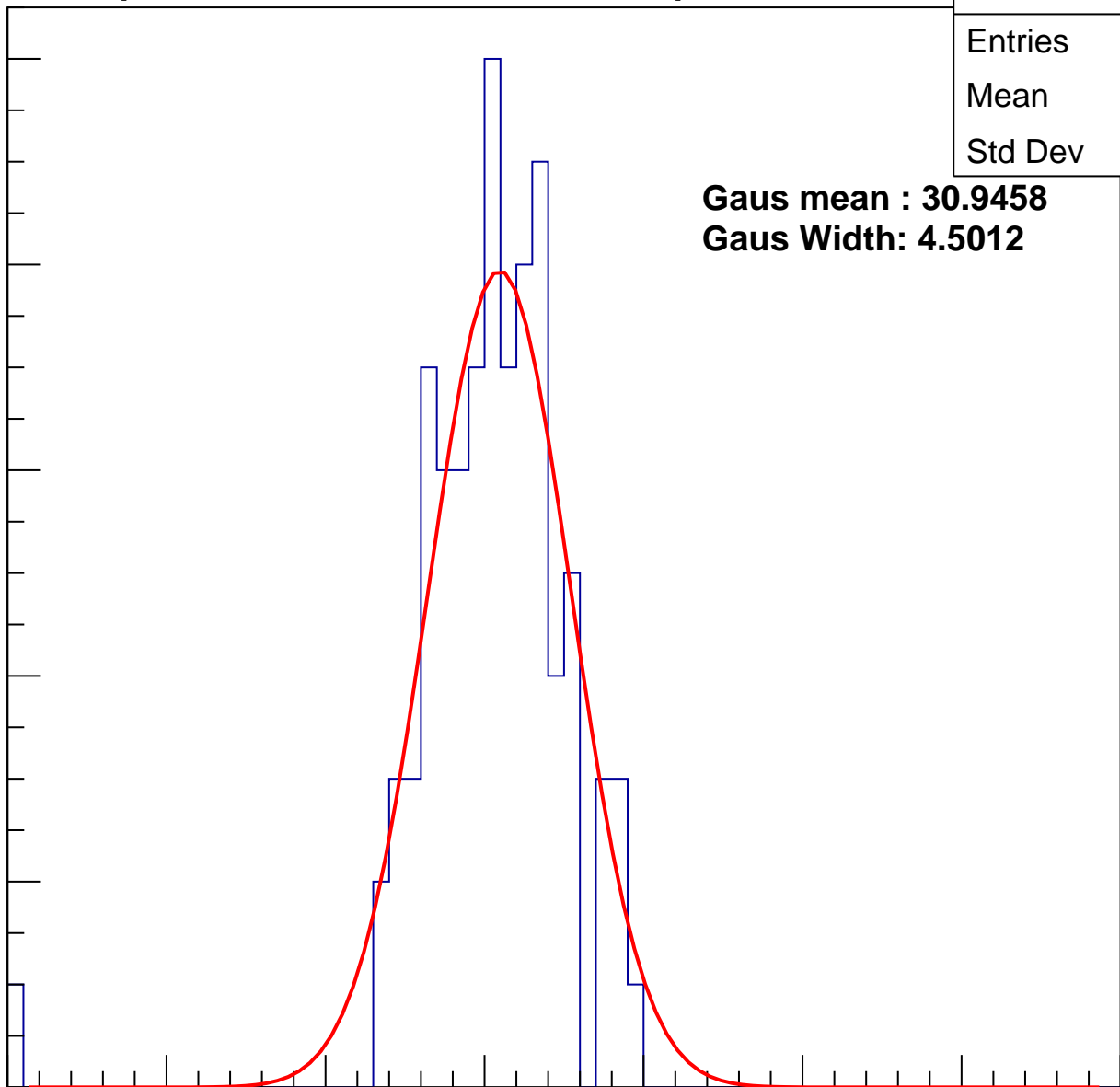
Gaus Width: 4.5012

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch30, adc1

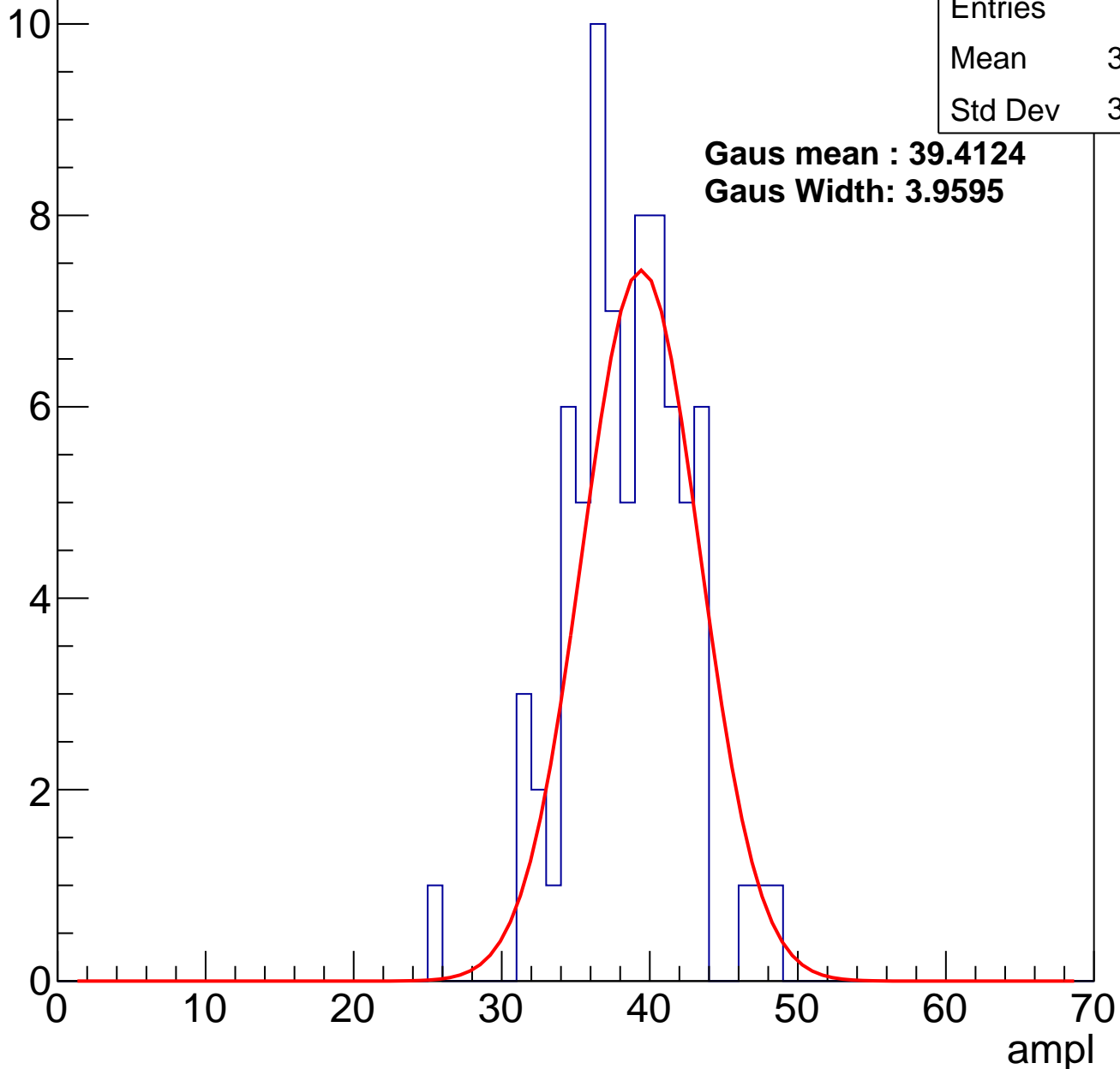
calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	38.03
Std Dev	3.924

Gaus mean : 39.4124

Gaus Width: 3.9595

Entry



B1L103S, U10-ch30, adc2

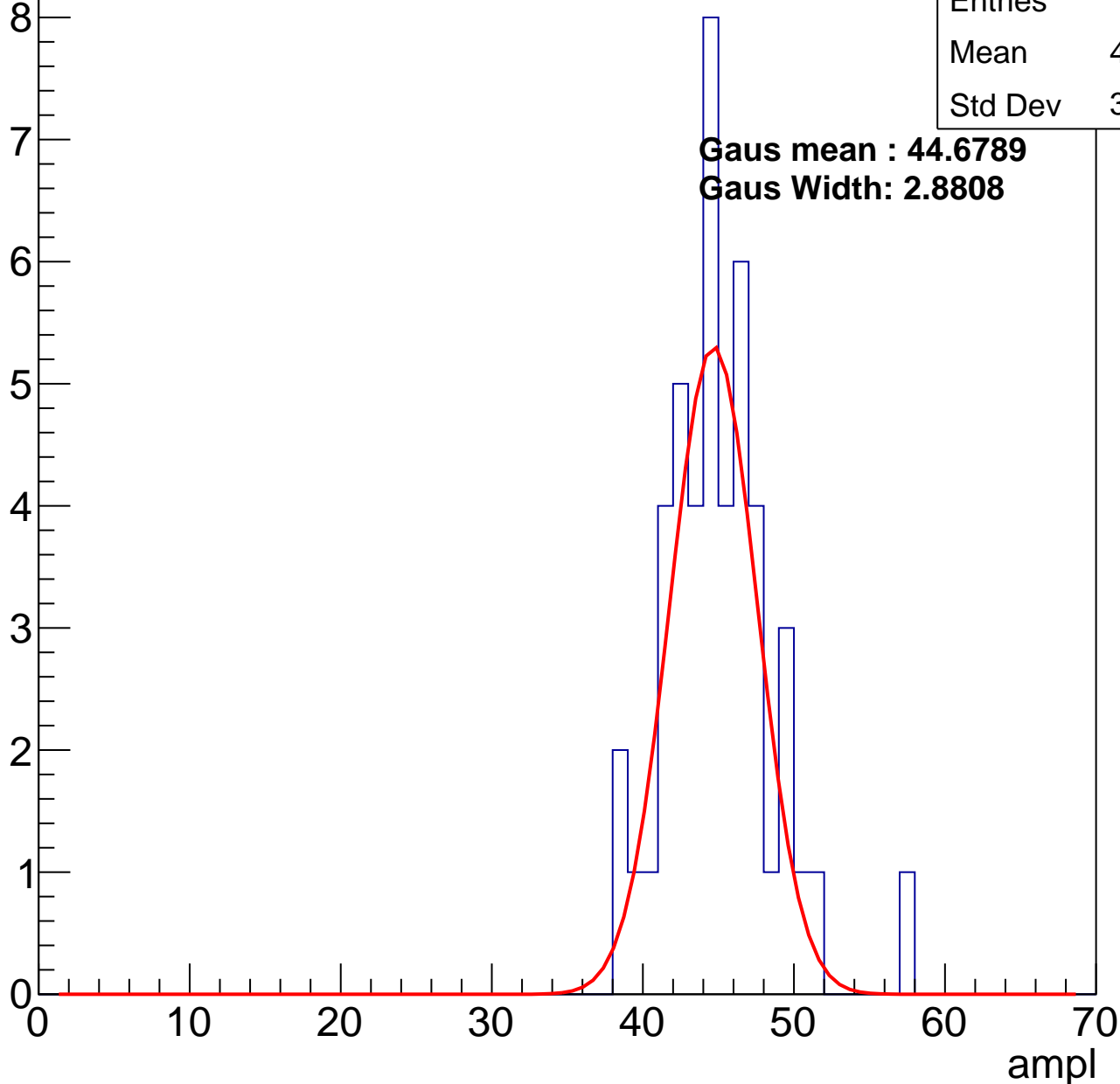
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	44.57
Std Dev	3.512

Gaus mean : 44.6789

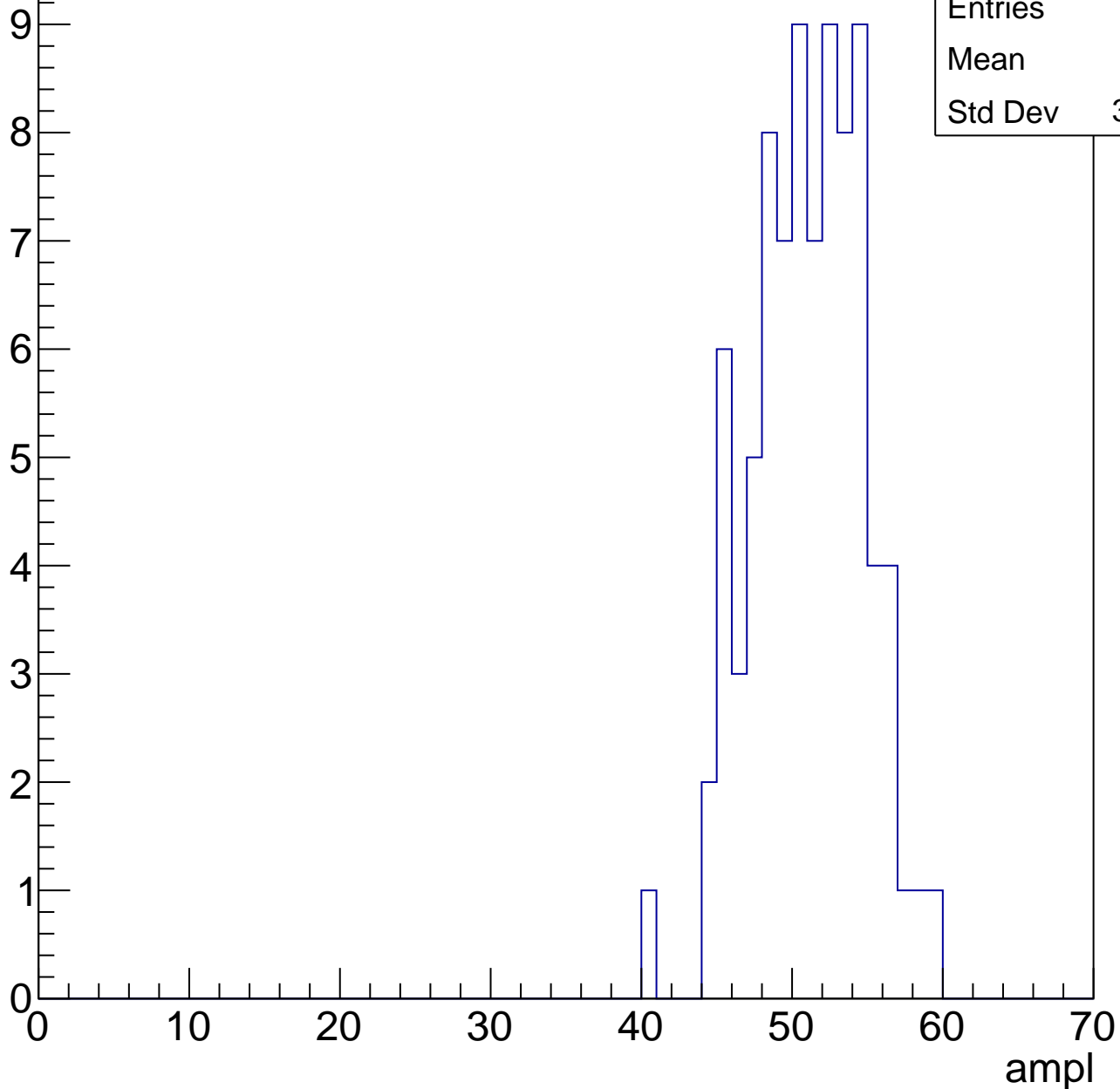
Gaus Width: 2.8808



B1L103S, U10-ch30, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

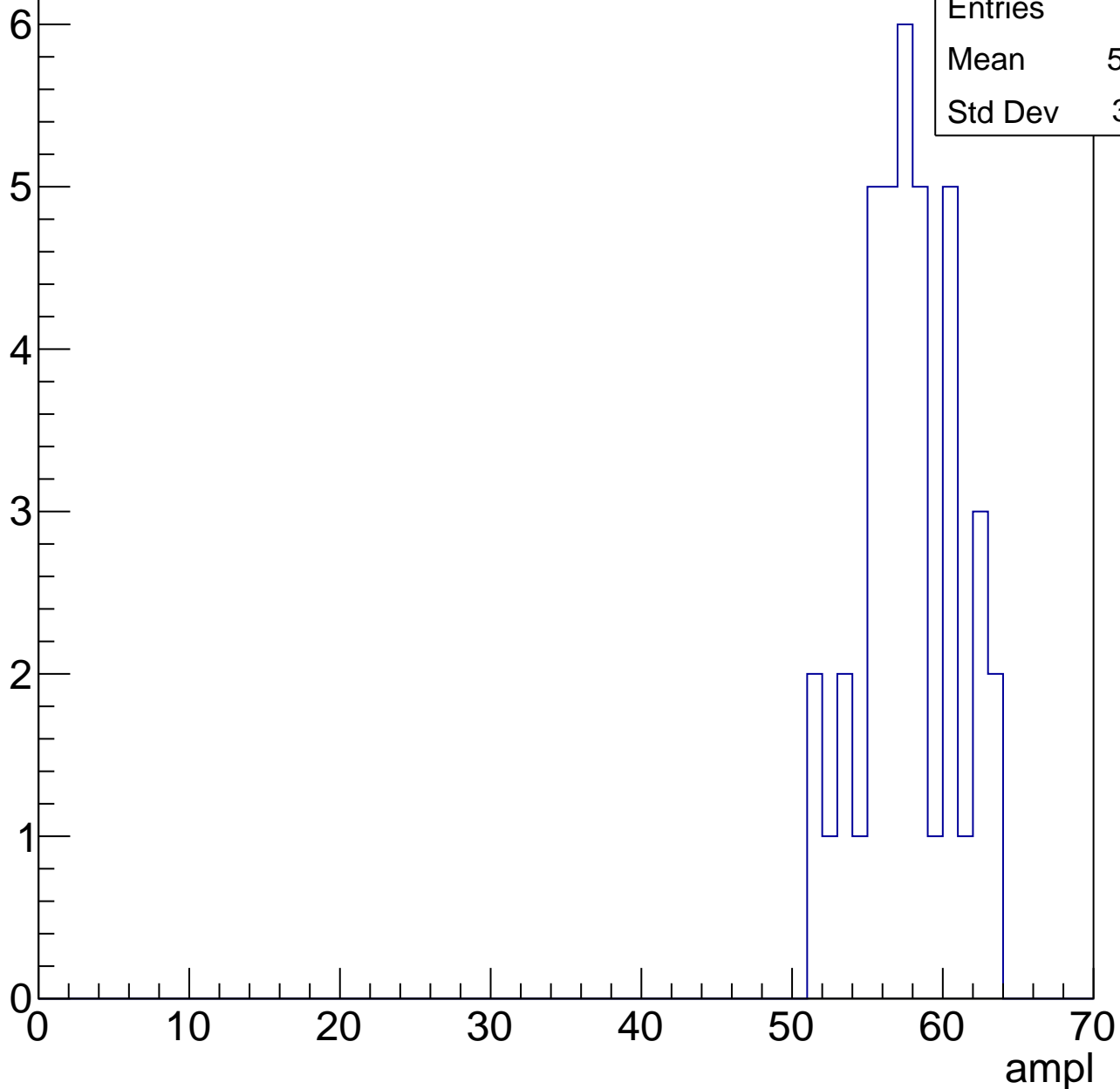


B1L103S, U10-ch30, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

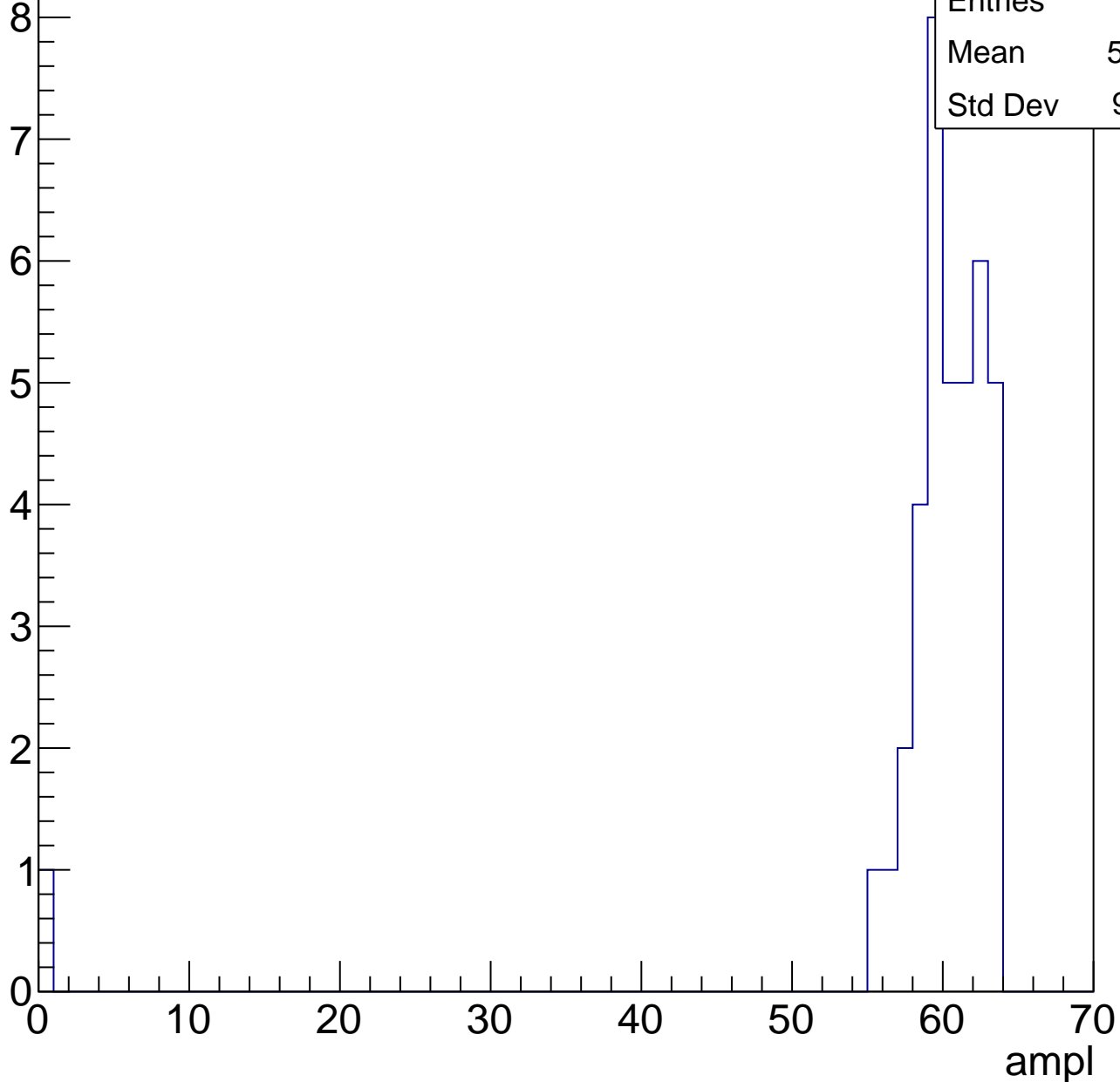
Entries	39
Mean	57.26
Std Dev	3.111



B1L103S, U10-ch30, adc5

calib_packv5_042523_0143.root, FC#7, port C2

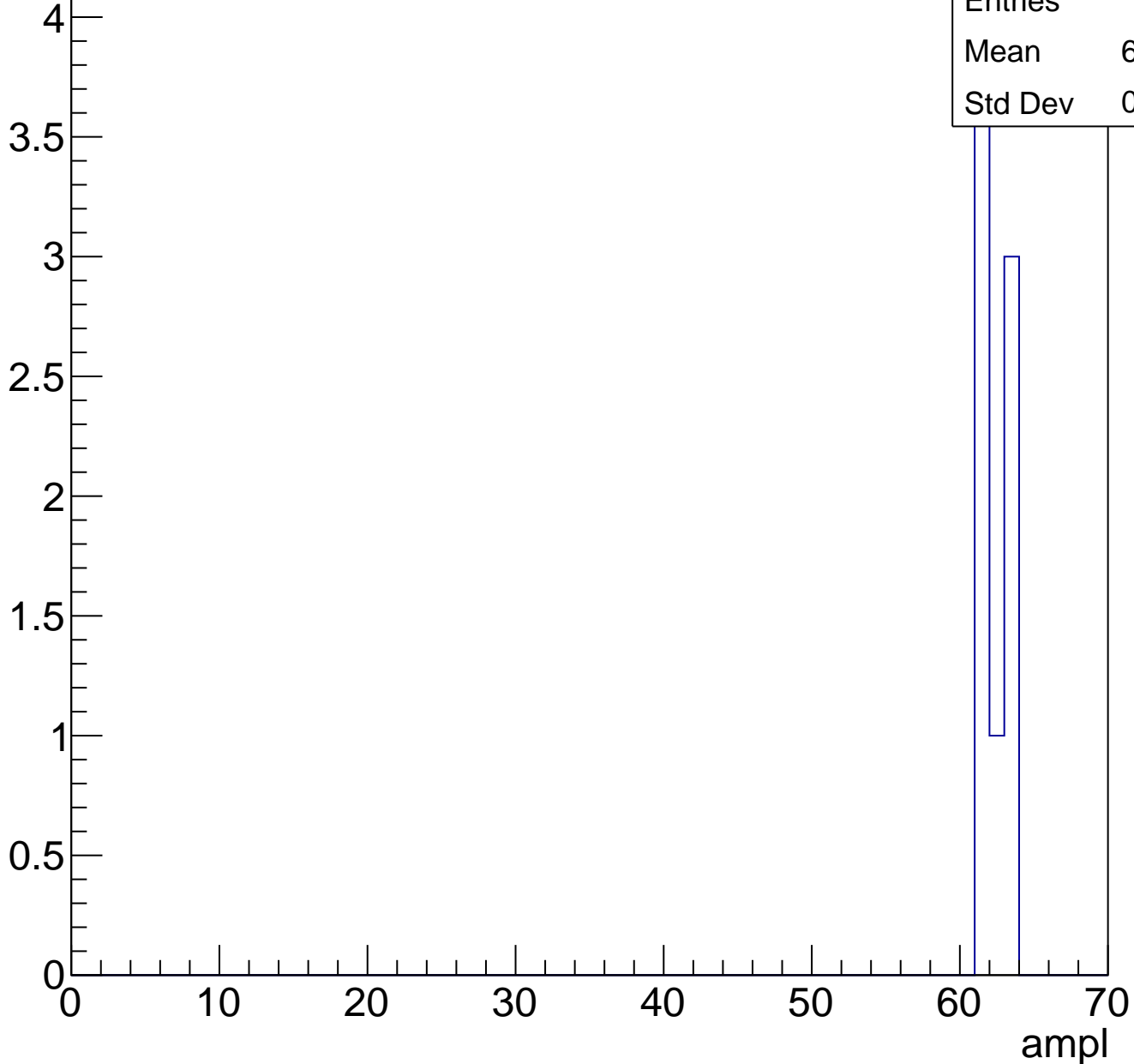
Entry



B1L103S, U10-ch30, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch30, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch31, adc0

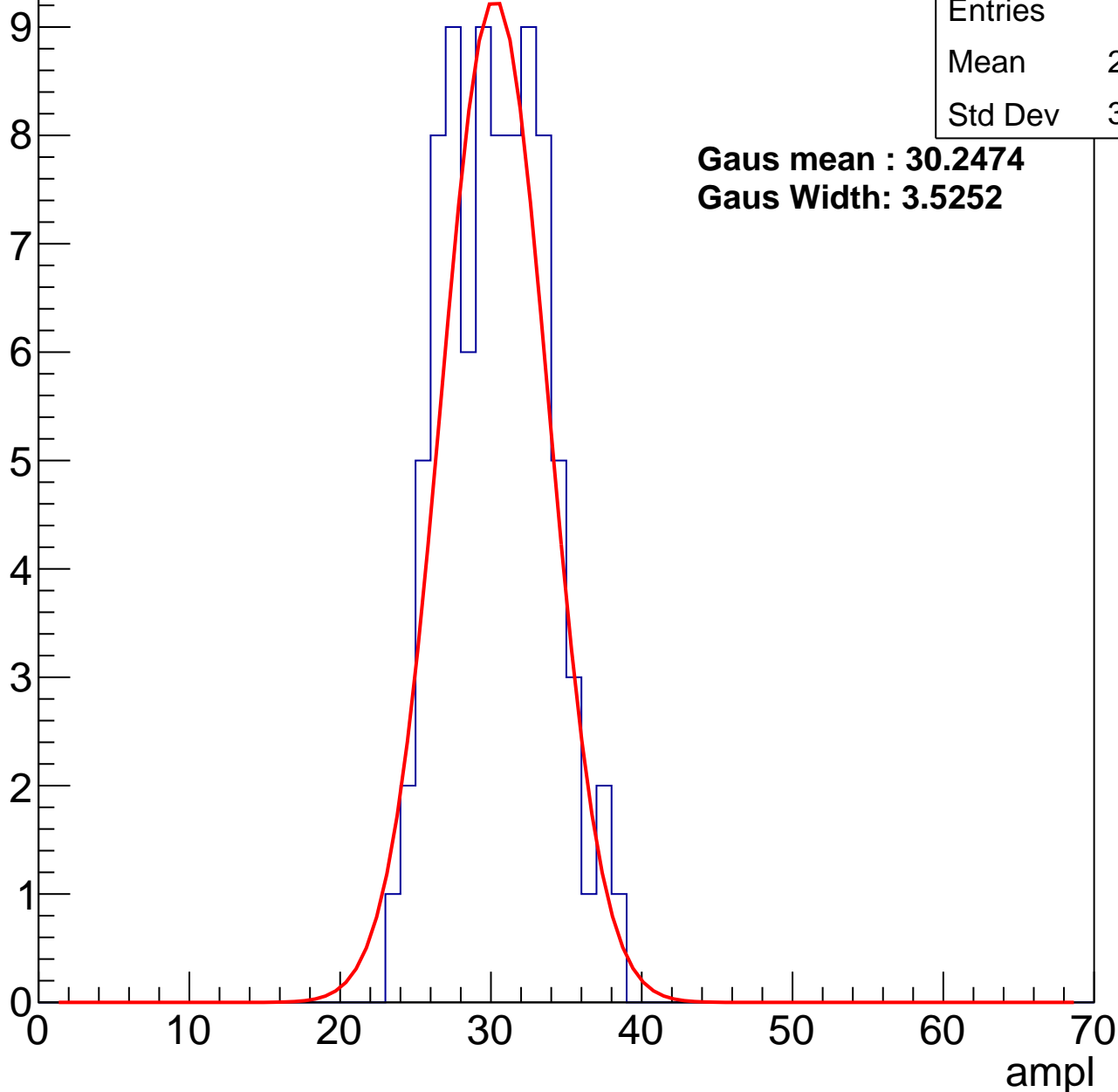
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	85
Mean	29.87
Std Dev	3.353

Gaus mean : 30.2474

Gaus Width: 3.5252



B1L103S, U10-ch31, adc1

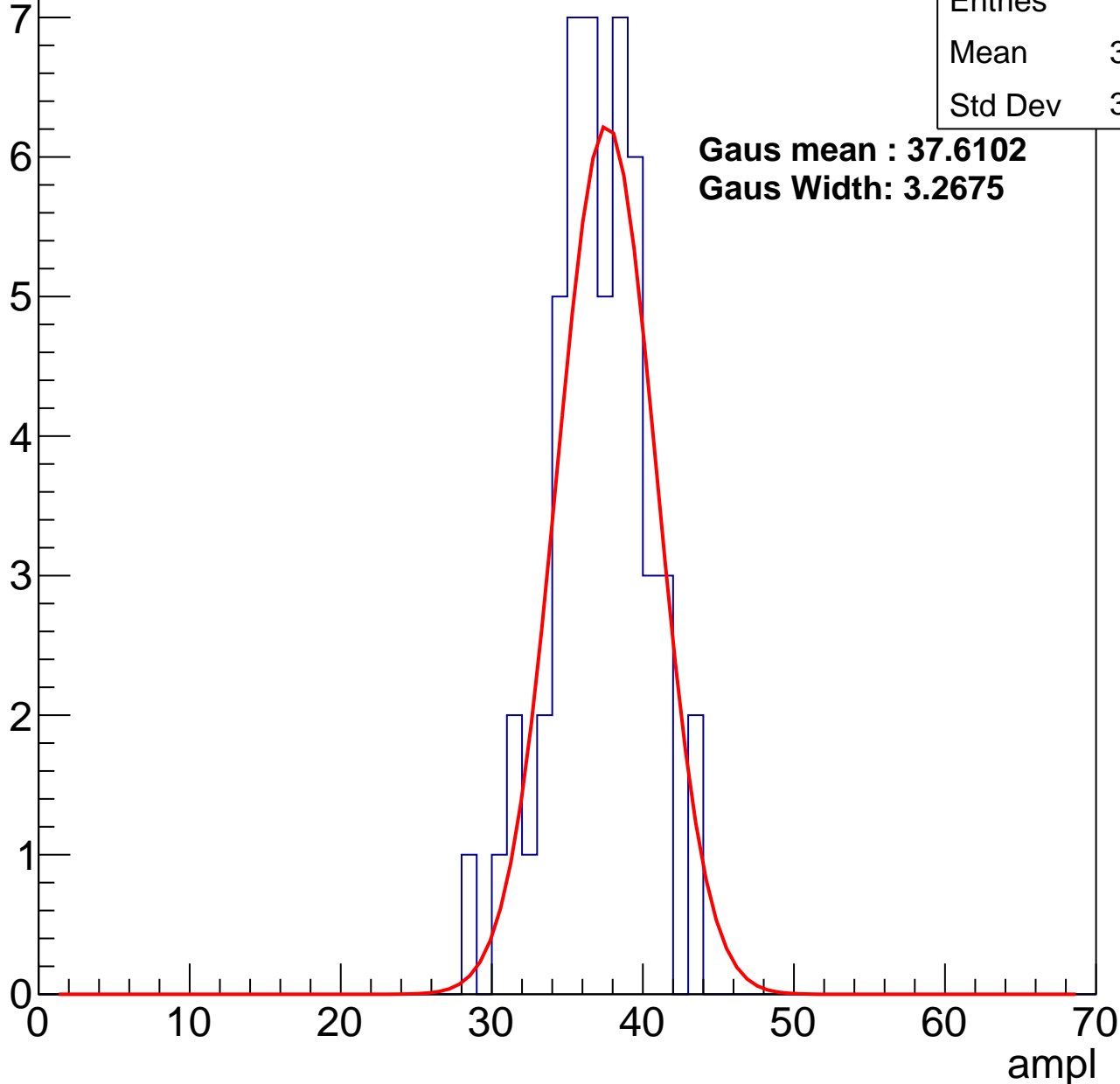
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	36.52
Std Dev	3.116

Gaus mean : 37.6102

Gaus Width: 3.2675



B1L103S, U10-ch31, adc2

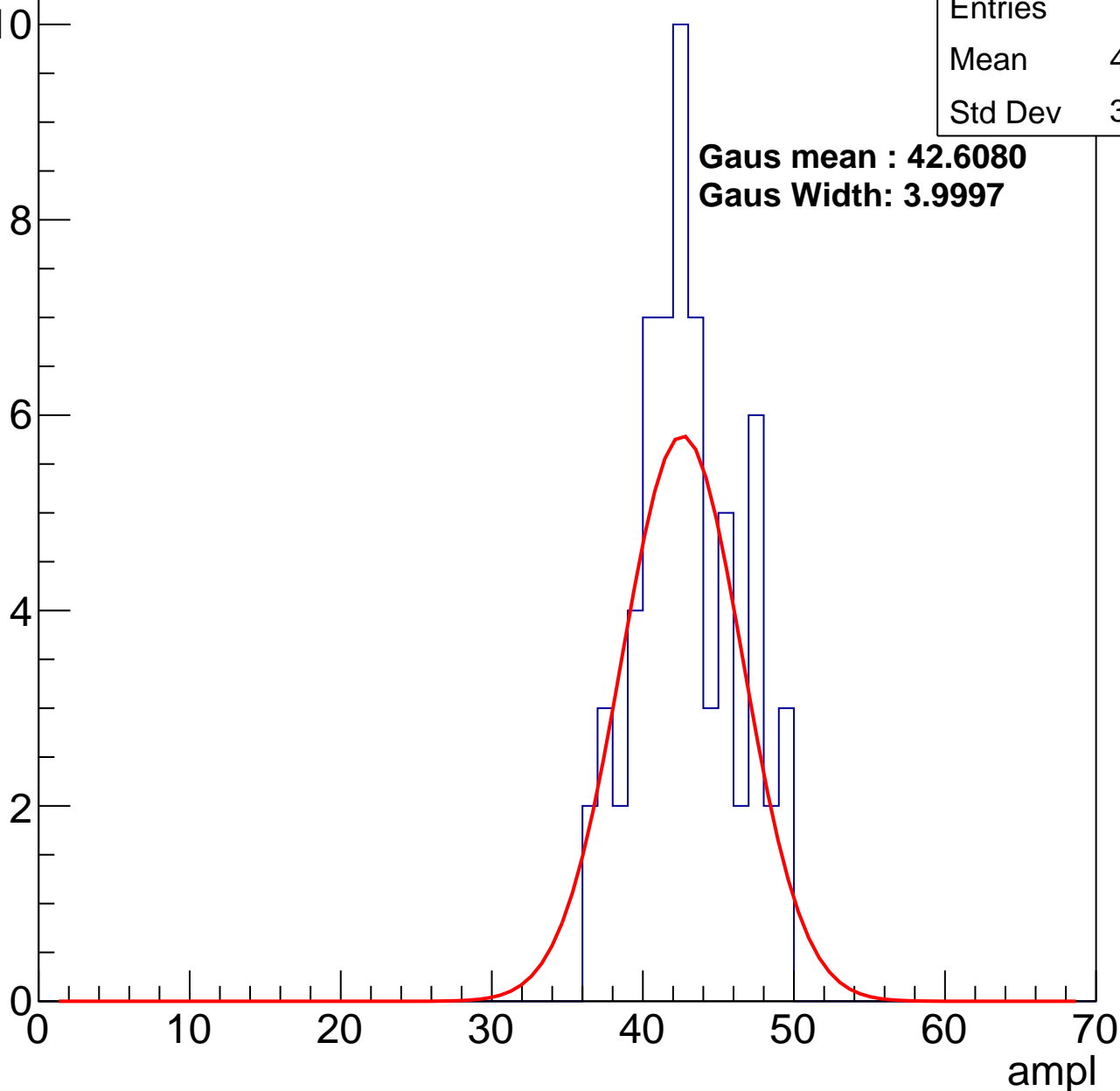
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	42.49
Std Dev	3.333

Gaus mean : 42.6080

Gaus Width: 3.9997



B1L103S, U10-ch31, adc3

calib_packv5_042523_0143.root, FC#7, port C2

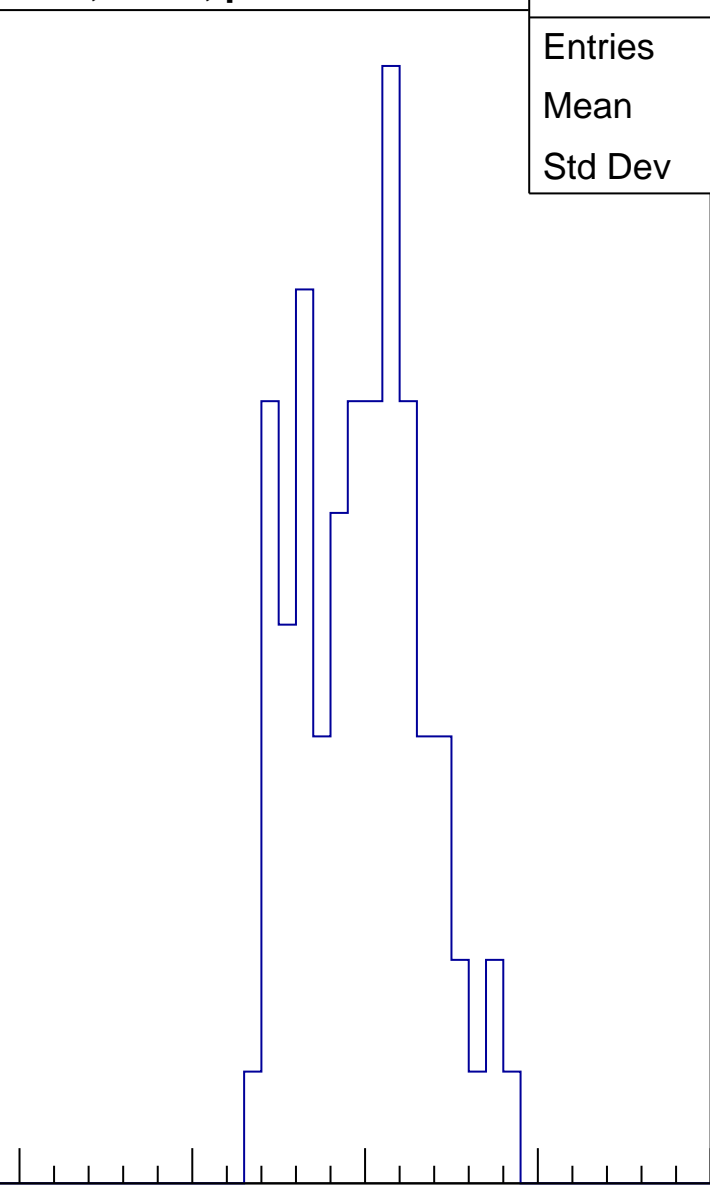
Entries	76
Mean	49.38
Std Dev	3.587

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

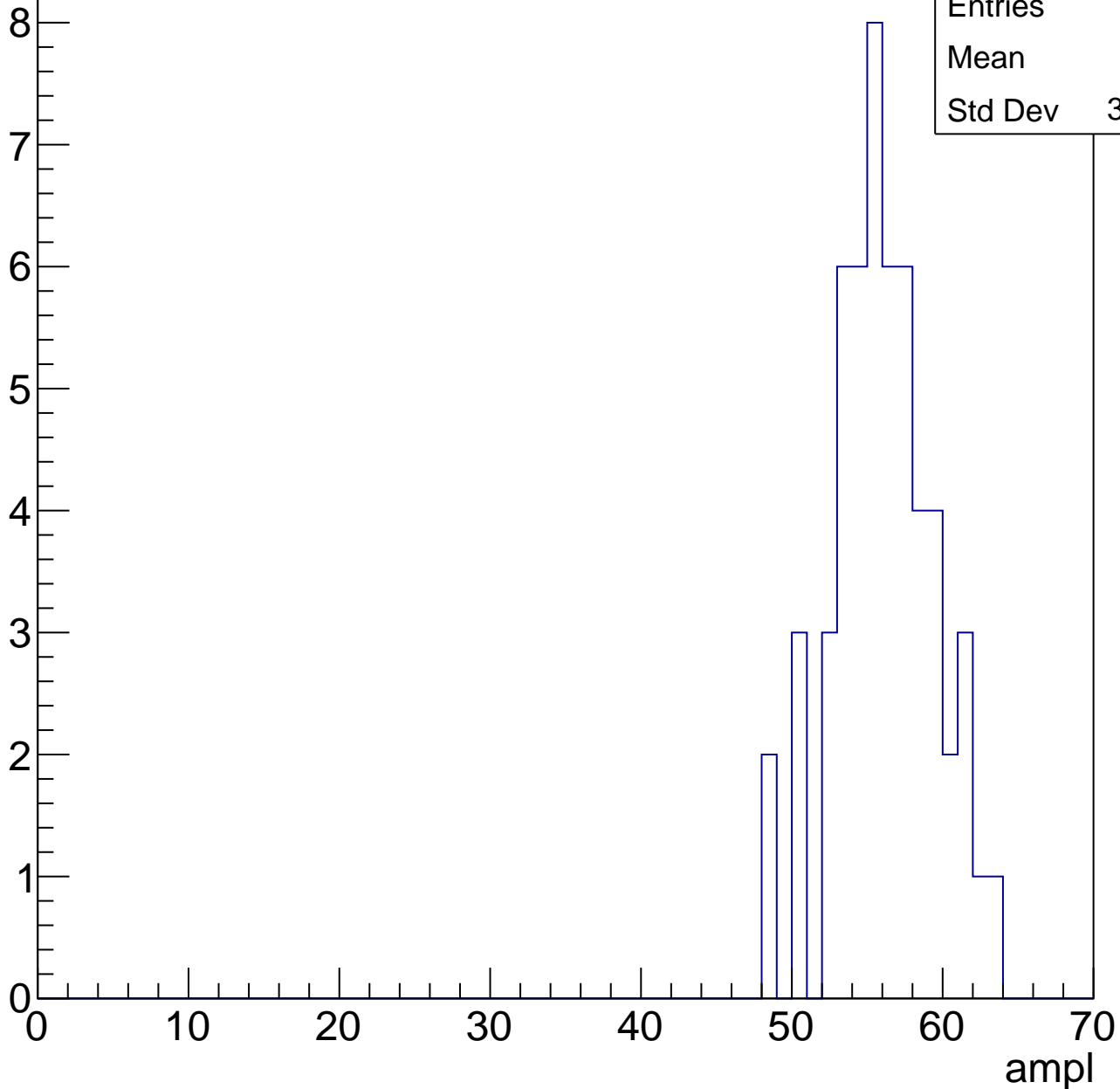


B1L103S, U10-ch31, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	55.6
Std Dev	3.334

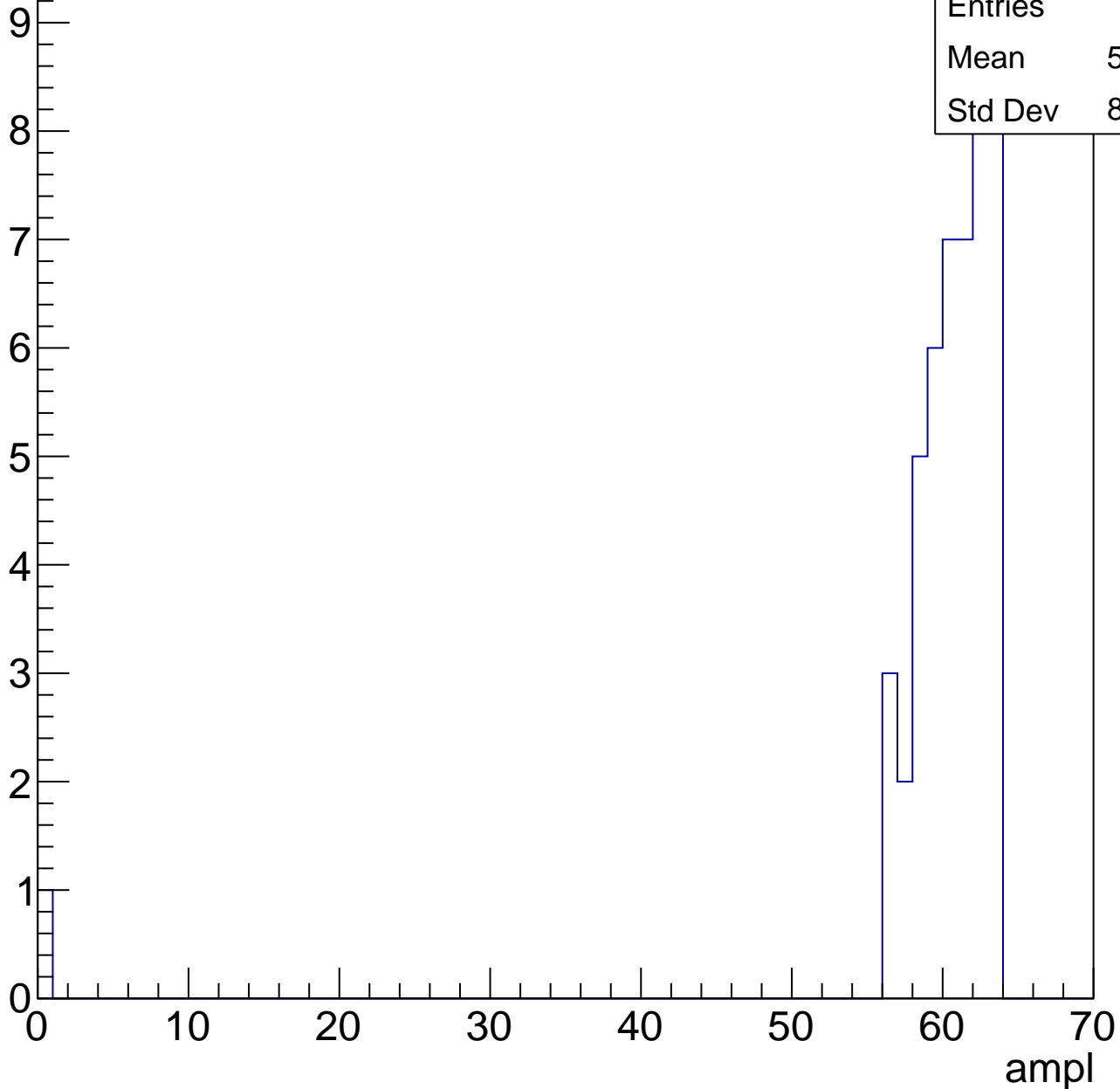


B1L103S, U10-ch31, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	59.08
Std Dev	8.864



B1L103S, U10-ch31, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch31, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch32, adc0

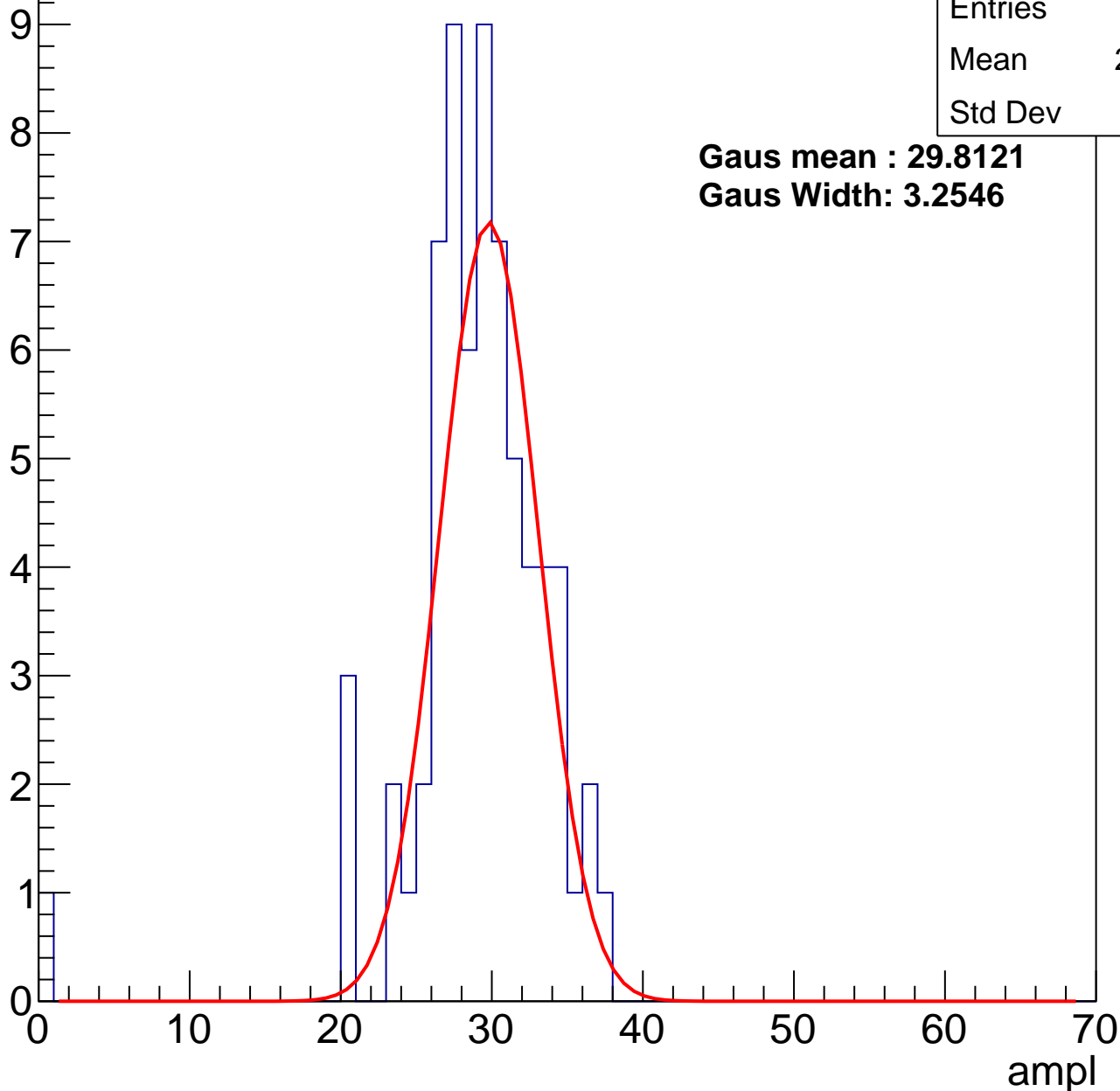
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.51
Std Dev	5.04

Gaus mean : 29.8121

Gaus Width: 3.2546



B1L103S, U10-ch32, adc1

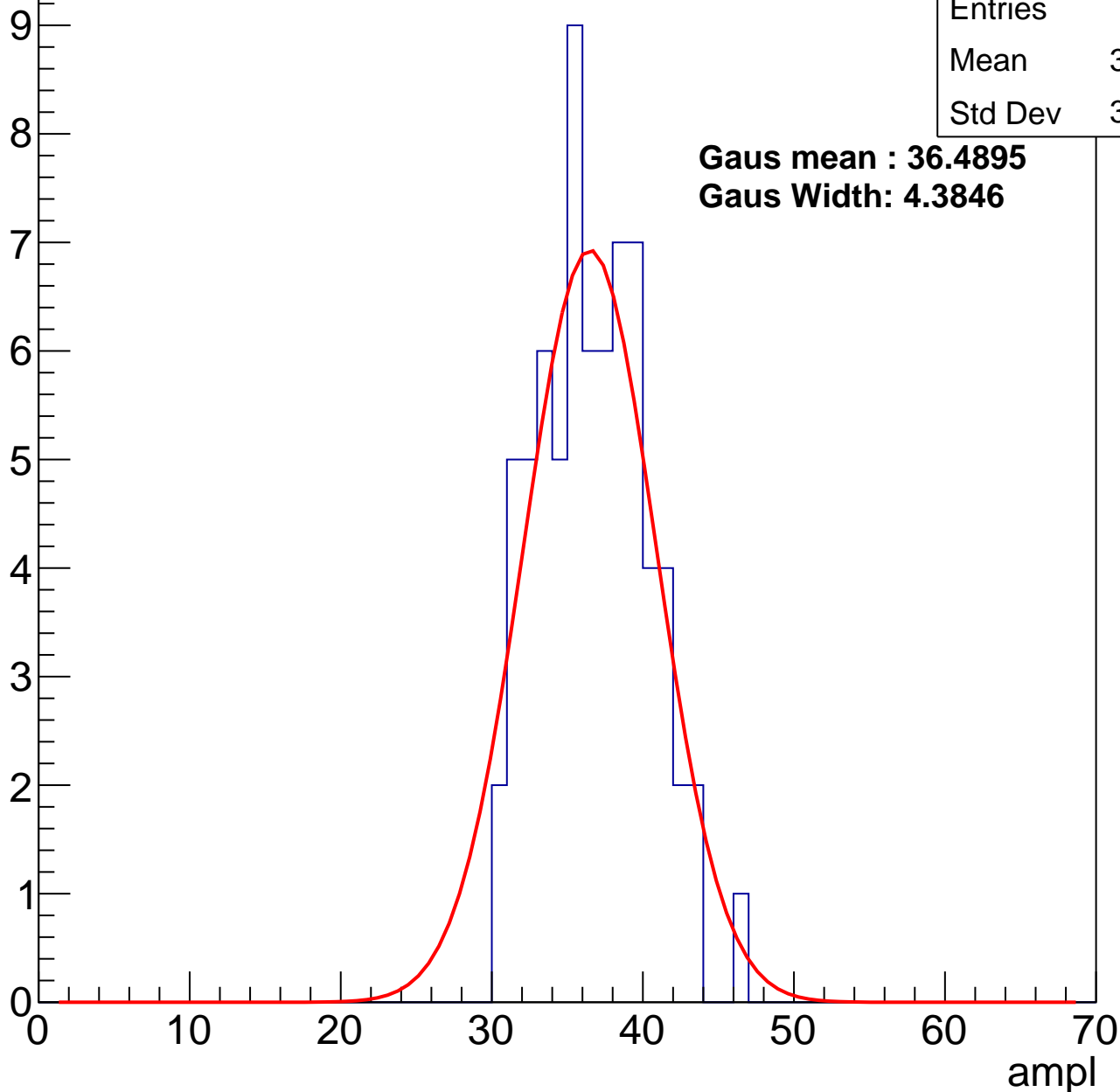
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.27
Std Dev	3.532

Gaus mean : 36.4895

Gaus Width: 4.3846



B1L103S, U10-ch32, adc2

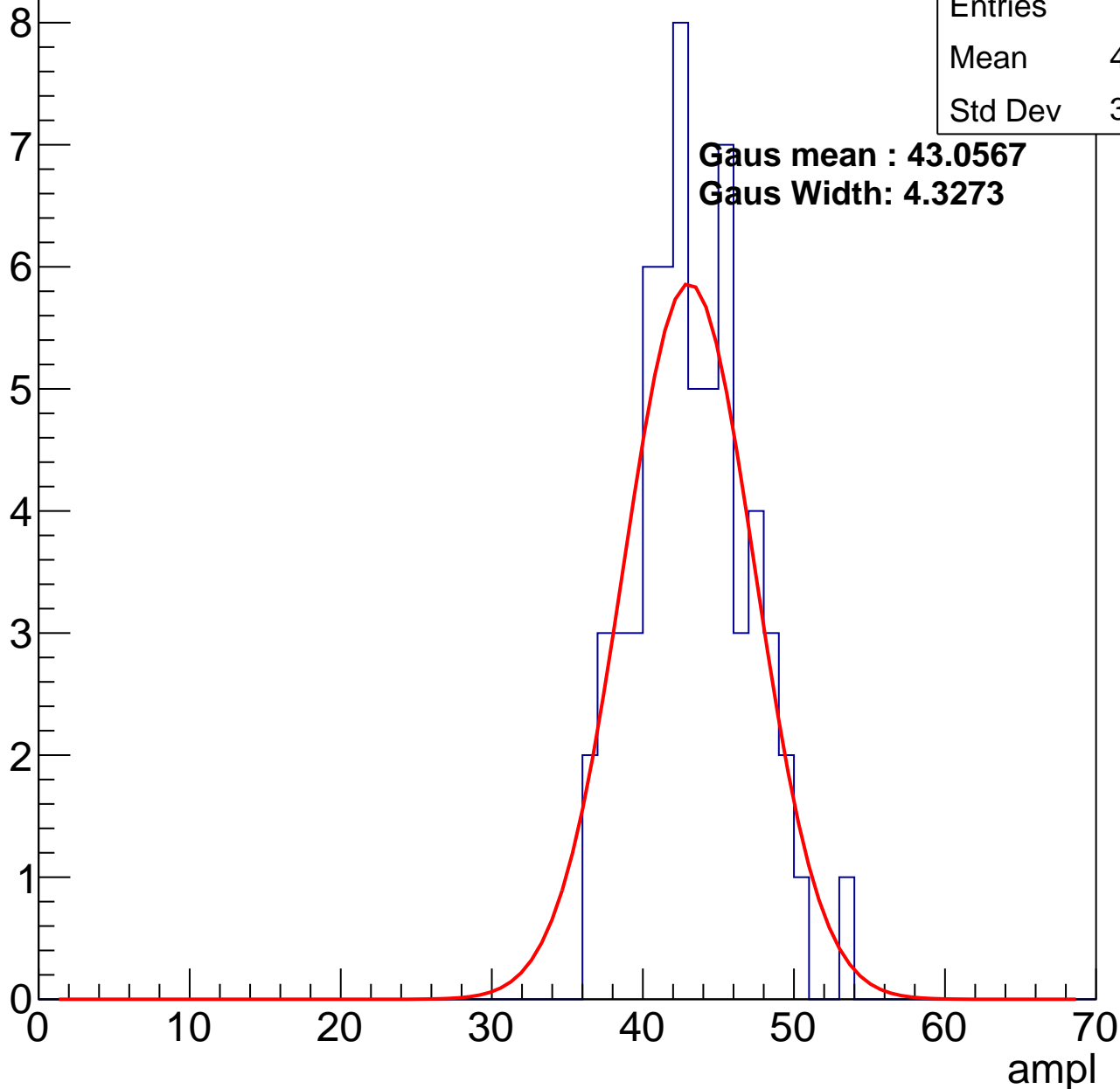
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	42.85
Std Dev	3.667

Gaus mean : 43.0567

Gaus Width: 4.3273

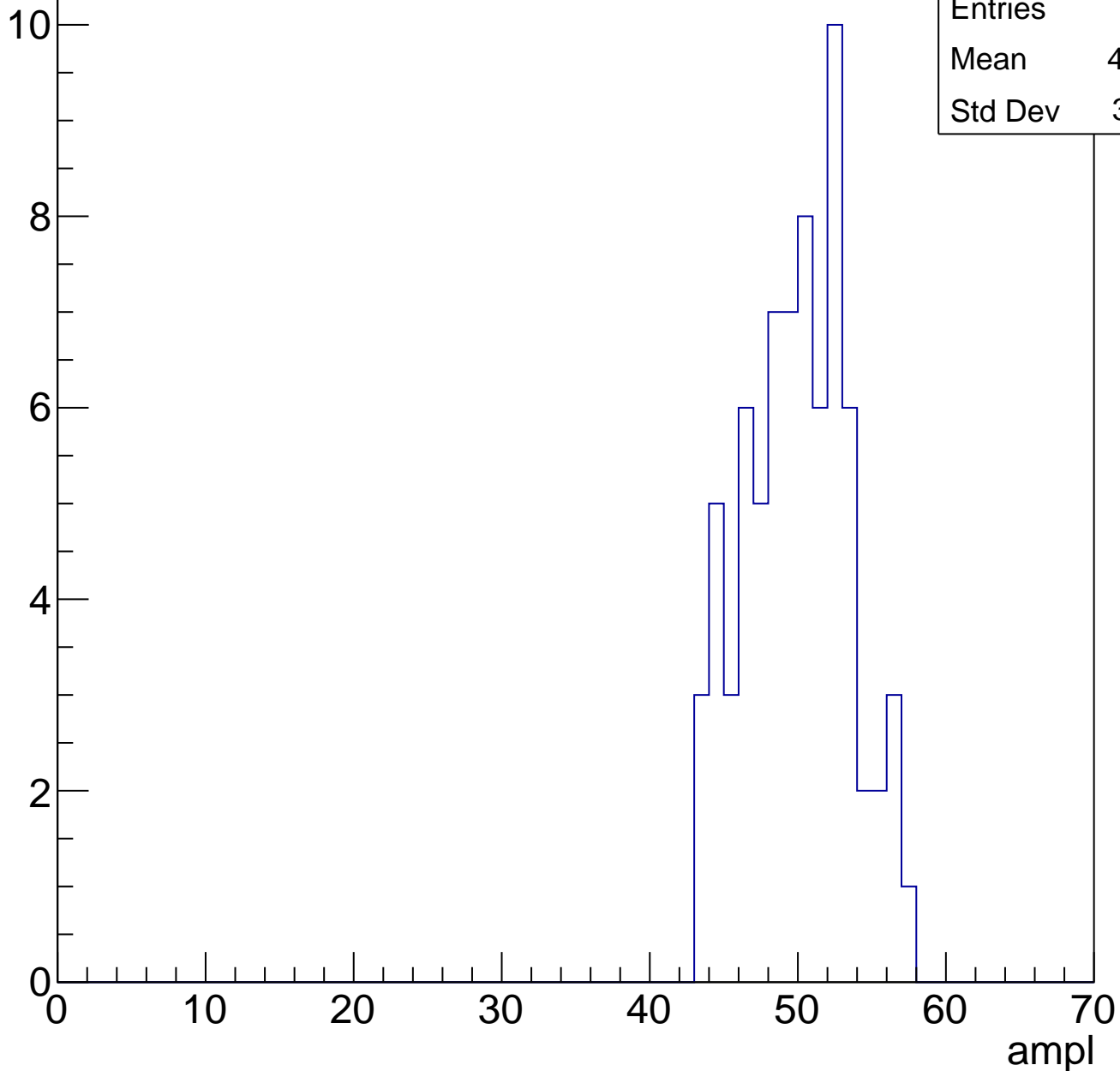


B1L103S, U10-ch32, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.47
Std Dev	3.481

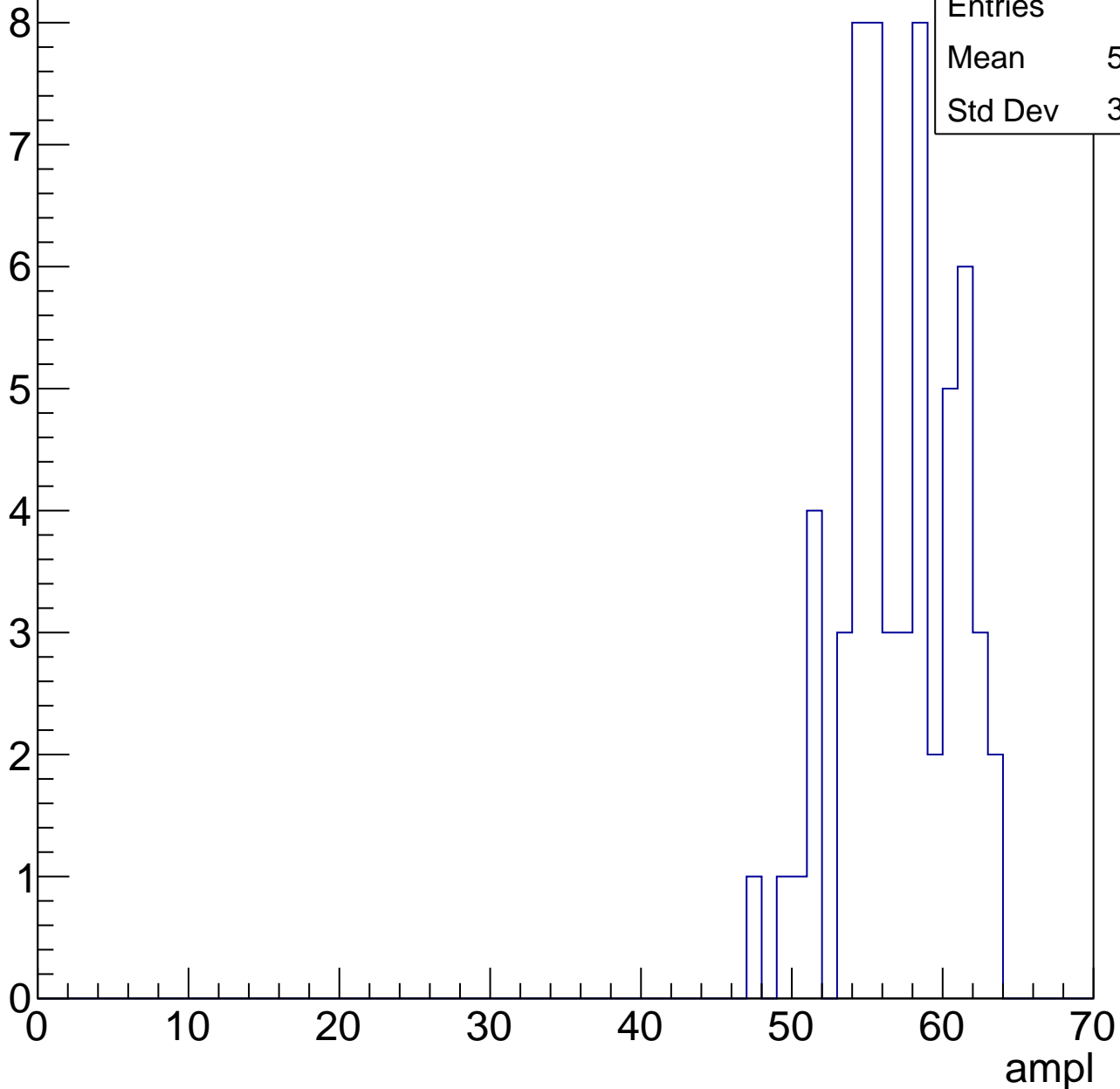


B1L103S, U10-ch32, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

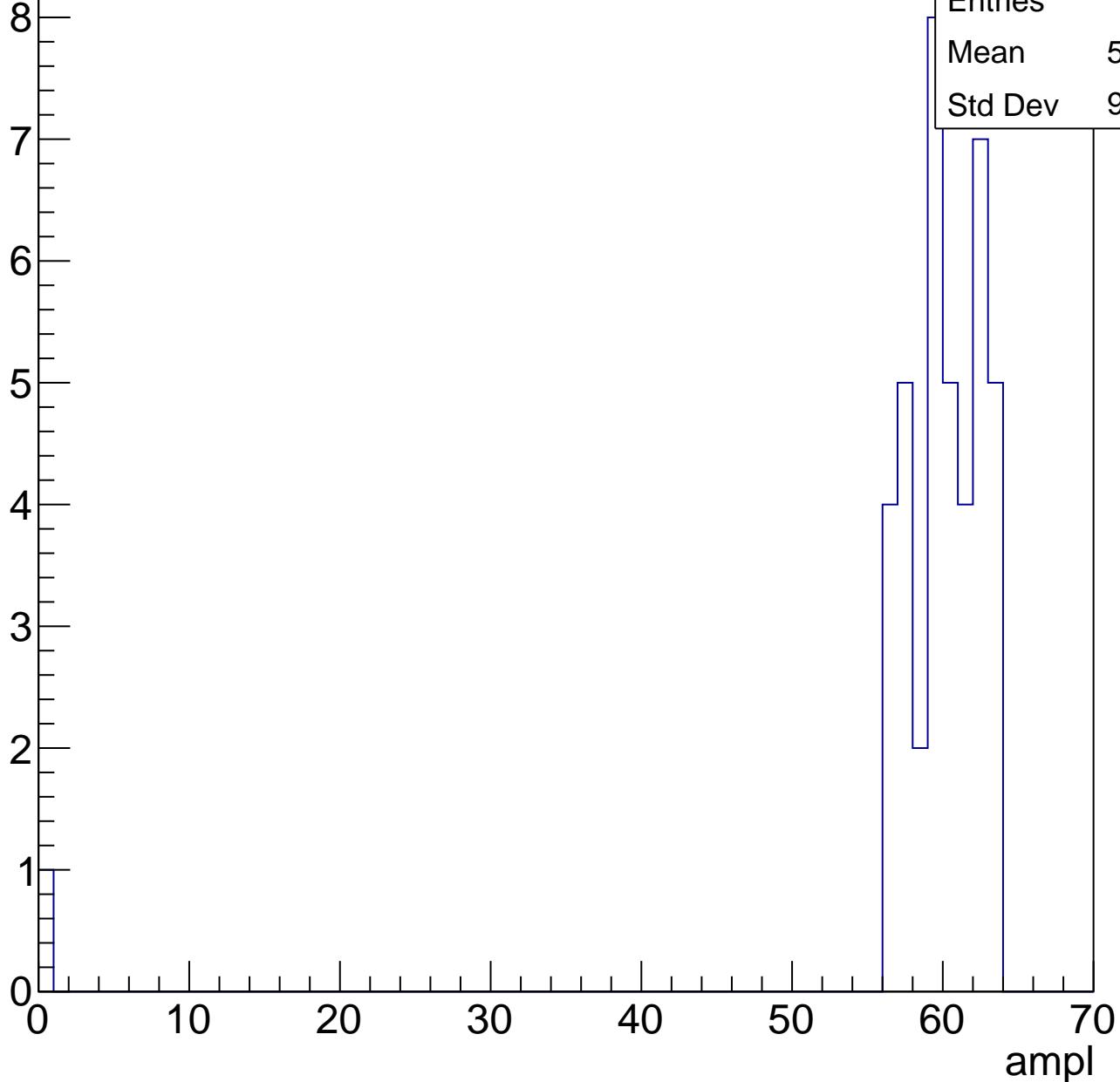
Entries	58
Mean	56.55
Std Dev	3.724



B1L103S, U10-ch32, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

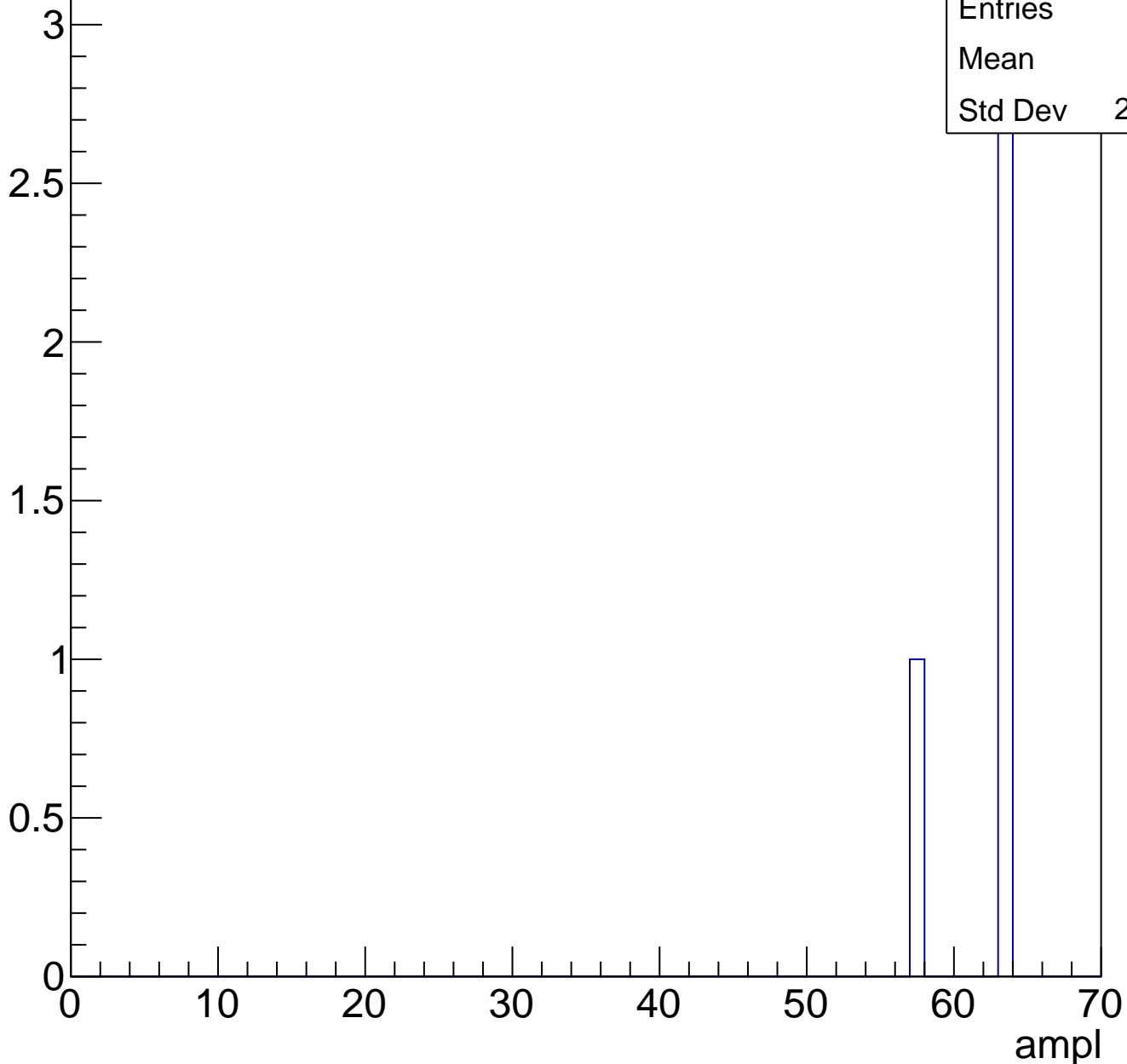


Entries	41
Mean	58.29
Std Dev	9.477

B1L103S, U10-ch32, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch32, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch33, adc0

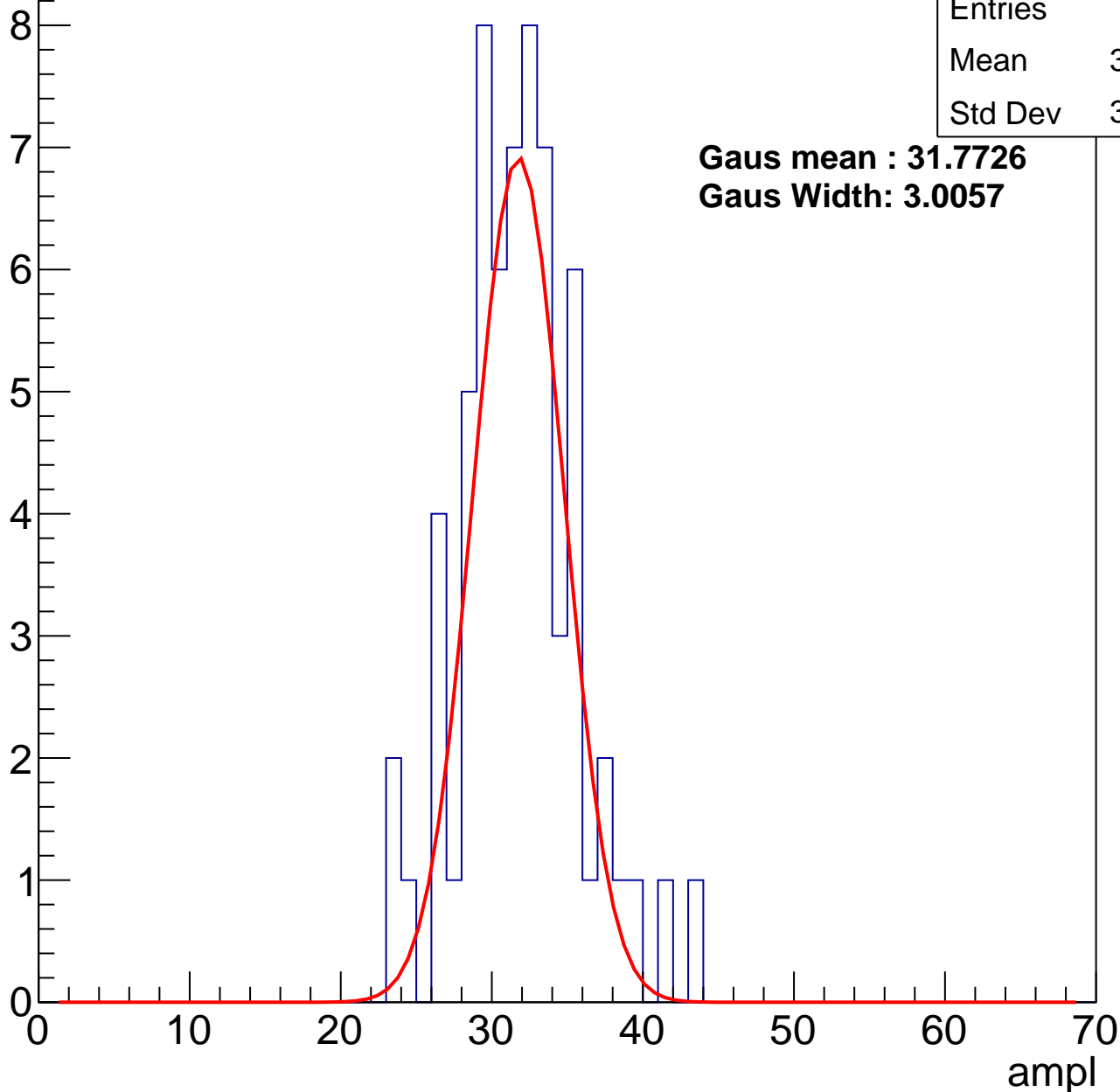
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	31.38
Std Dev	3.882

Gaus mean : 31.7726

Gaus Width: 3.0057



B1L103S, U10-ch33, adc1

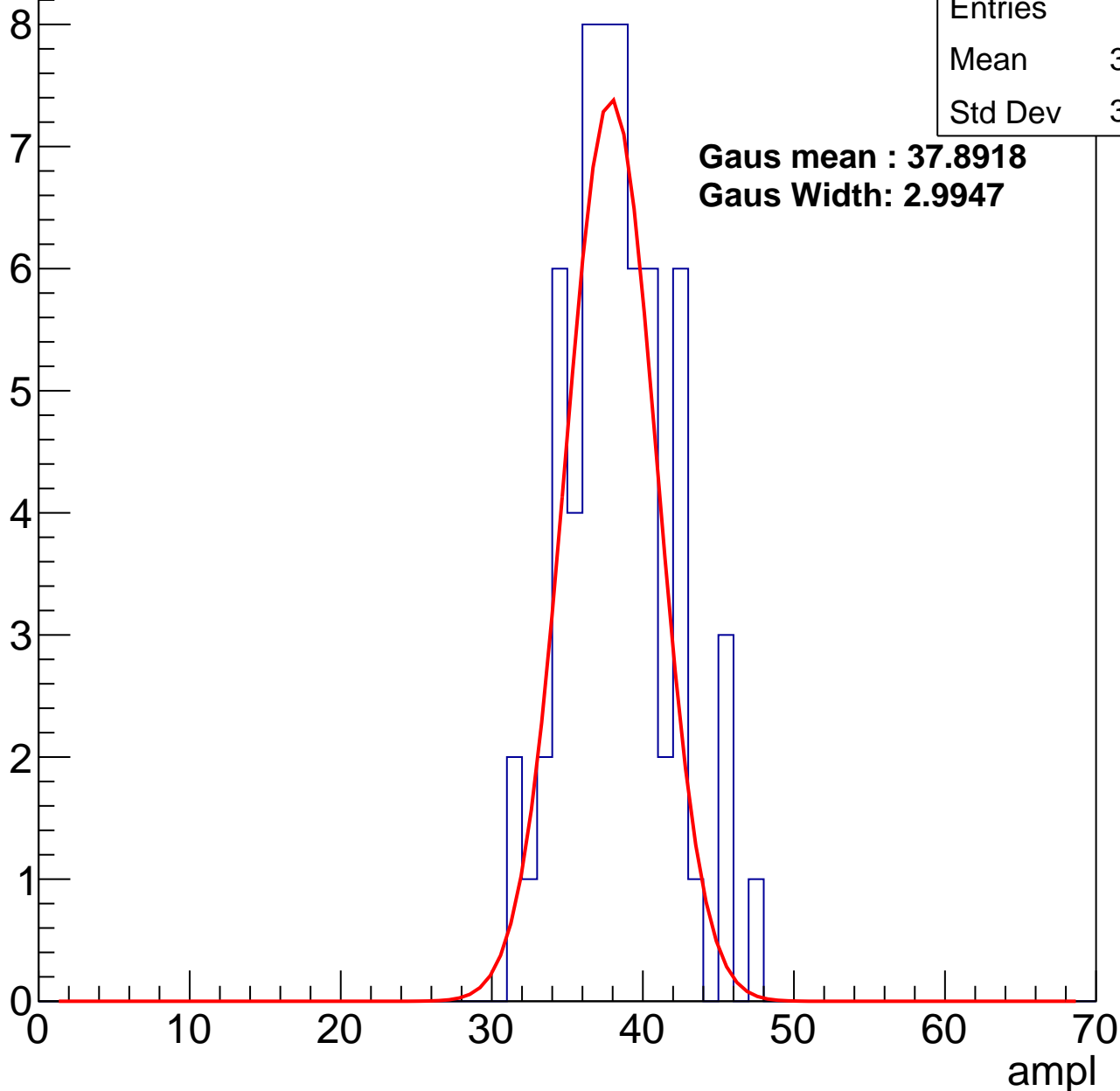
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	37.89
Std Dev	3.428

Gaus mean : 37.8918

Gaus Width: 2.9947



B1L103S, U10-ch33, adc2

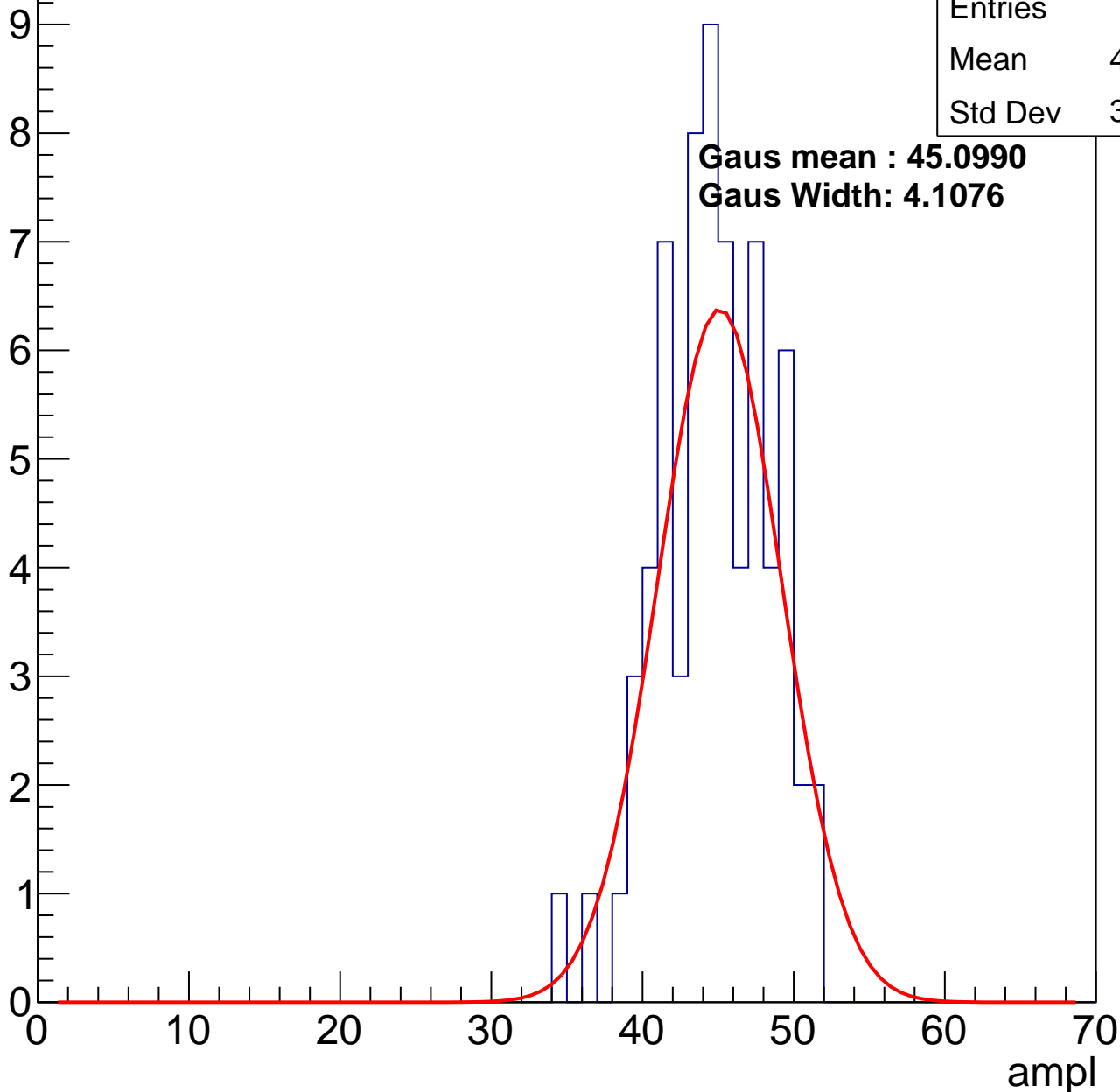
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	44.26
Std Dev	3.586

Gaus mean : 45.0990

Gaus Width: 4.1076

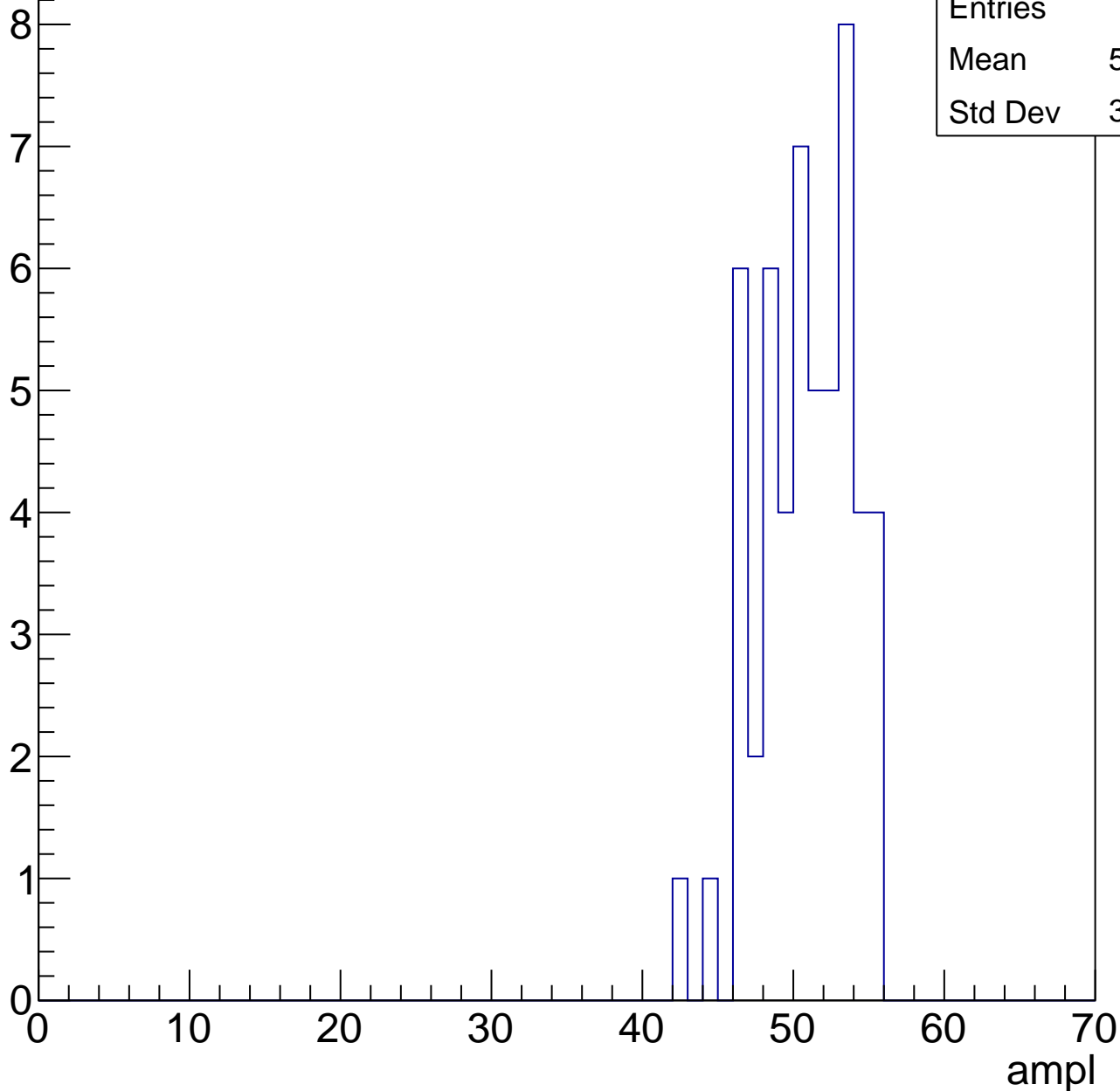


B1L103S, U10-ch33, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

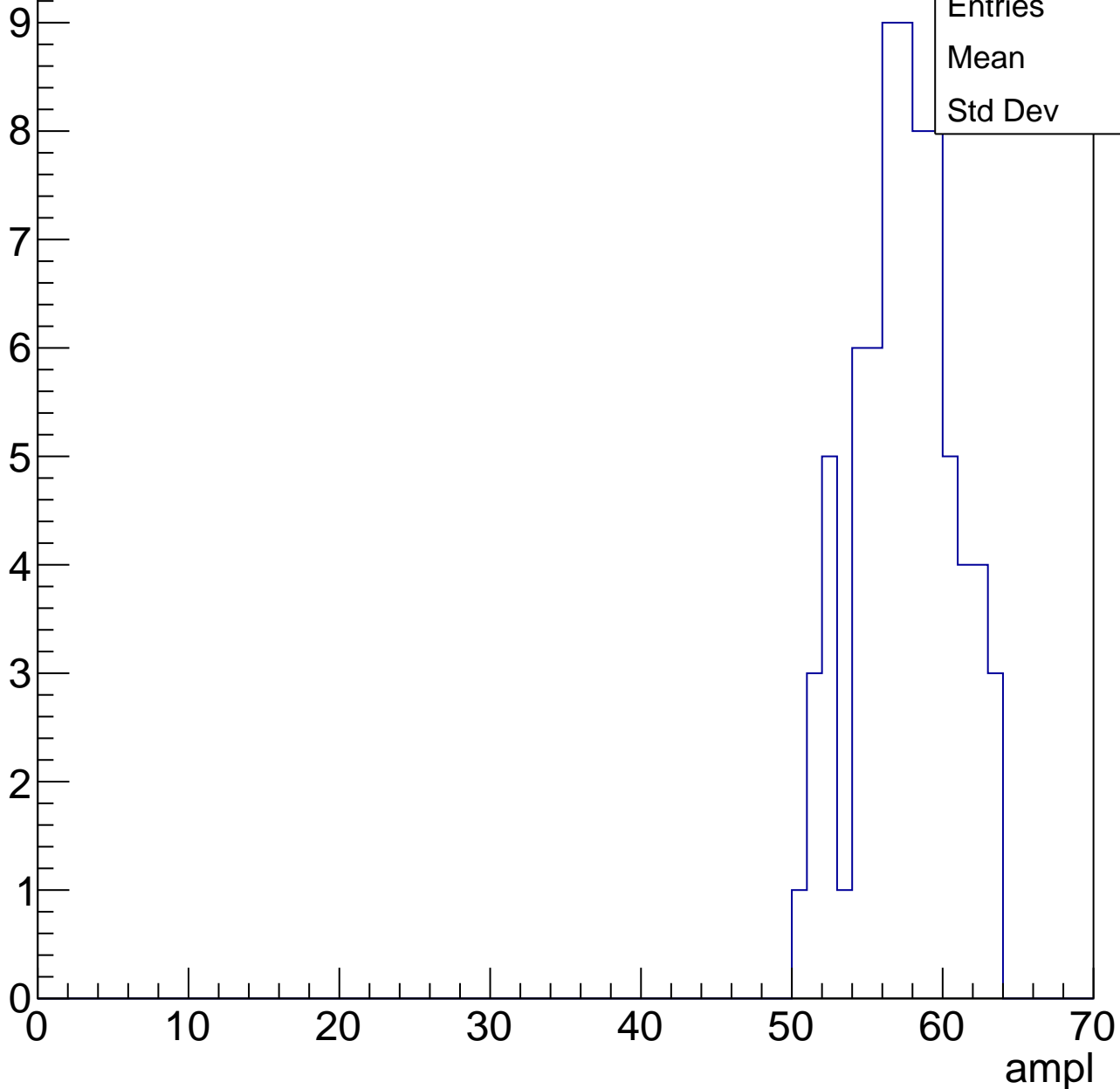
Entries	53
Mean	50.28
Std Dev	3.068



B1L103S, U10-ch33, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

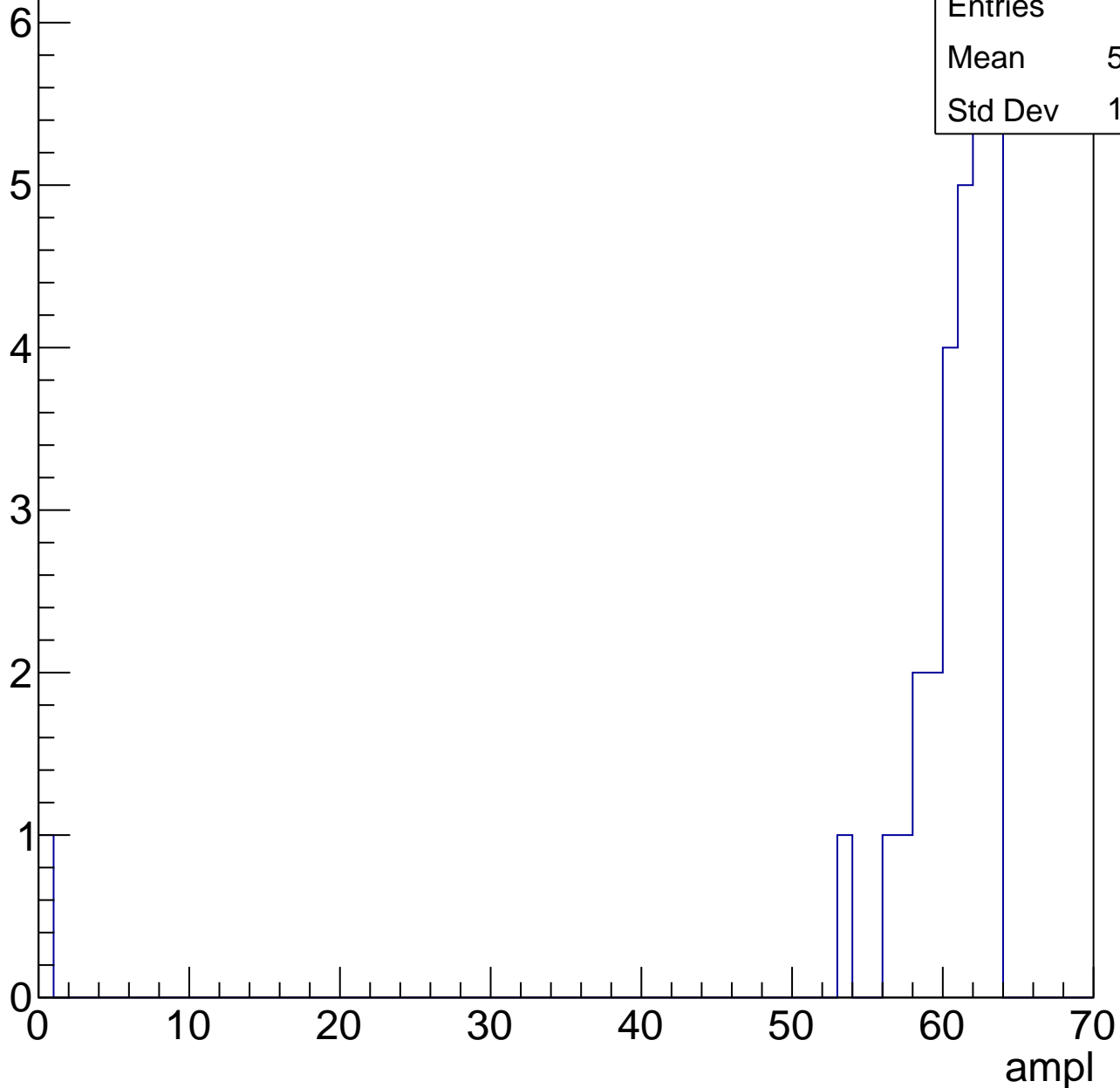


B1L103S, U10-ch33, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

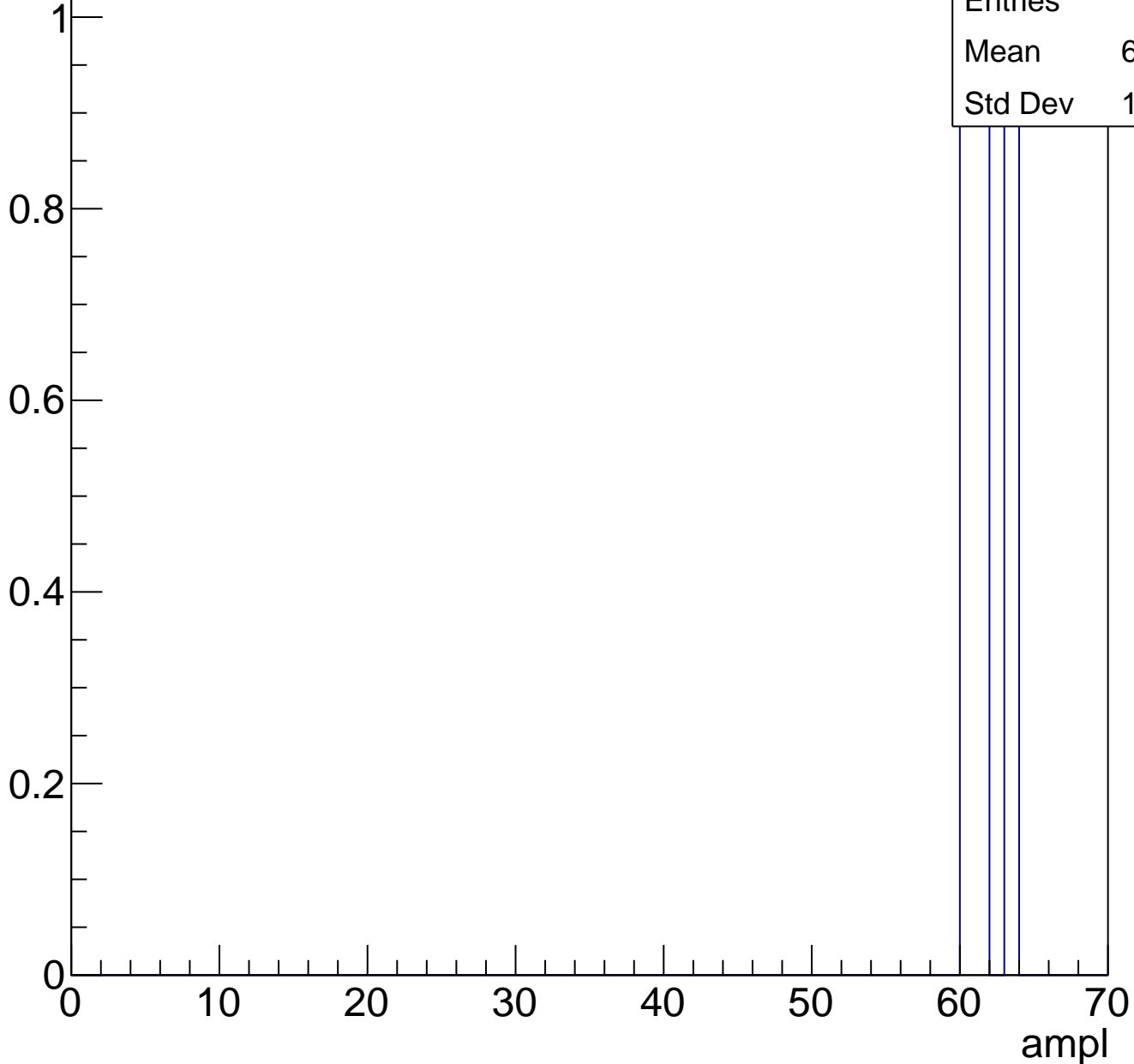
Entries	29
Mean	58.45
Std Dev	11.29



B1L103S, U10-ch33, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch33, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch34, adc0

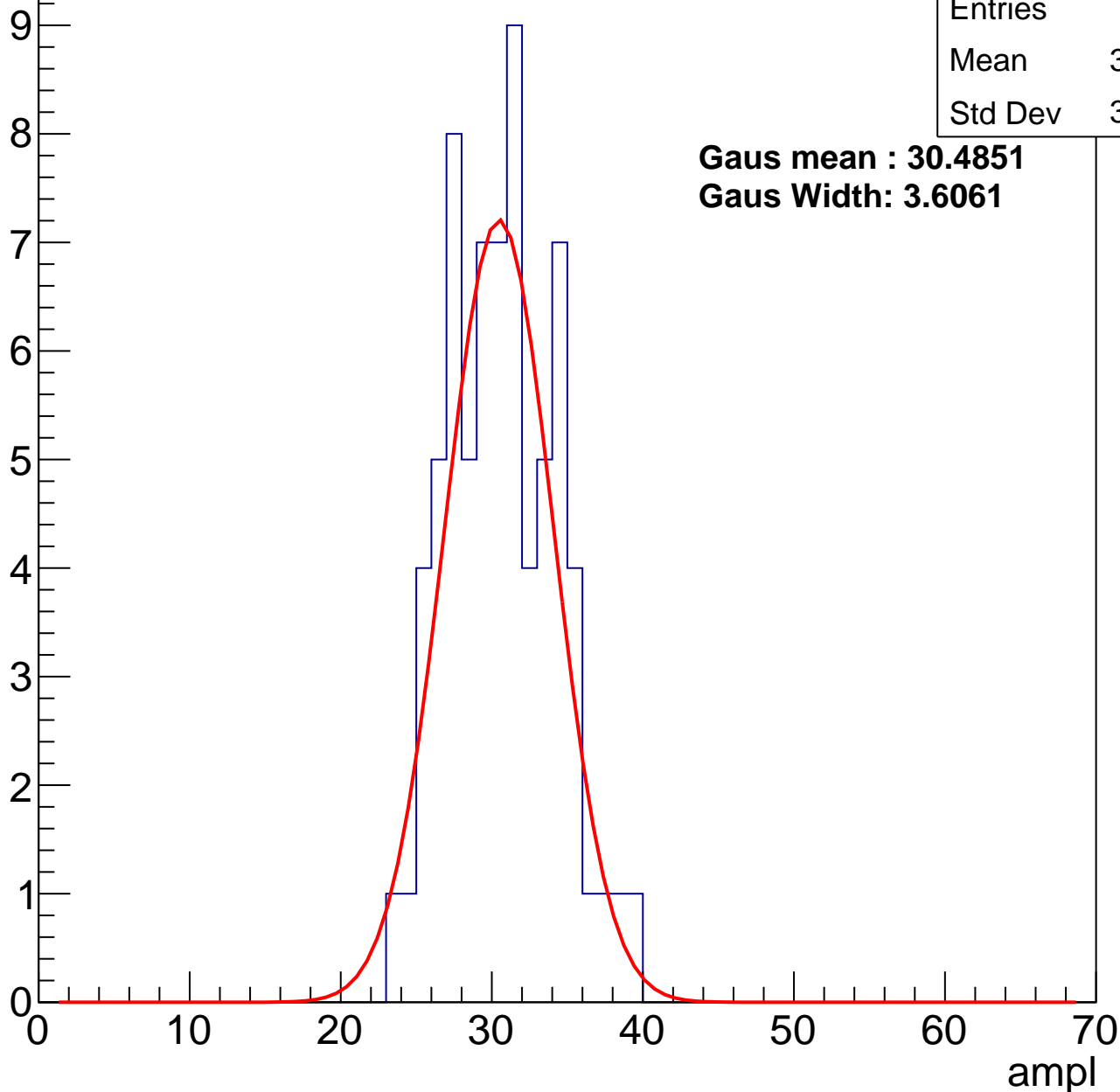
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	30.23
Std Dev	3.505

Gaus mean : 30.4851

Gaus Width: 3.6061



B1L103S, U10-ch34, adc1

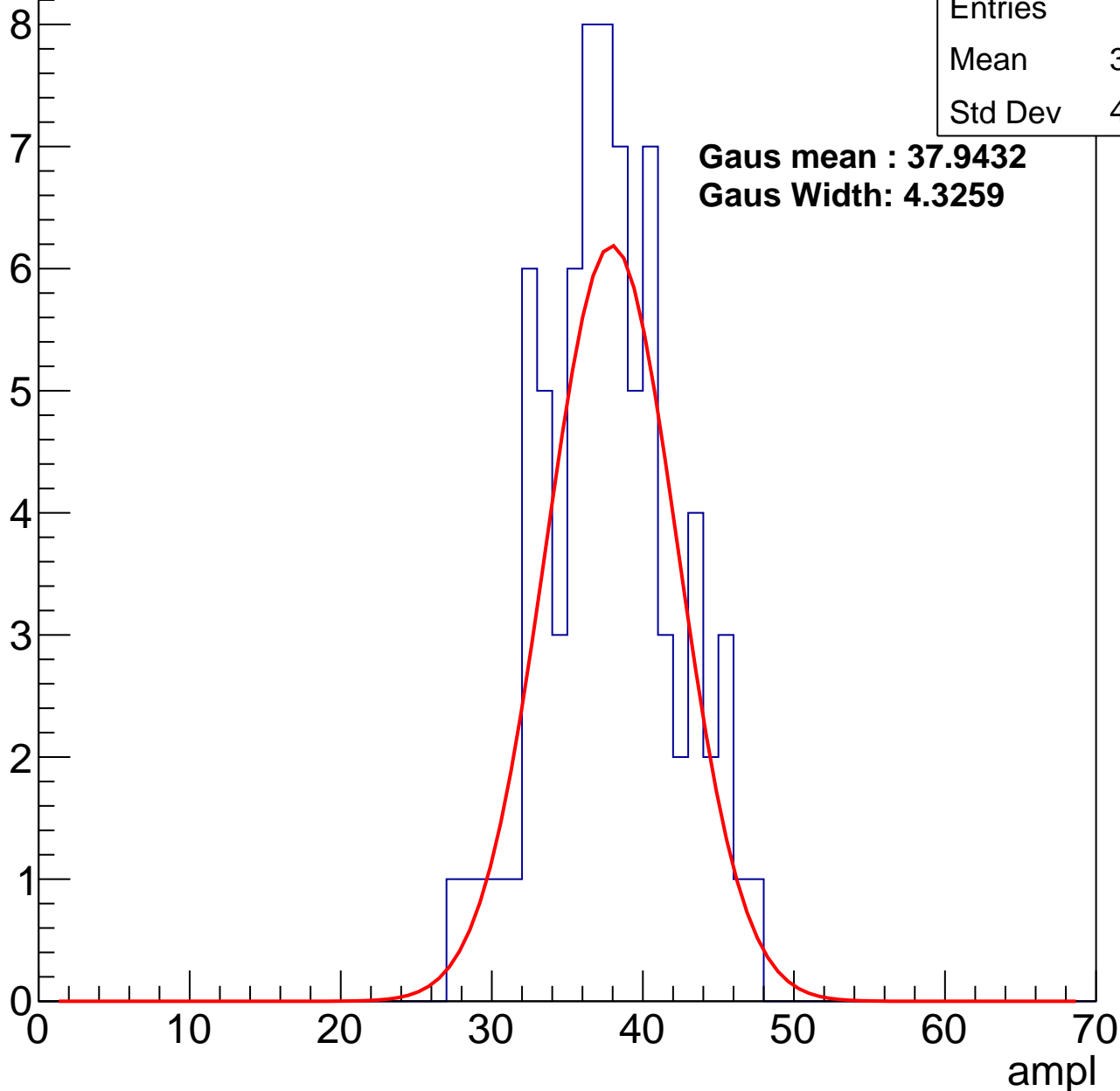
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	37.29
Std Dev	4.316

Gaus mean : 37.9432

Gaus Width: 4.3259



B1L103S, U10-ch34, adc2

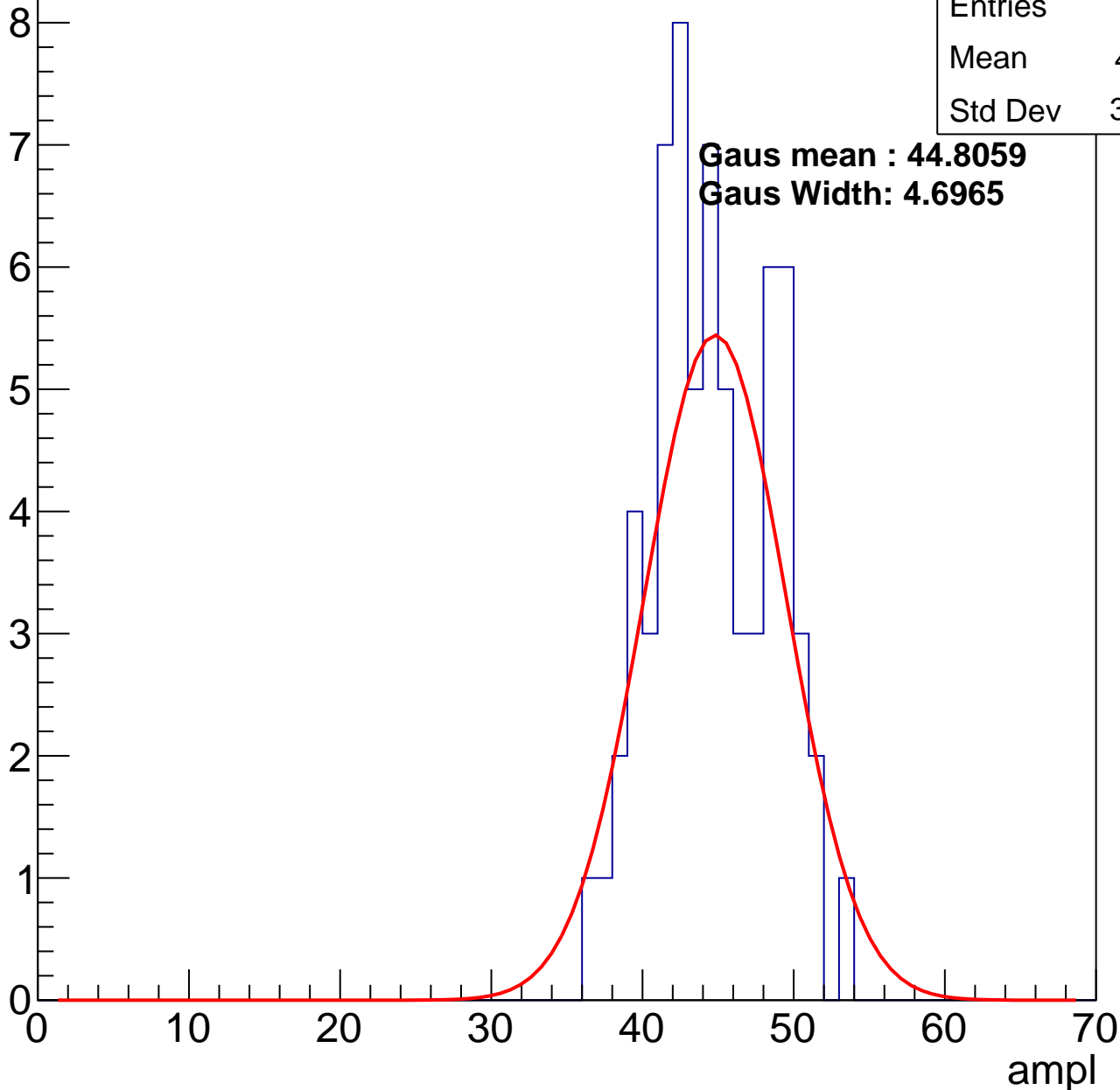
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	44.21
Std Dev	3.877

Gaus mean : 44.8059

Gaus Width: 4.6965

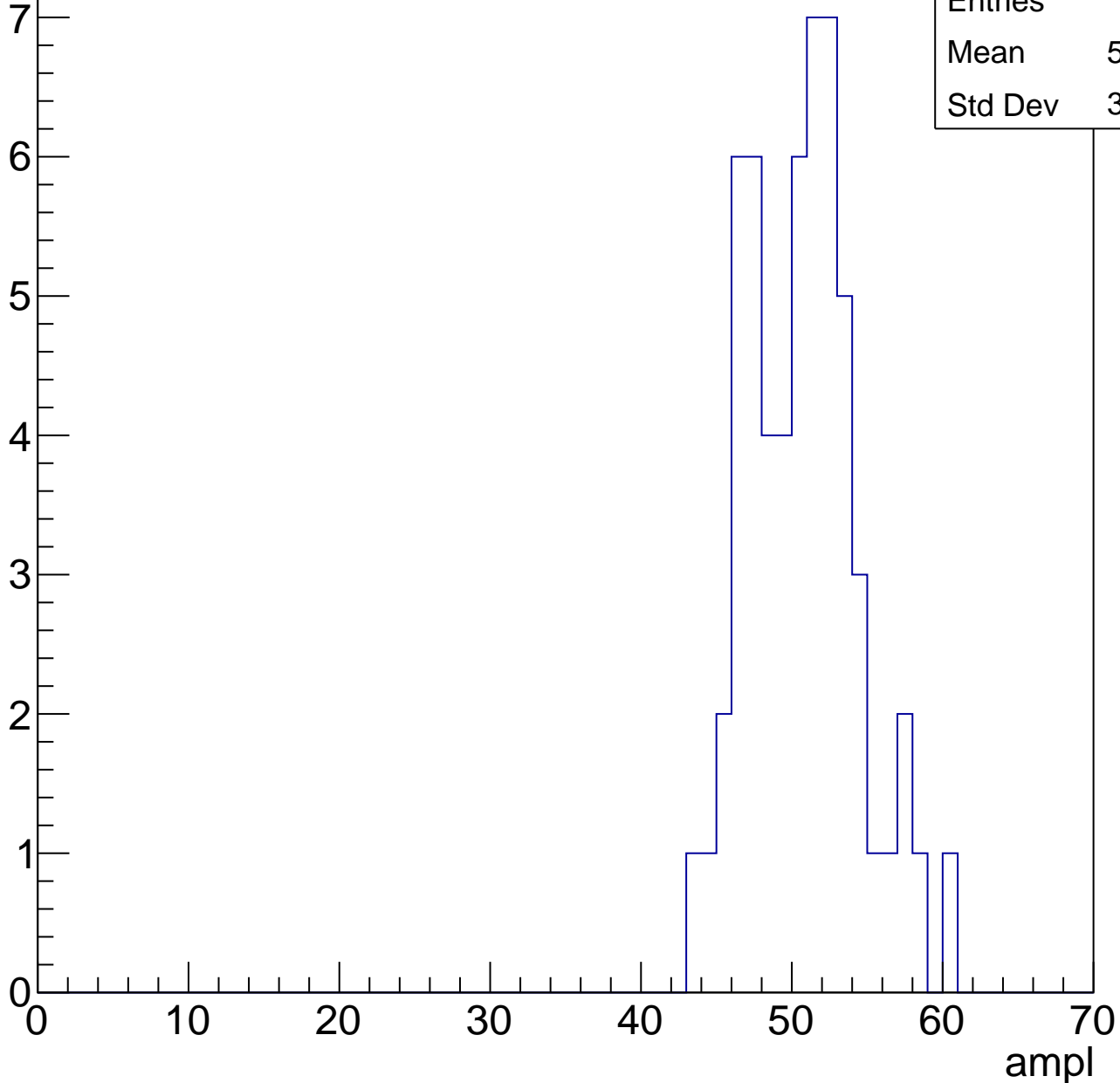


B1L103S, U10-ch34, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	50.24
Std Dev	3.602

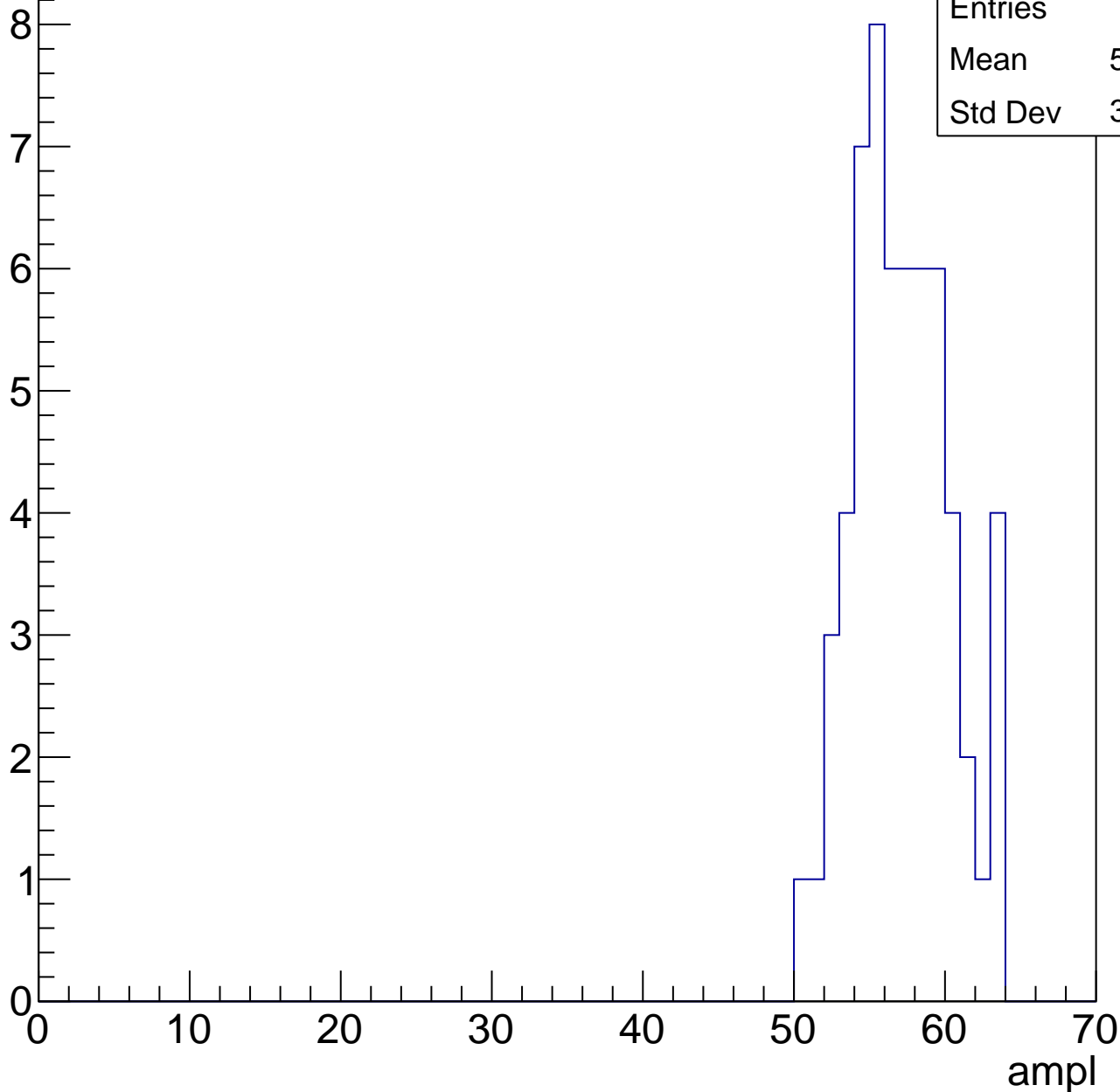


B1L103S, U10-ch34, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	56.66
Std Dev	3.149

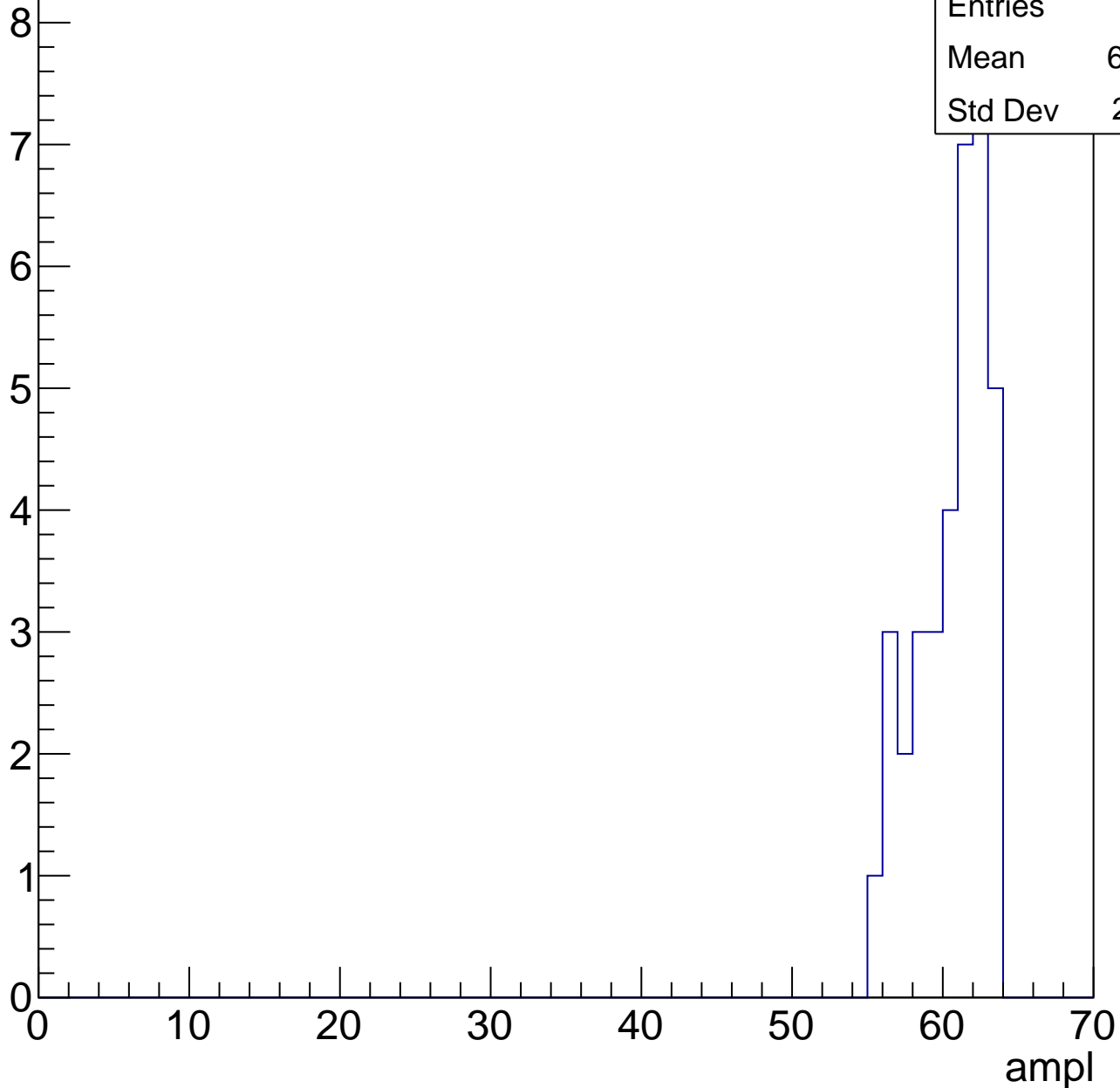


B1L103S, U10-ch34, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	60.17
Std Dev	2.291



B1L103S, U10-ch34, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	50.83
Std Dev	22.77

0 10 20 30 40 50 60 70

ampl

B1L103S, U10-ch34, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch35, adc0

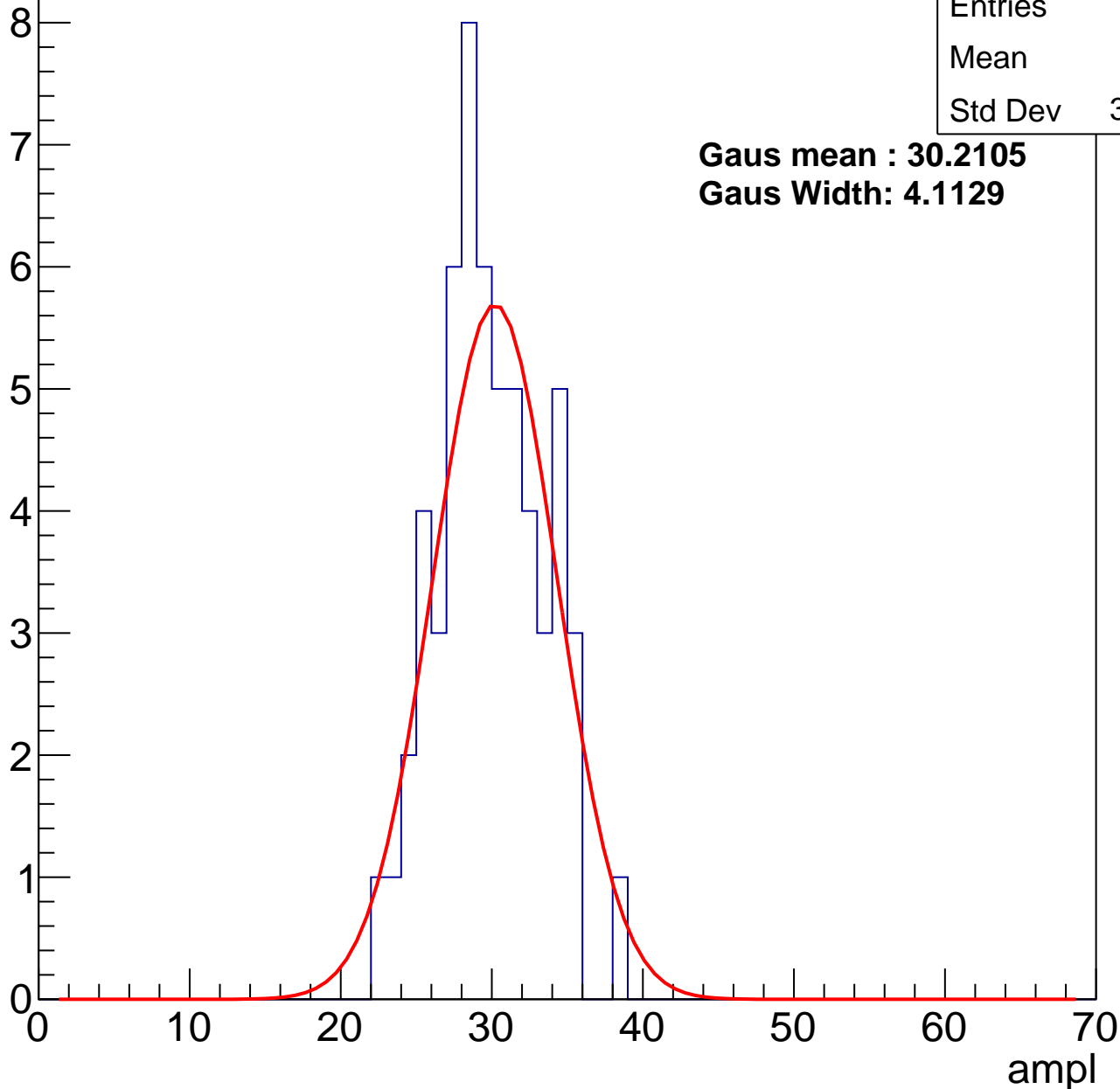
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	29.4
Std Dev	3.433

Gaus mean : 30.2105

Gaus Width: 4.1129



B1L103S, U10-ch35, adc1

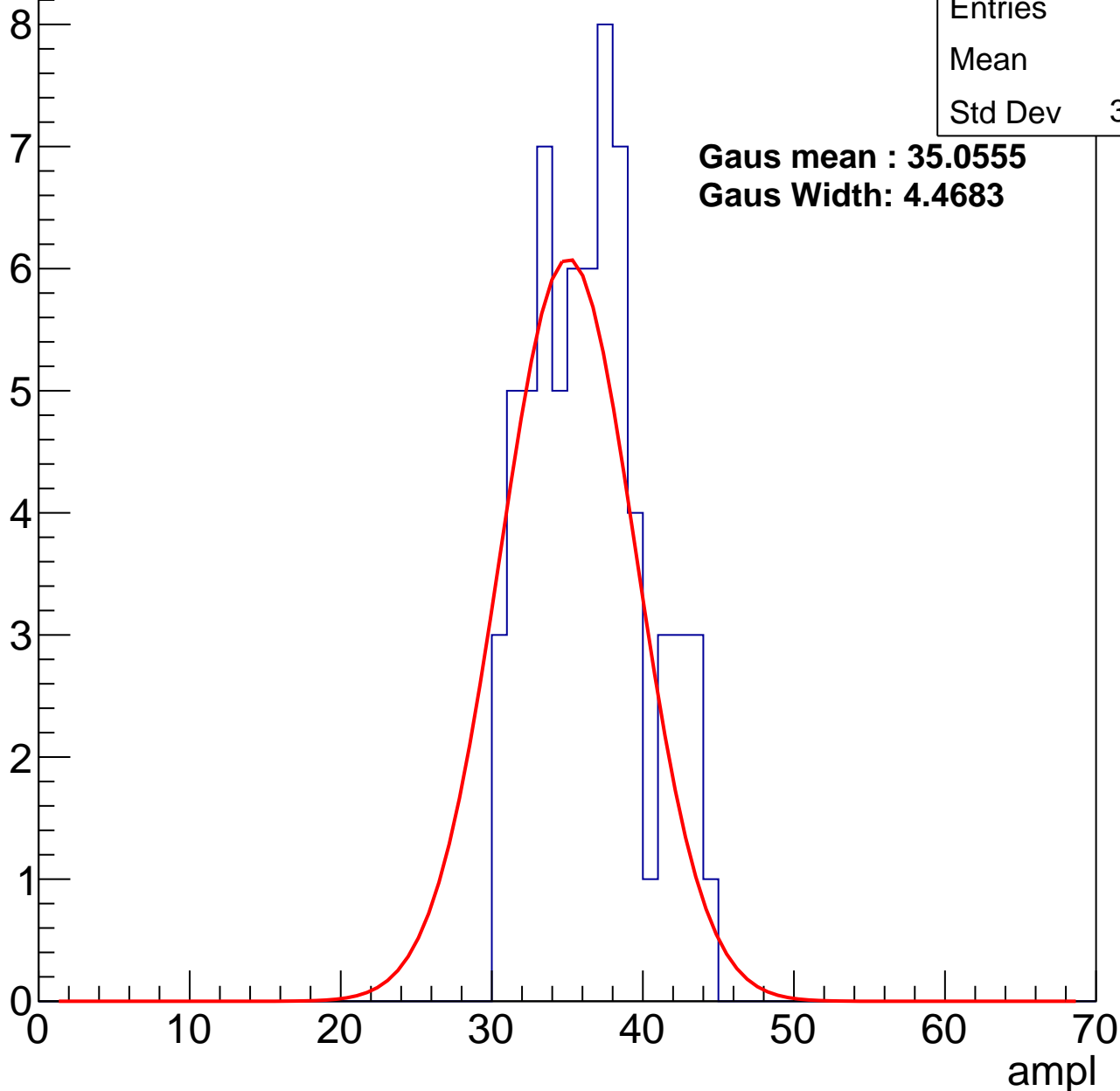
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	36
Std Dev	3.632

Gaus mean : 35.0555

Gaus Width: 4.4683



B1L103S, U10-ch35, adc2

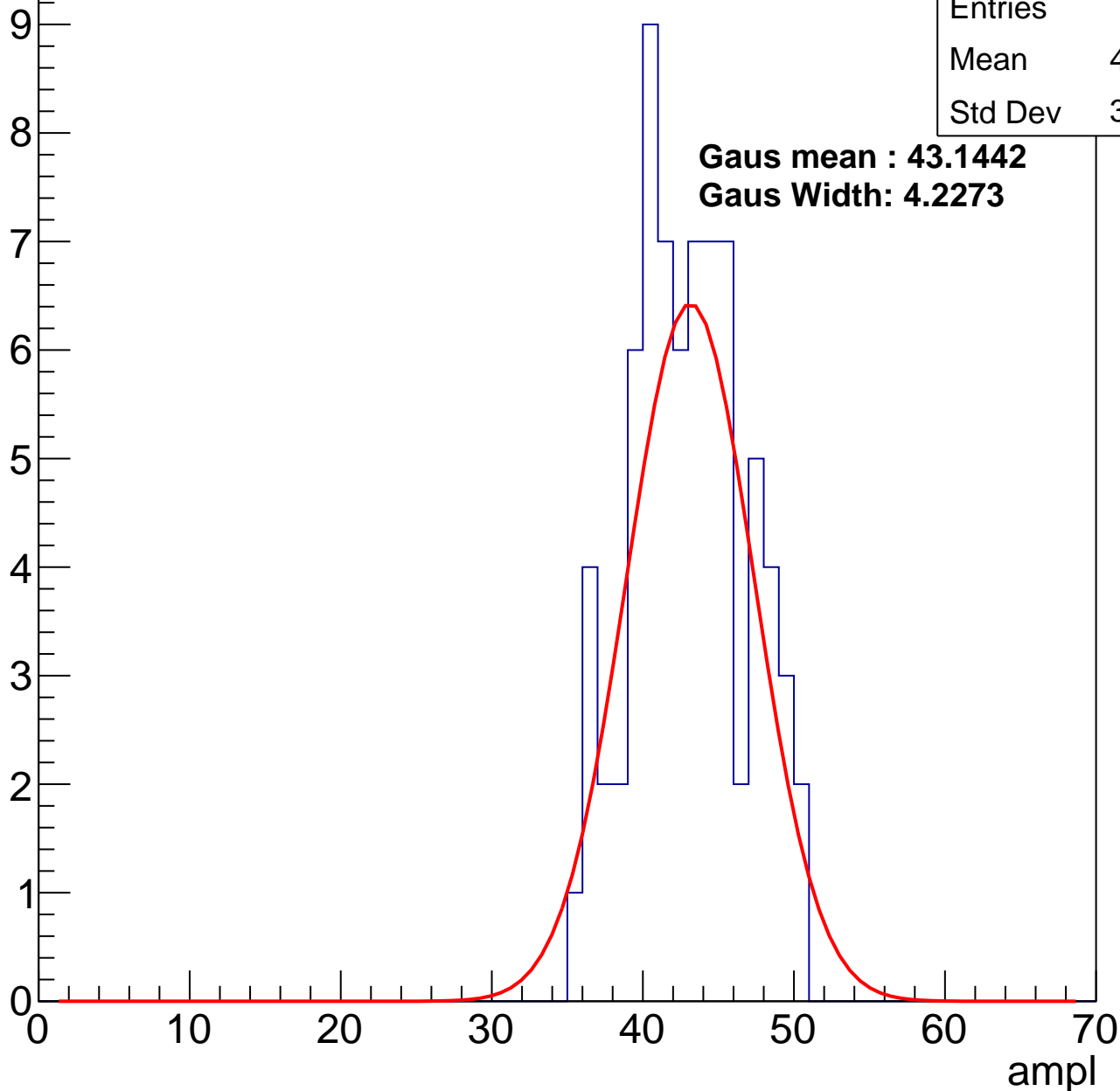
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	42.59
Std Dev	3.712

Gaus mean : 43.1442

Gaus Width: 4.2273

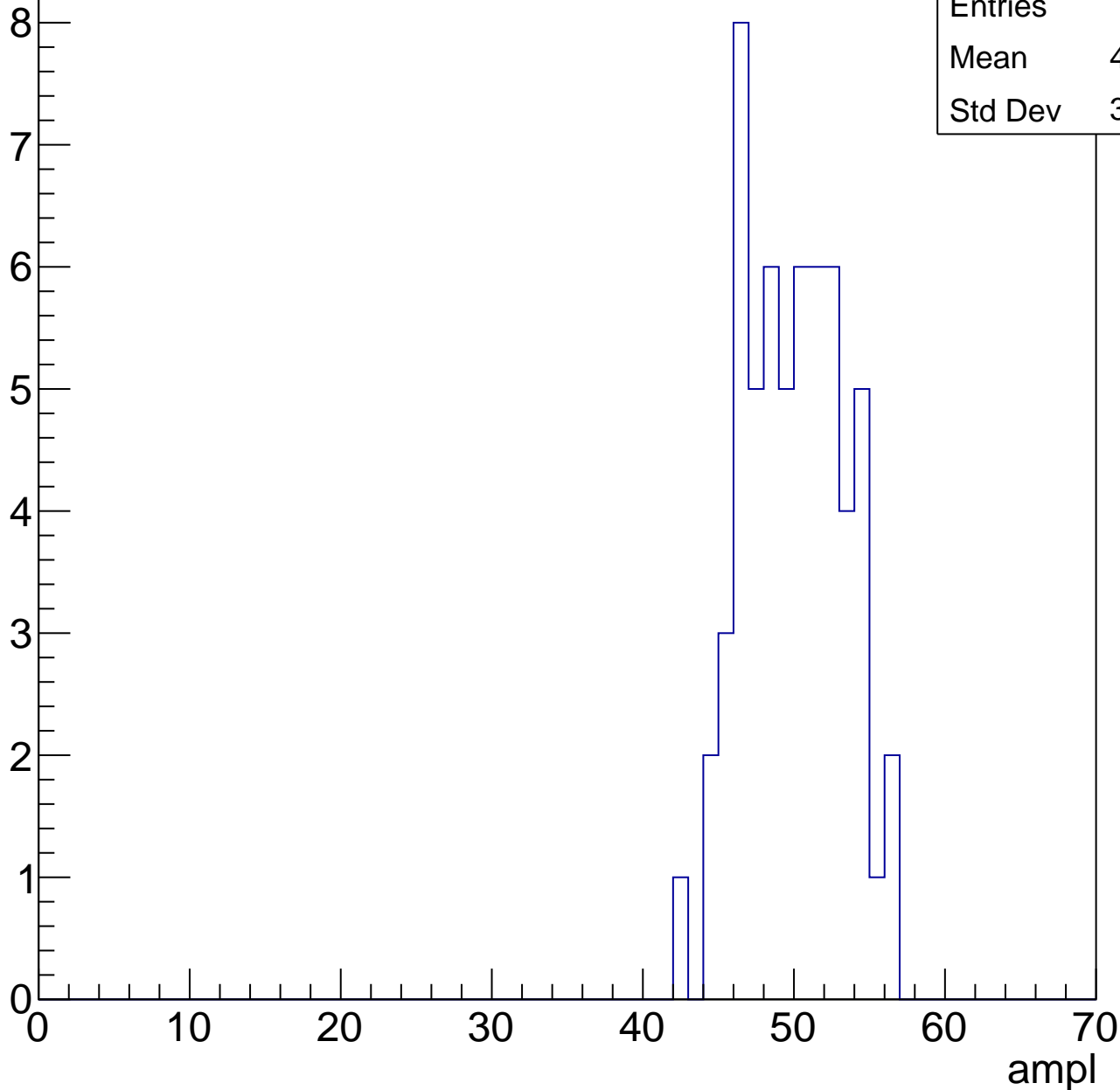


B1L103S, U10-ch35, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	49.47
Std Dev	3.263

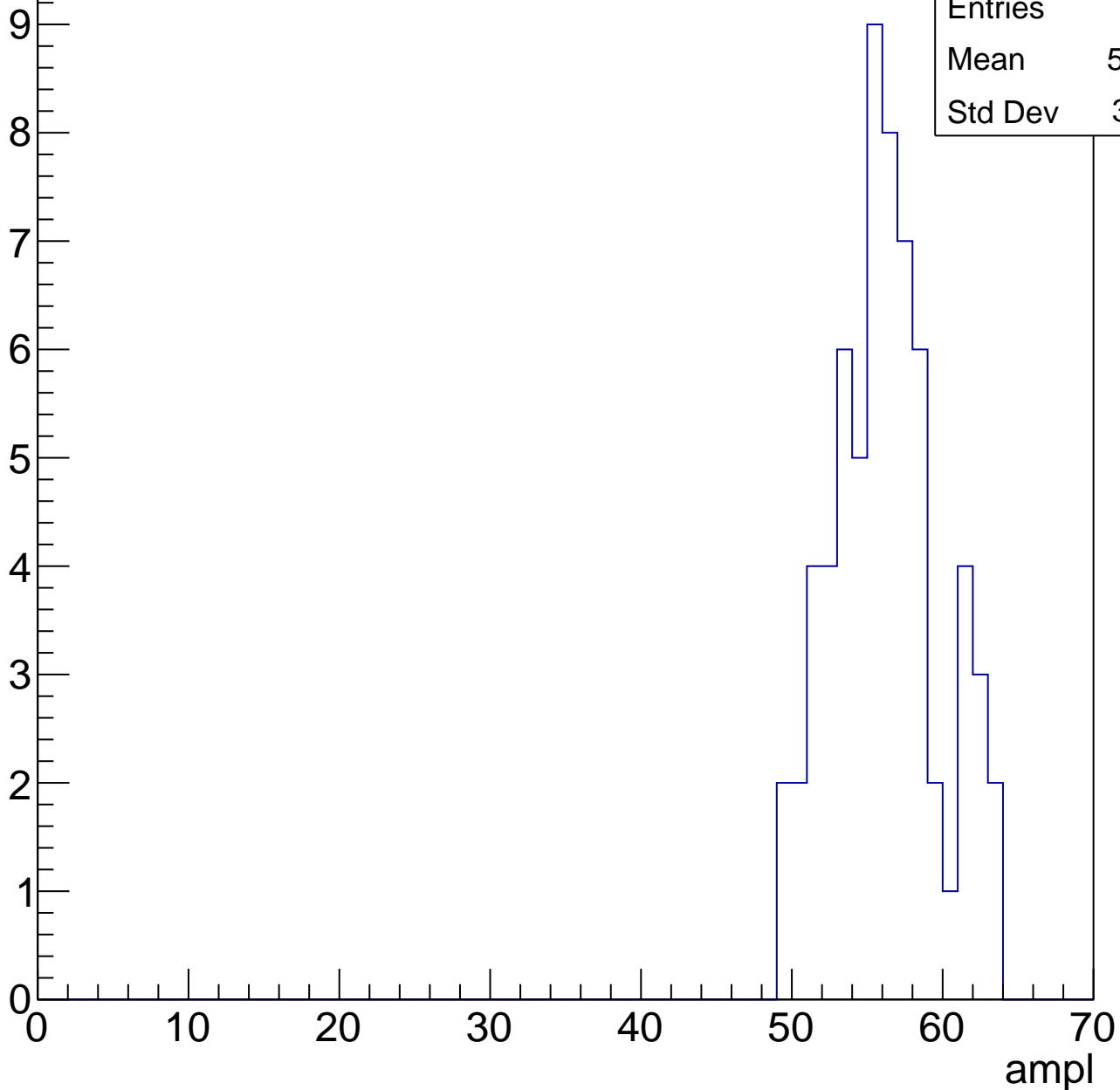


B1L103S, U10-ch35, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.72
Std Dev	3.471

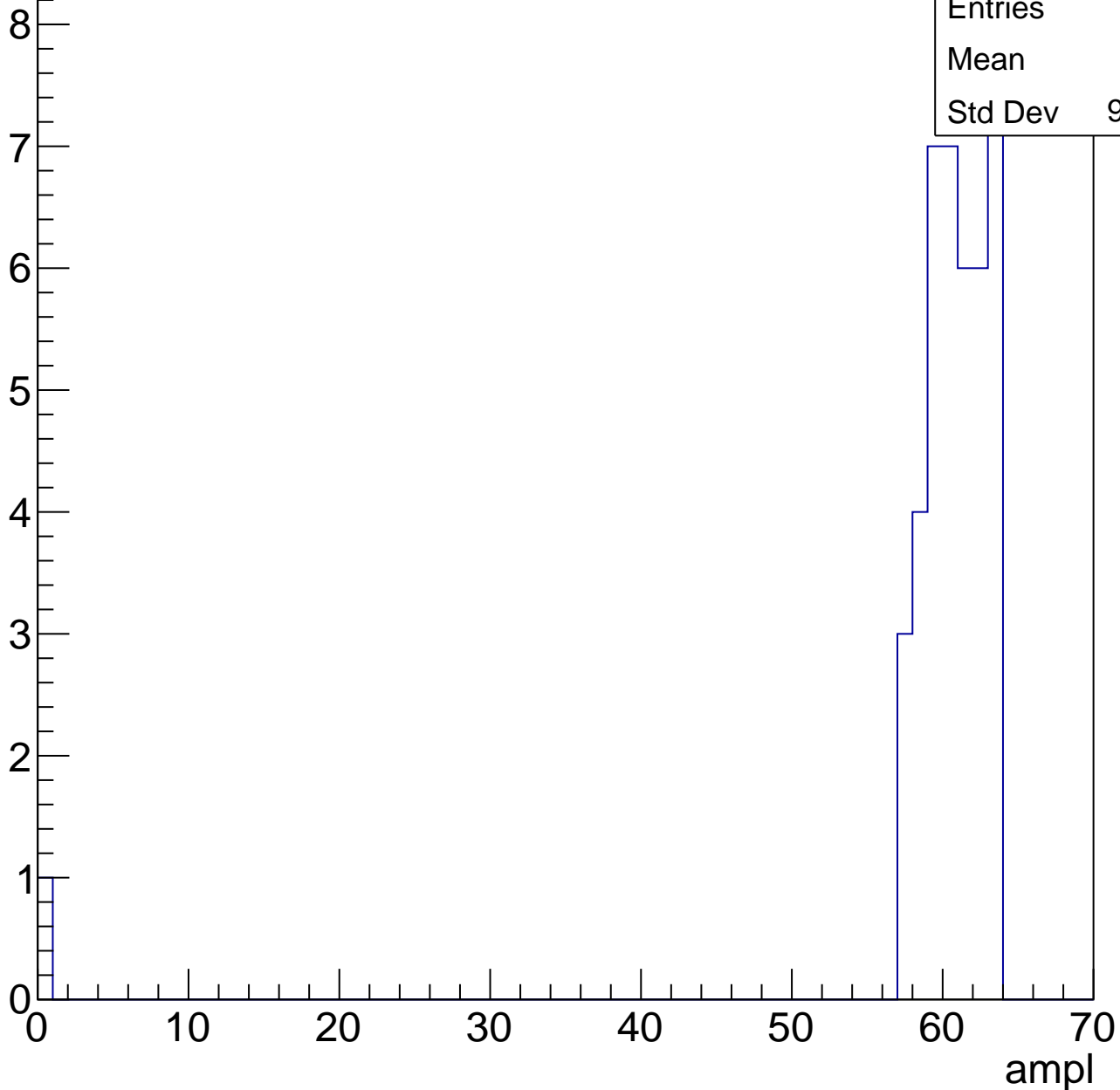


B1L103S, U10-ch35, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59
Std Dev	9.399



B1L103S, U10-ch35, adc6

calib_packv5_042523_0143.root, FC#7, port C2

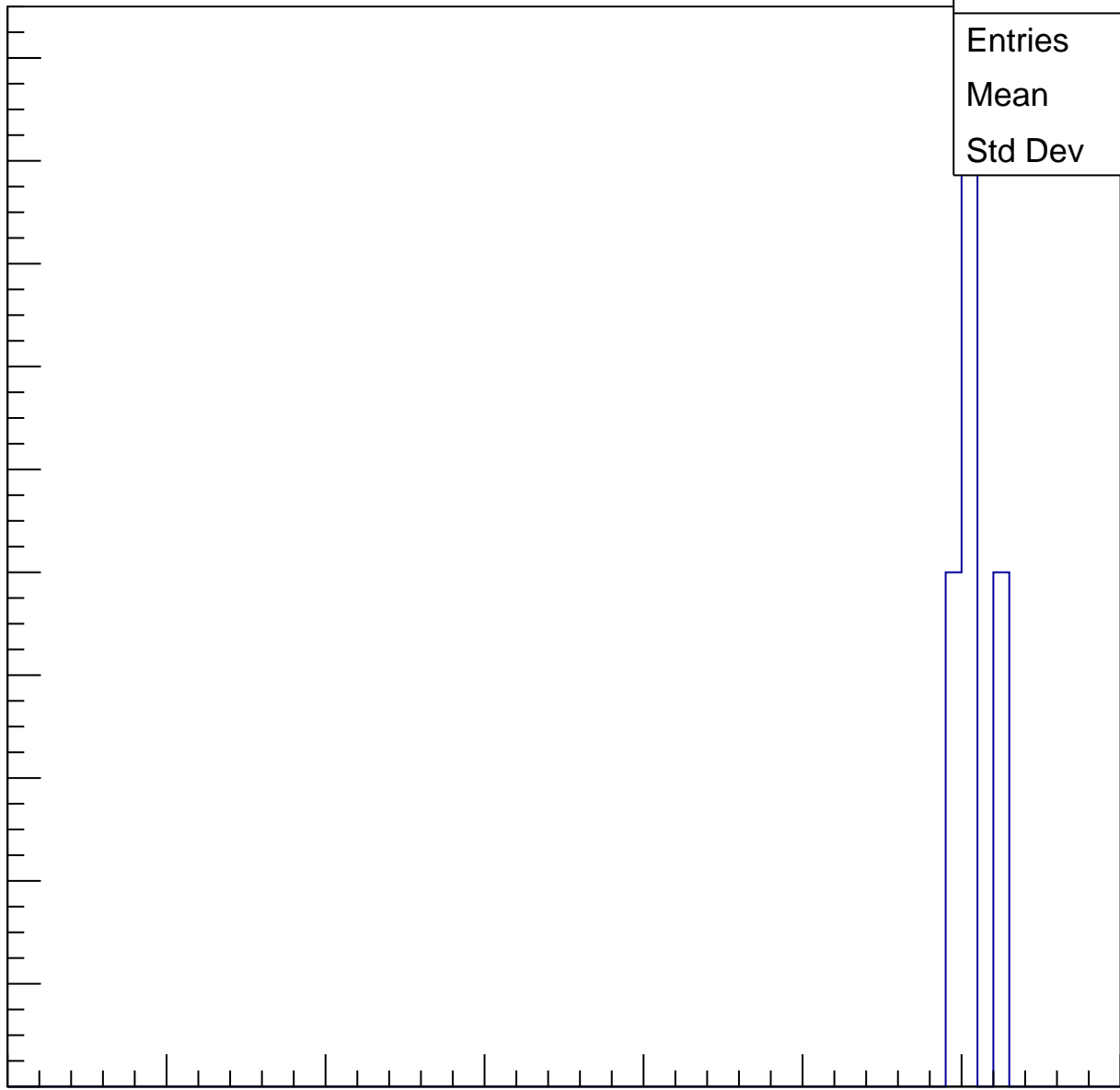
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	60.25
Std Dev	1.09

ampl

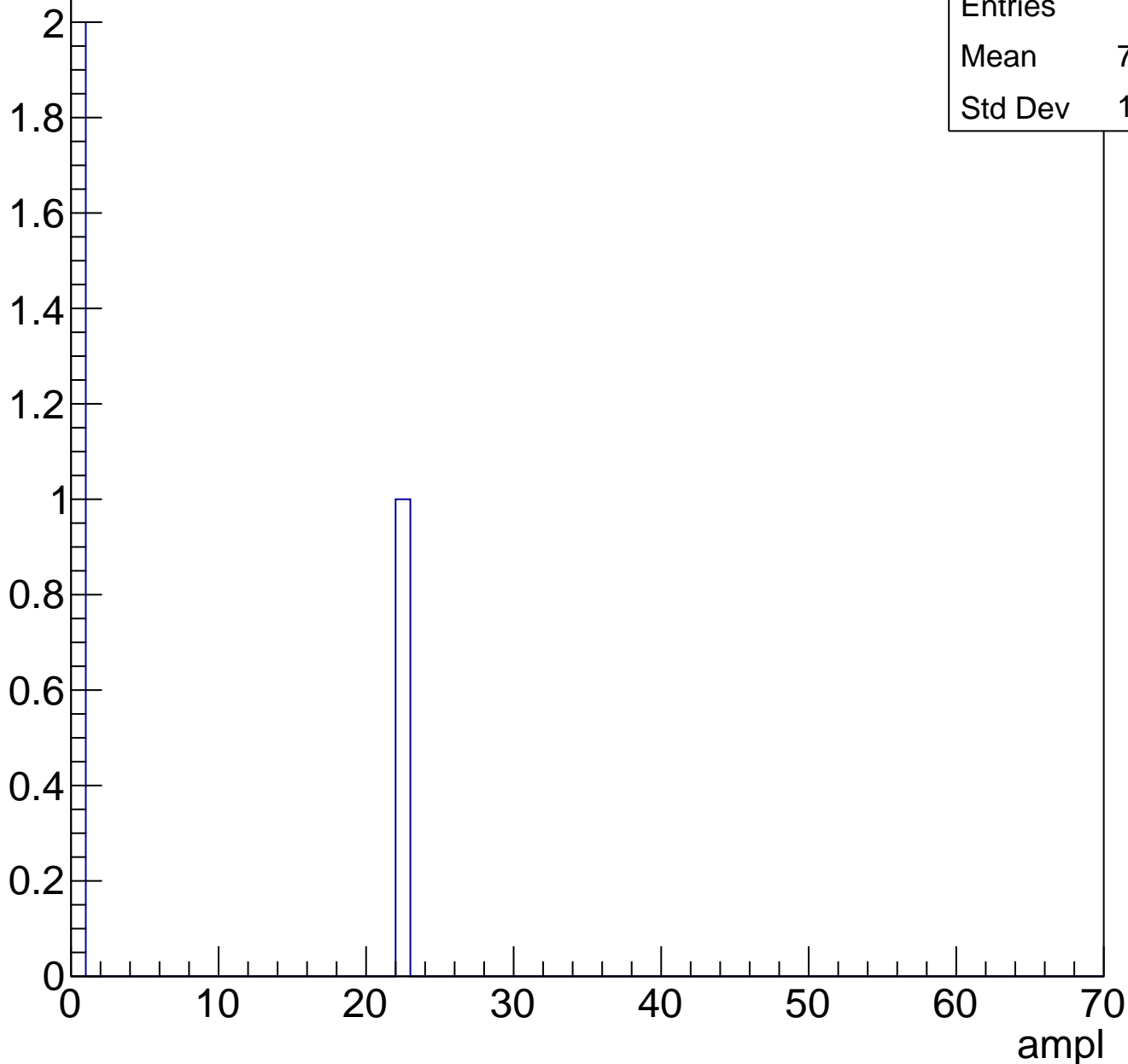
0 10 20 30 40 50 60 70



B1L103S, U10-ch35, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B1L103S, U10-ch36, adc0

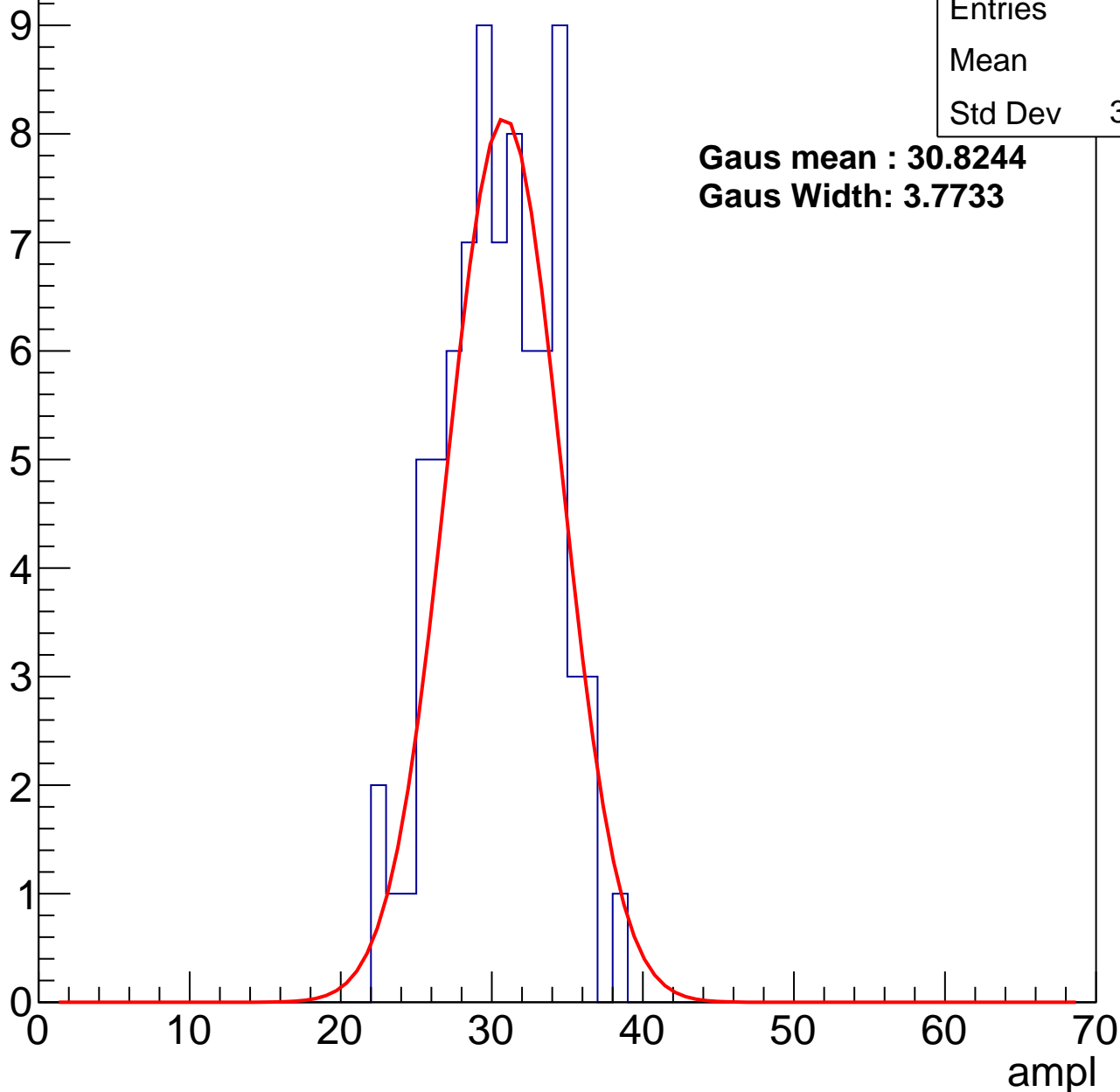
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	30
Std Dev	3.533

Gaus mean : 30.8244

Gaus Width: 3.7733



B1L103S, U10-ch36, adc1

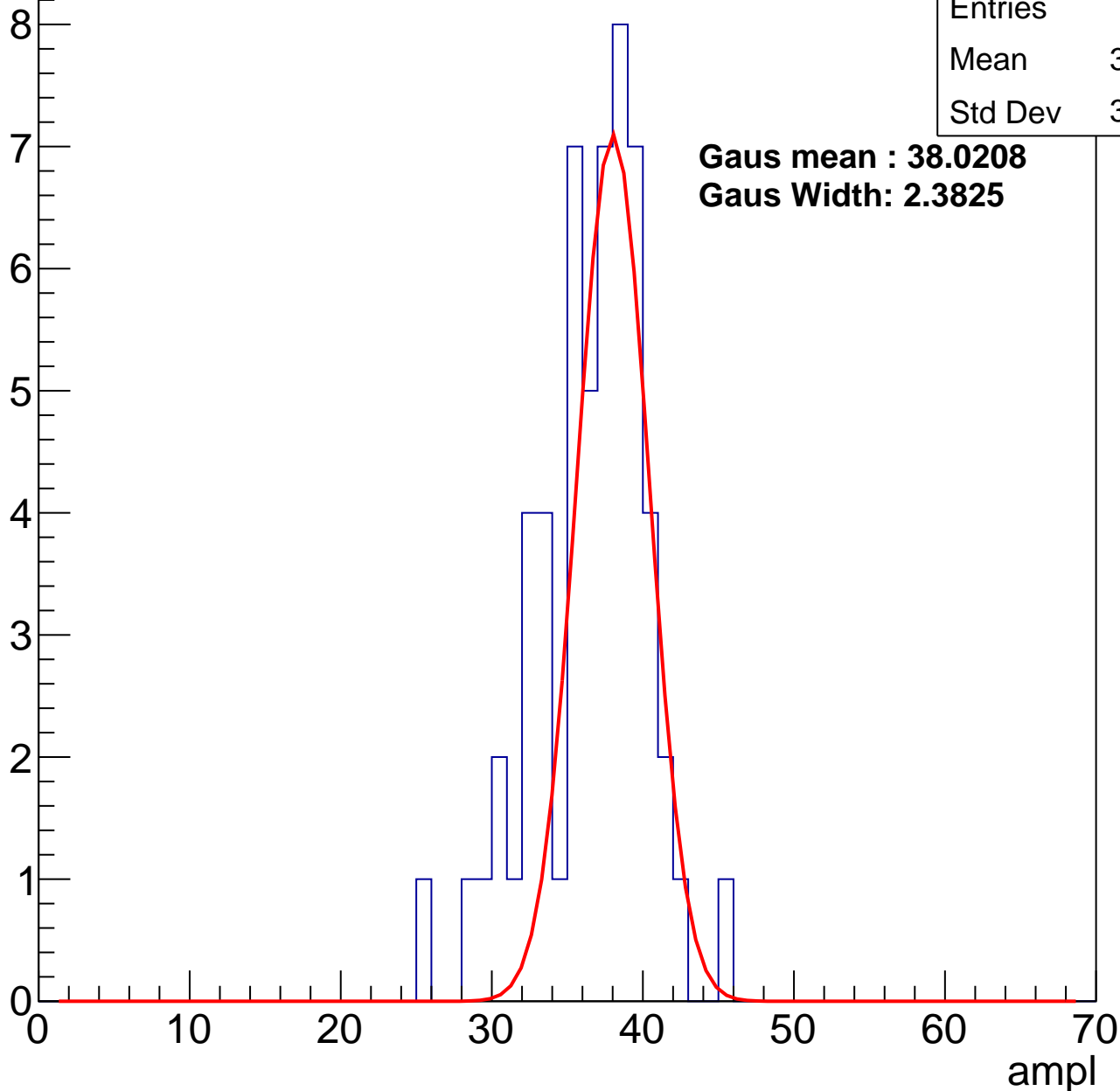
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	36.09
Std Dev	3.686

Gaus mean : 38.0208

Gaus Width: 2.3825



B1L103S, U10-ch36, adc2

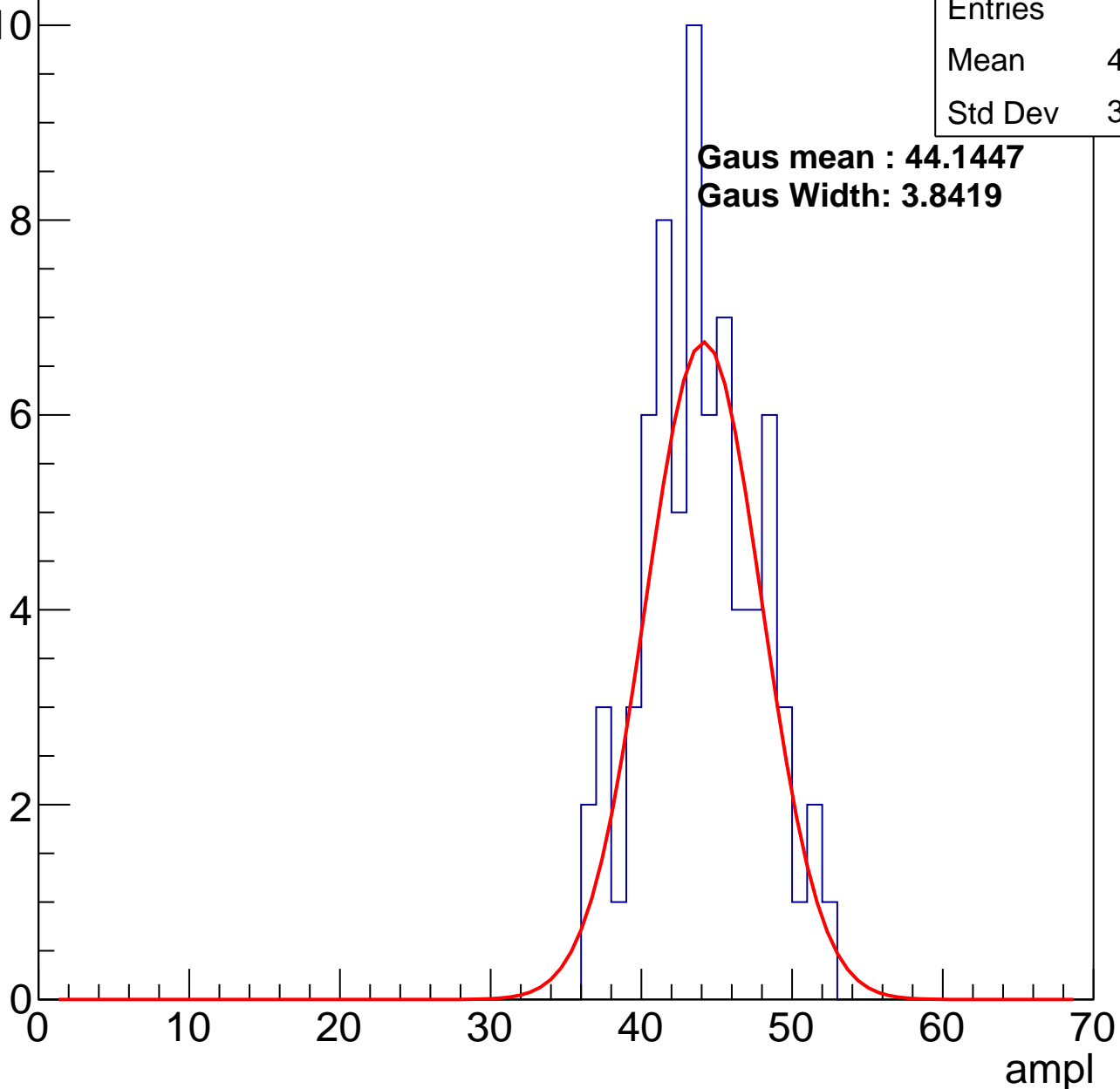
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43.56
Std Dev	3.723

Gaus mean : 44.1447

Gaus Width: 3.8419

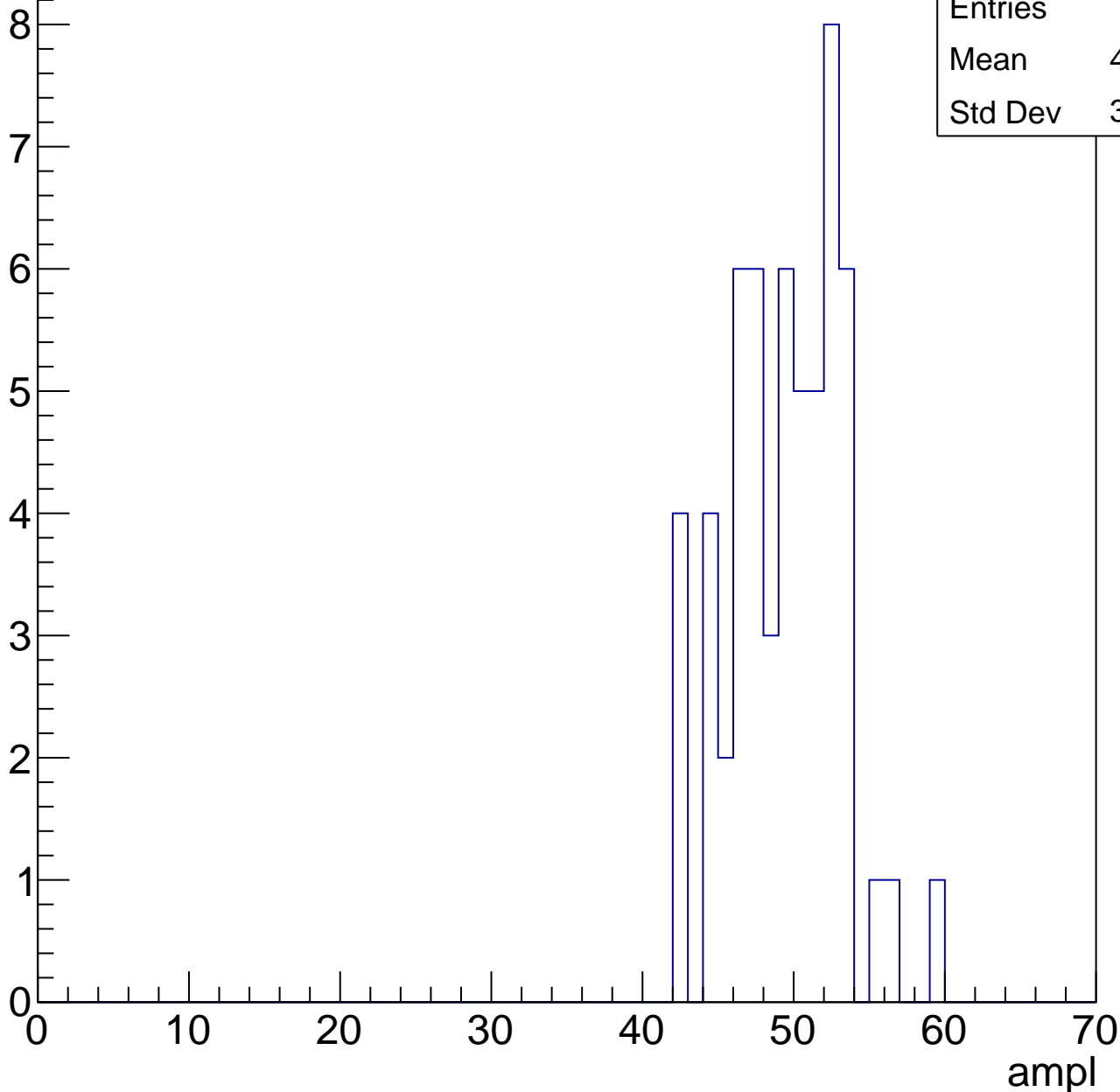


B1L103S, U10-ch36, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	48.95
Std Dev	3.683



B1L103S, U10-ch36, adc4

calib_packv5_042523_0143.root, FC#7, port C2

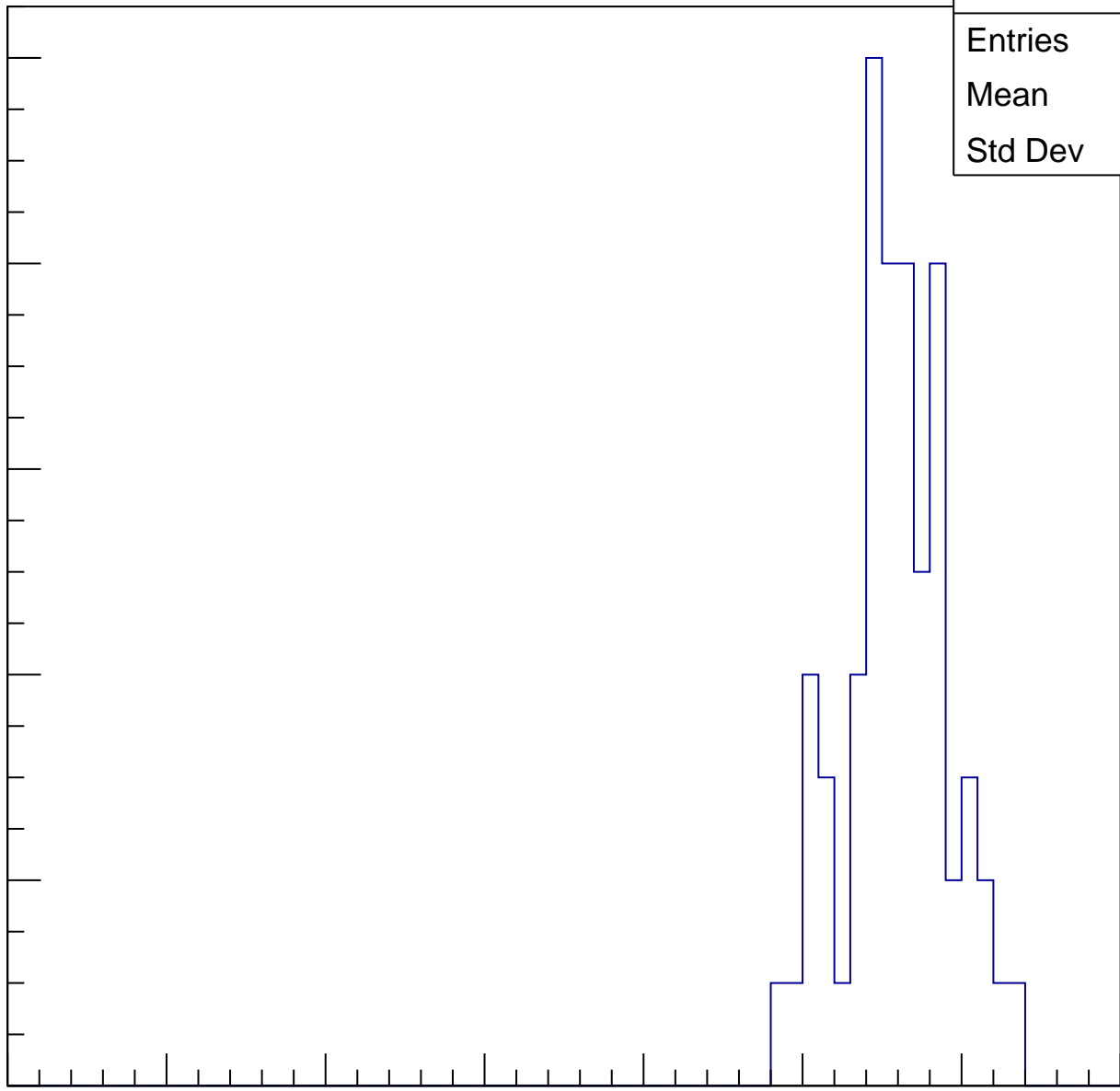
Entries	62
Mean	55.42
Std Dev	3.236

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

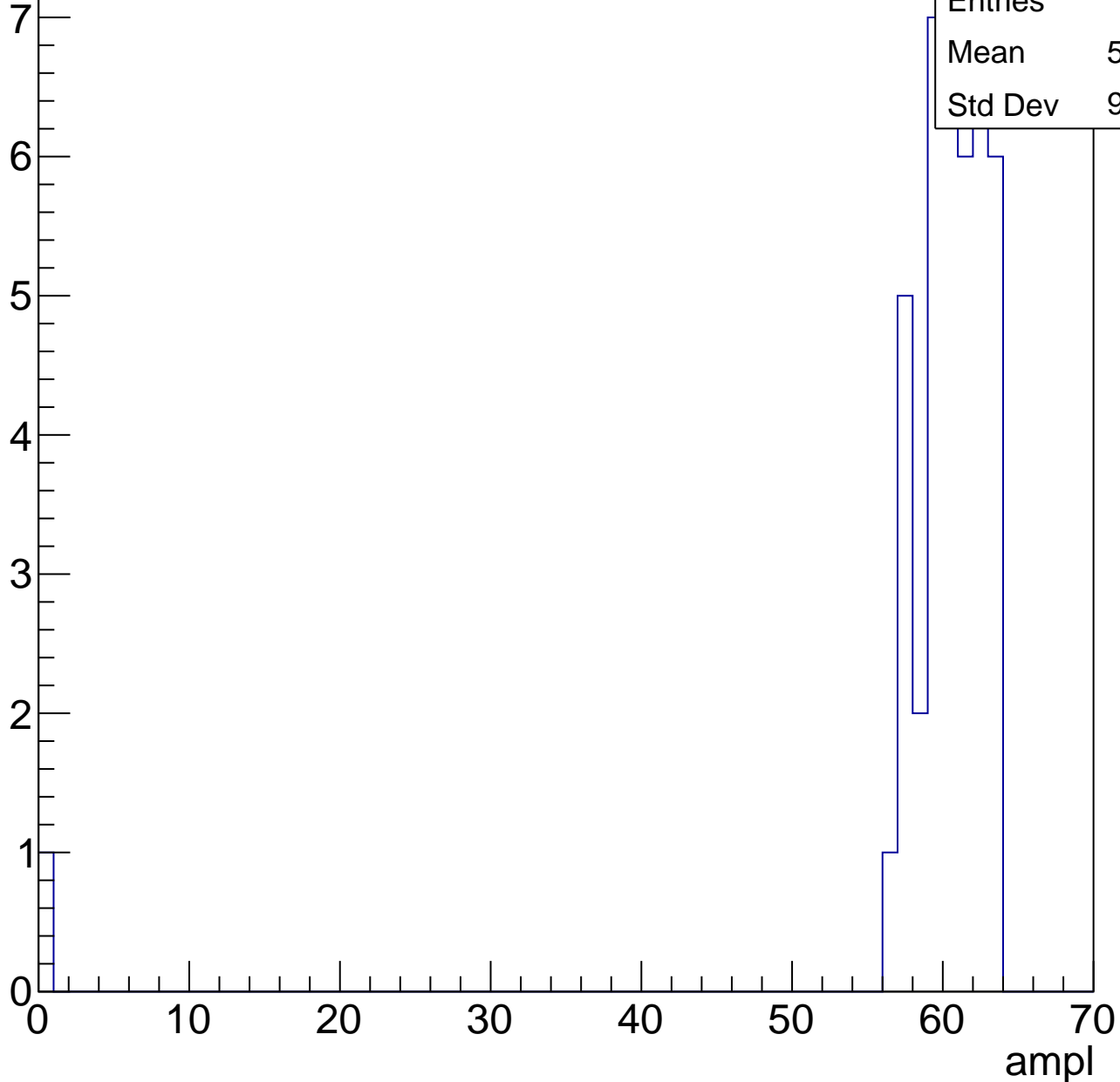
ampl



B1L103S, U10-ch36, adc5

calib_packv5_042523_0143.root, FC#7, port C2

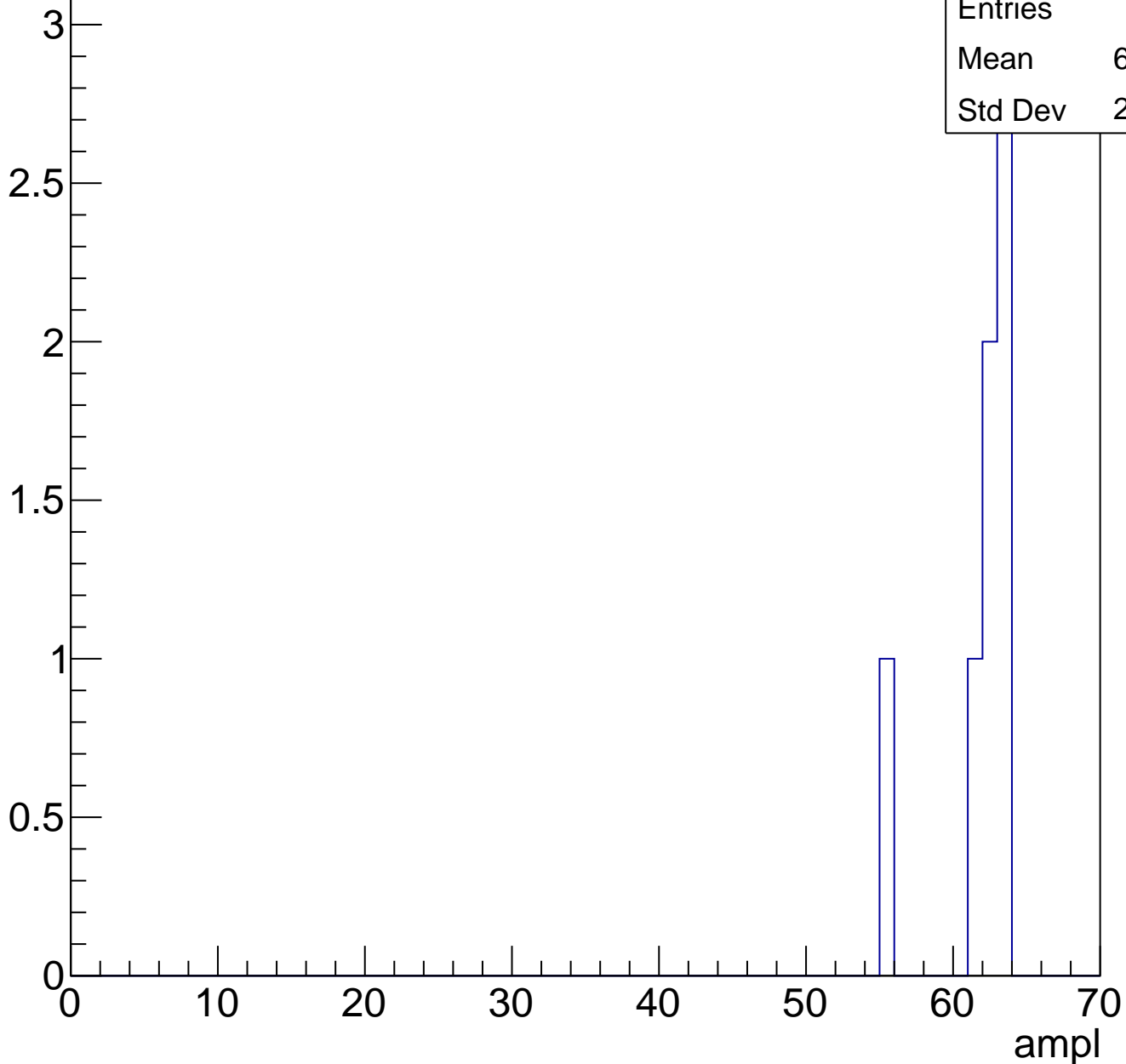
Entry



B1L103S, U10-ch36, adc6

calib_packv5_042523_0143.root, FC#7, port C2

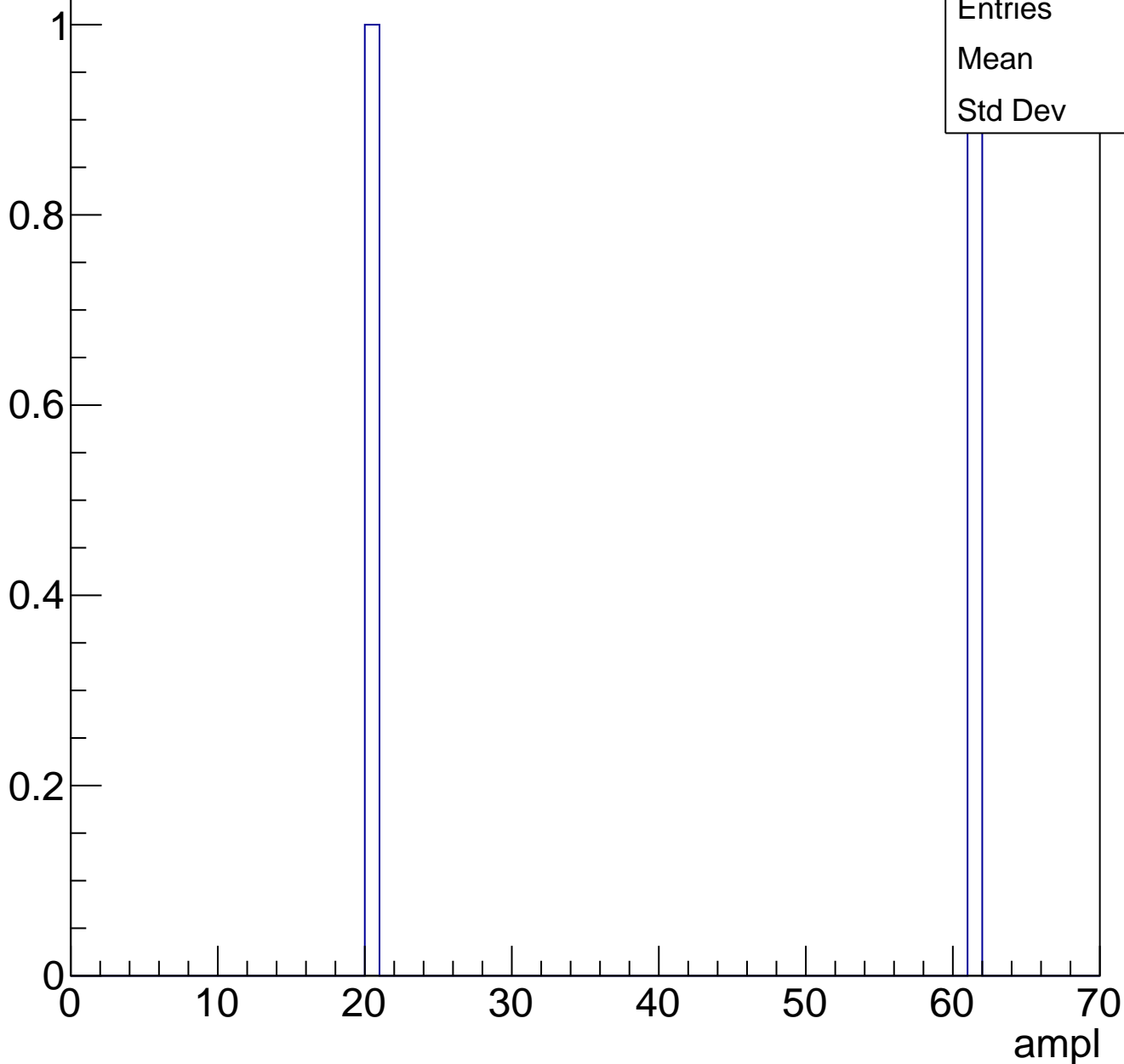
Entry



B1L103S, U10-ch36, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch37, adc0

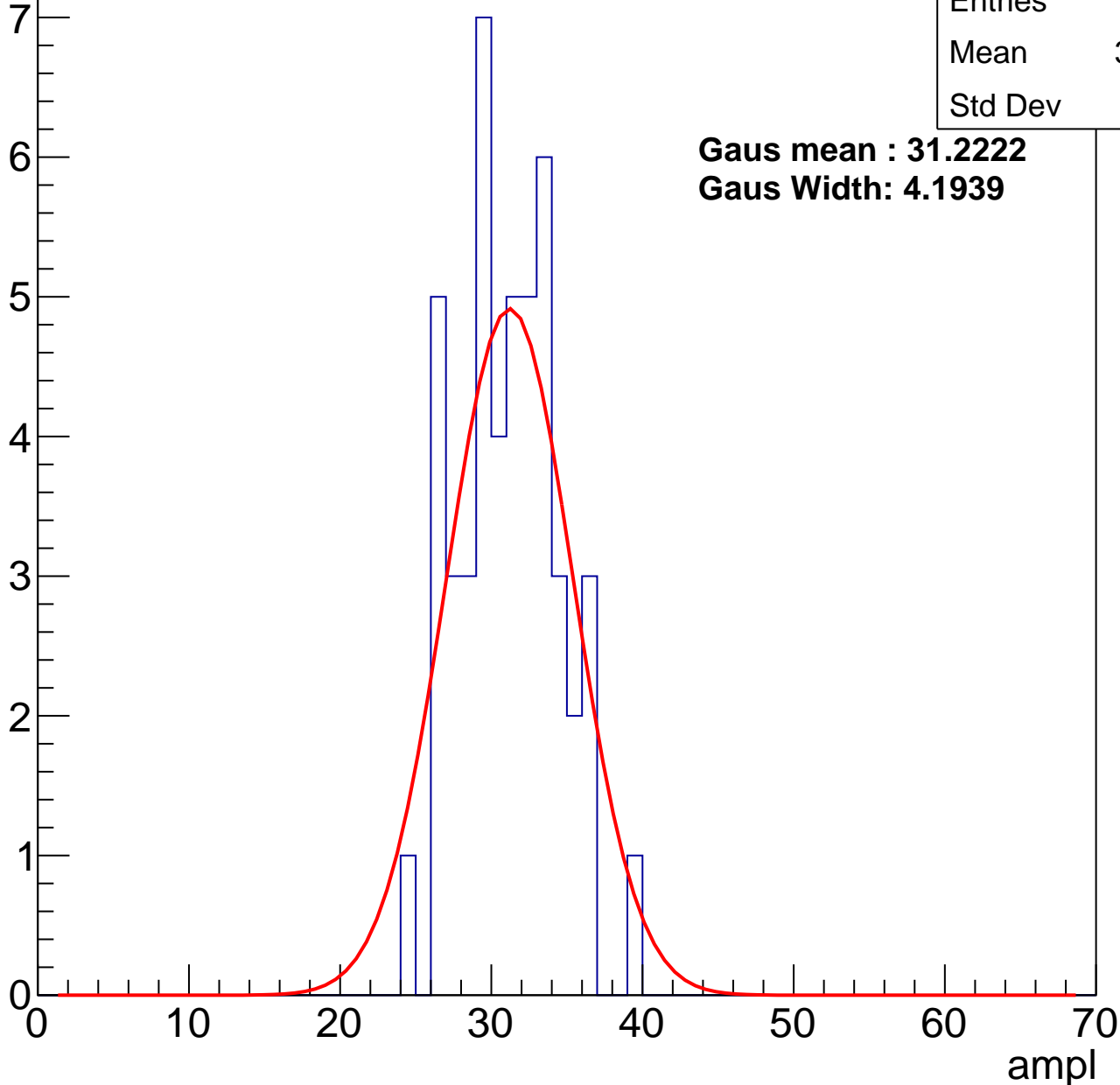
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	30.71
Std Dev	3.24

Gaus mean : 31.2222

Gaus Width: 4.1939



B1L103S, U10-ch37, adc1

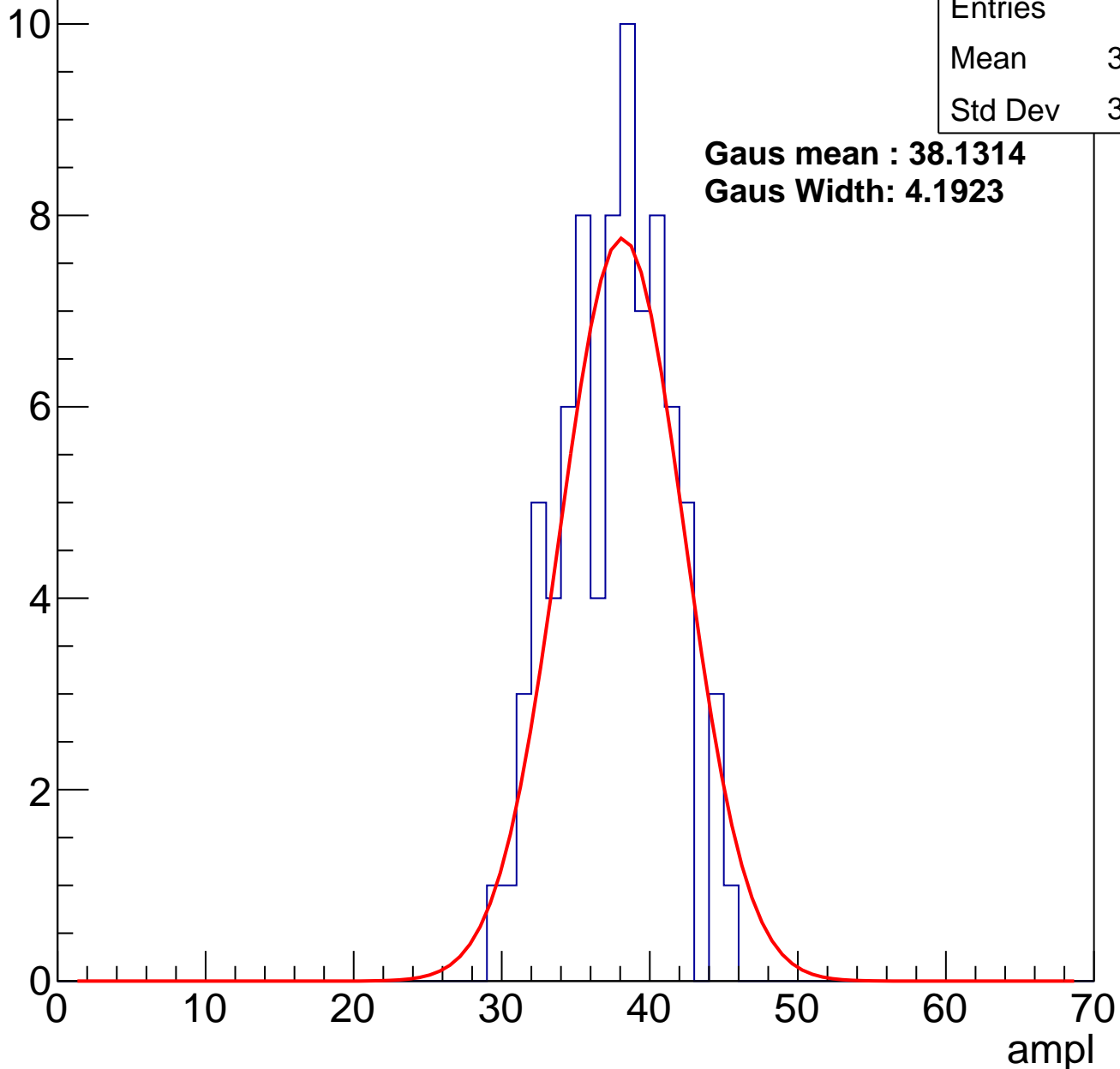
calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	37.17
Std Dev	3.605

Gaus mean : 38.1314

Gaus Width: 4.1923

Entry



B1L103S, U10-ch37, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	59
Mean	43.85
Std Dev	3.579

Gaus mean : 45.3582

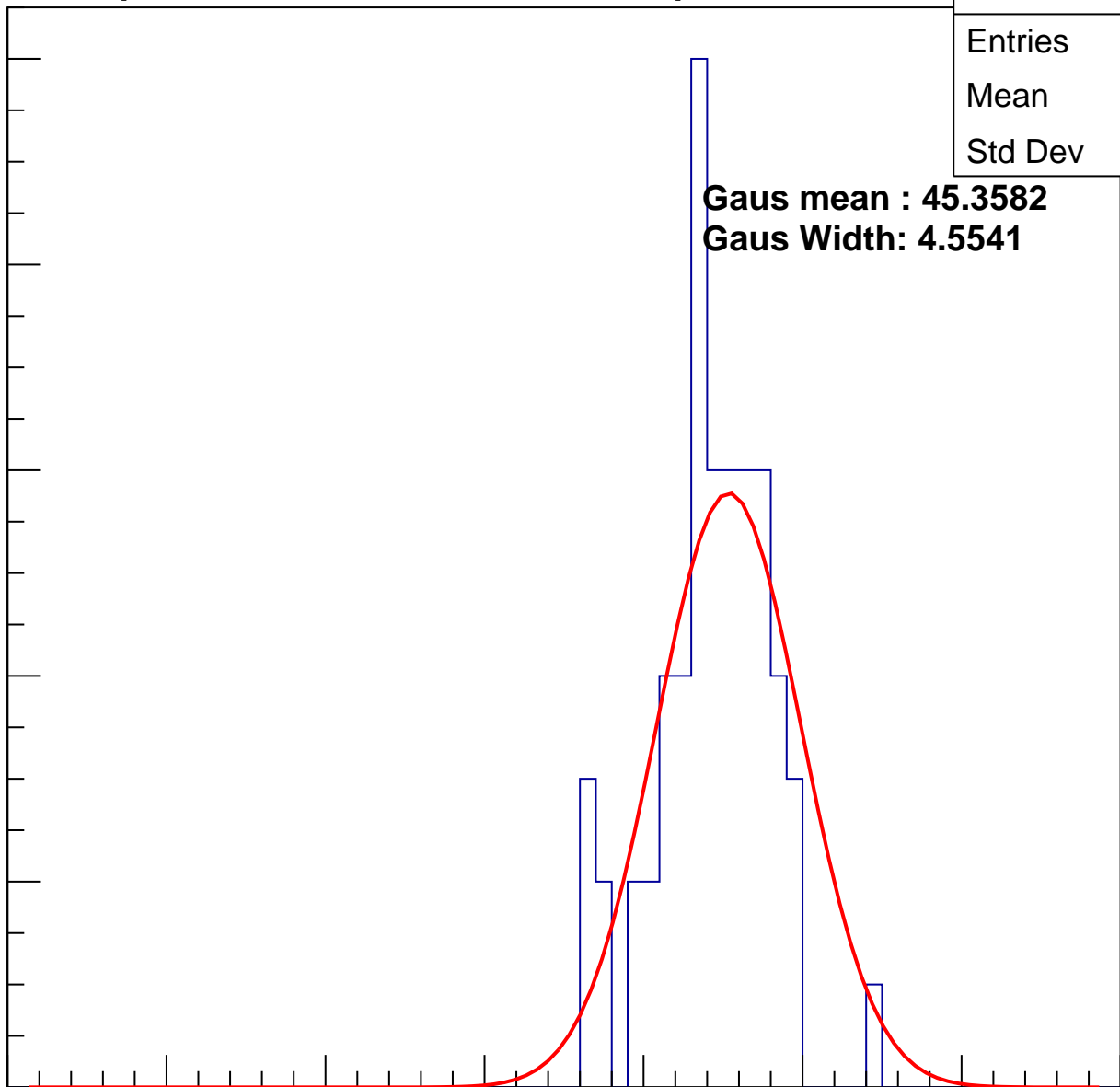
Gaus Width: 4.5541

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

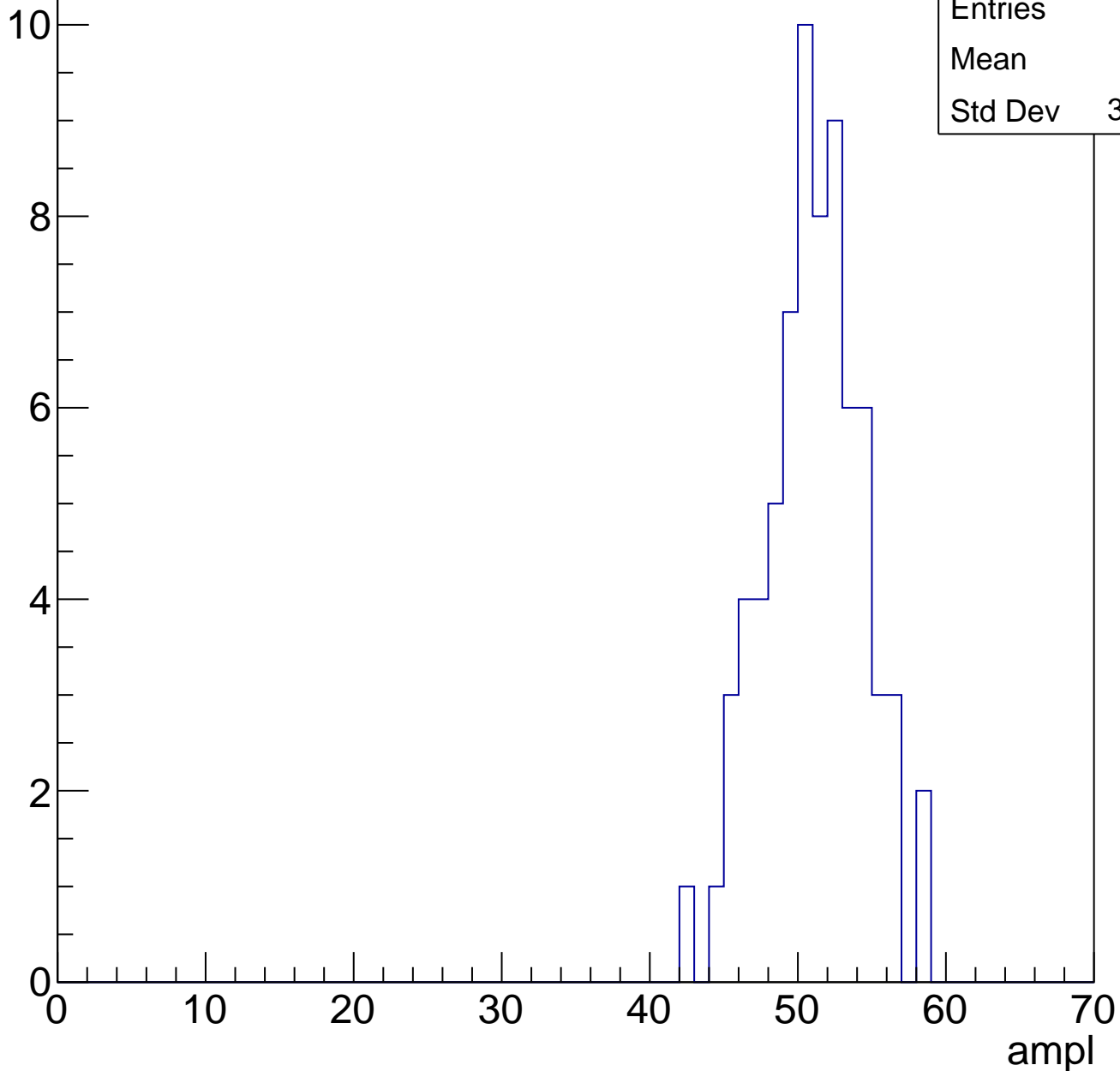


B1L103S, U10-ch37, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	50.6
Std Dev	3.294

Entry

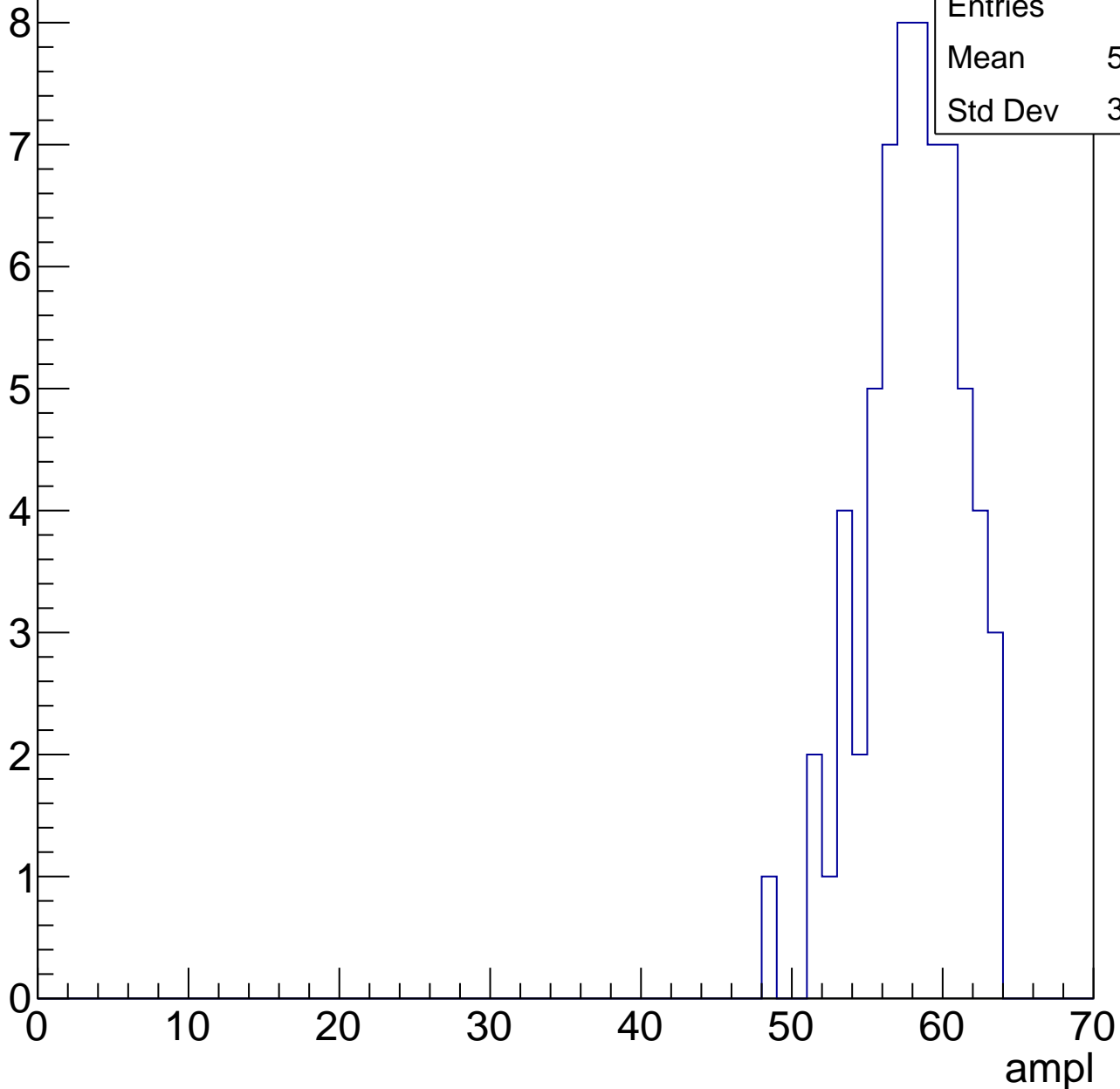


B1L103S, U10-ch37, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	57.56
Std Dev	3.196

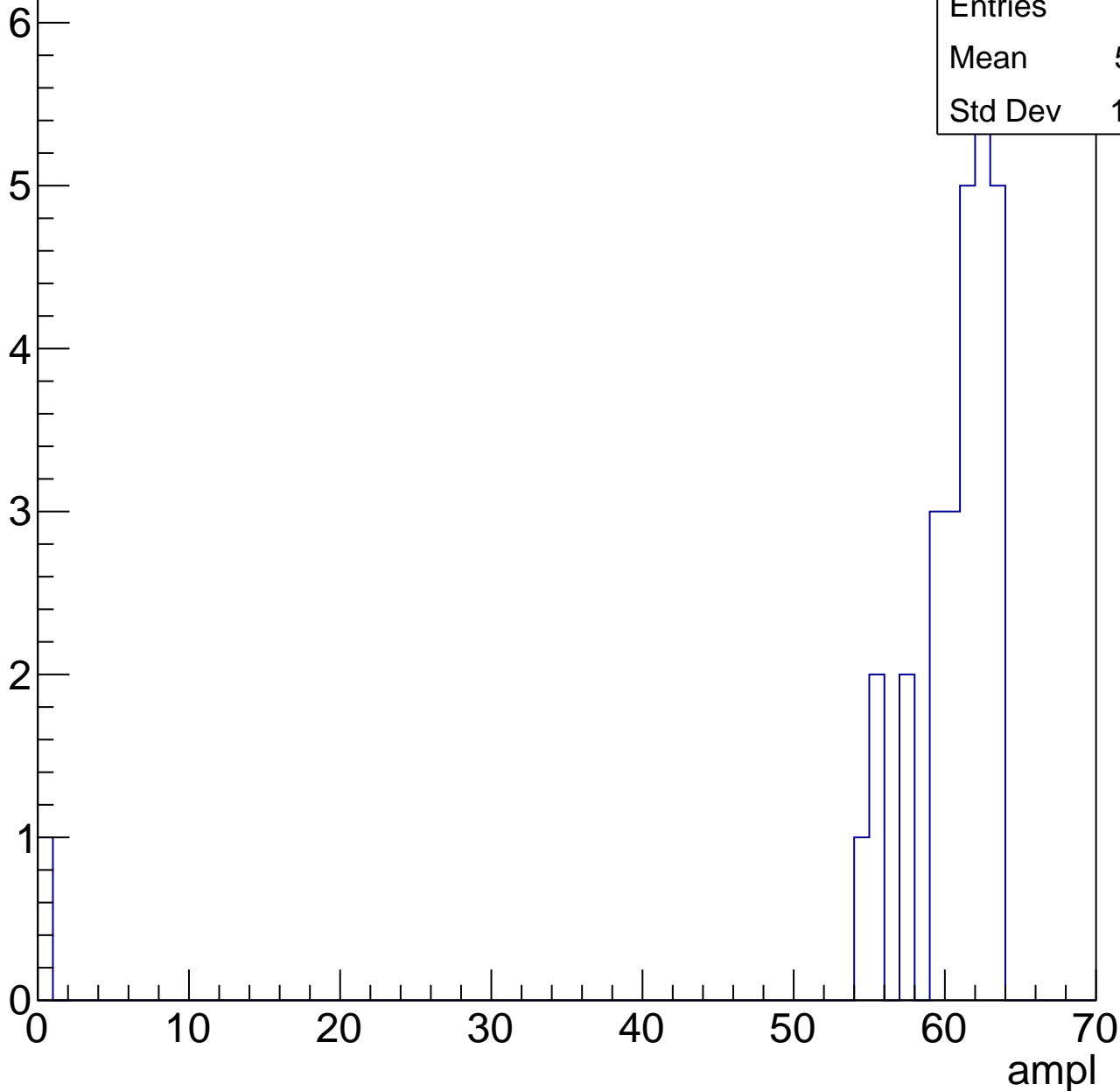


B1L103S, U10-ch37, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	58.11
Std Dev	11.47



B1L103S, U10-ch37, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch37, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L103S, U10-ch38, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	29.87
Std Dev	4.834

Gaus mean : 30.9110

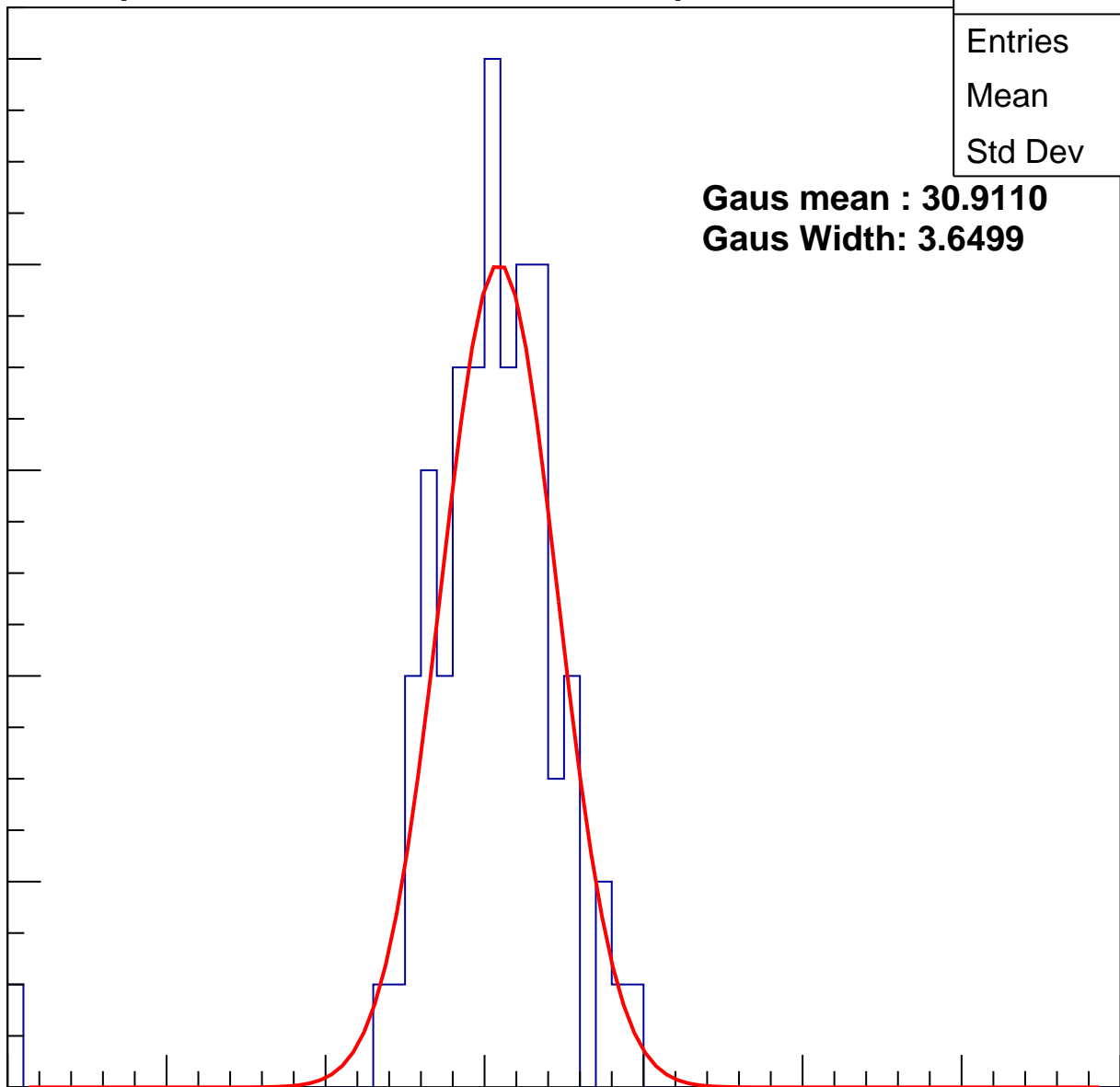
Gaus Width: 3.6499

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch38, adc1

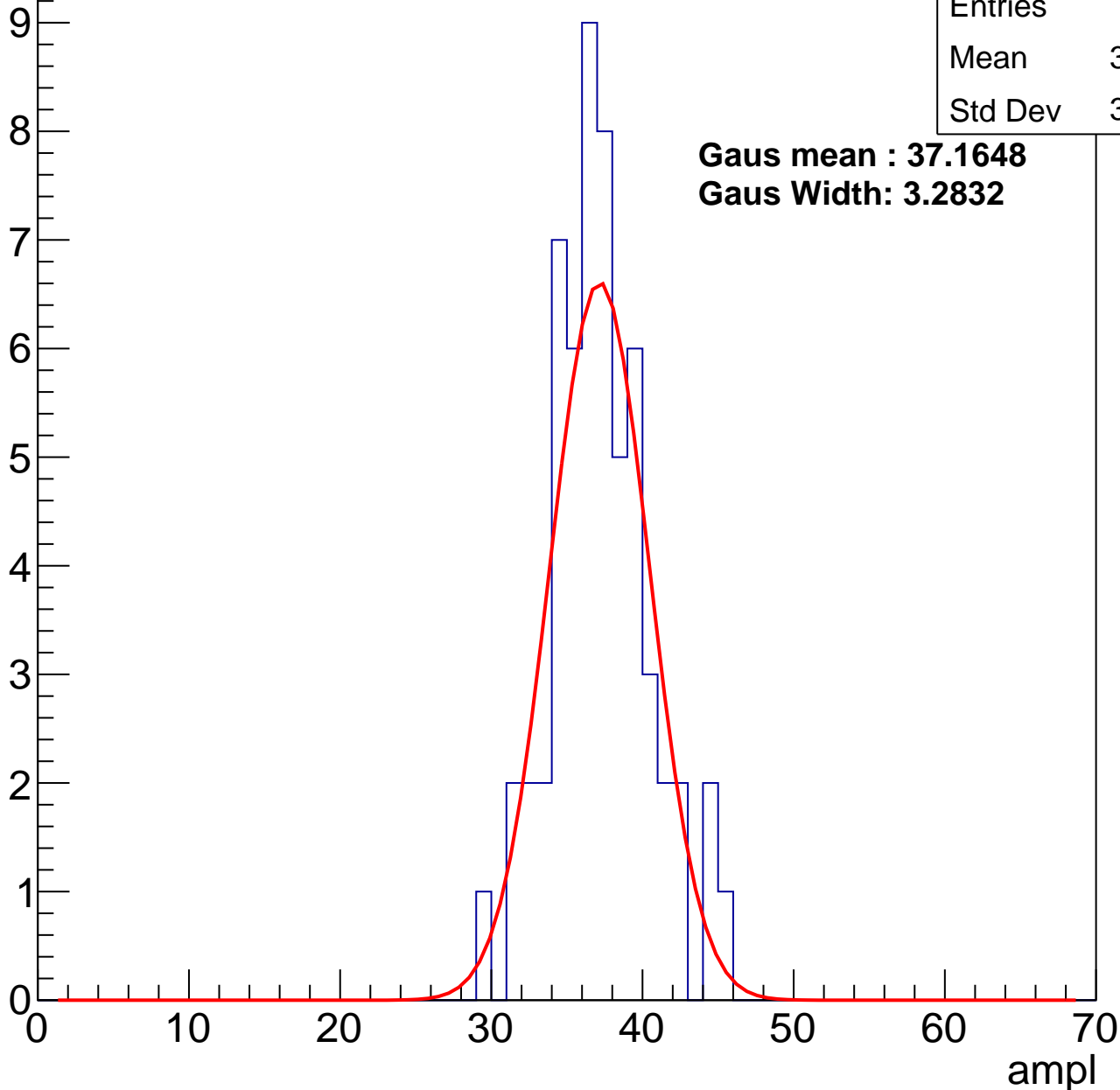
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.76
Std Dev	3.245

Gaus mean : 37.1648

Gaus Width: 3.2832



B1L103S, U10-ch38, adc2

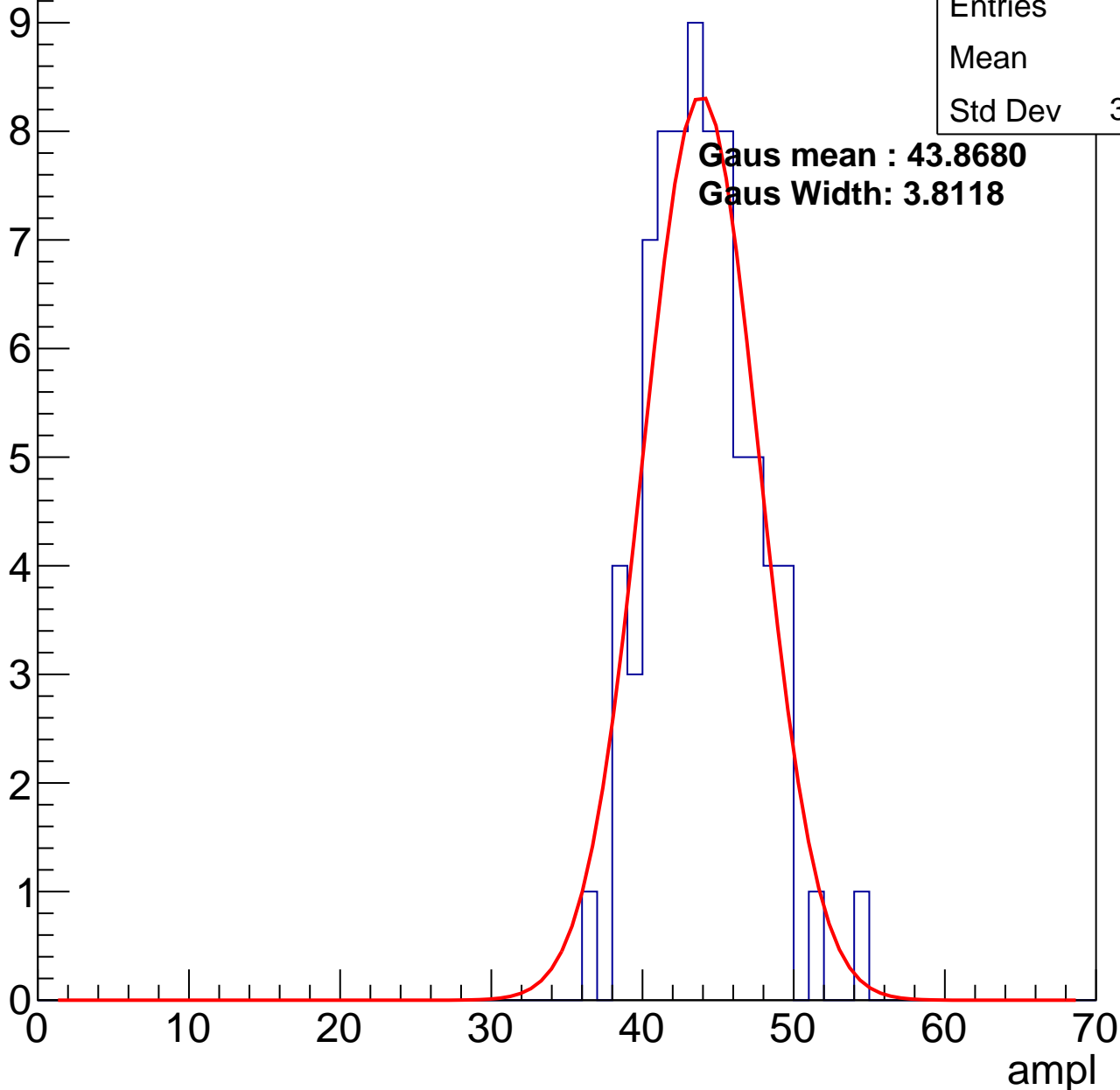
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.5
Std Dev	3.393

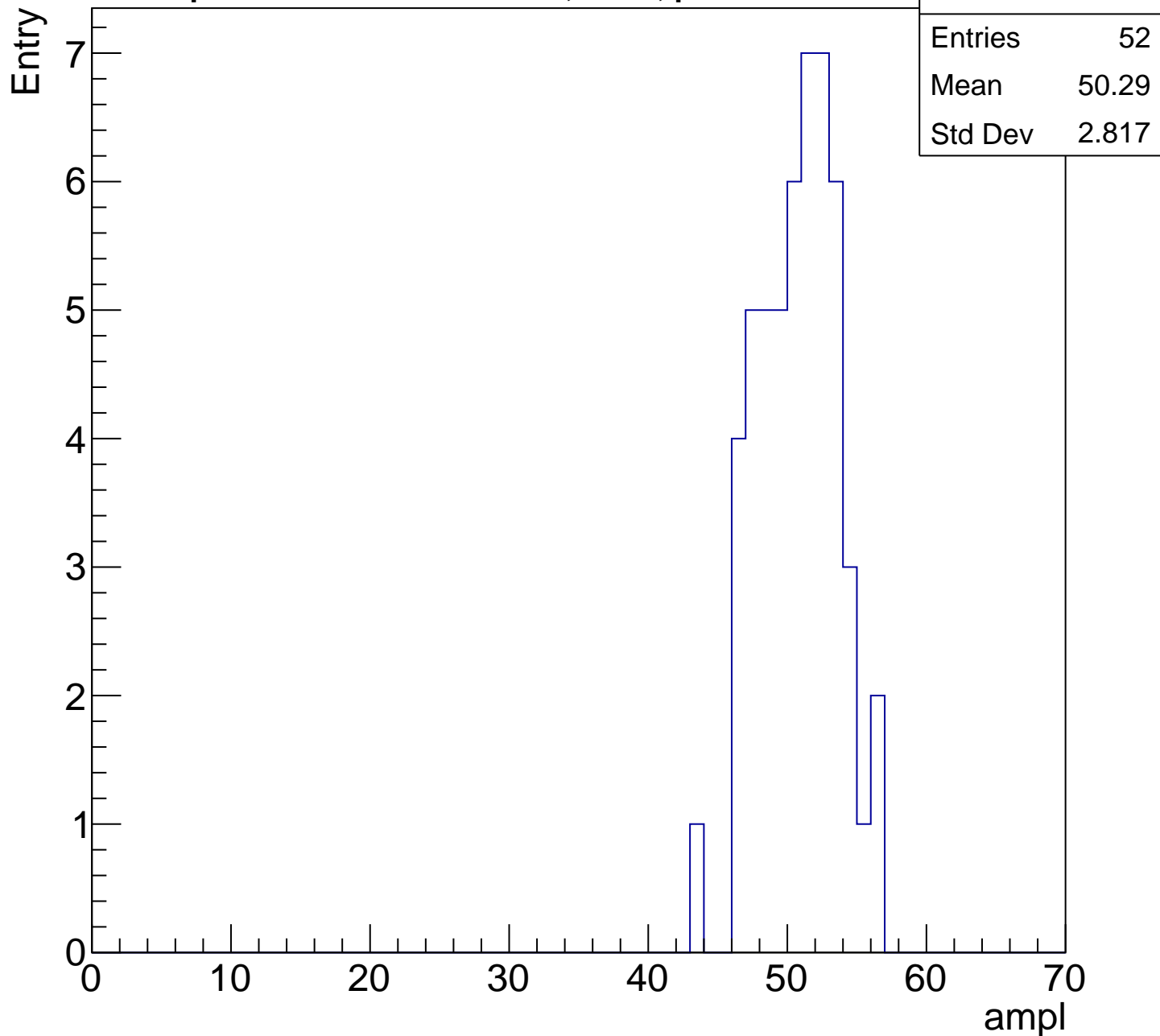
Gaus mean : 43.8680

Gaus Width: 3.8118



B1L103S, U10-ch38, adc3

calib_packv5_042523_0143.root, FC#7, port C2

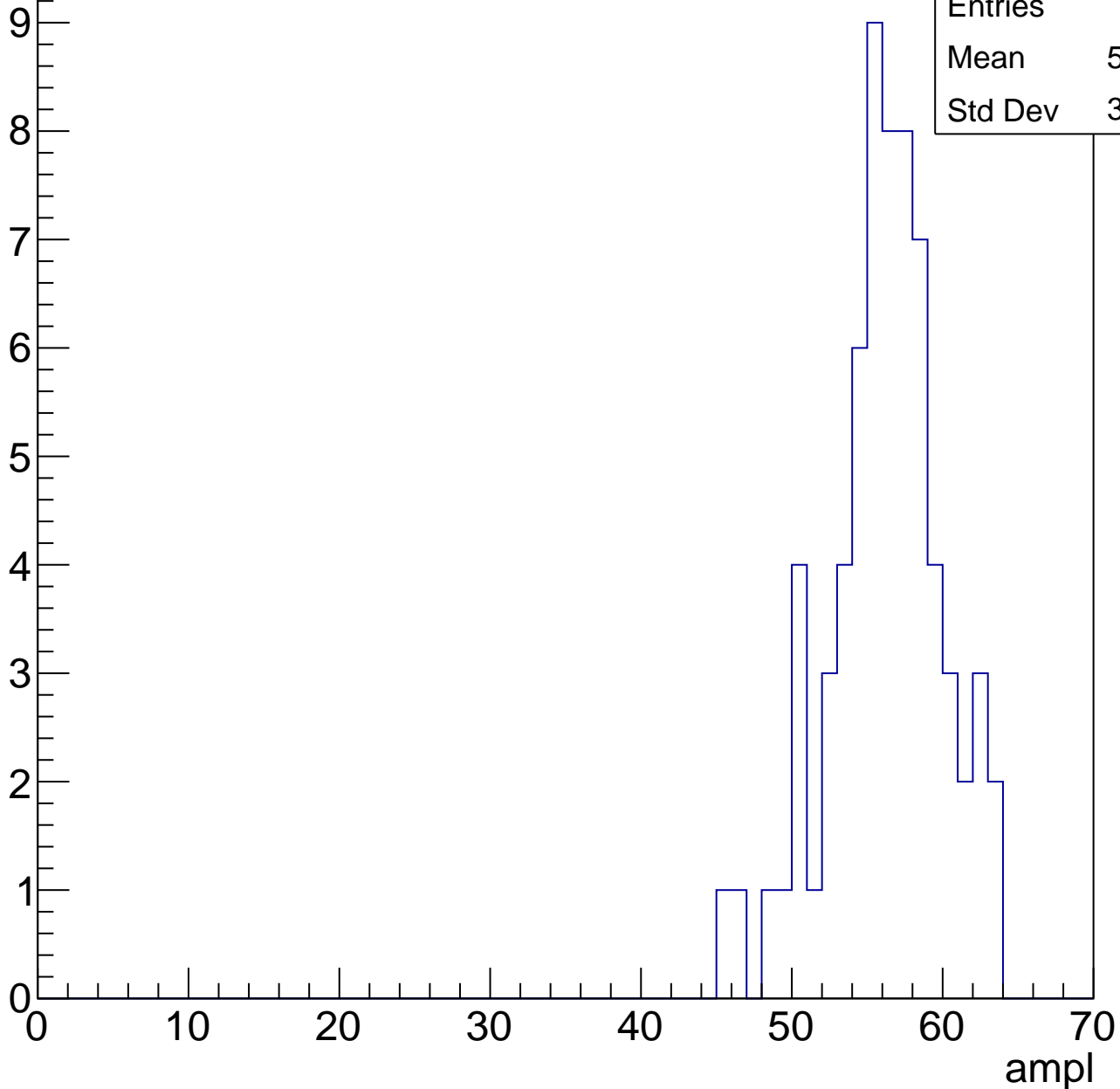


B1L103S, U10-ch38, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.68
Std Dev	3.813

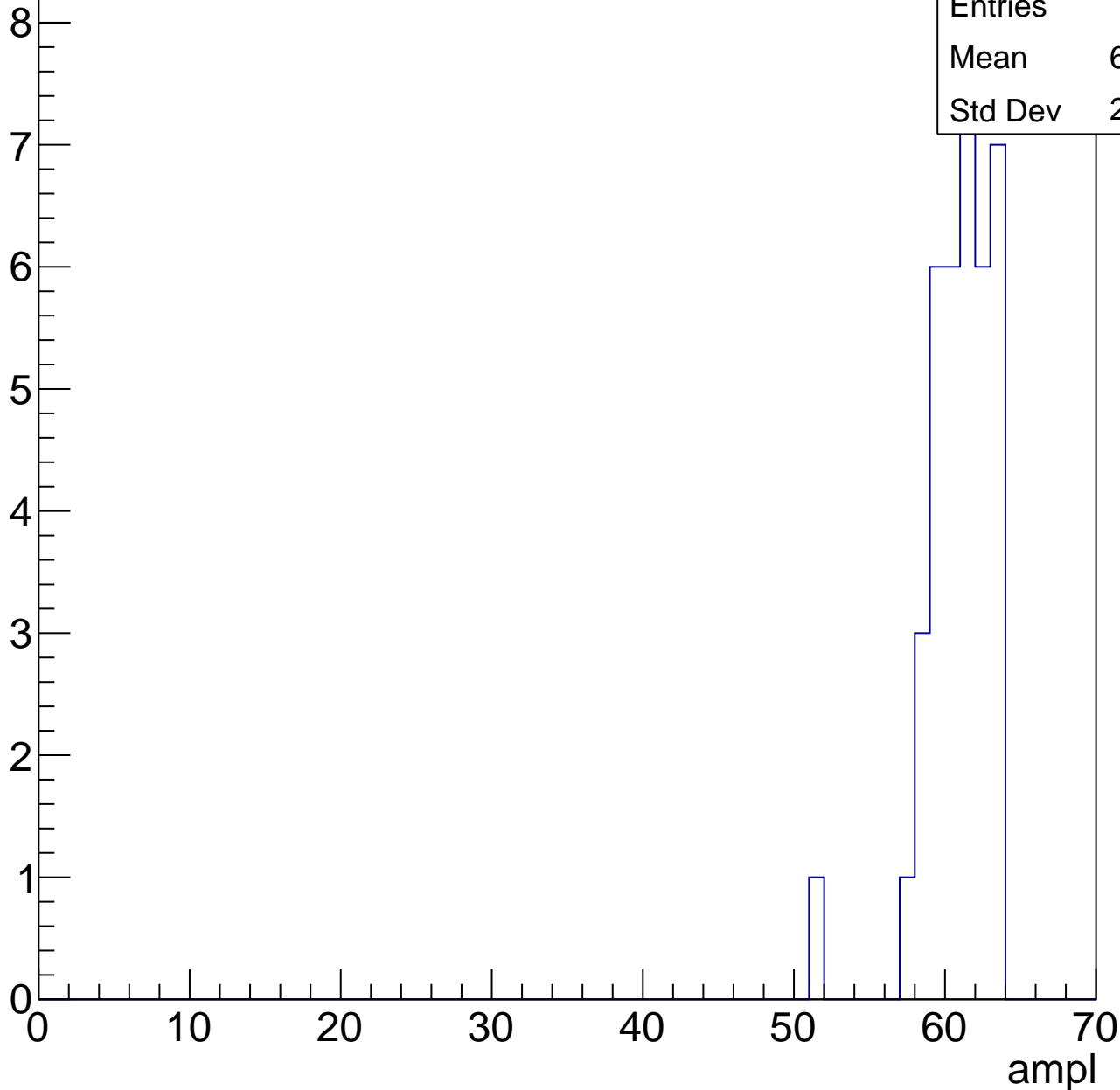


B1L103S, U10-ch38, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

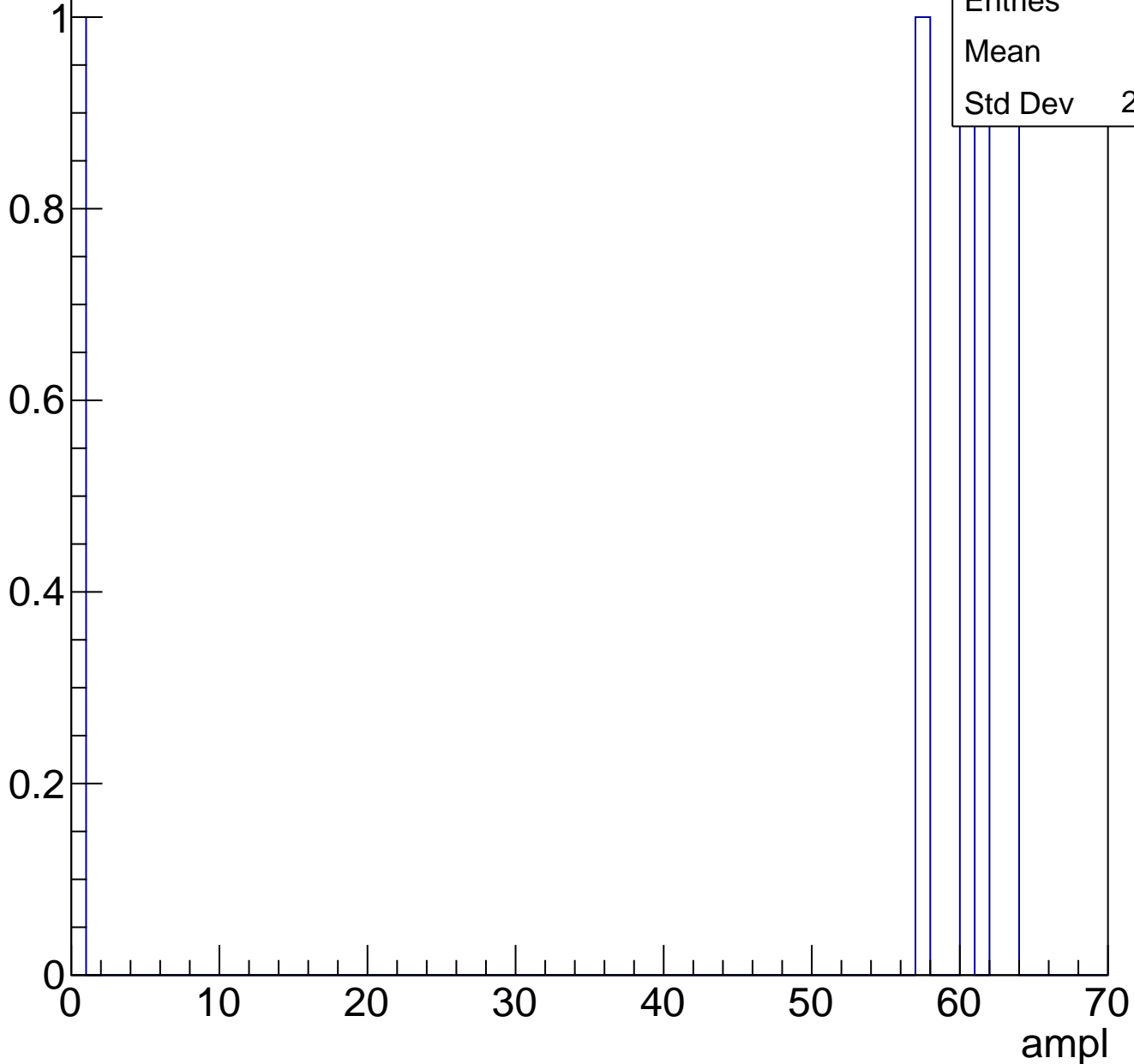
Entries	38
Mean	60.45
Std Dev	2.268



B1L103S, U10-ch38, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch38, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B1L103S, U10-ch39, adc0

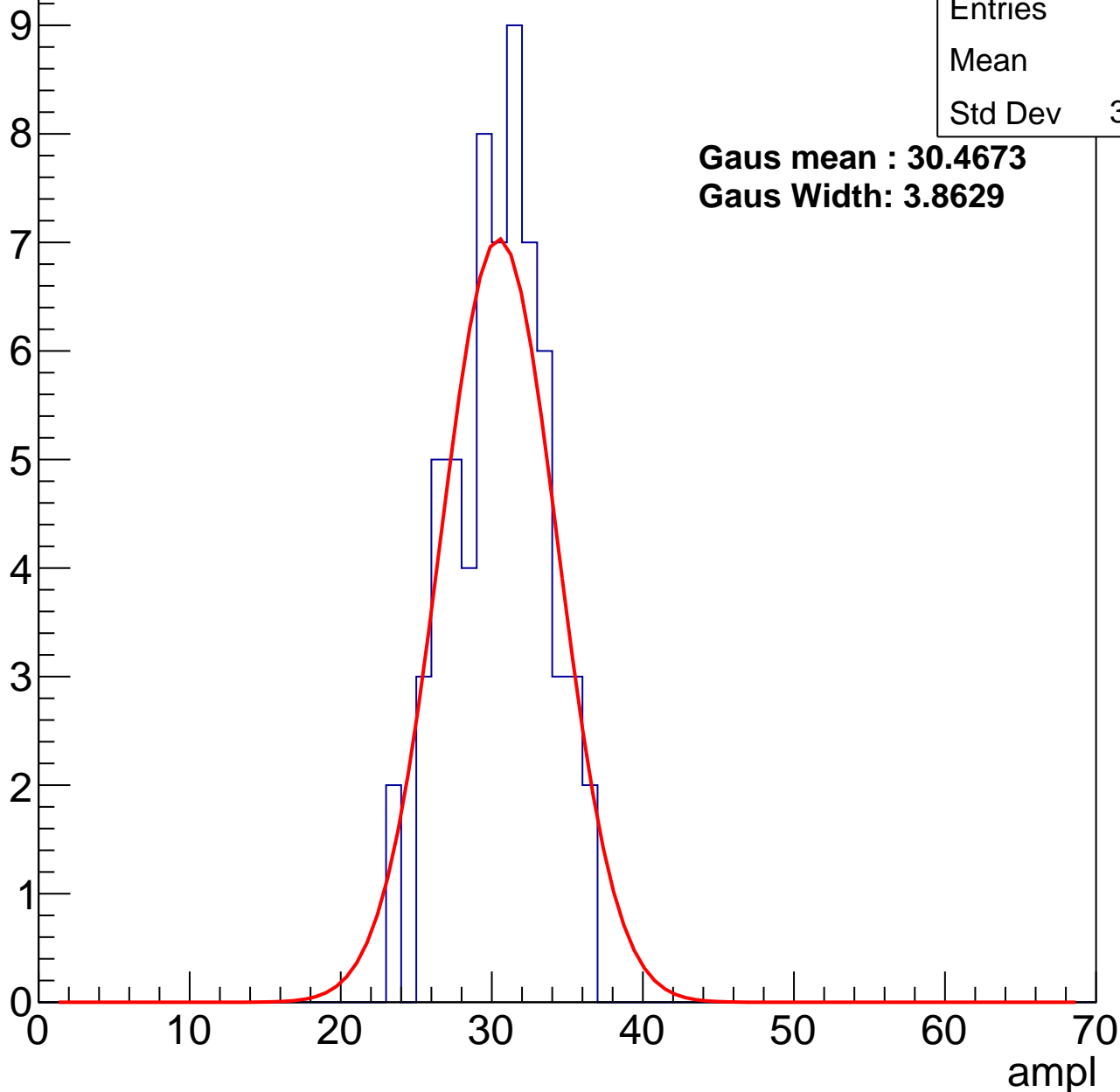
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	30
Std Dev	3.082

Gaus mean : 30.4673

Gaus Width: 3.8629



B1L103S, U10-ch39, adc1

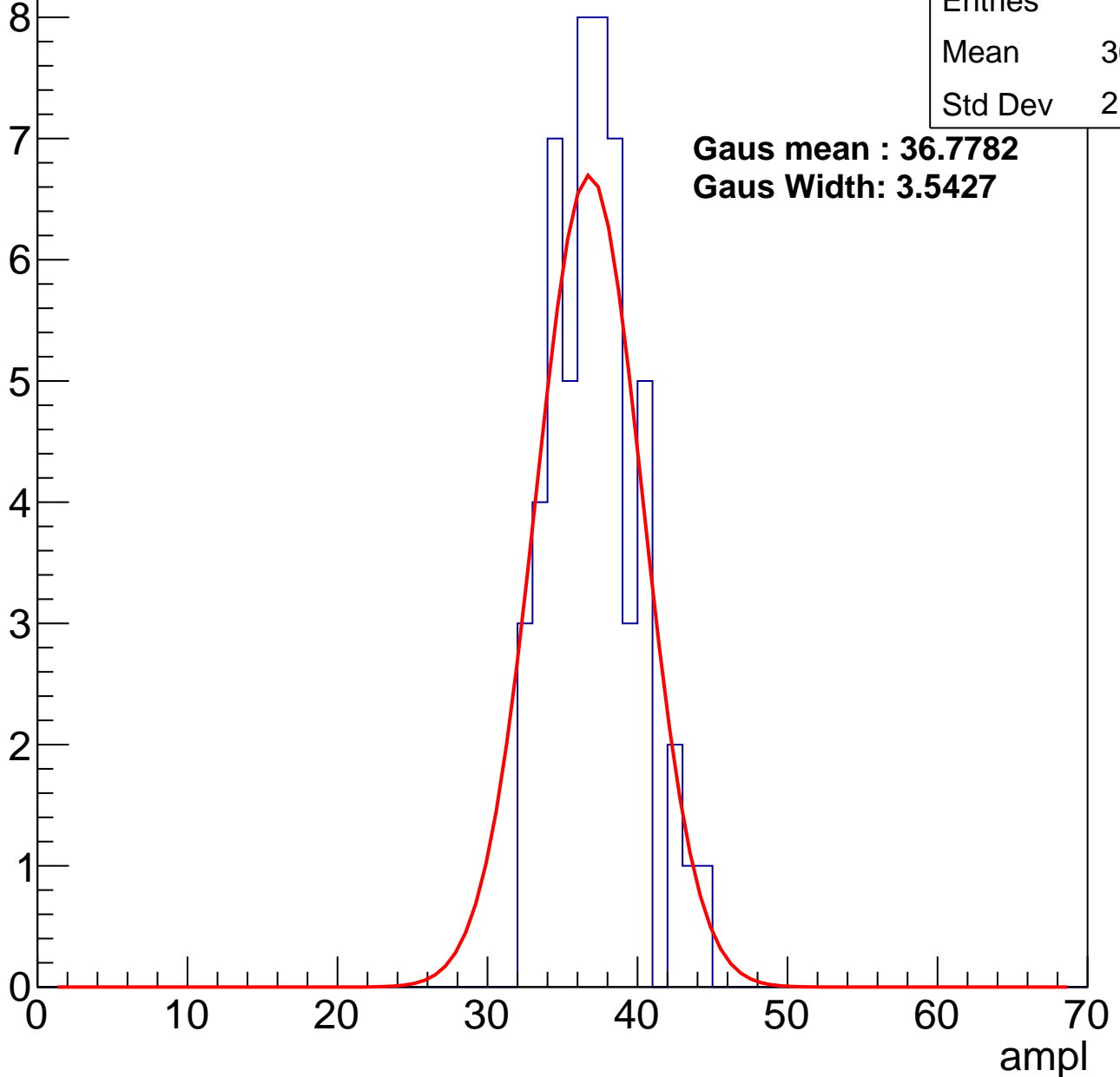
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.65
Std Dev	2.797

Gaus mean : 36.7782

Gaus Width: 3.5427



B1L103S, U10-ch39, adc2

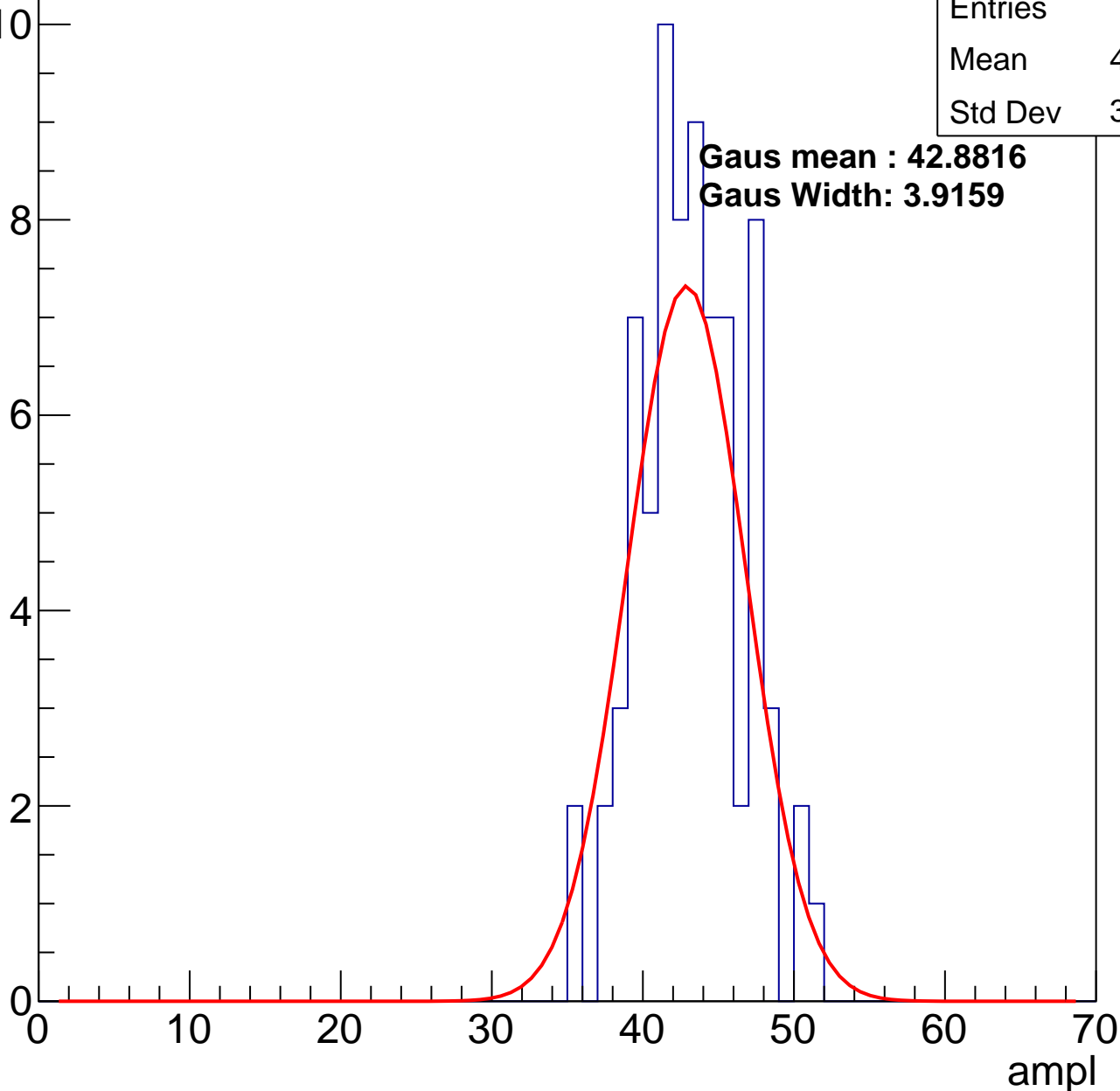
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	42.76
Std Dev	3.429

Gaus mean : 42.8816

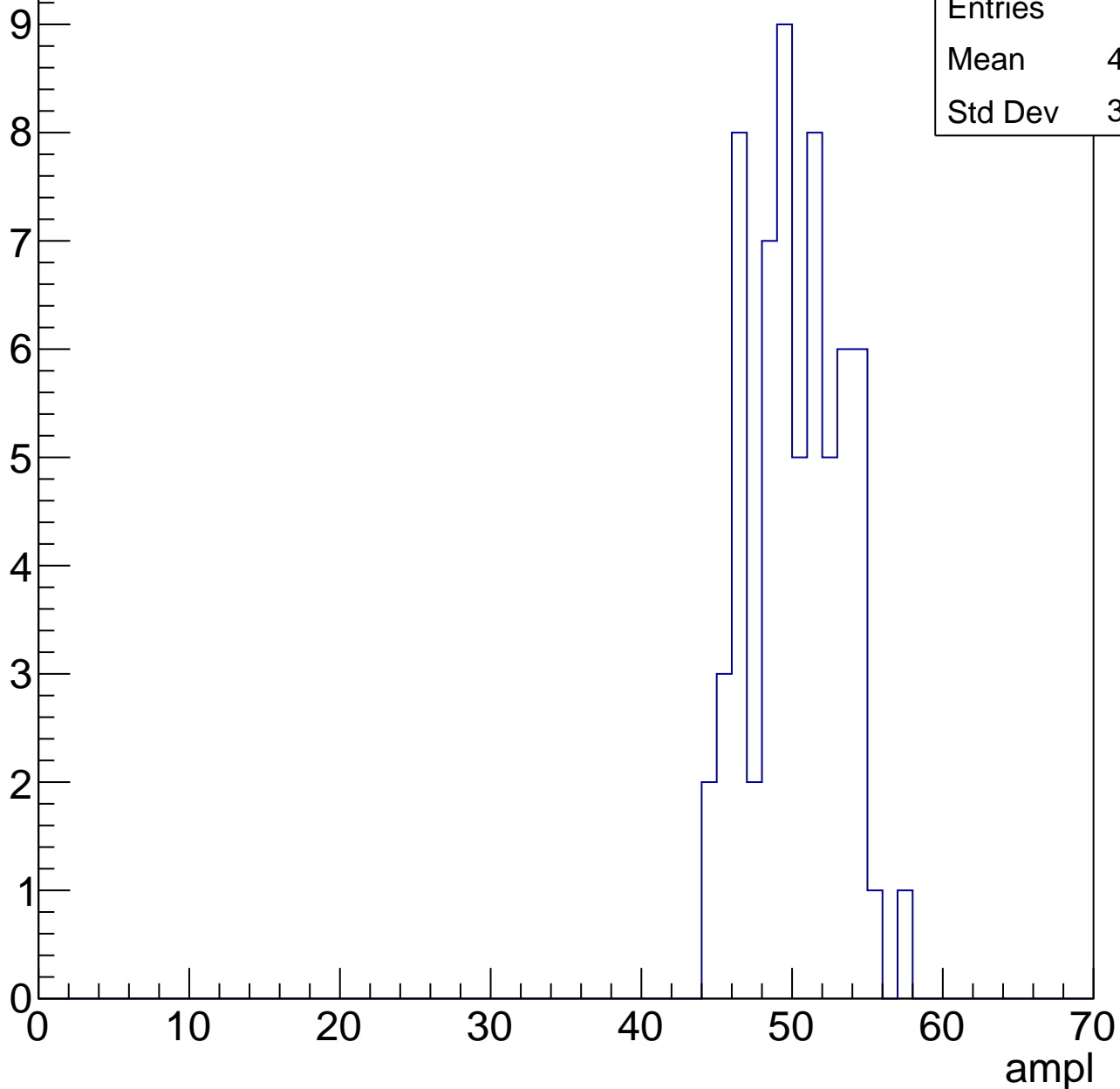
Gaus Width: 3.9159



B1L103S, U10-ch39, adc3

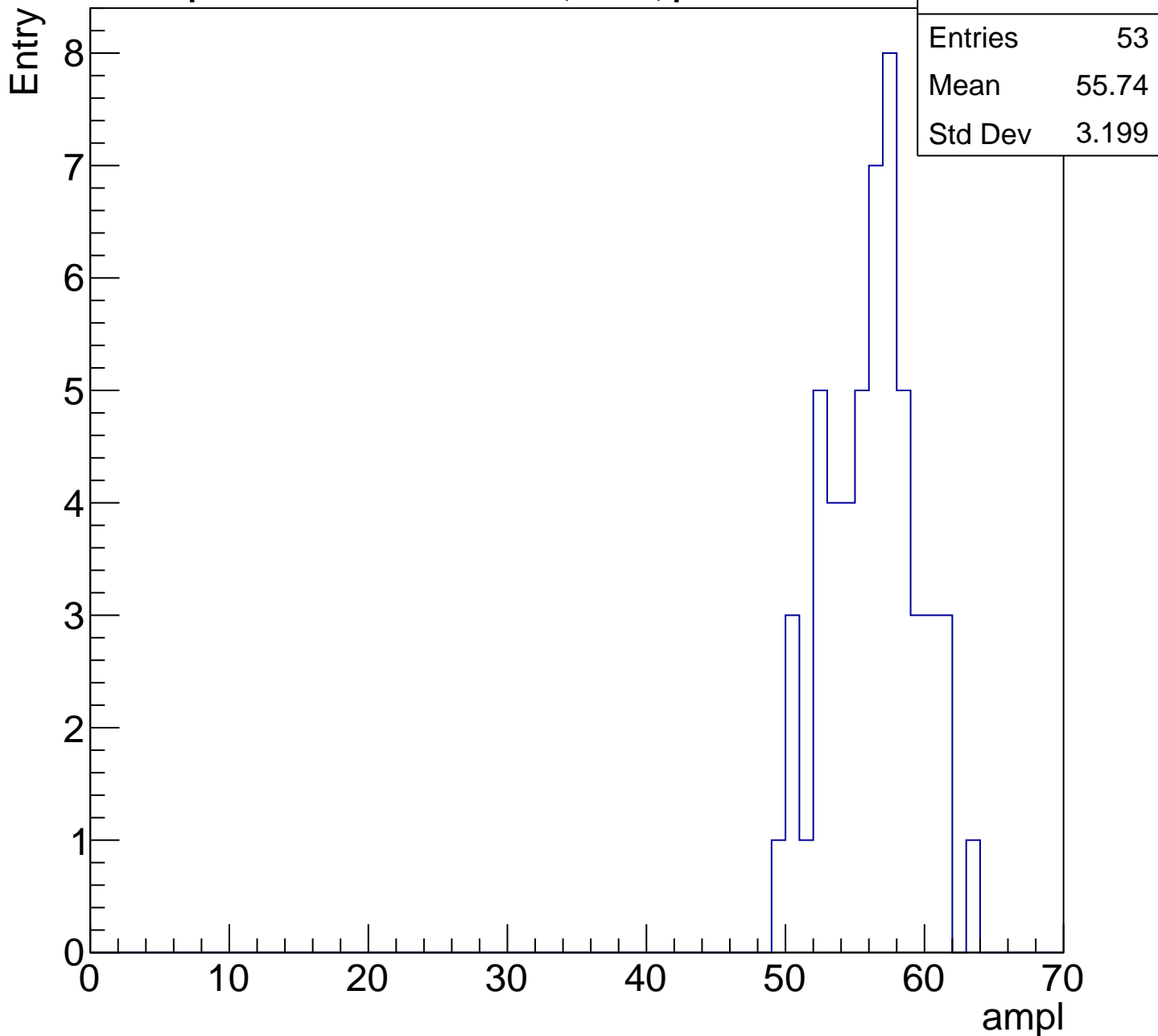
calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch39, adc4

calib_packv5_042523_0143.root, FC#7, port C2

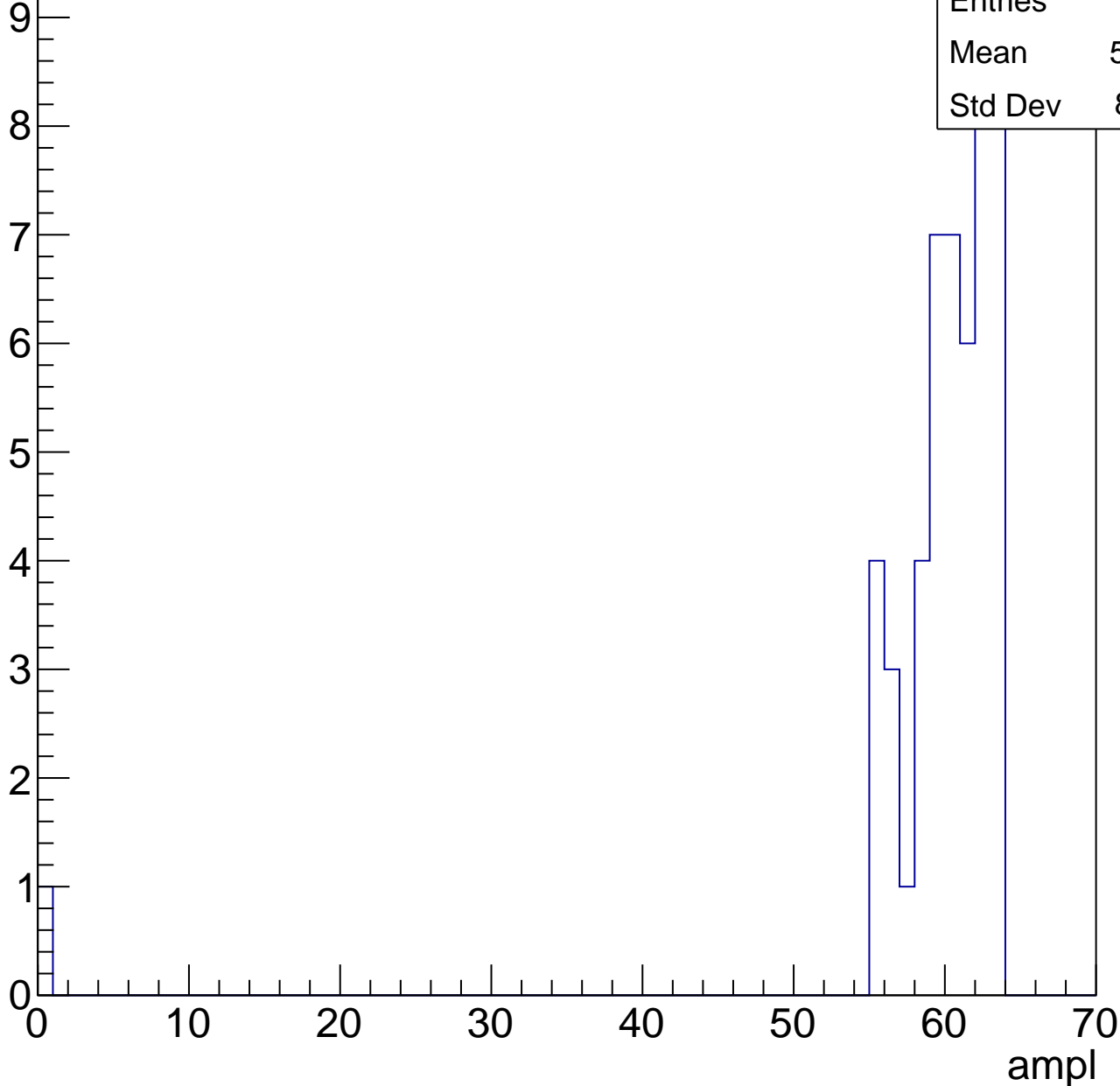


B1L103S, U10-ch39, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	58.84
Std Dev	8.671



B1L103S, U10-ch39, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

3

Mean

60.33

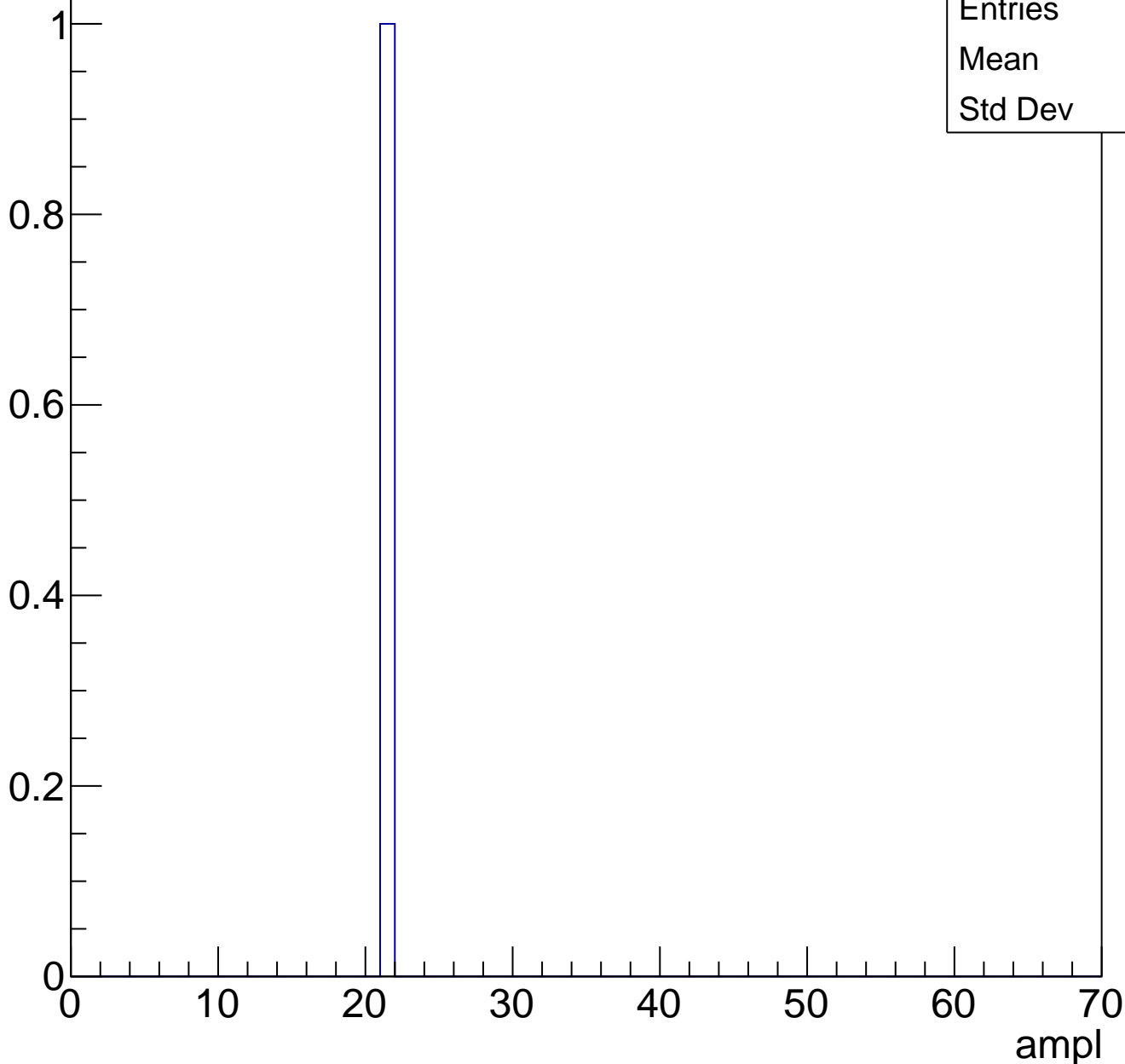
Std Dev

1.7

B1L103S, U10-ch39, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

B1L103S, U10-ch40, adc0

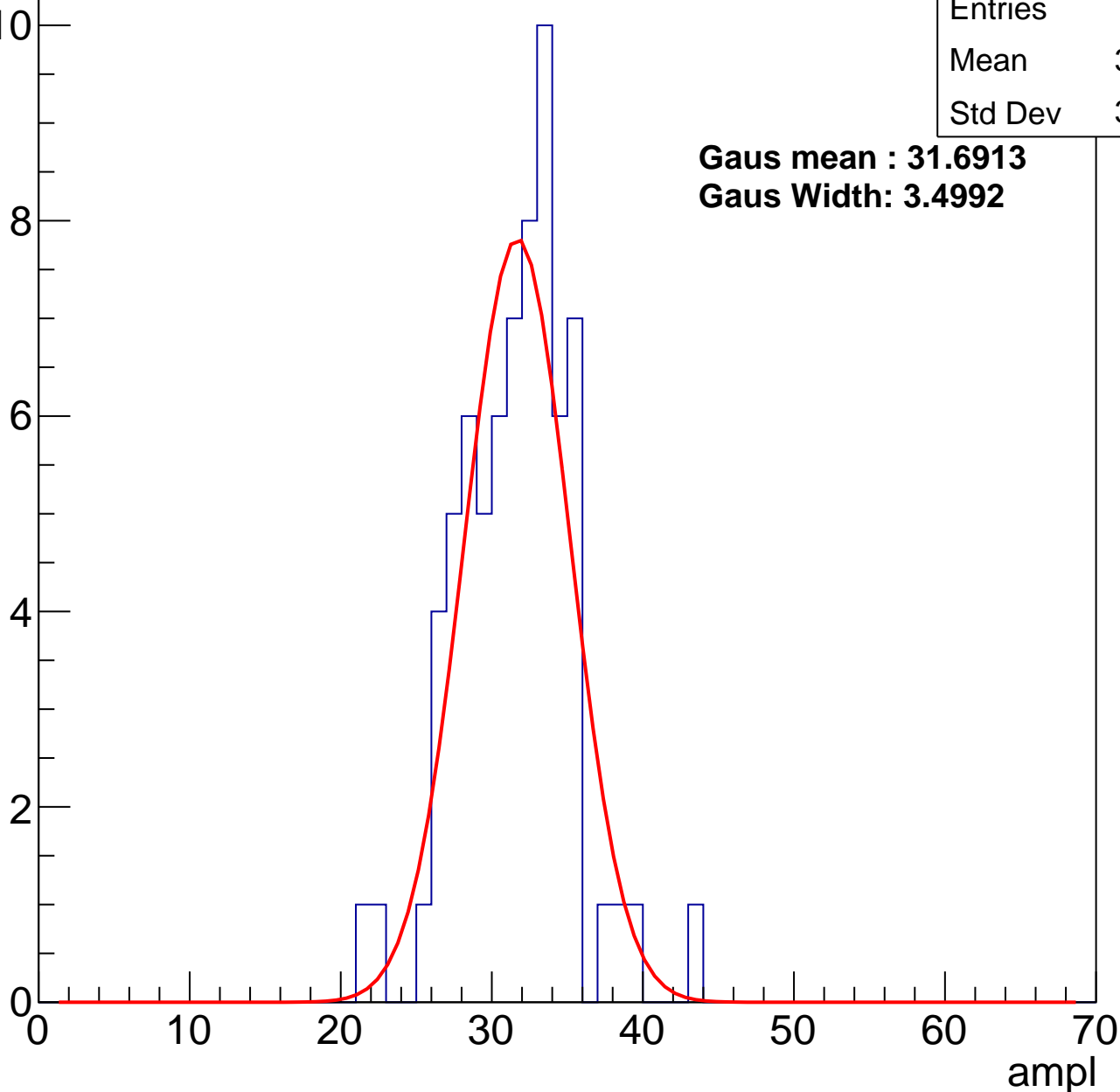
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	31.11
Std Dev	3.721

Gaus mean : 31.6913

Gaus Width: 3.4992



B1L103S, U10-ch40, adc1

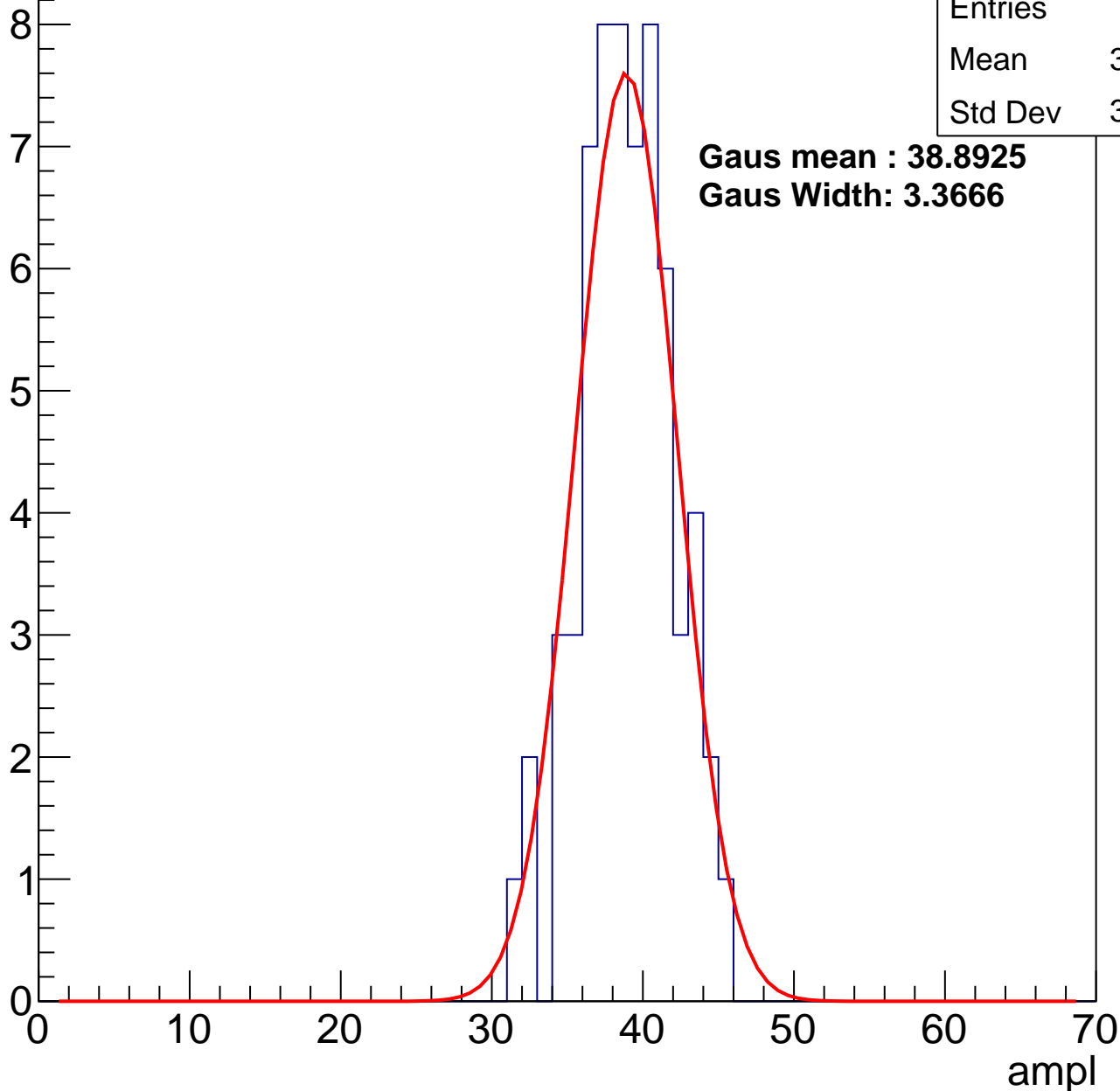
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	38.48
Std Dev	3.033

Gaus mean : 38.8925

Gaus Width: 3.3666



B1L103S, U10-ch40, adc2

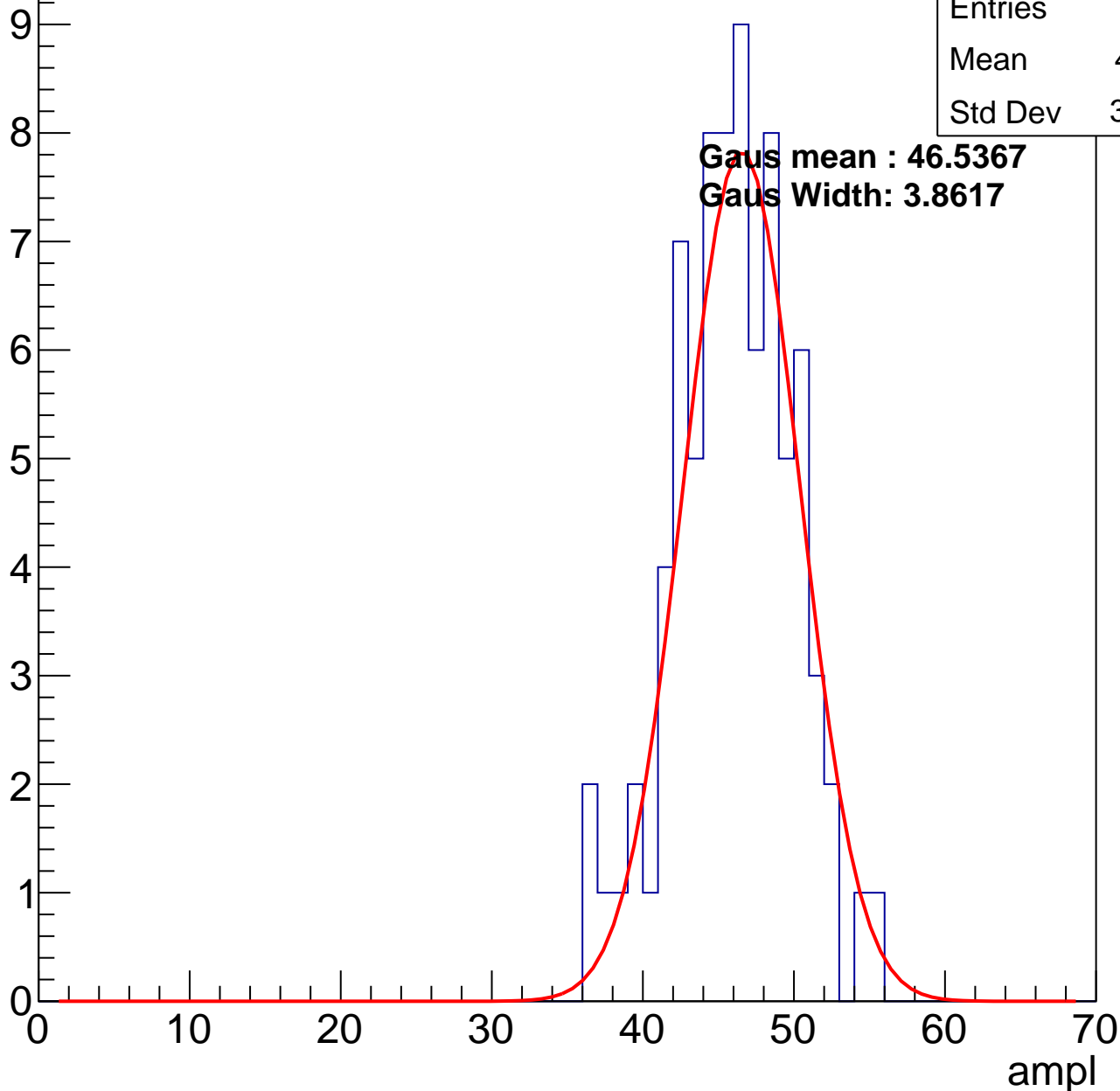
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	45.51
Std Dev	3.918

Gaus mean : 46.5367

Gaus Width: 3.8617

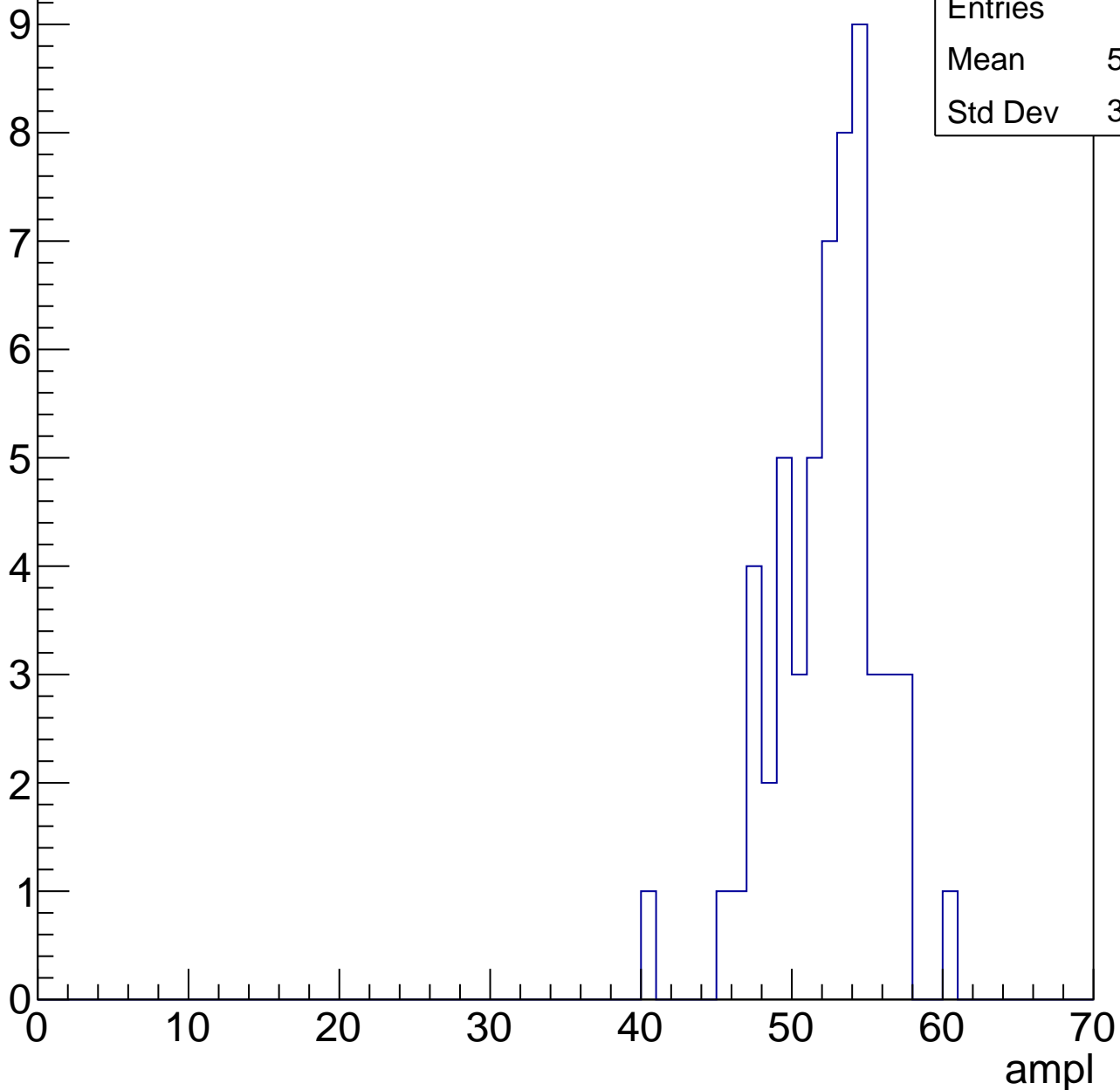


B1L103S, U10-ch40, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

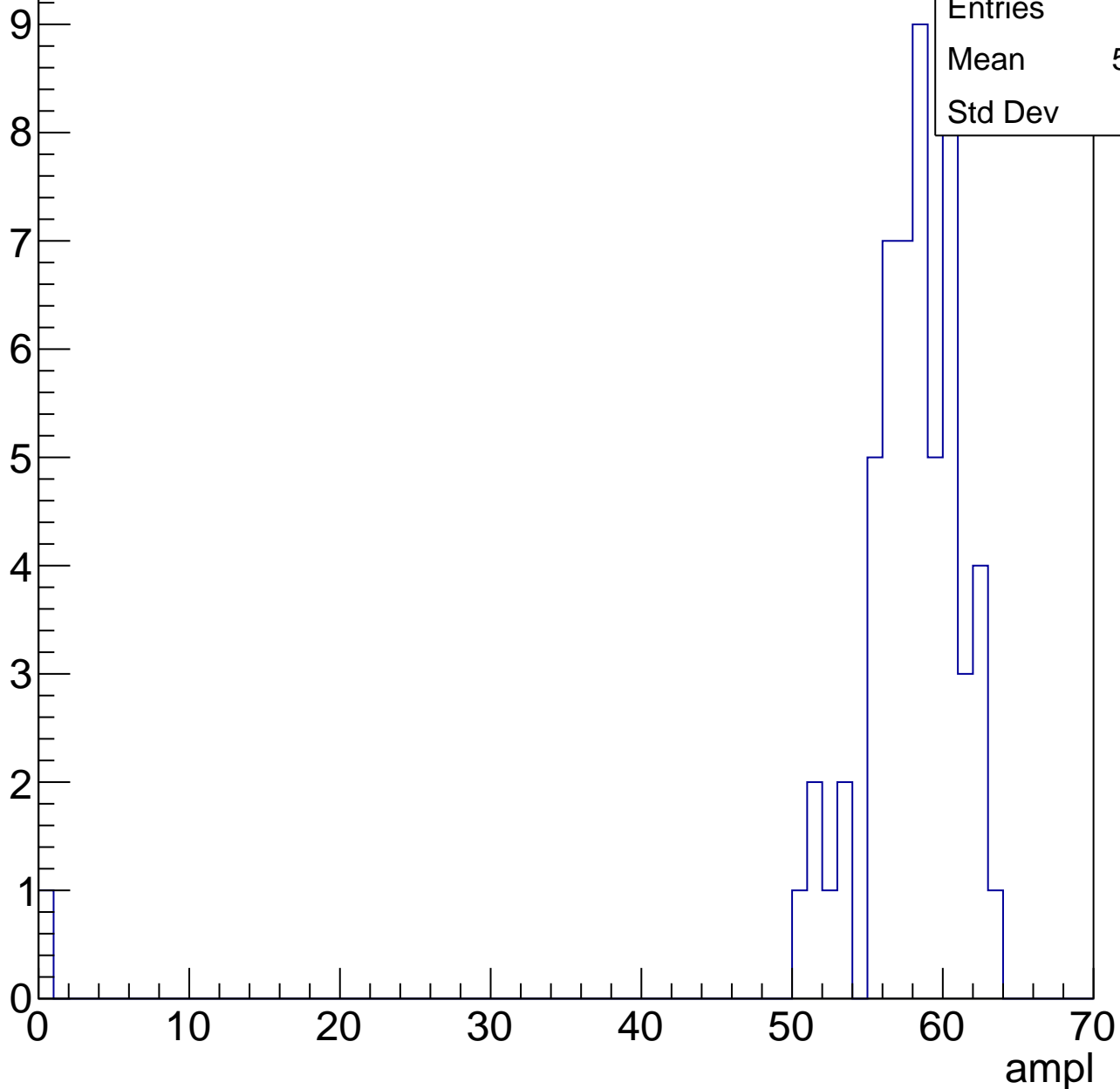
Entries	56
Mean	51.84
Std Dev	3.484



B1L103S, U10-ch40, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

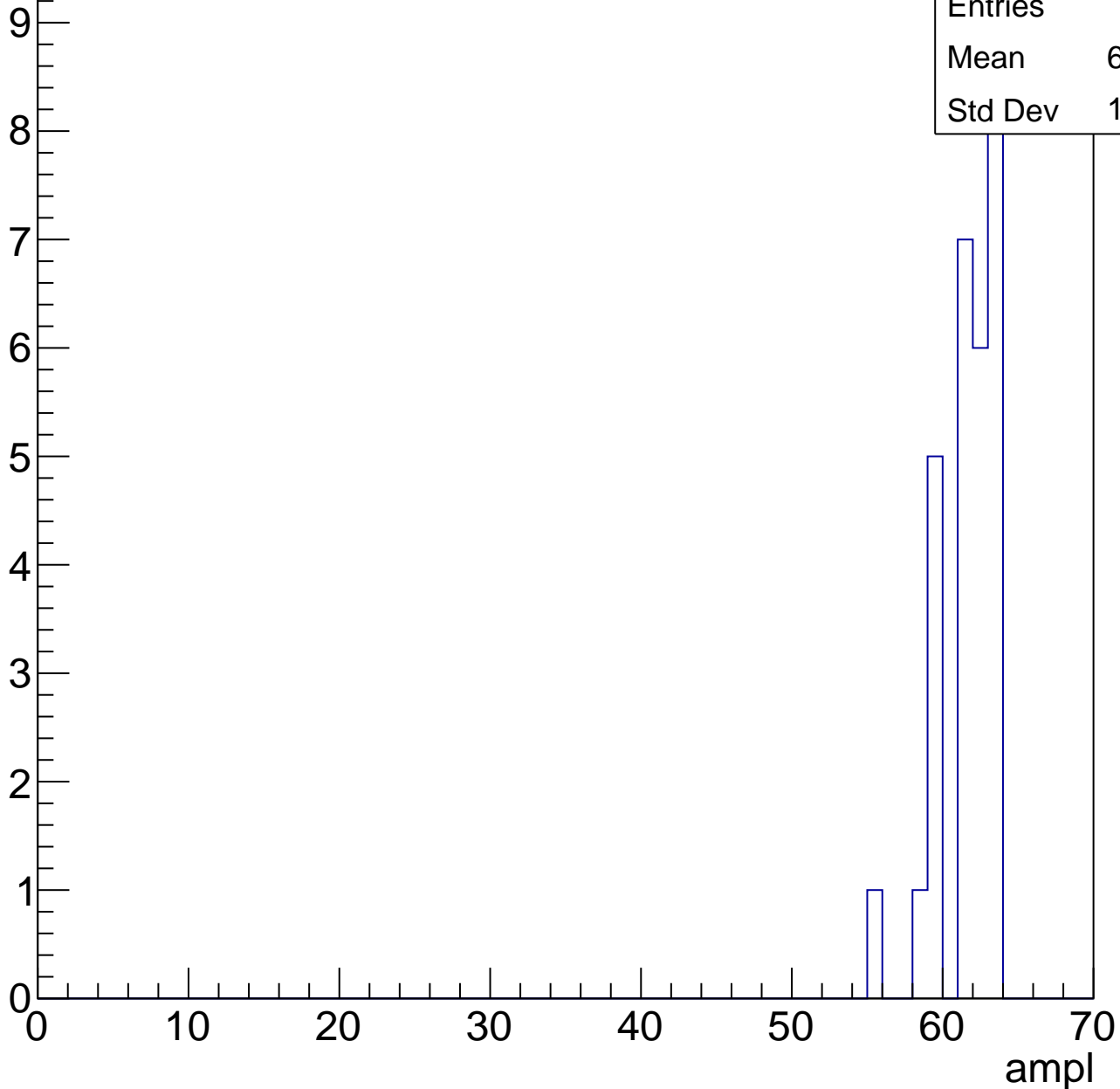


B1L103S, U10-ch40, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	29
Mean	61.17
Std Dev	1.913



B1L103S, U10-ch40, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch40, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch41, adc0

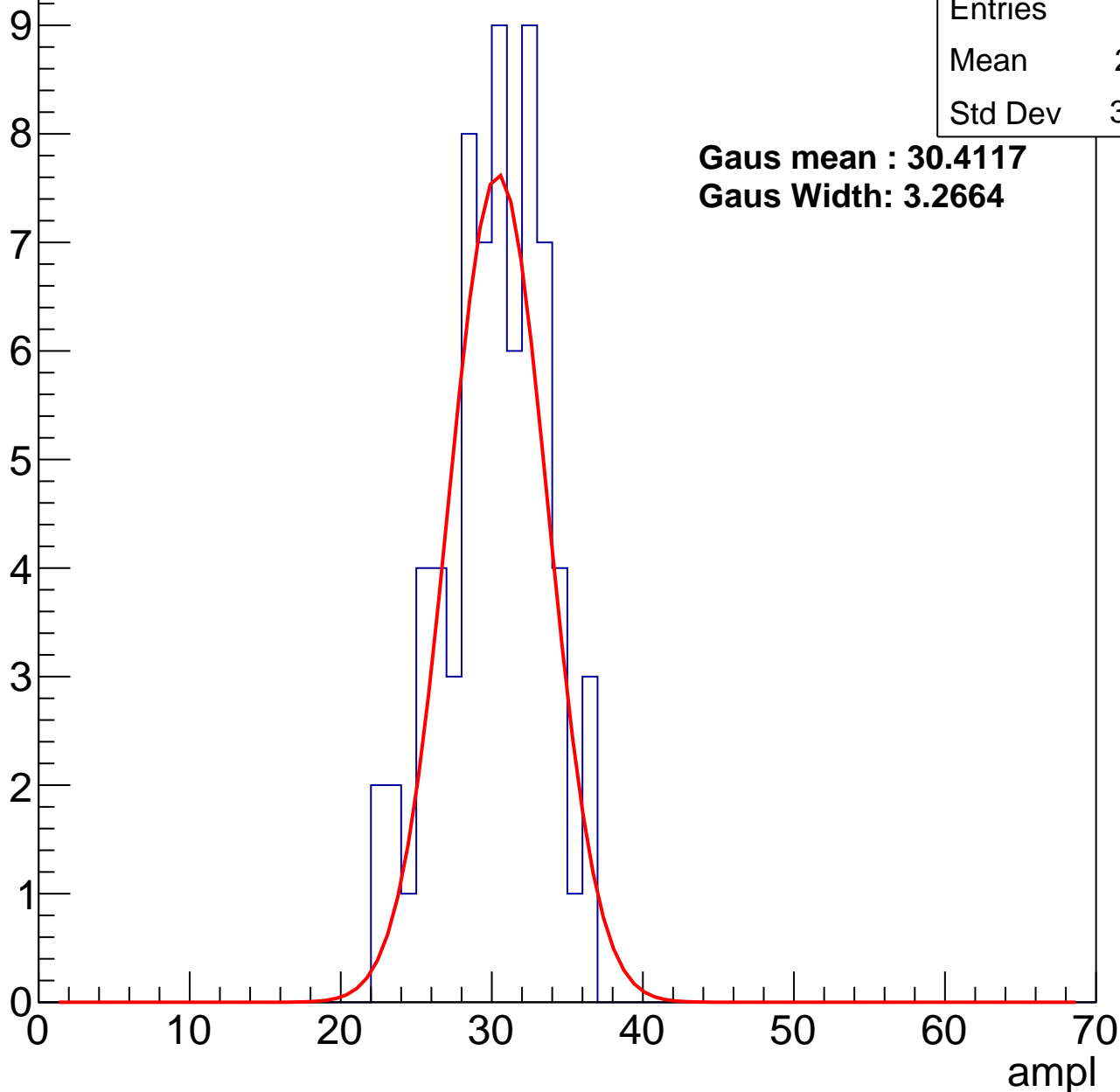
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.71
Std Dev	3.356

Gaus mean : 30.4117

Gaus Width: 3.2664



B1L103S, U10-ch41, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	64
Mean	36.89
Std Dev	2.995

Gaus mean : 37.2295

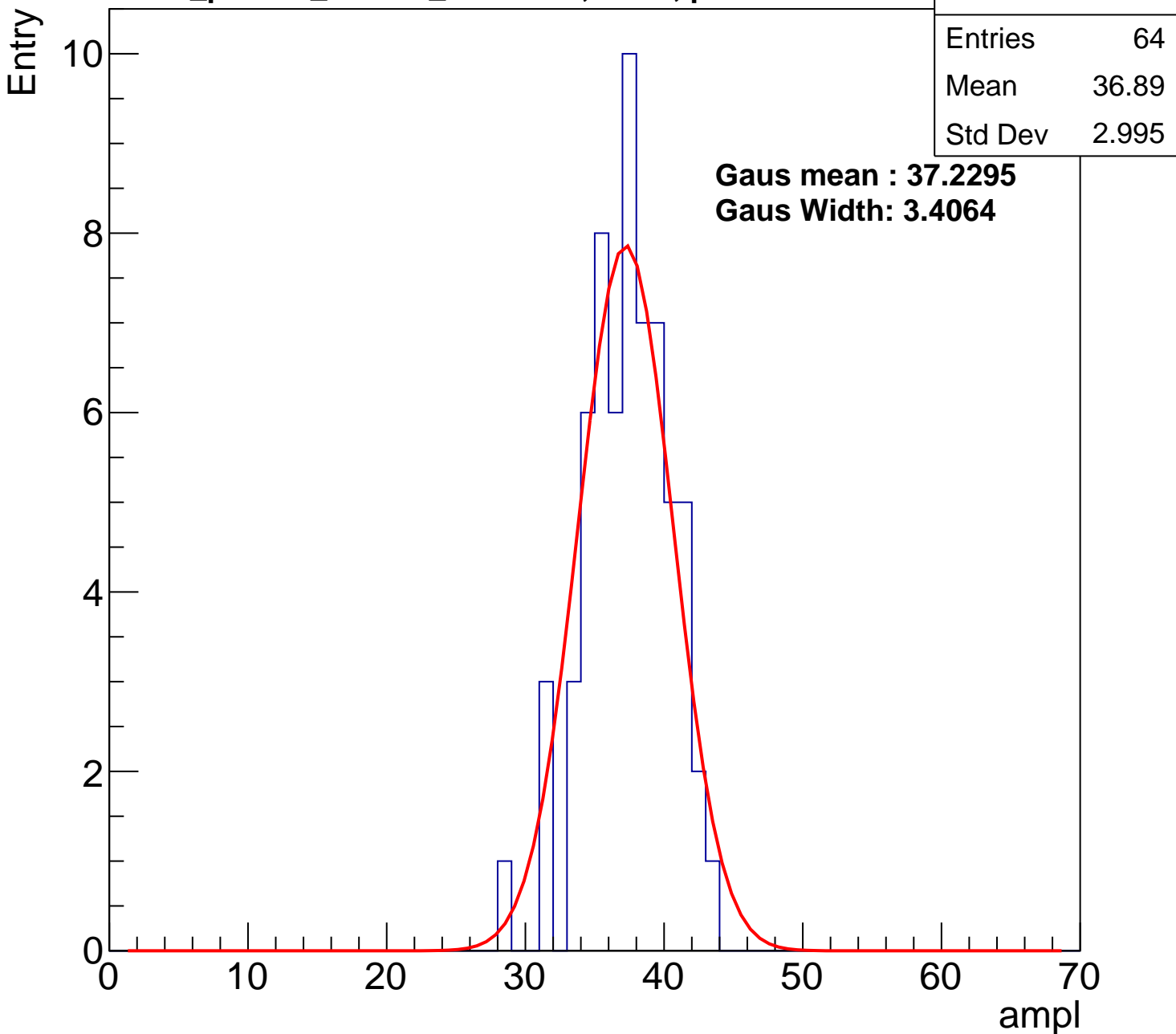
Gaus Width: 3.4064

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch41, adc2

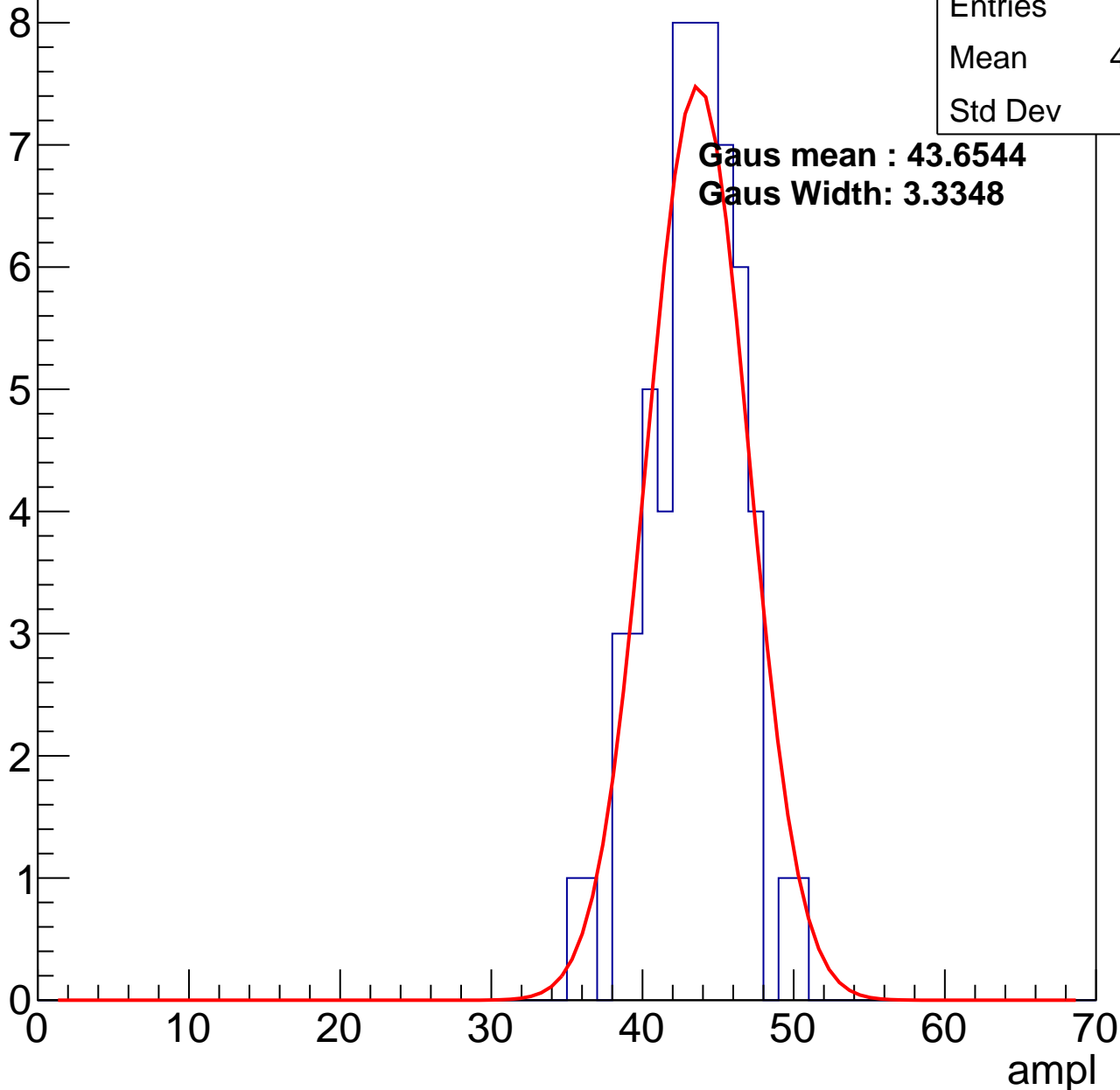
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.93
Std Dev	3.01

Gaus mean : 43.6544

Gaus Width: 3.3348



B1L103S, U10-ch41, adc3

calib_packv5_042523_0143.root, FC#7, port C2

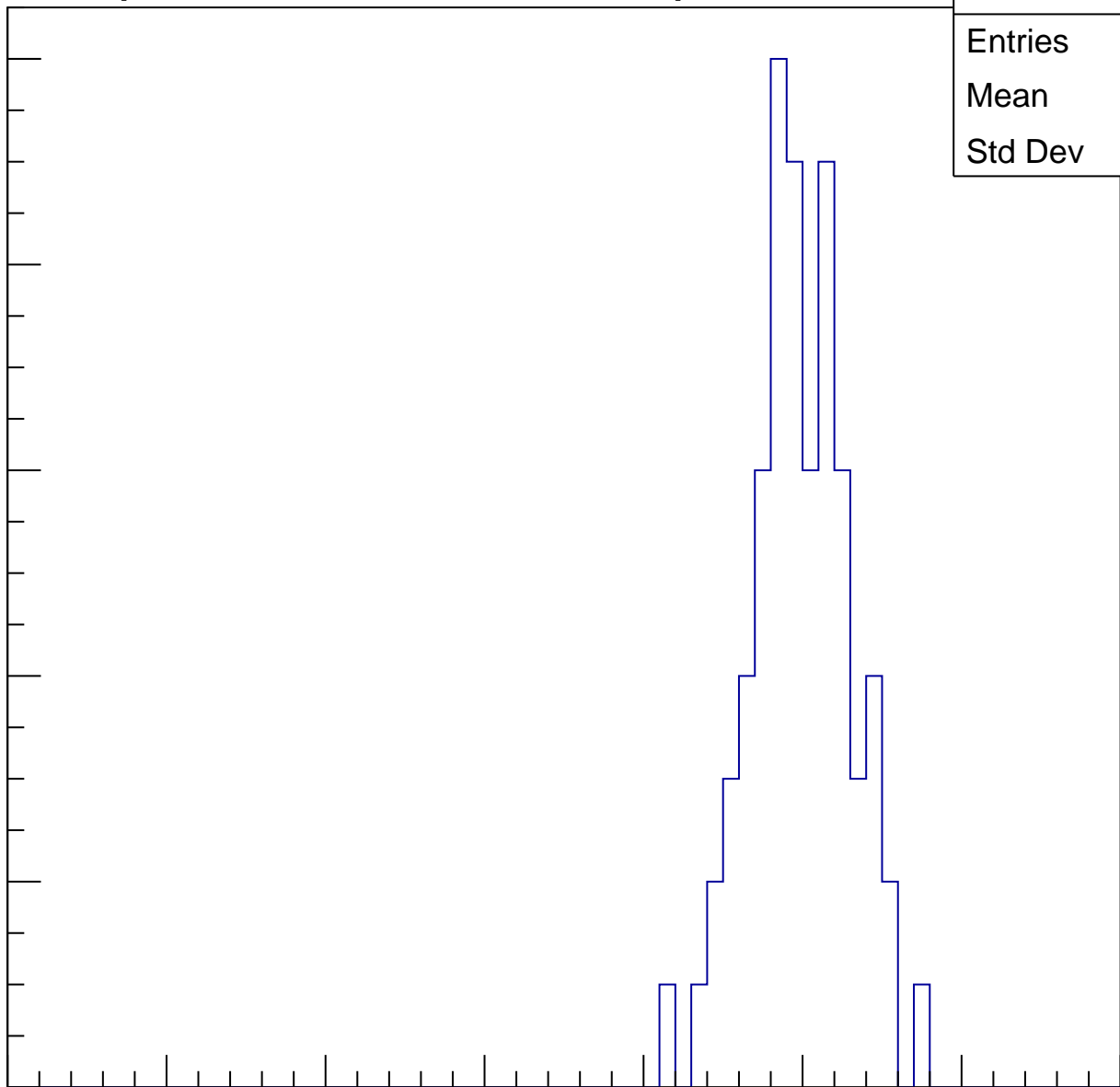
Entries	67
Mean	49.36
Std Dev	3.089

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

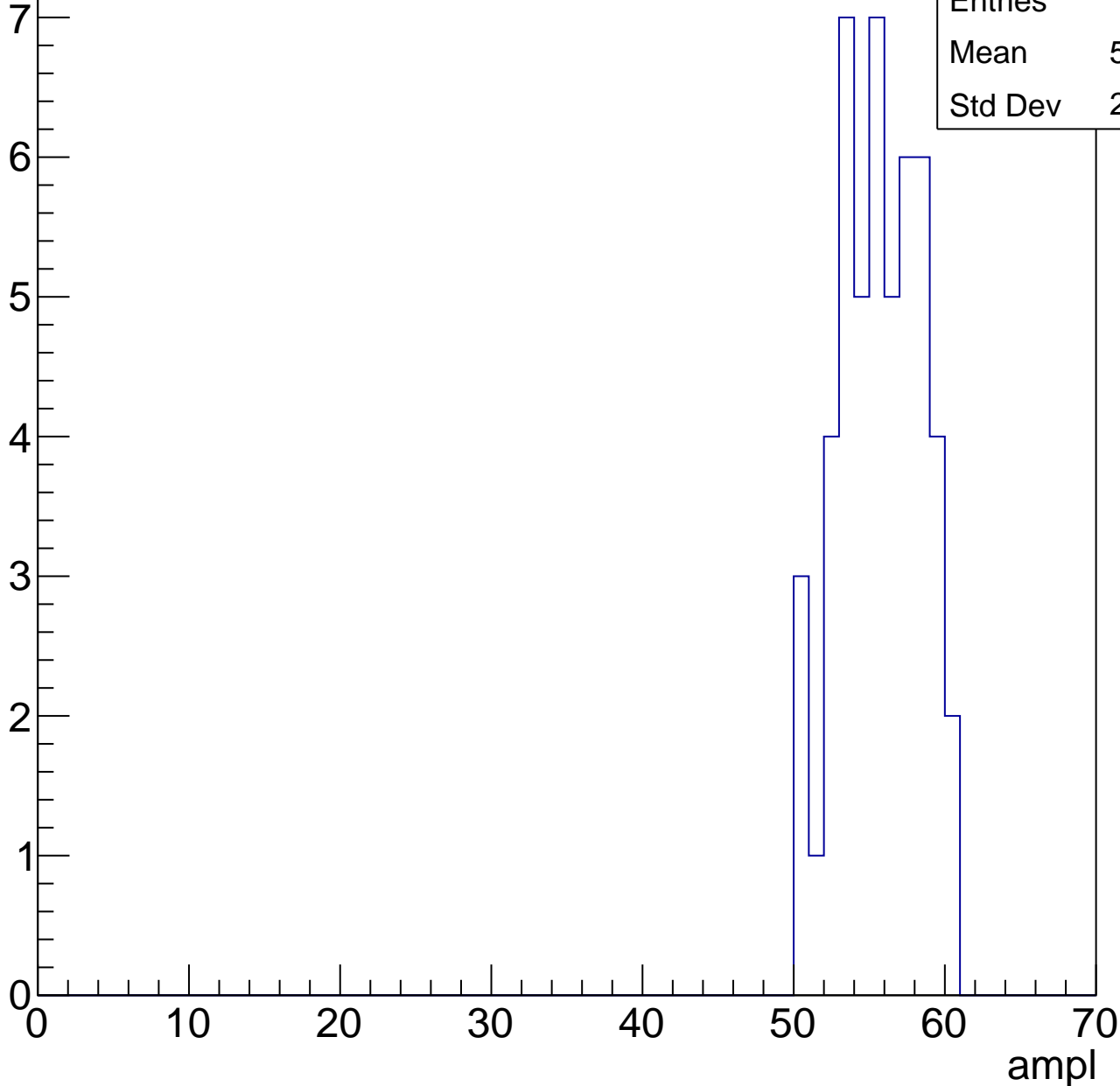


B1L103S, U10-ch41, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	55.22
Std Dev	2.663

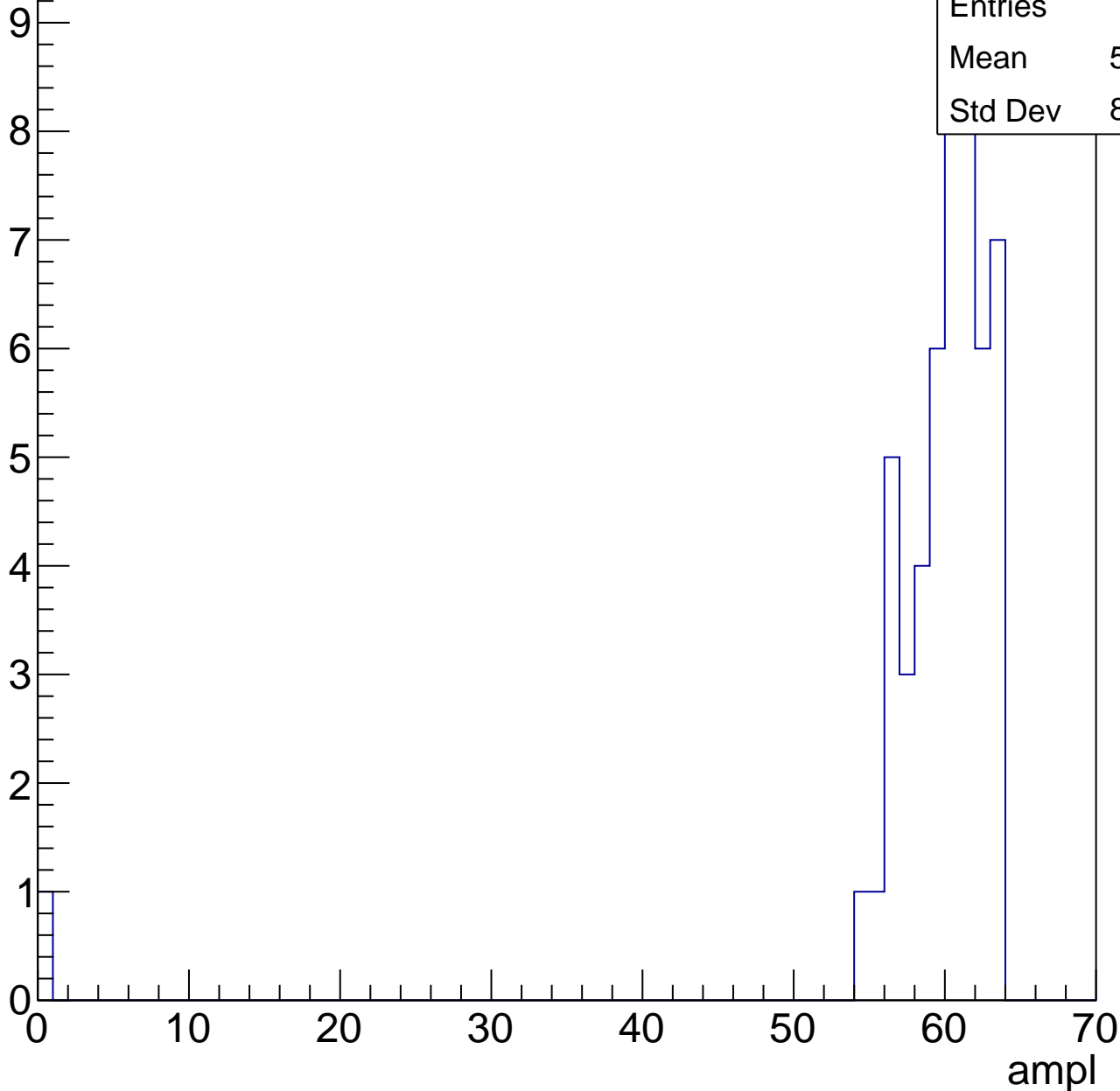


B1L103S, U10-ch41, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

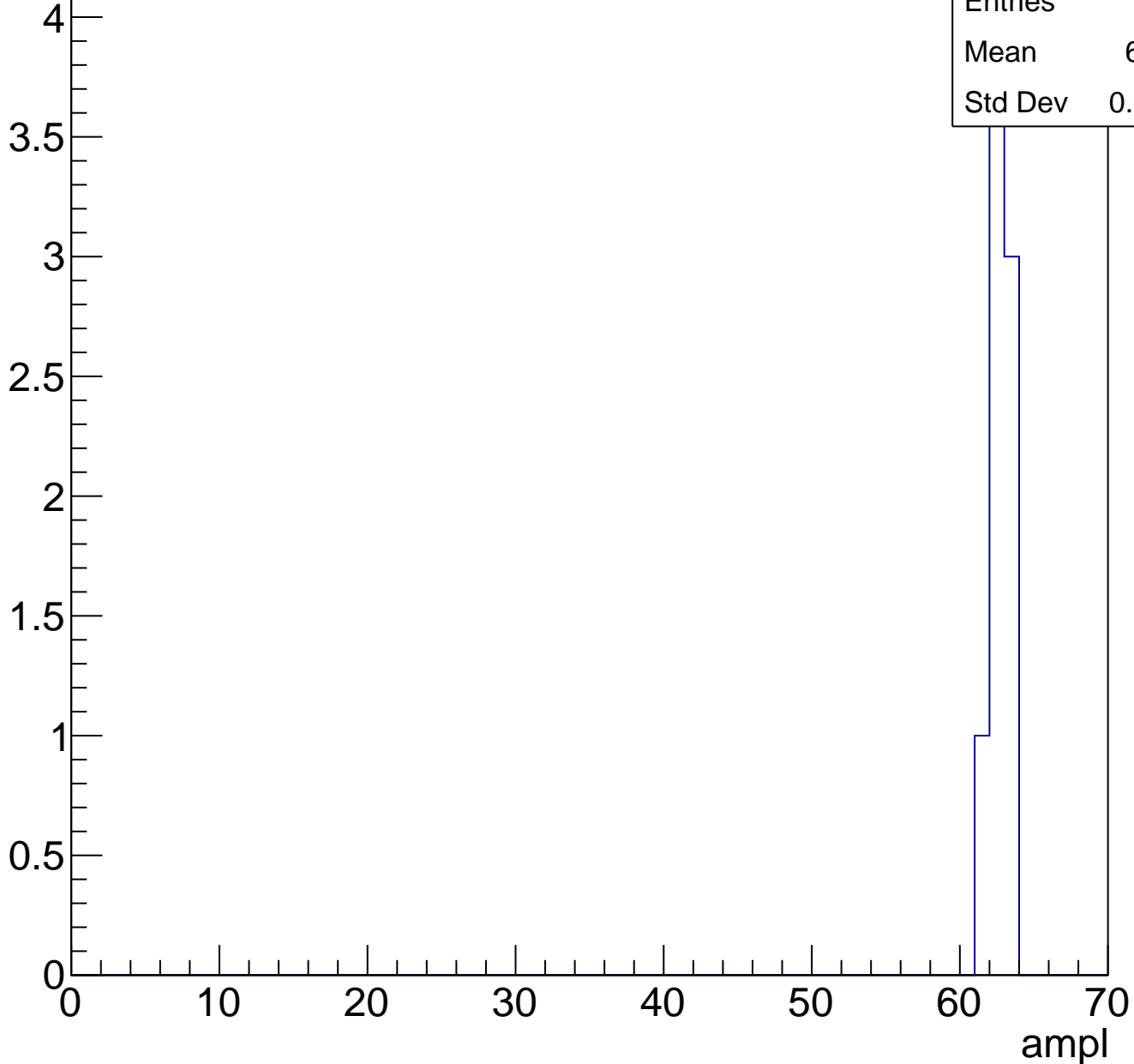
Entries	51
Mean	58.59
Std Dev	8.614



B1L103S, U10-ch41, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch41, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch42, adc0

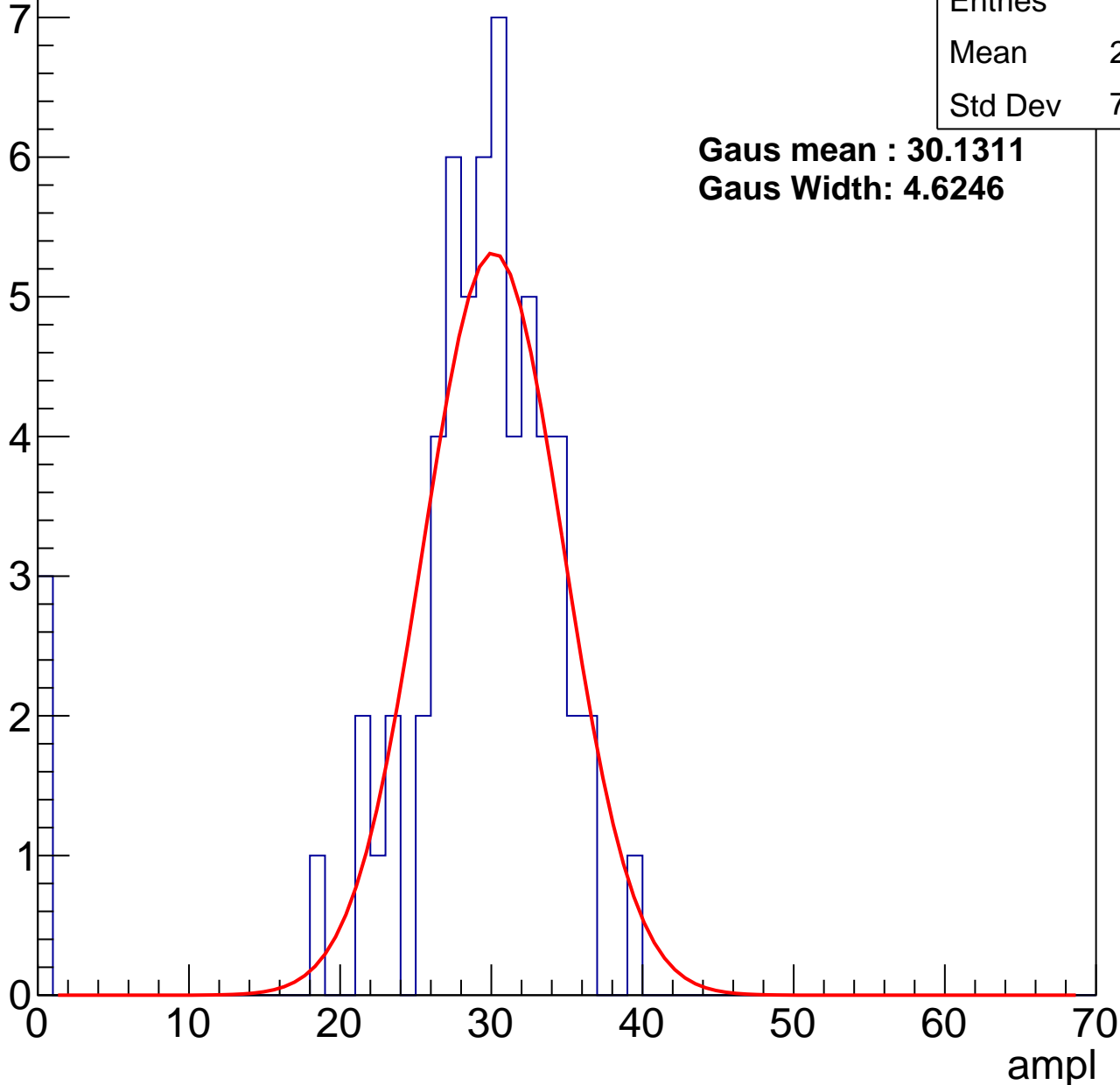
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	27.89
Std Dev	7.484

Gaus mean : 30.1311

Gaus Width: 4.6246



B1L103S, U10-ch42, adc1

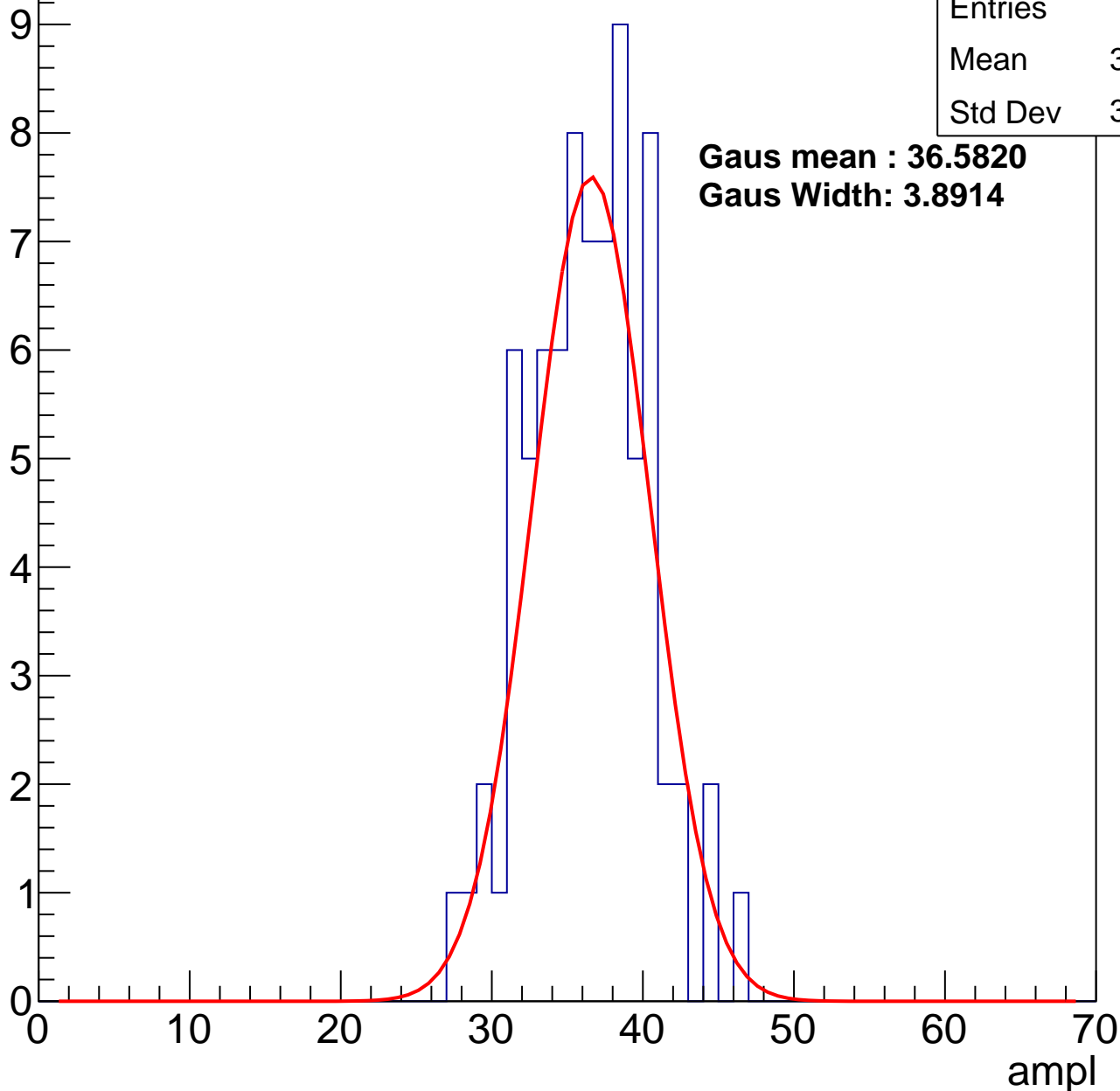
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.94
Std Dev	3.833

Gaus mean : 36.5820

Gaus Width: 3.8914



B1L103S, U10-ch42, adc2

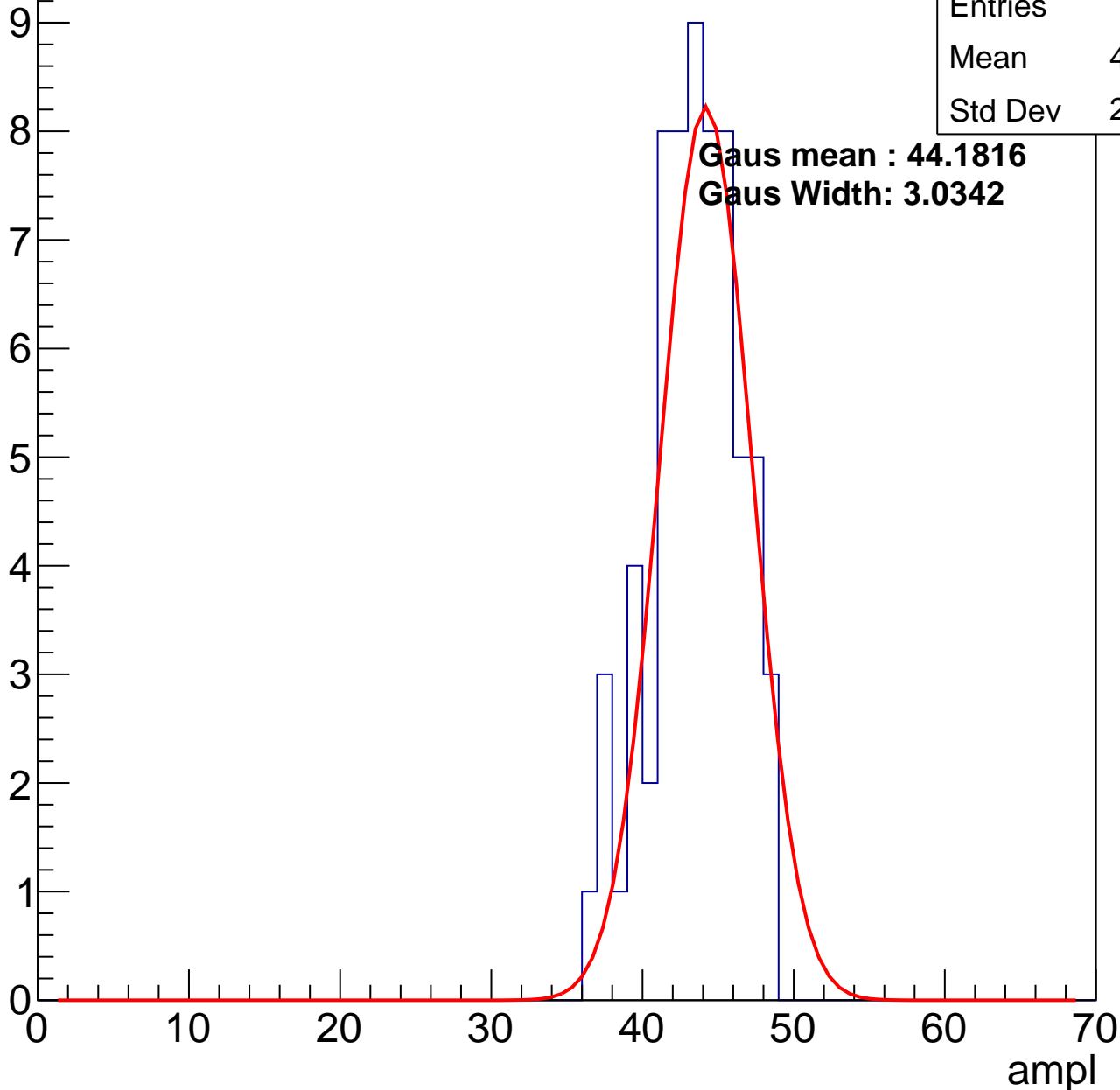
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.97
Std Dev	2.893

Gaus mean : 44.1816

Gaus Width: 3.0342

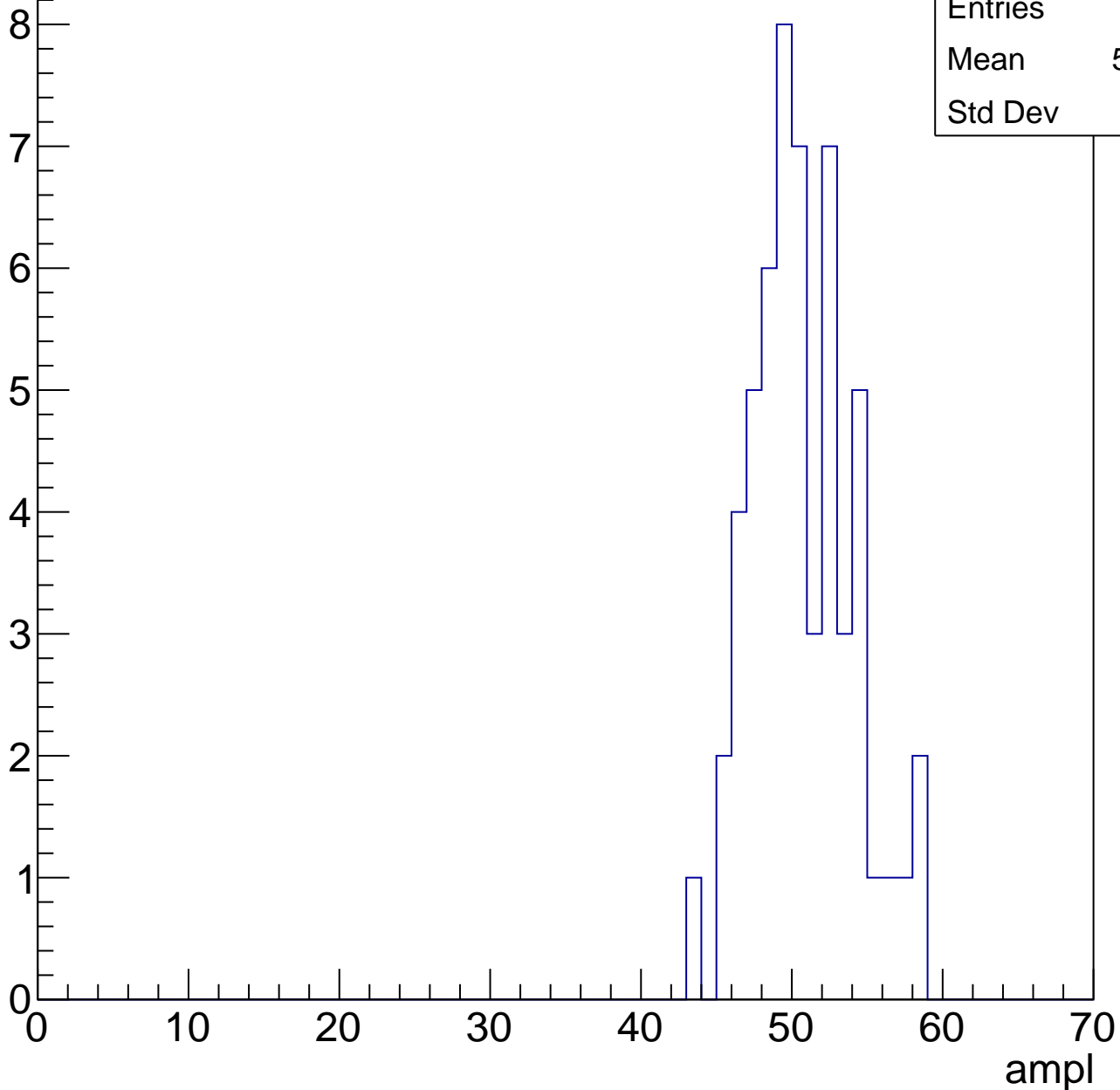


B1L103S, U10-ch42, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	50.21
Std Dev	3.31



B1L103S, U10-ch42, adc4

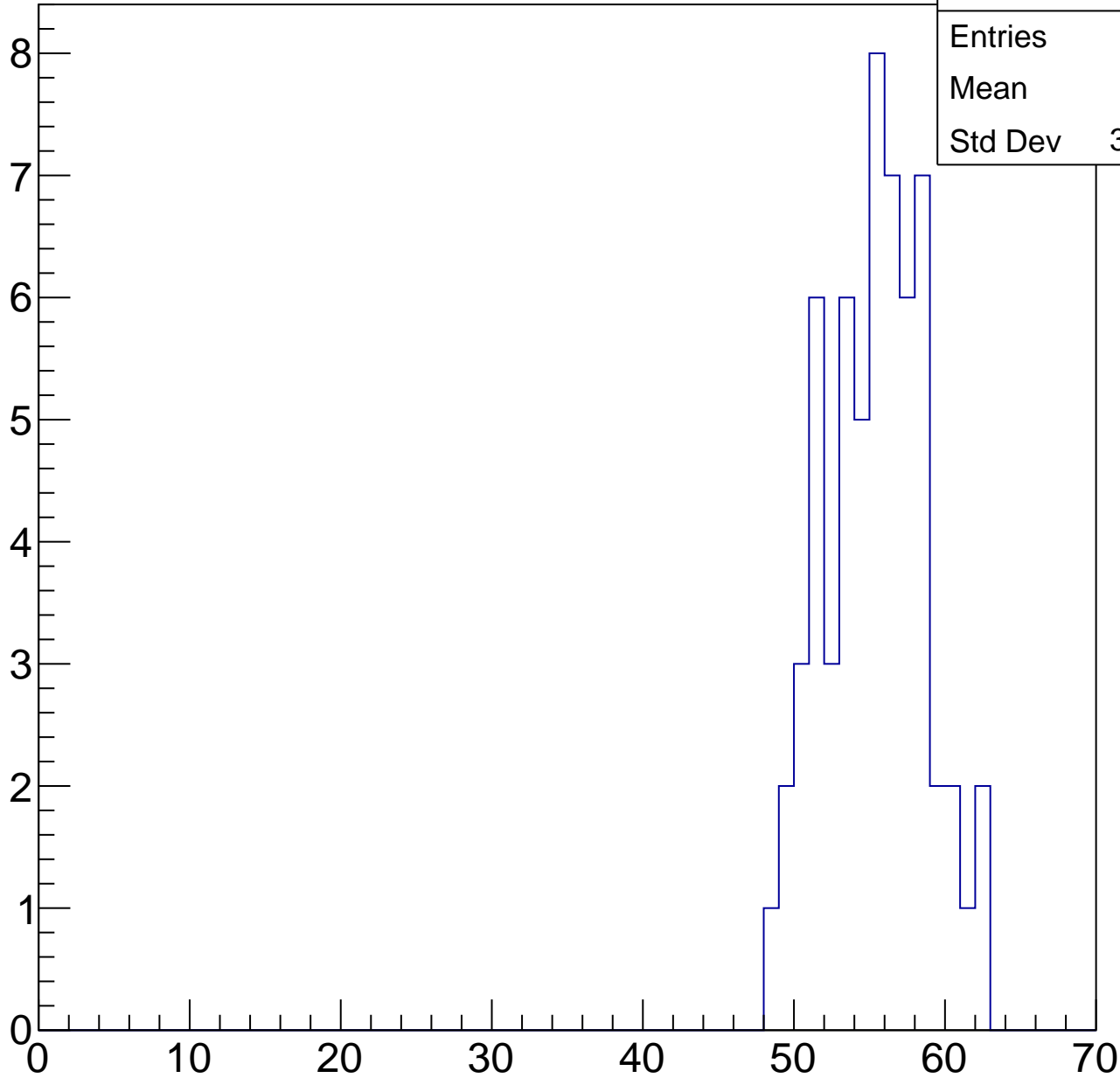
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	61
Mean	54.9
Std Dev	3.283

ampl

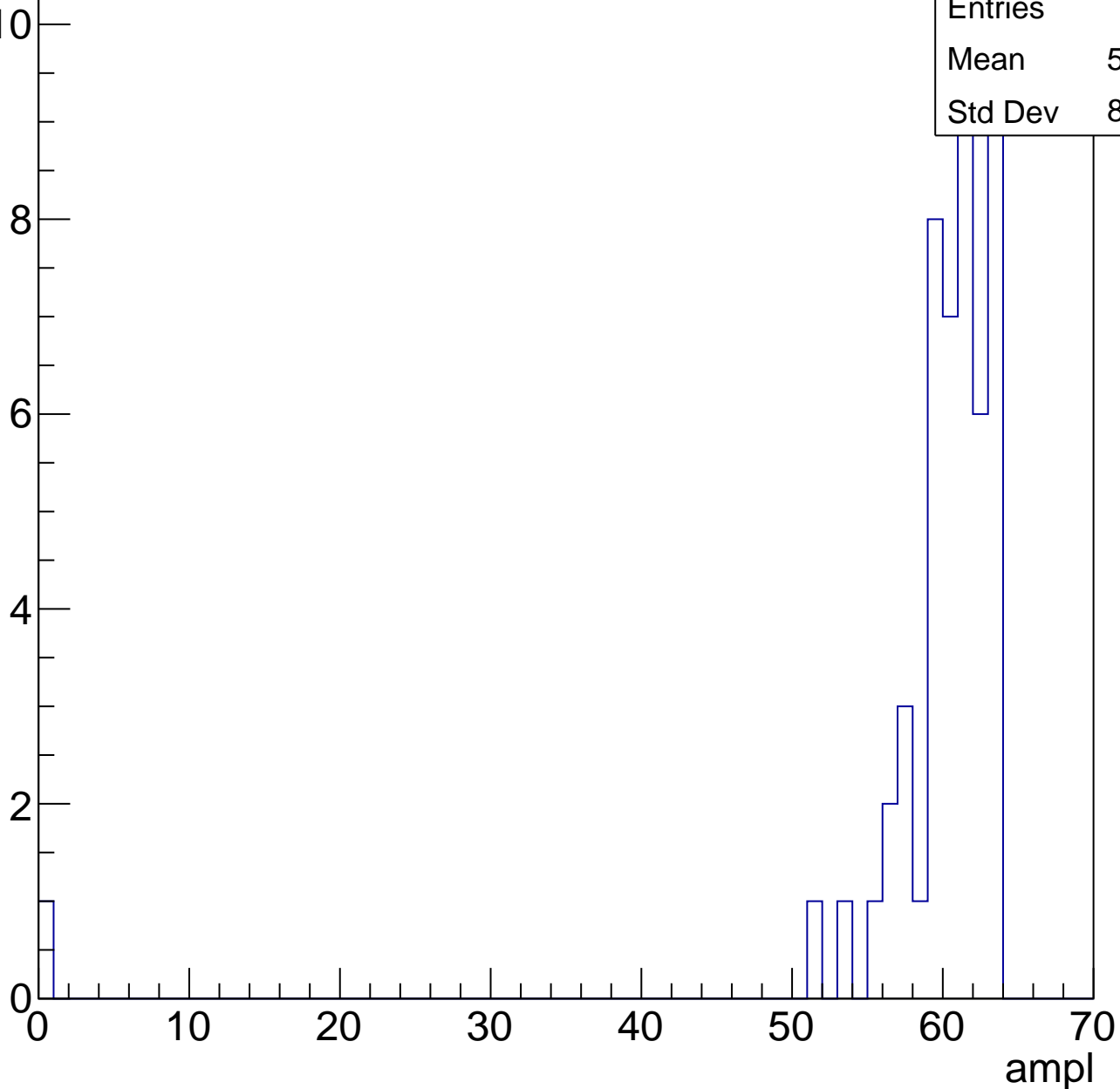


B1L103S, U10-ch42, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.86
Std Dev	8.814



B1L103S, U10-ch42, adc6

calib_packv5_042523_0143.root, FC#7, port C2

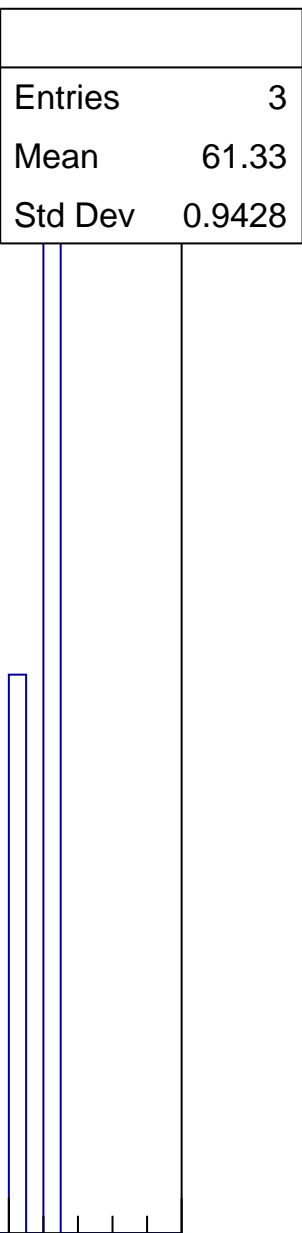
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch42, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch43, adc0

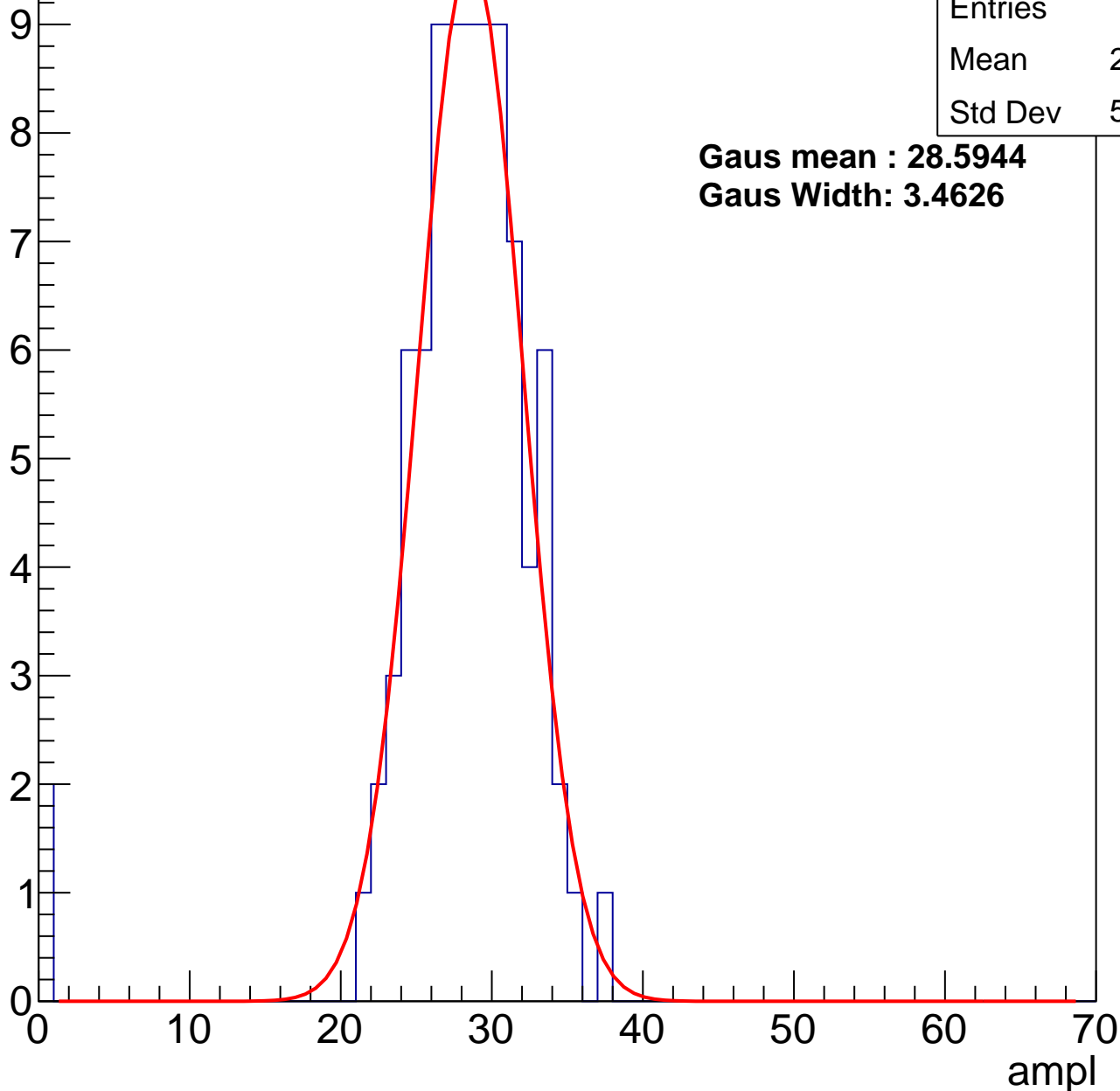
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	27.57
Std Dev	5.356

Gaus mean : 28.5944

Gaus Width: 3.4626



B1L103S, U10-ch43, adc1

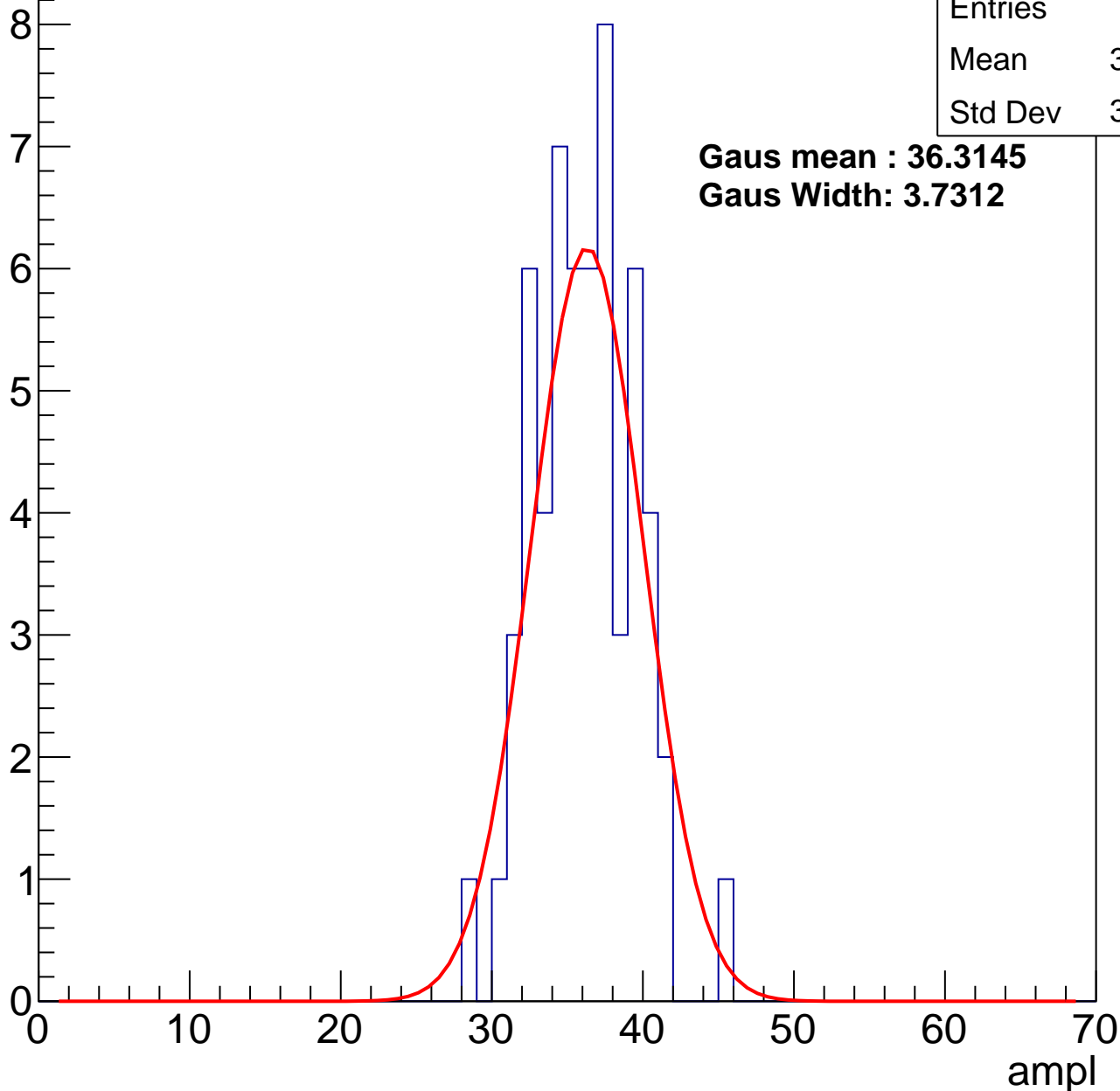
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	35.69
Std Dev	3.228

Gaus mean : 36.3145

Gaus Width: 3.7312



B1L103S, U10-ch43, adc2

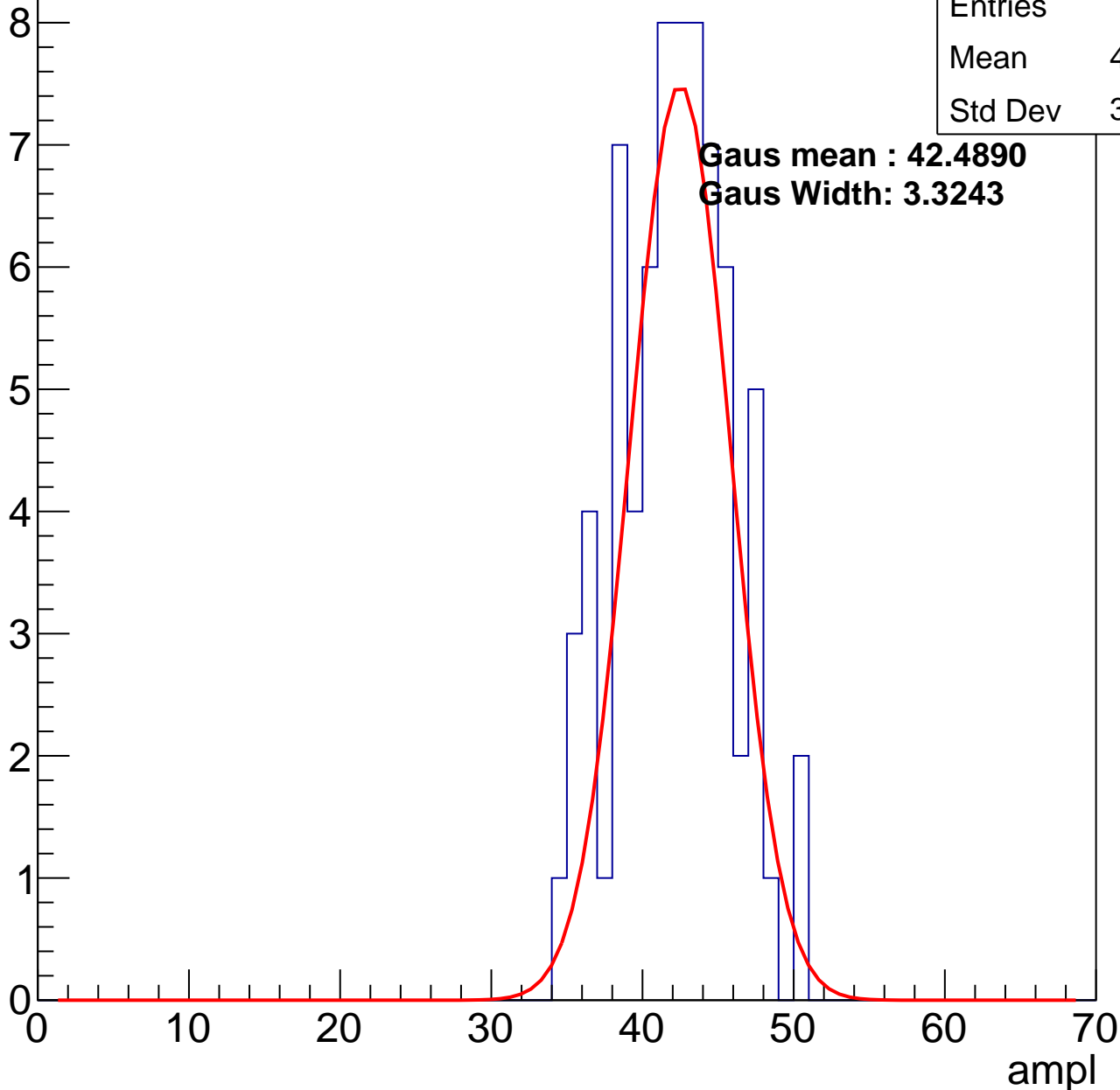
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	41.68
Std Dev	3.633

Gaus mean : 42.4890

Gaus Width: 3.3243

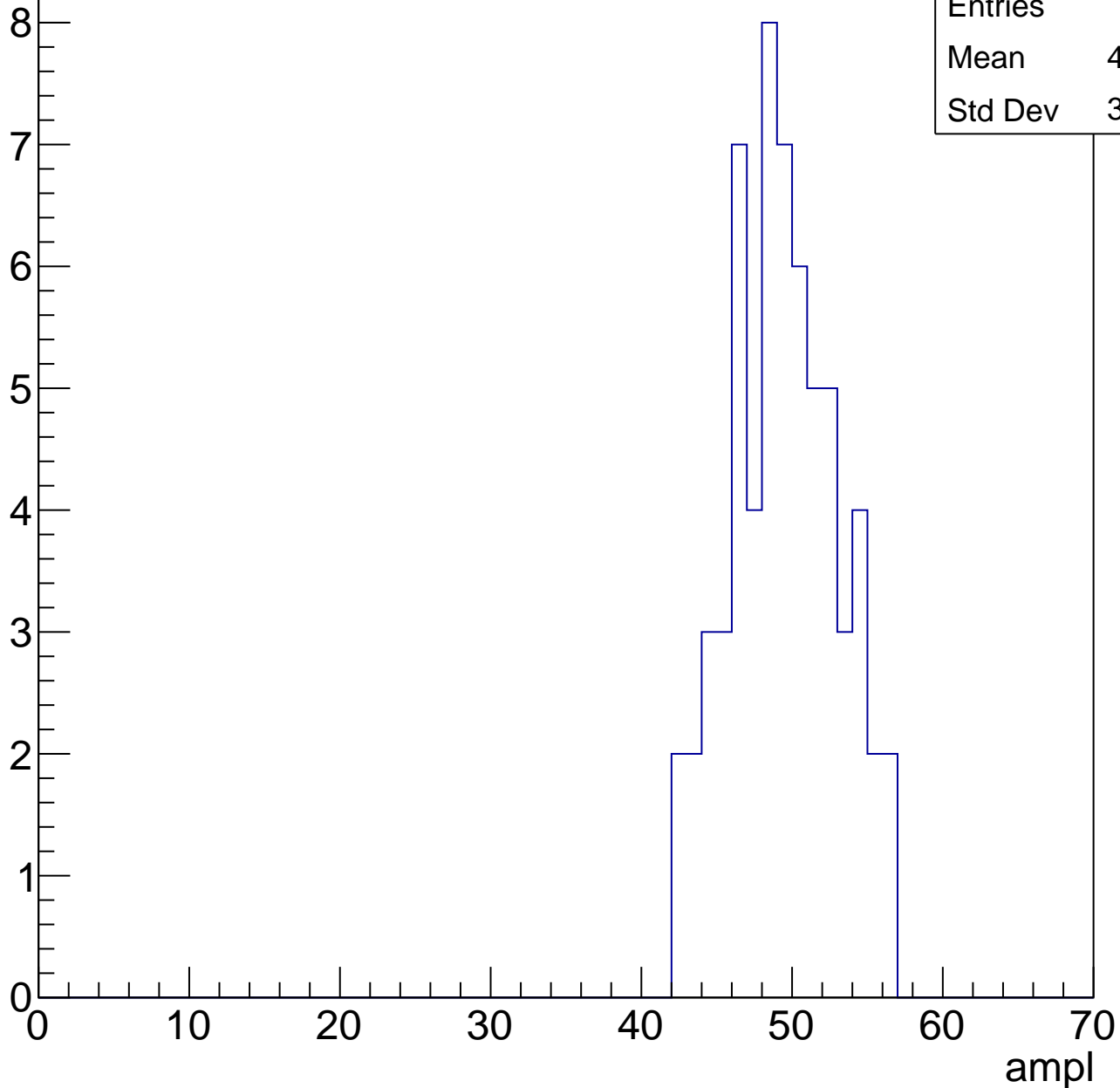


B1L103S, U10-ch43, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	48.98
Std Dev	3.494

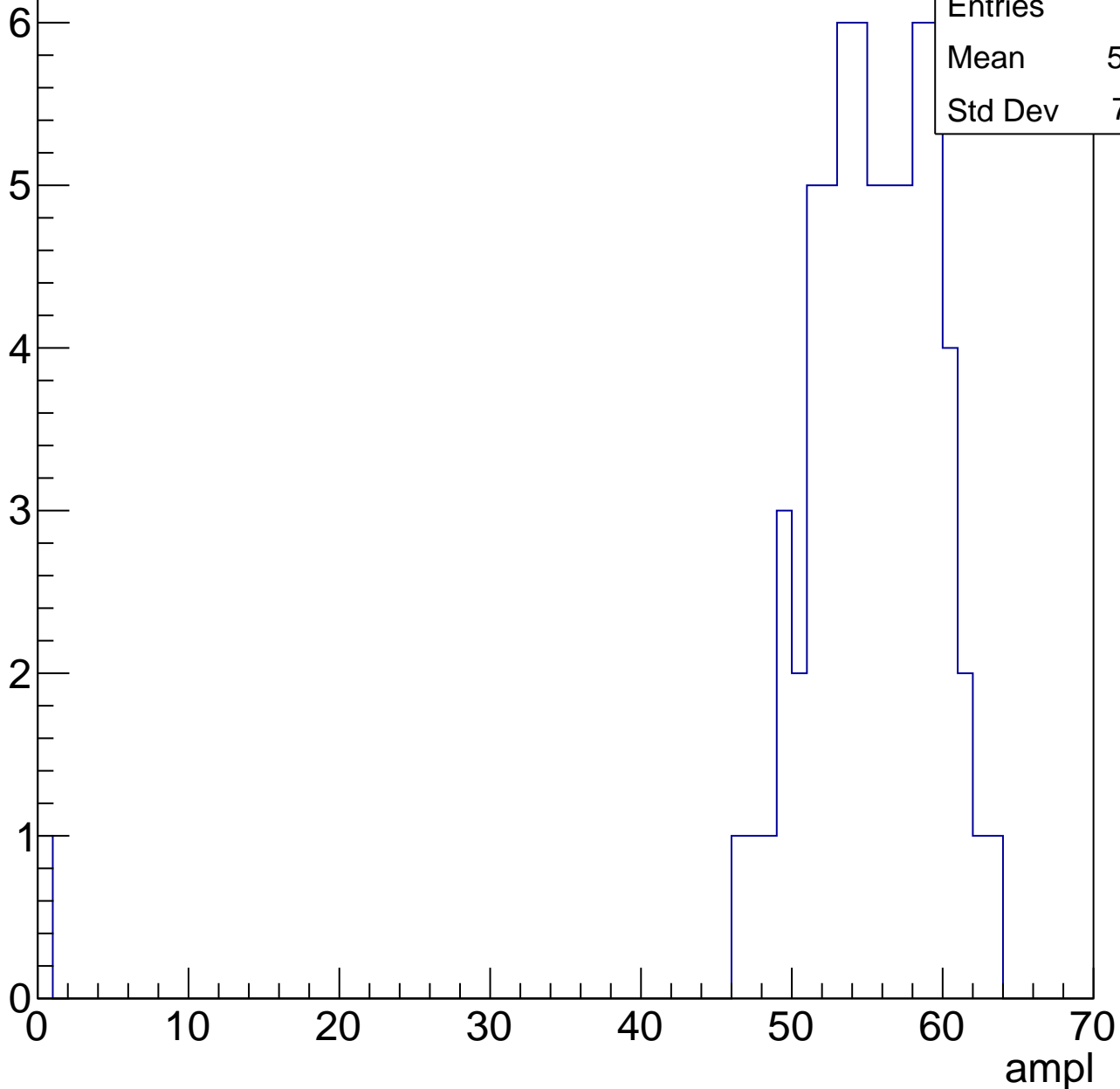


B1L103S, U10-ch43, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	54.15
Std Dev	7.731

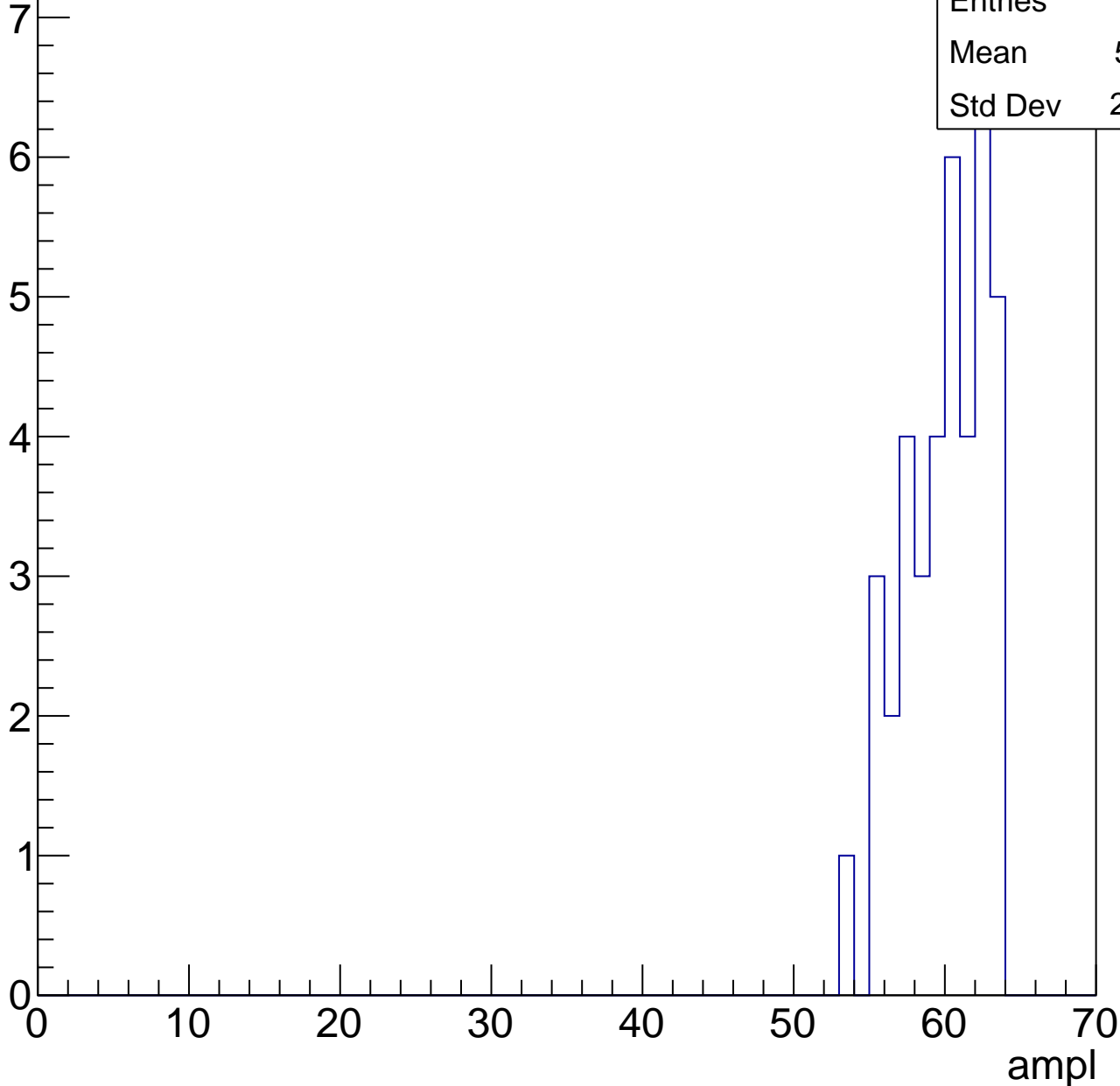


B1L103S, U10-ch43, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

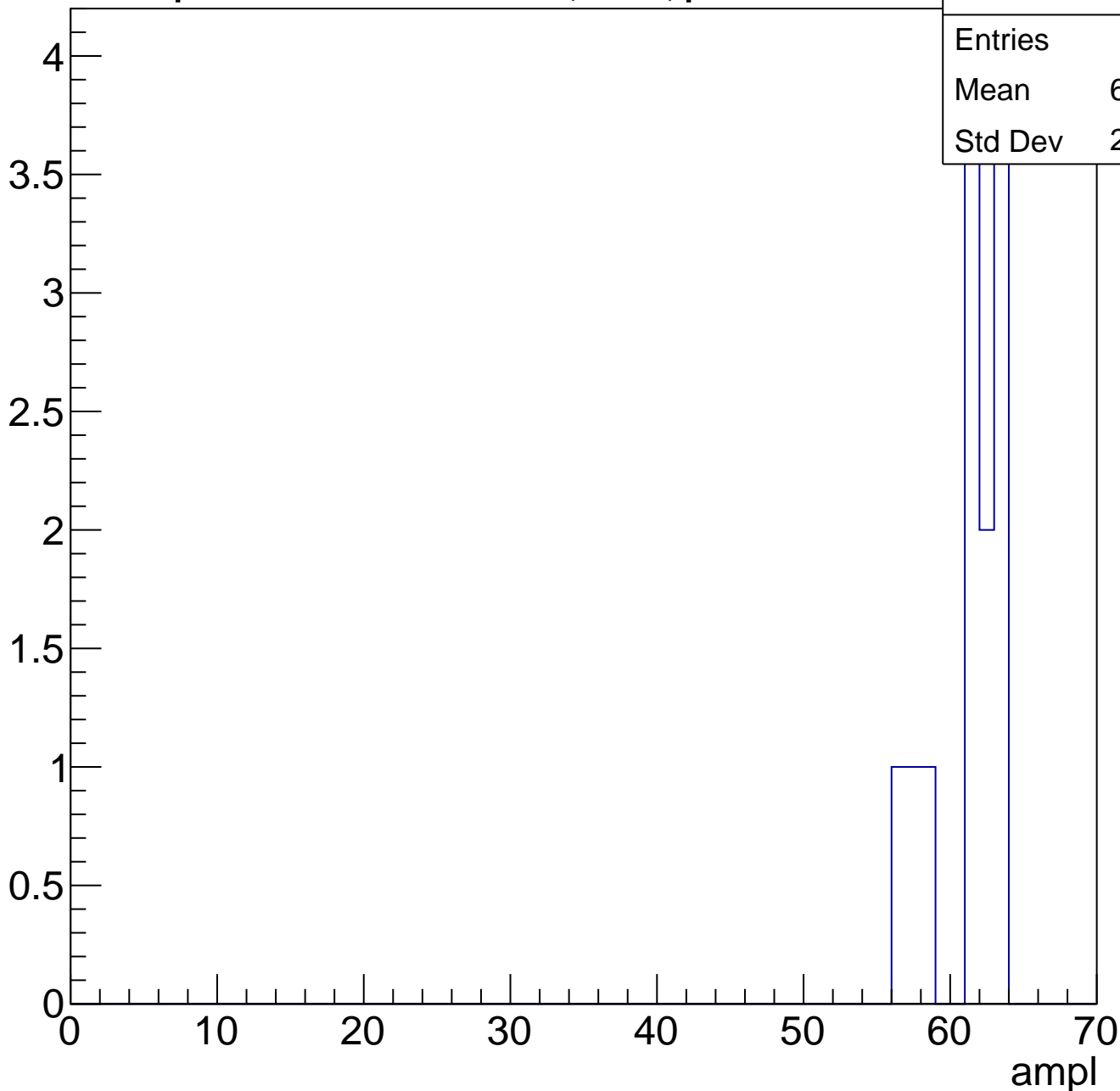
Entries	39
Mean	59.51
Std Dev	2.659



B1L103S, U10-ch43, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch43, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch44, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	29.19
Std Dev	5.147

Gaus mean : 30.3827

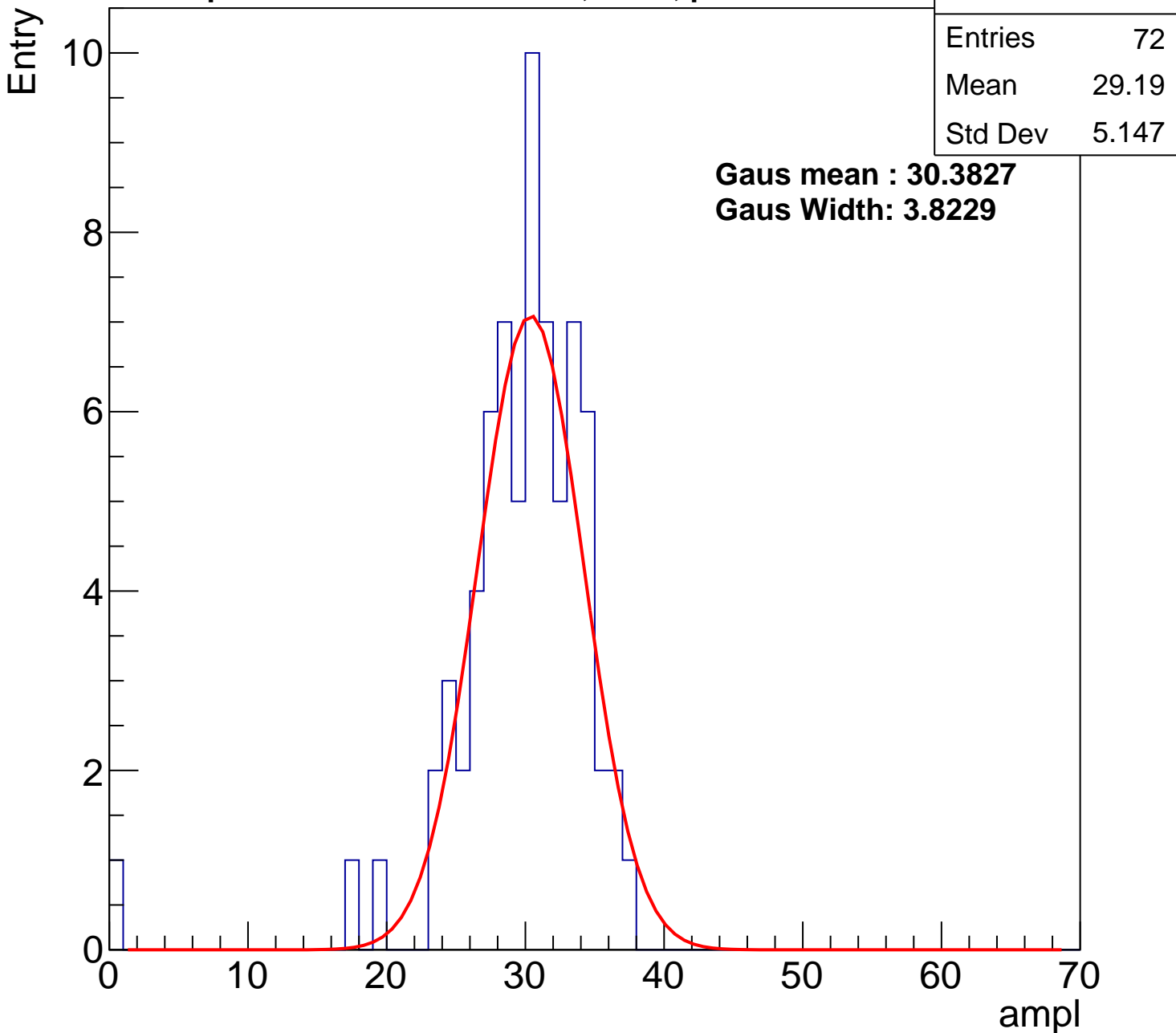
Gaus Width: 3.8229

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch44, adc1

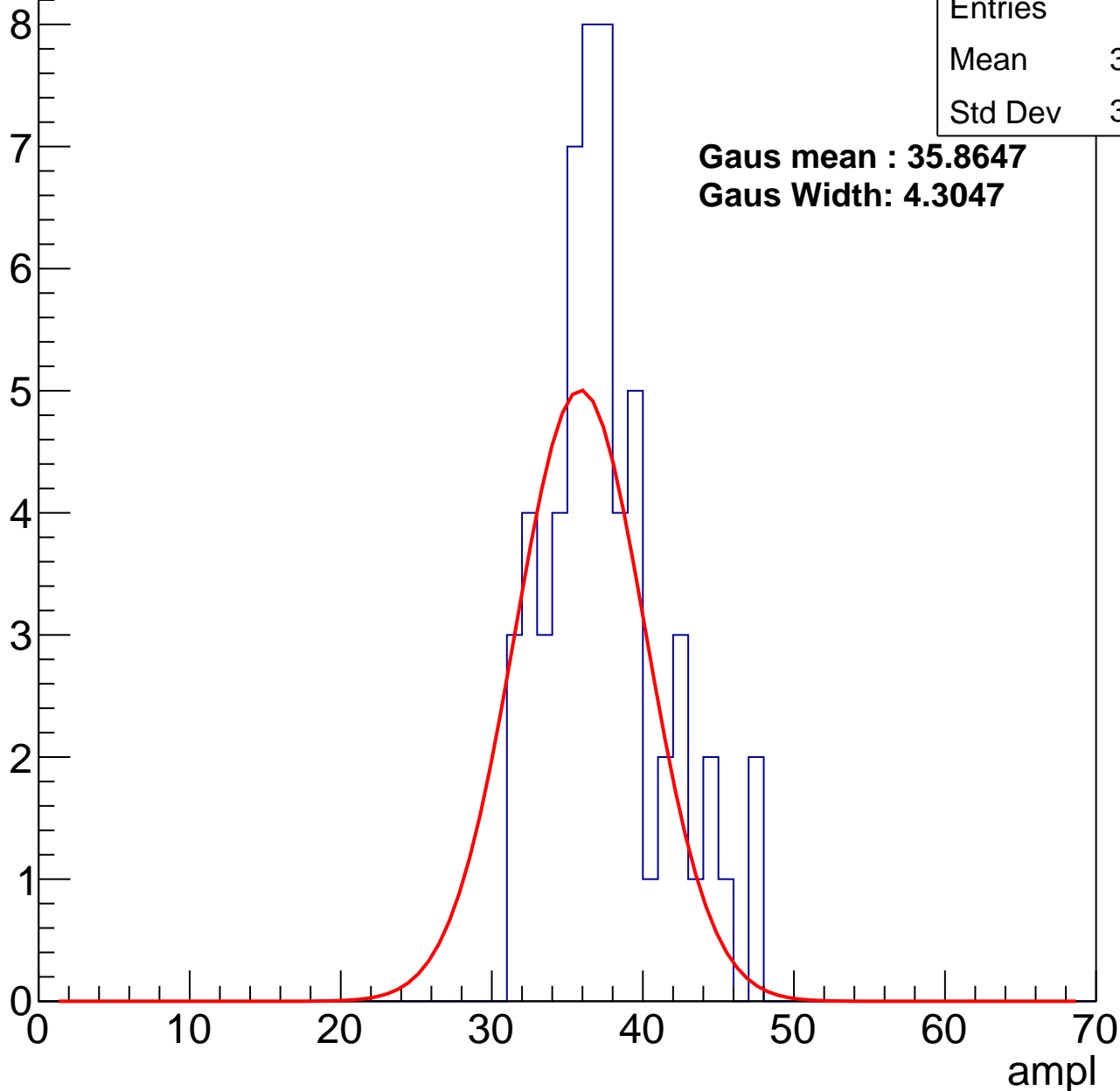
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	37.07
Std Dev	3.863

Gaus mean : 35.8647

Gaus Width: 4.3047



B1L103S, U10-ch44, adc2

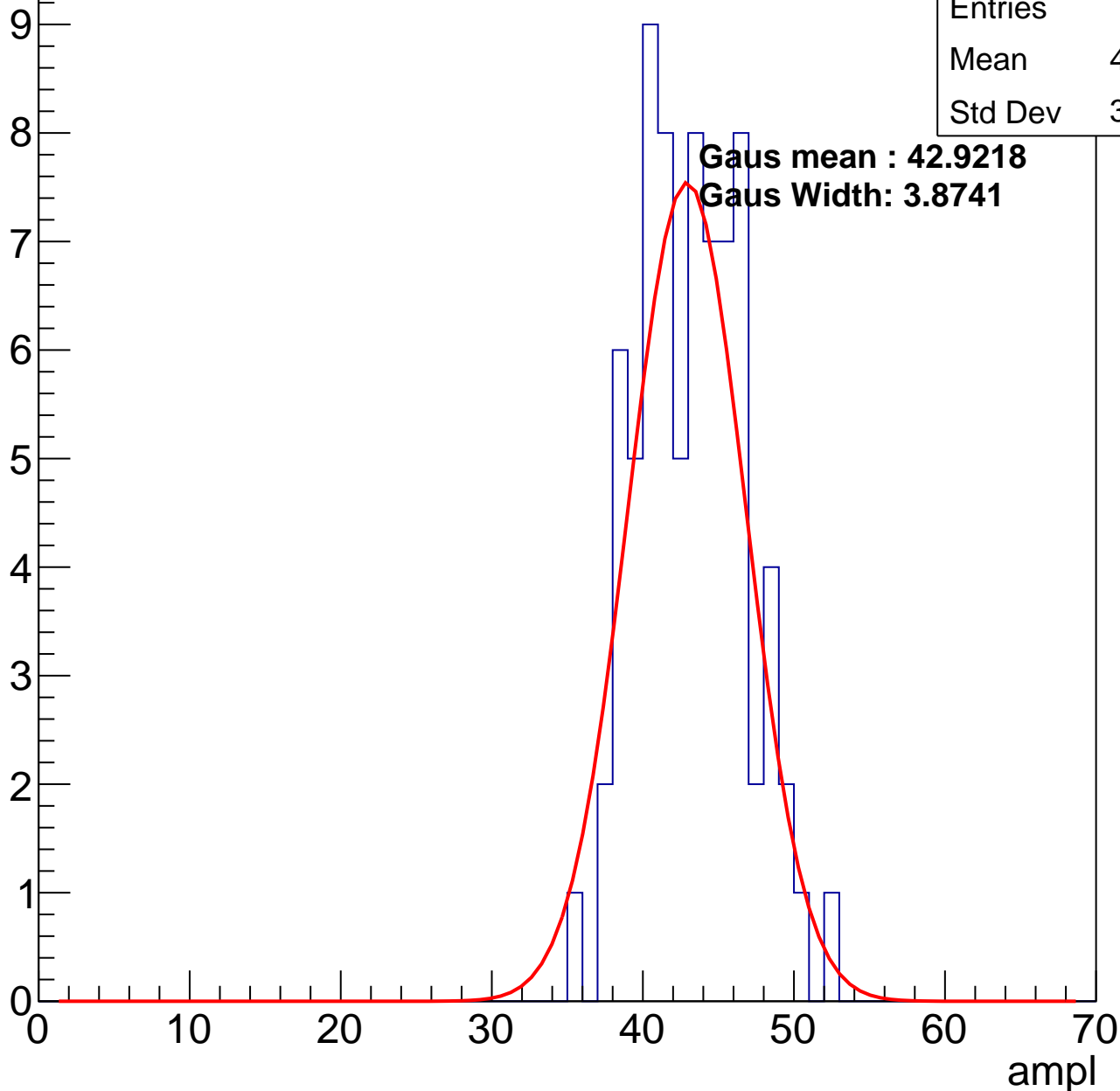
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	42.78
Std Dev	3.482

Gaus mean : 42.9218

Gaus Width: 3.8741

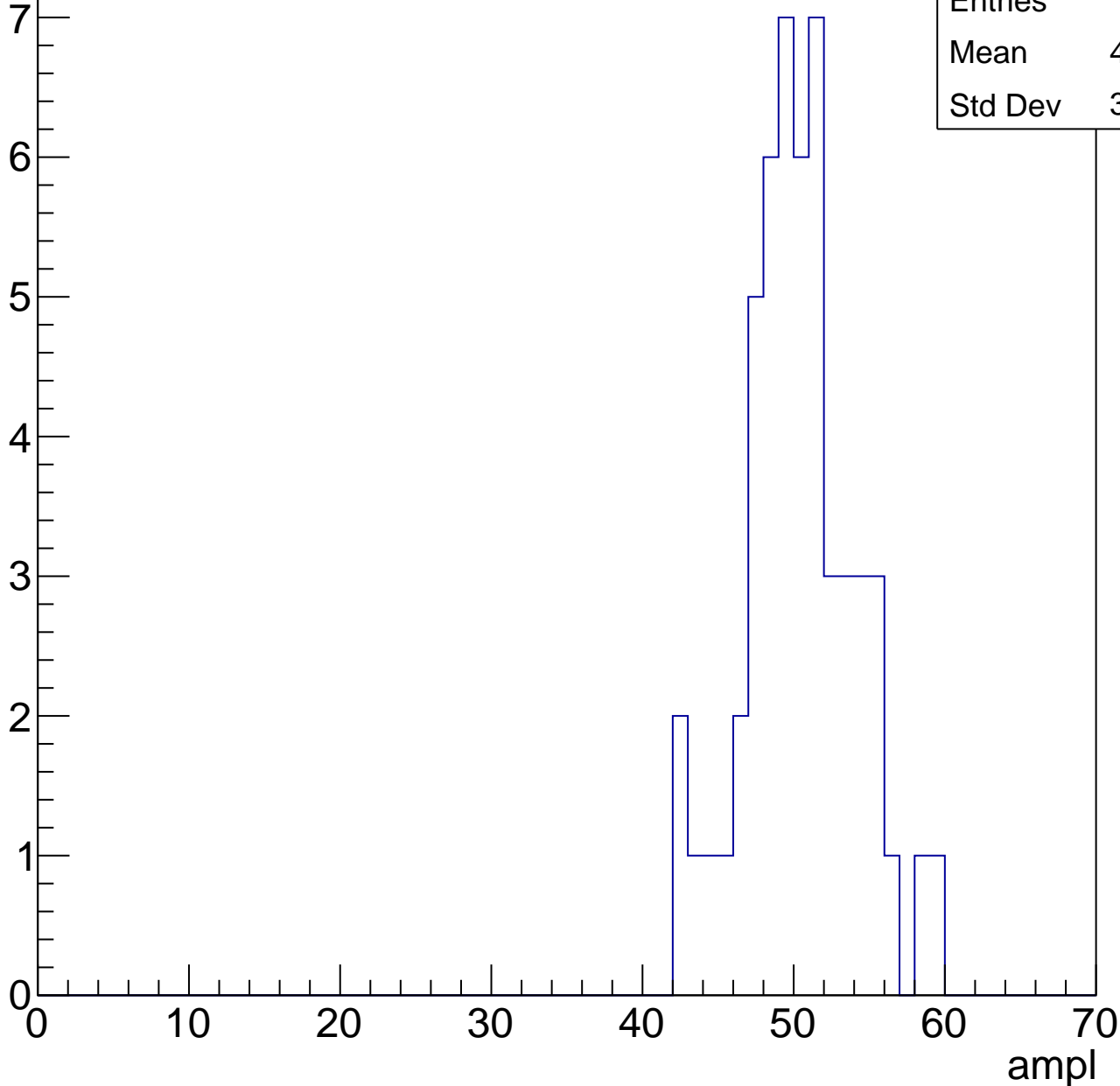


B1L103S, U10-ch44, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	49.92
Std Dev	3.623

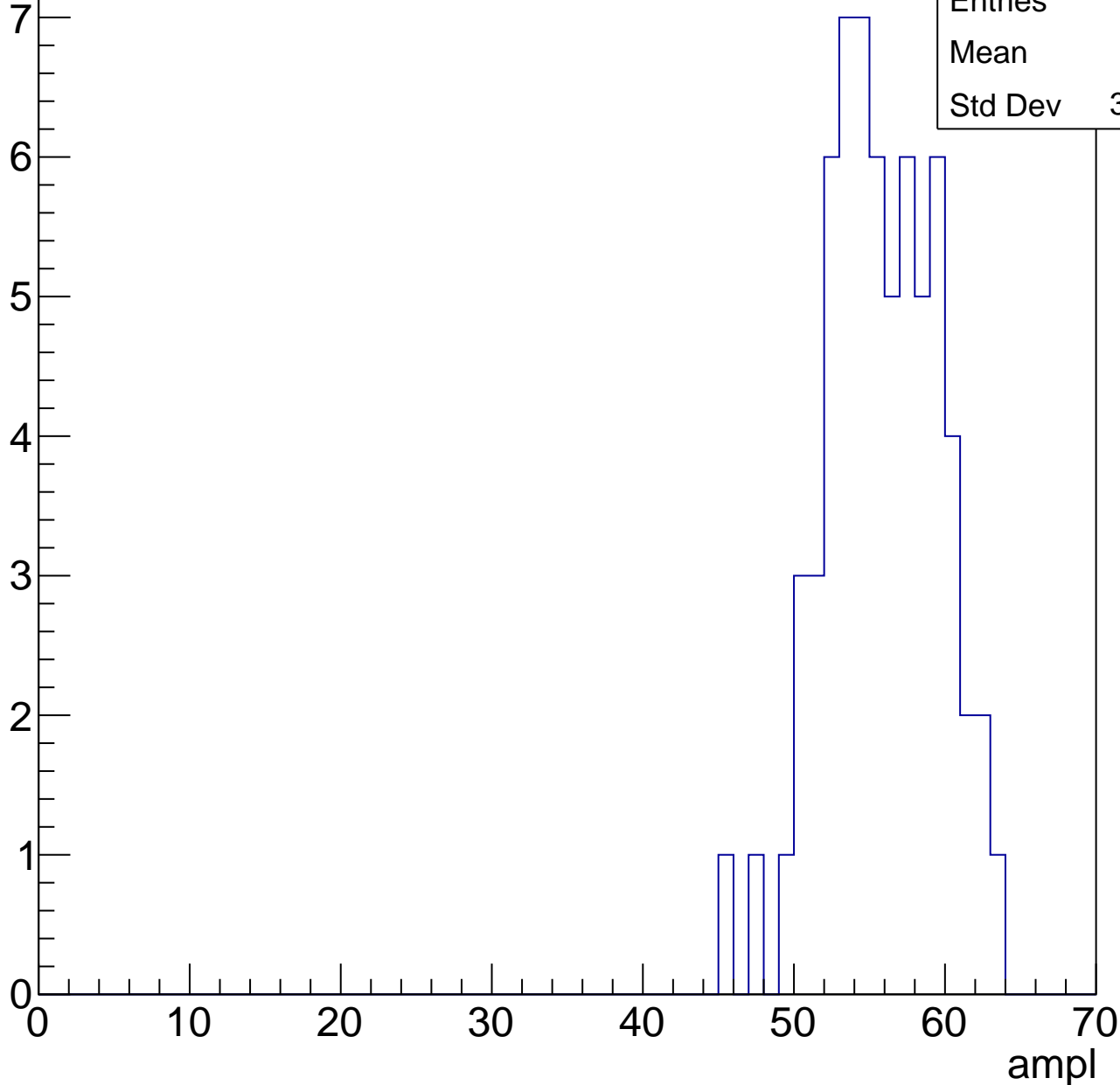


B1L103S, U10-ch44, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	55.3
Std Dev	3.713

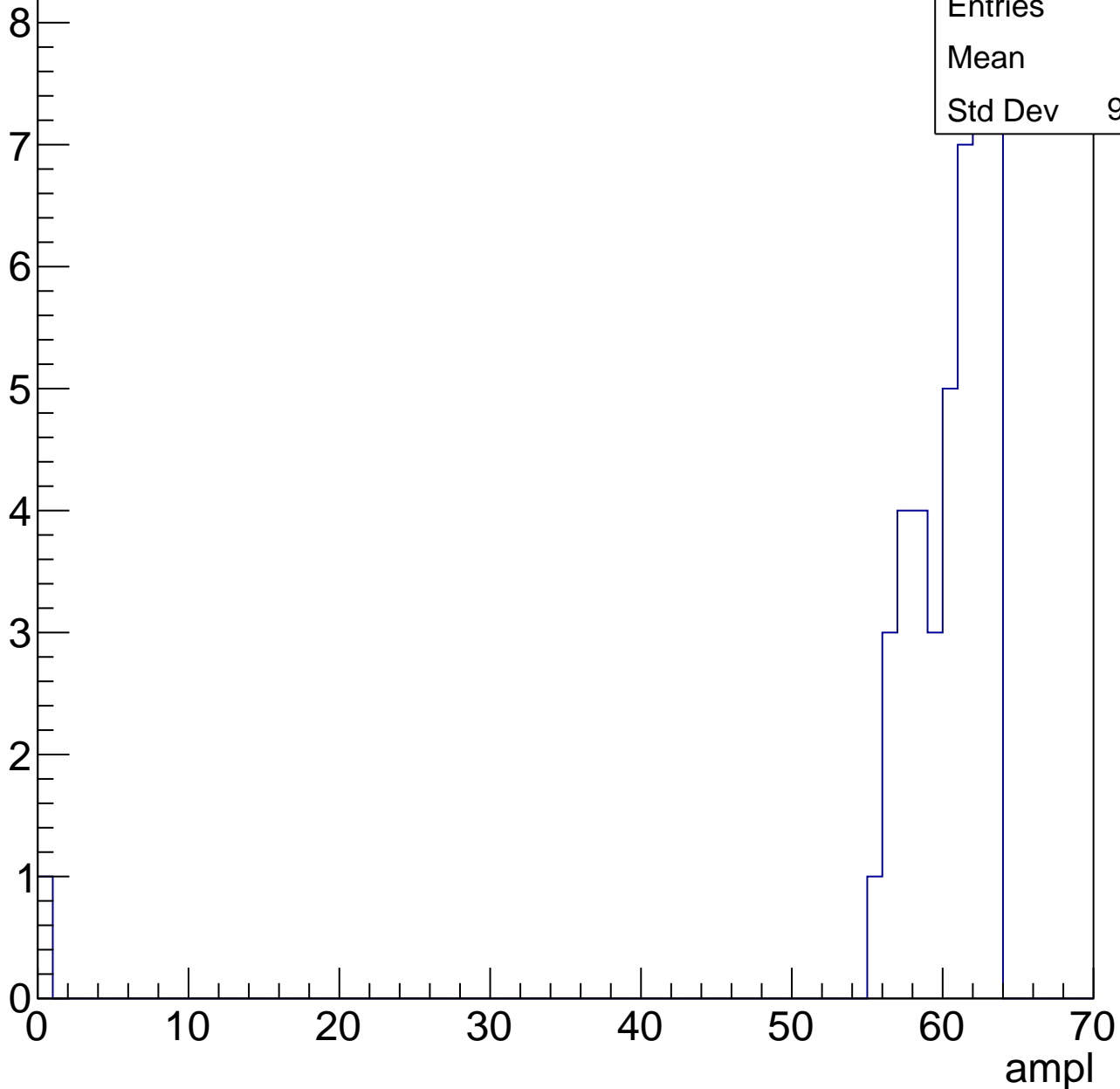


B1L103S, U10-ch44, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	58.8
Std Dev	9.263



B1L103S, U10-ch44, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

4

Mean

60

Std Dev

2.55

B1L103S, U10-ch44, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch45, adc0

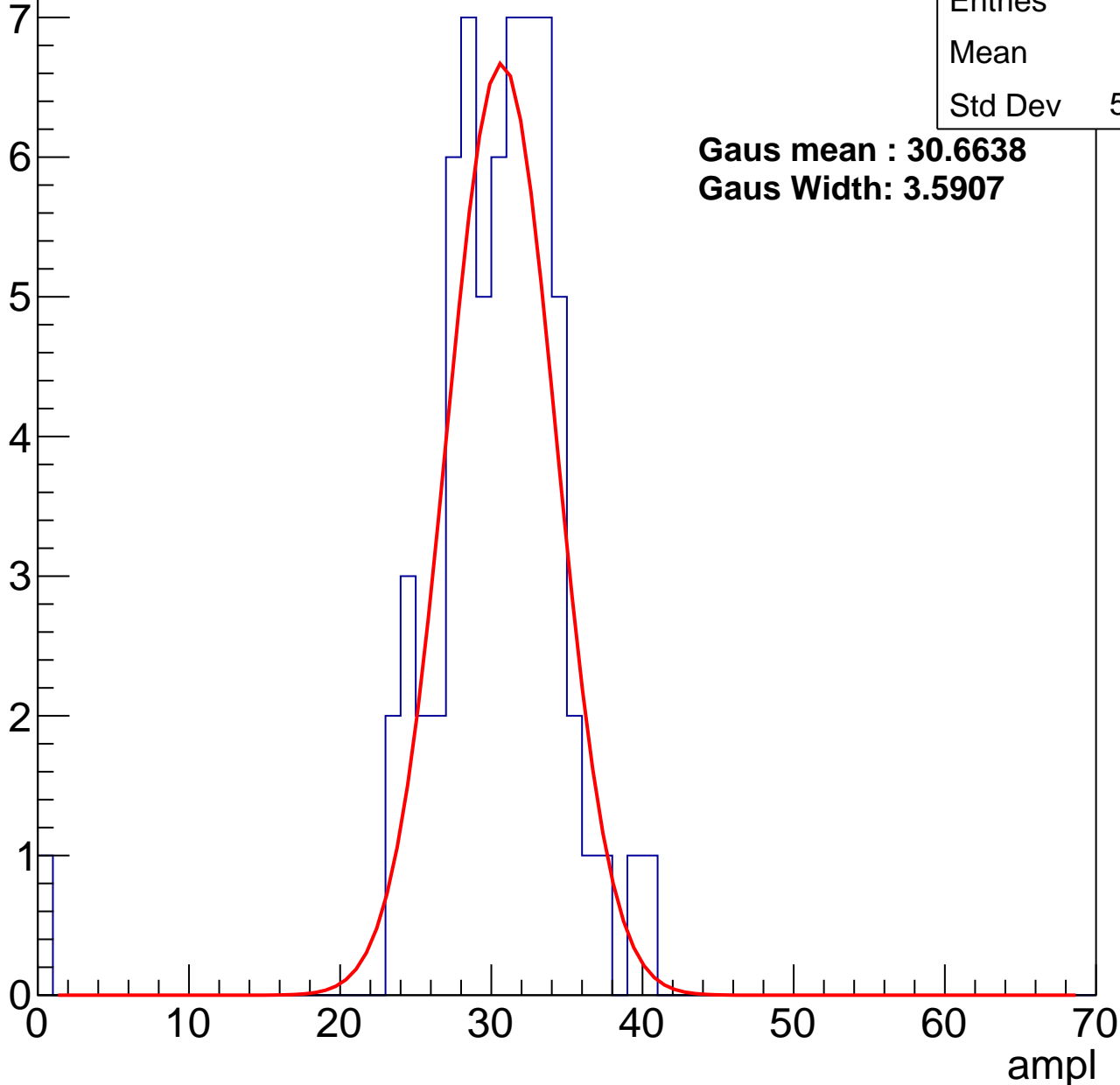
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.8
Std Dev	5.173

Gaus mean : 30.6638

Gaus Width: 3.5907



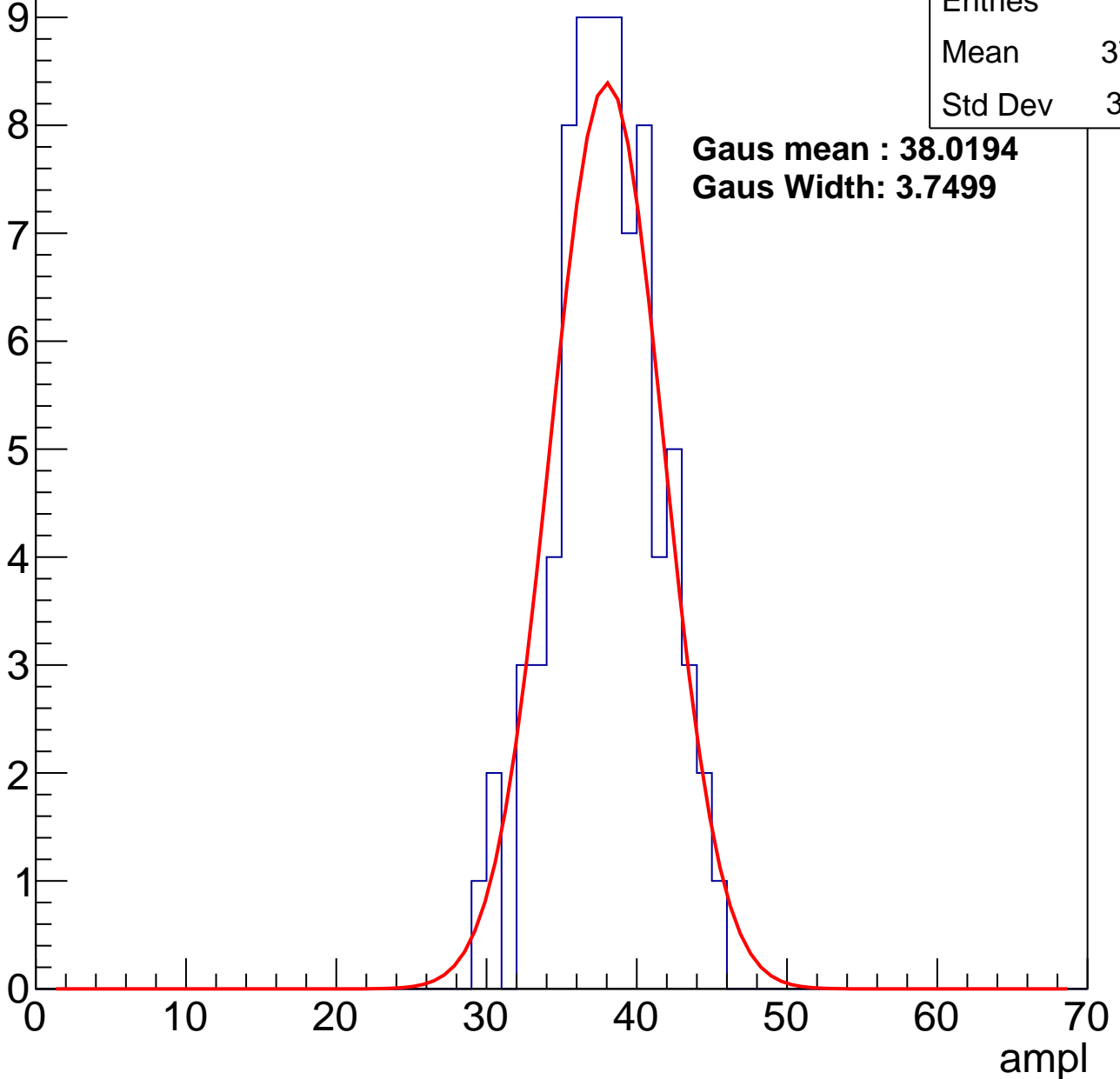
B1L103S, U10-ch45, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	37.54
Std Dev	3.411

Gaus mean : 38.0194
Gaus Width: 3.7499



B1L103S, U10-ch45, adc2

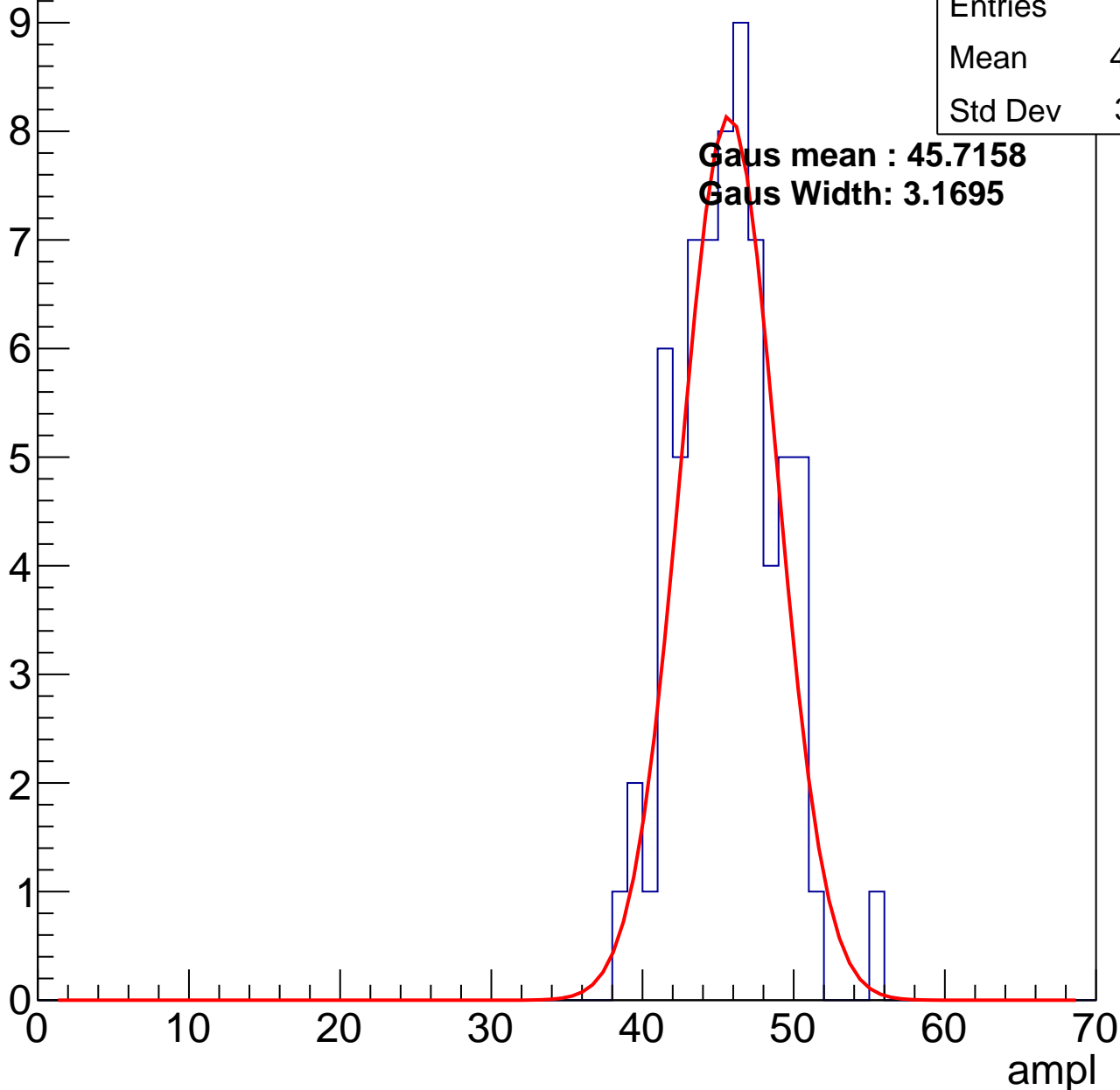
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	45.17
Std Dev	3.261

Gaus mean : 45.7158

Gaus Width: 3.1695

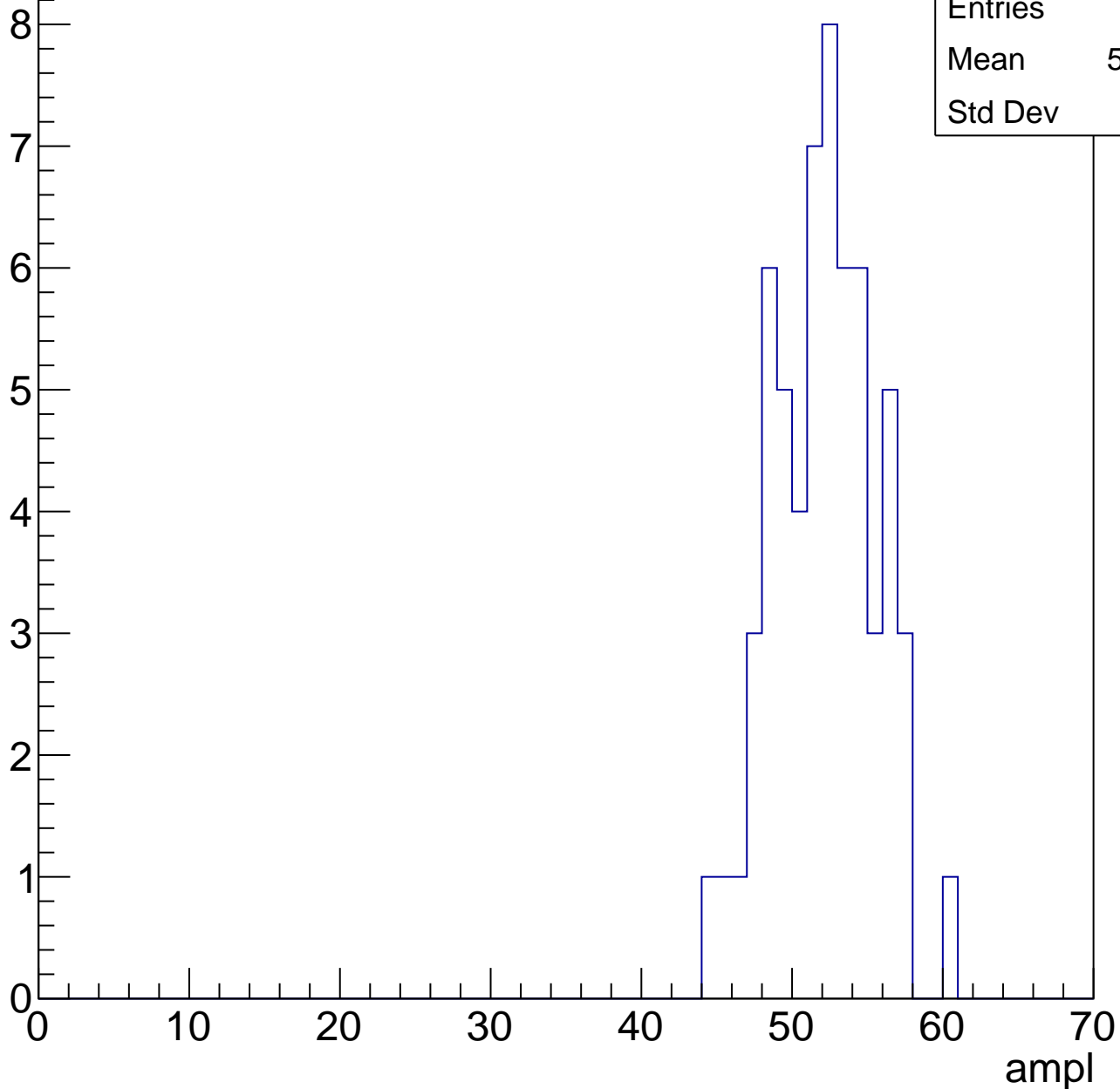


B1L103S, U10-ch45, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	51.67
Std Dev	3.31

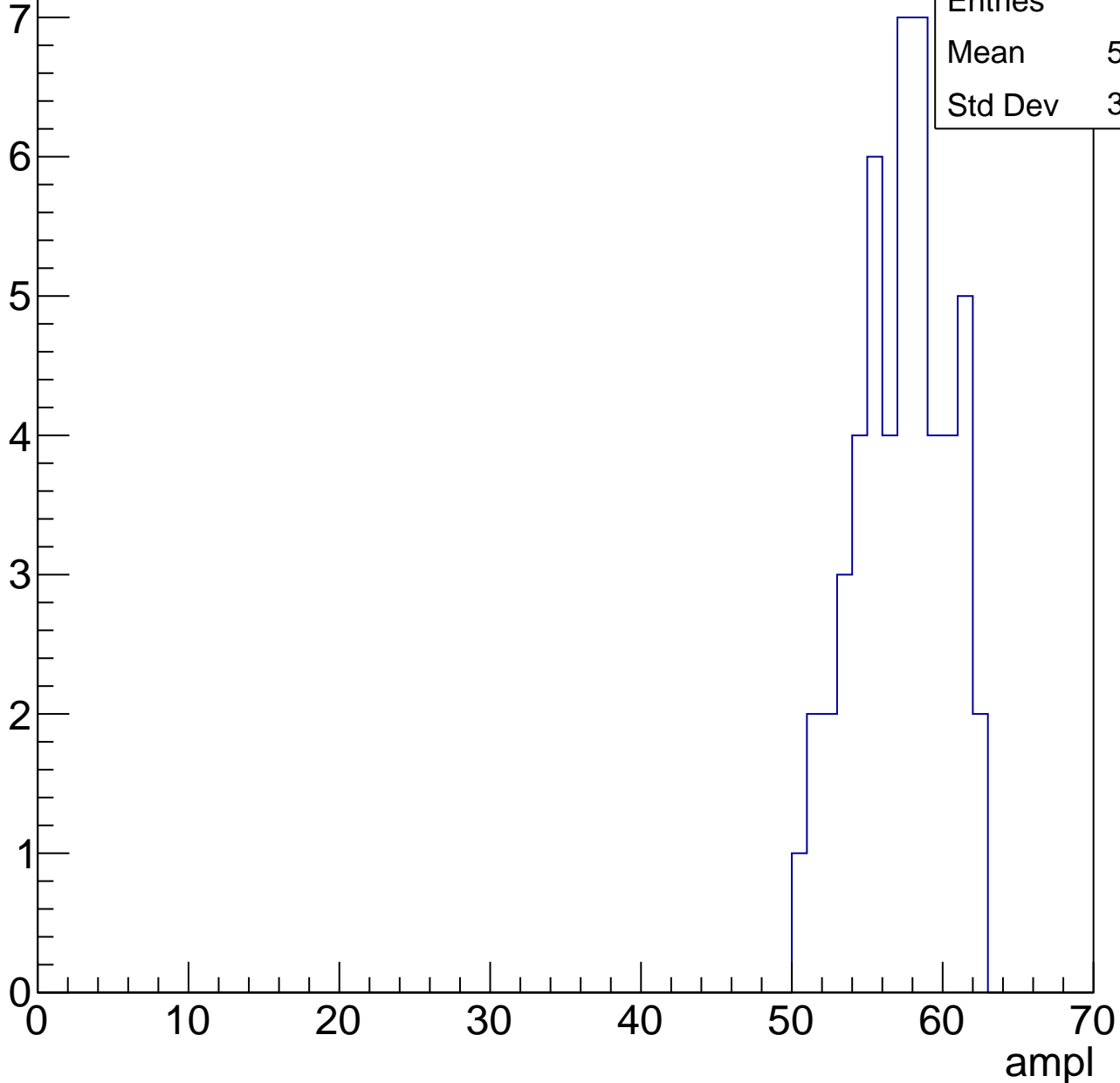


B1L103S, U10-ch45, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	56.76
Std Dev	3.033

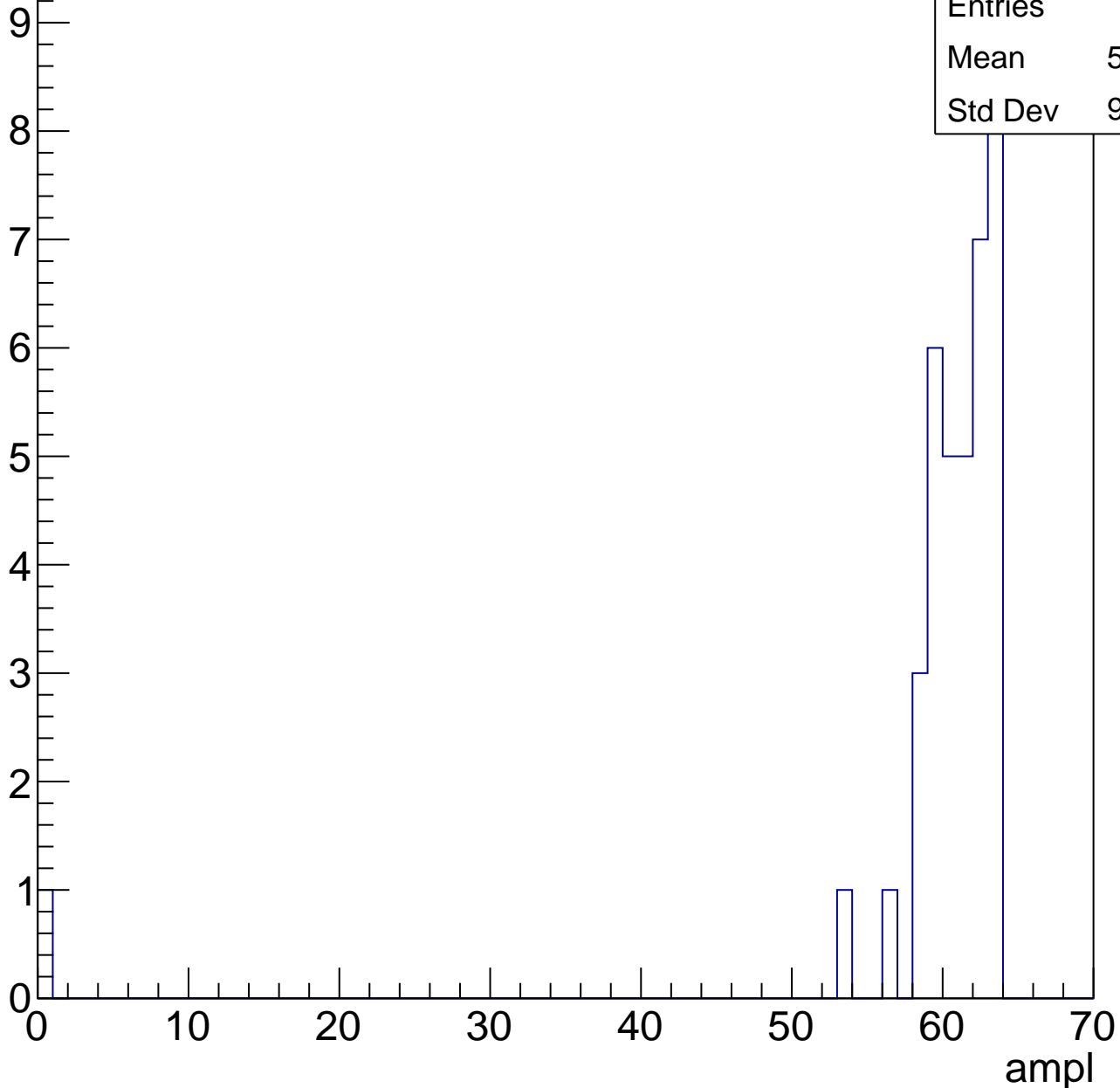


B1L103S, U10-ch45, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	38
Mean	59.03
Std Dev	9.949



B1L103S, U10-ch45, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch45, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch46, adc0

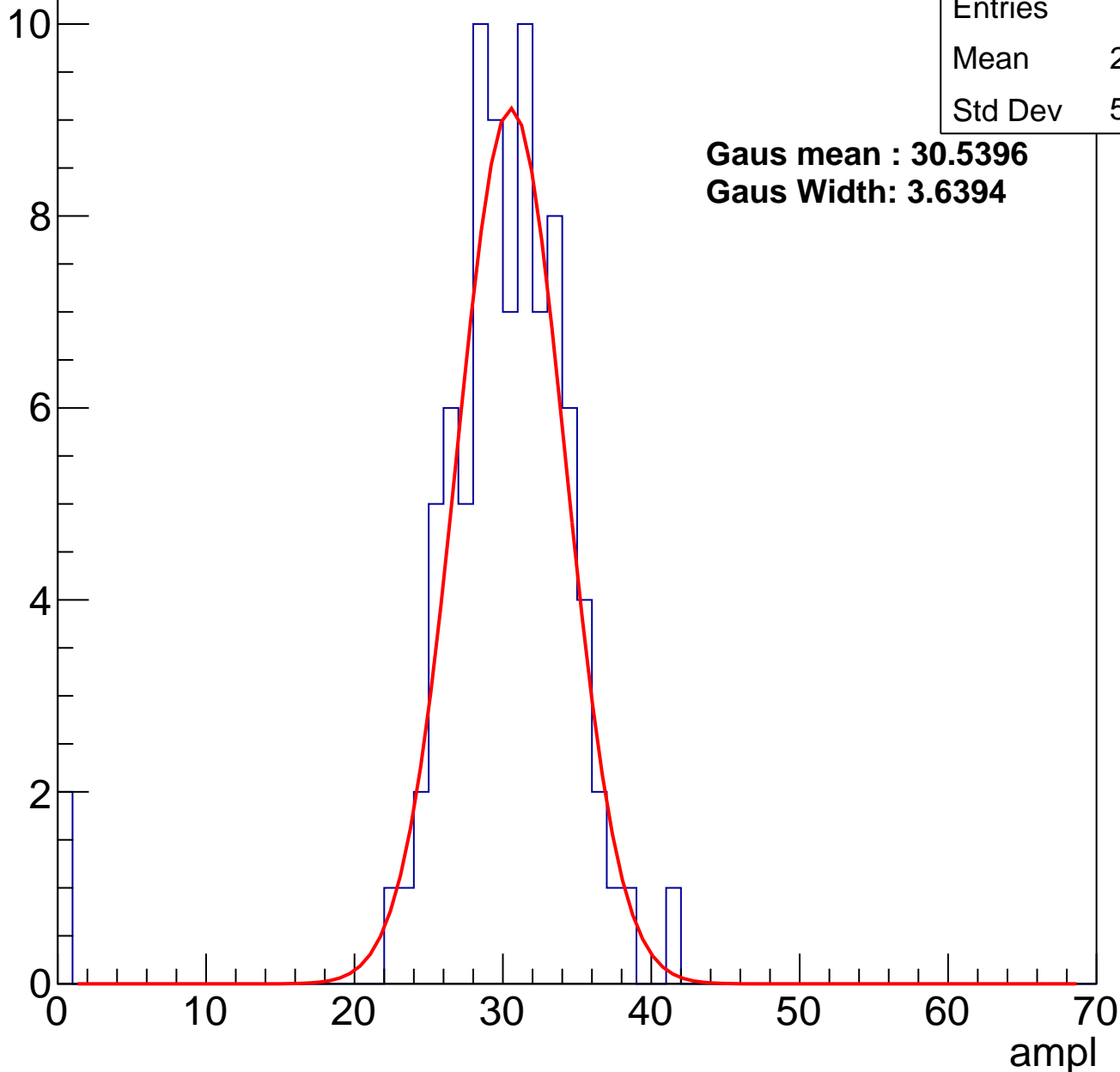
calib_packv5_042523_0143.root, FC#7, port C2

Entries	88
Mean	29.43
Std Dev	5.724

Gaus mean : 30.5396

Gaus Width: 3.6394

Entry



B1L103S, U10-ch46, adc1

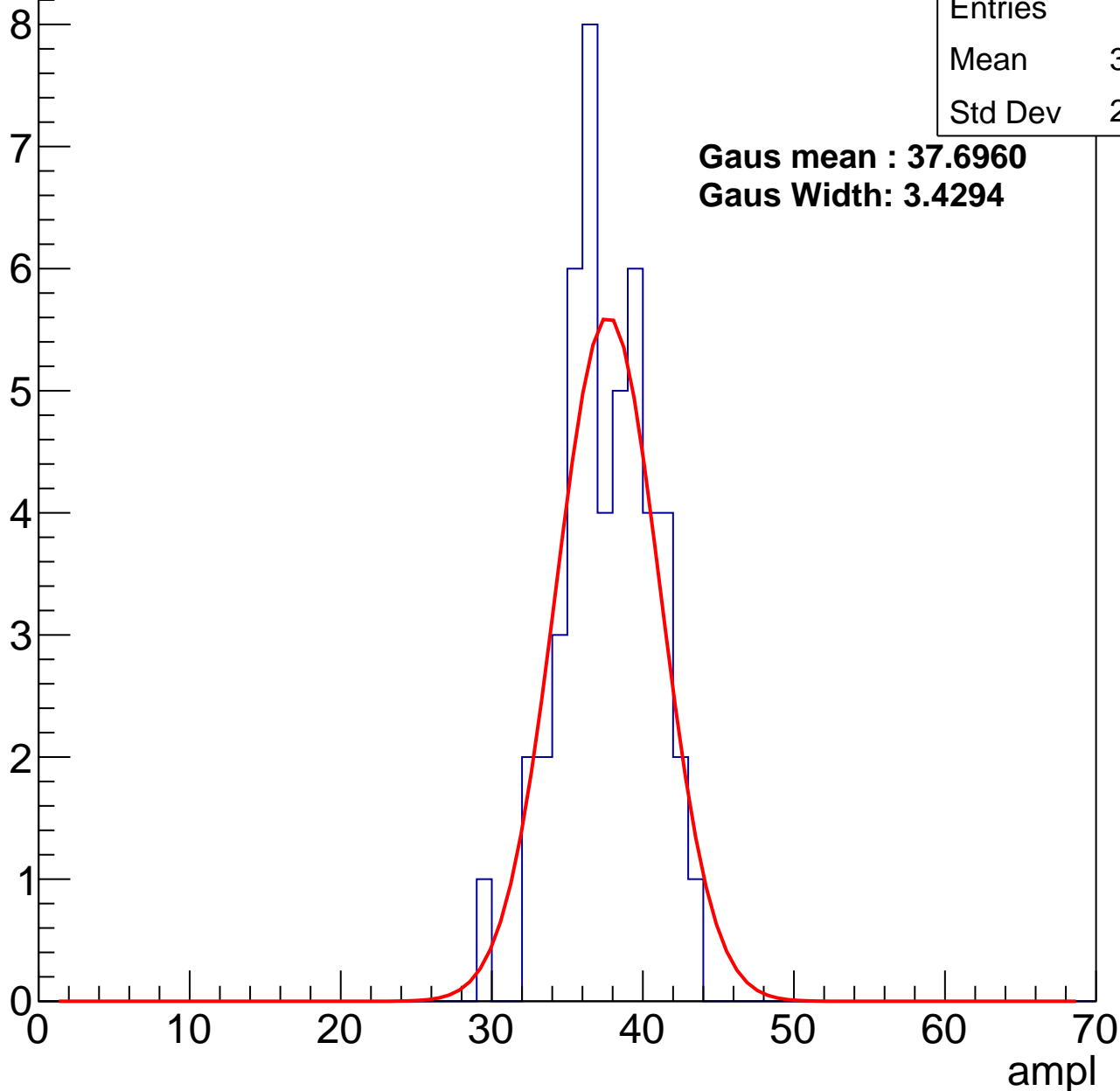
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	37.12
Std Dev	2.955

Gaus mean : 37.6960

Gaus Width: 3.4294



B1L103S, U10-ch46, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	76
Mean	43.25
Std Dev	4.158

Gaus mean : 43.2052

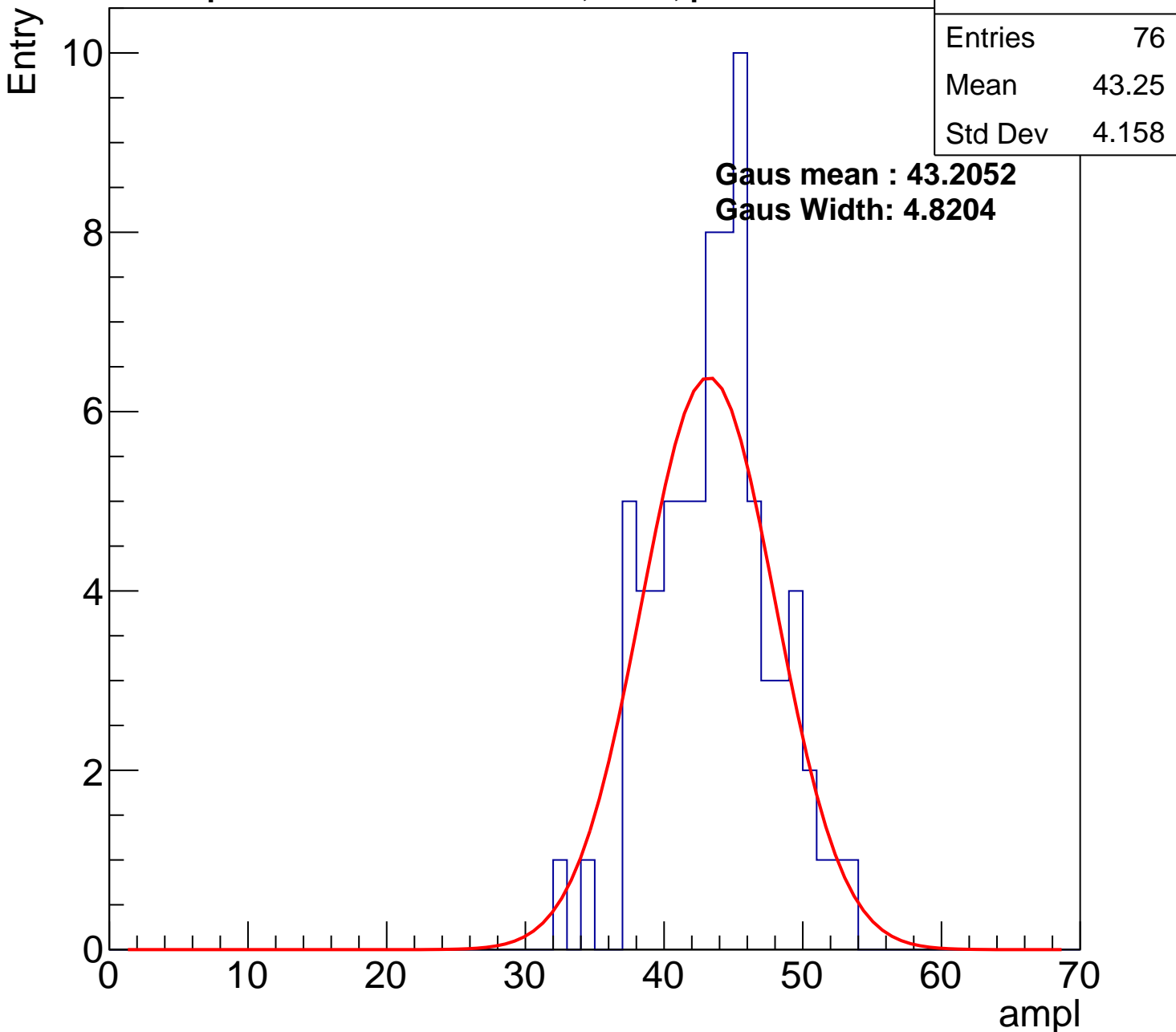
Gaus Width: 4.8204

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

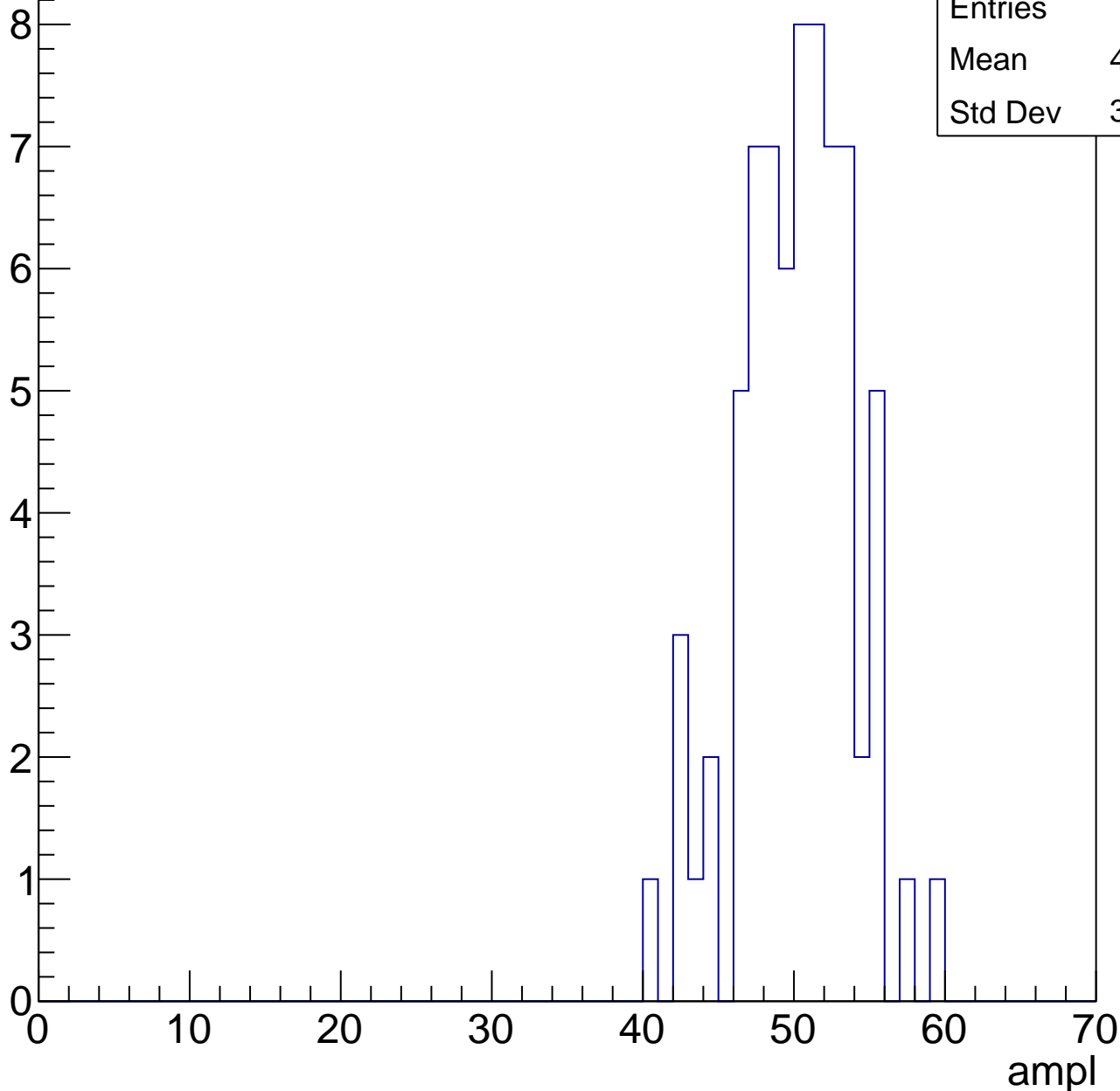


B1L103S, U10-ch46, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	49.69
Std Dev	3.695

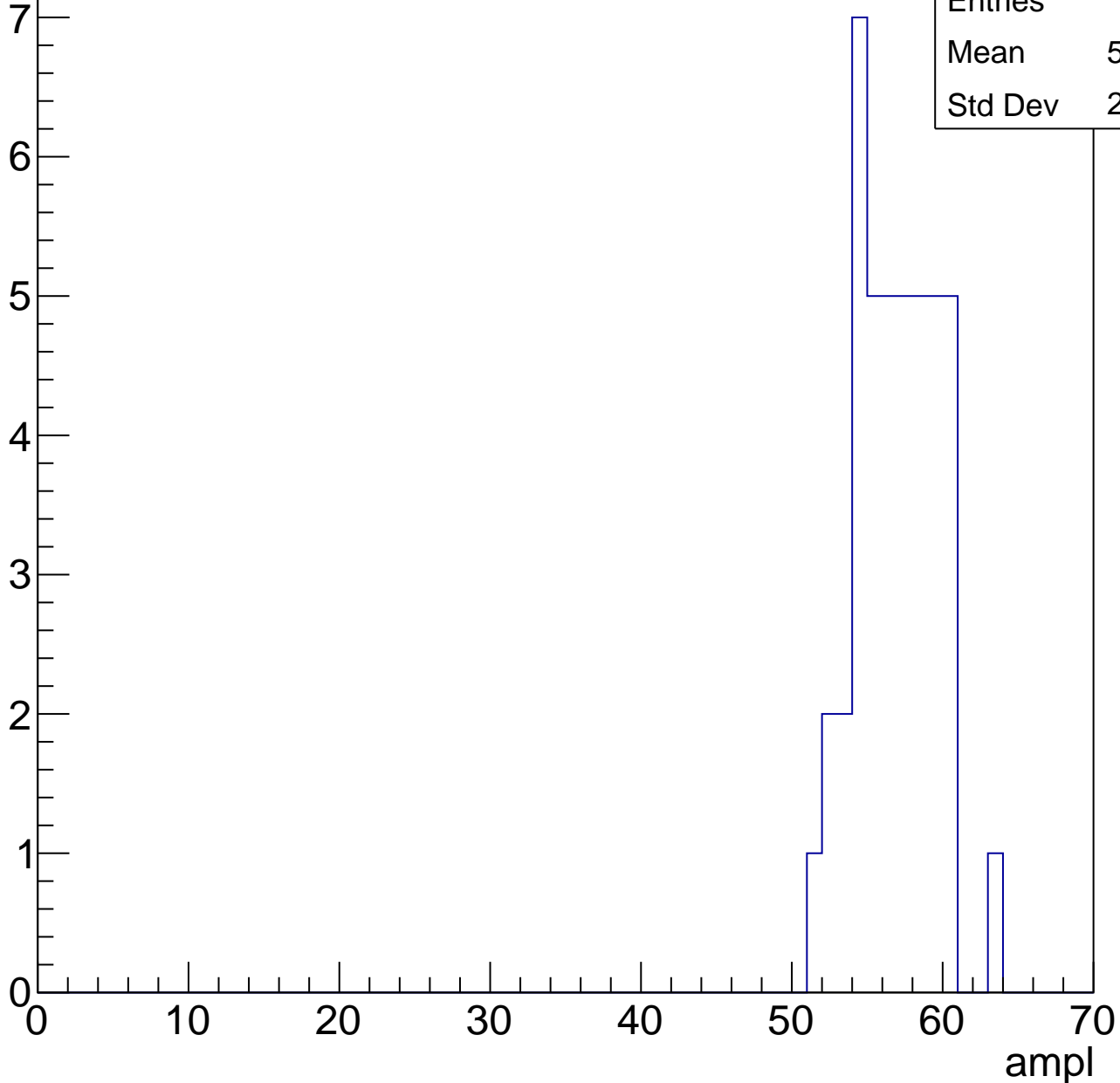


B1L103S, U10-ch46, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	56.44
Std Dev	2.635



B1L103S, U10-ch46, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	50
Mean	58.68
Std Dev	8.705

Entry

10

8

6

4

2

0

0

10

20

30

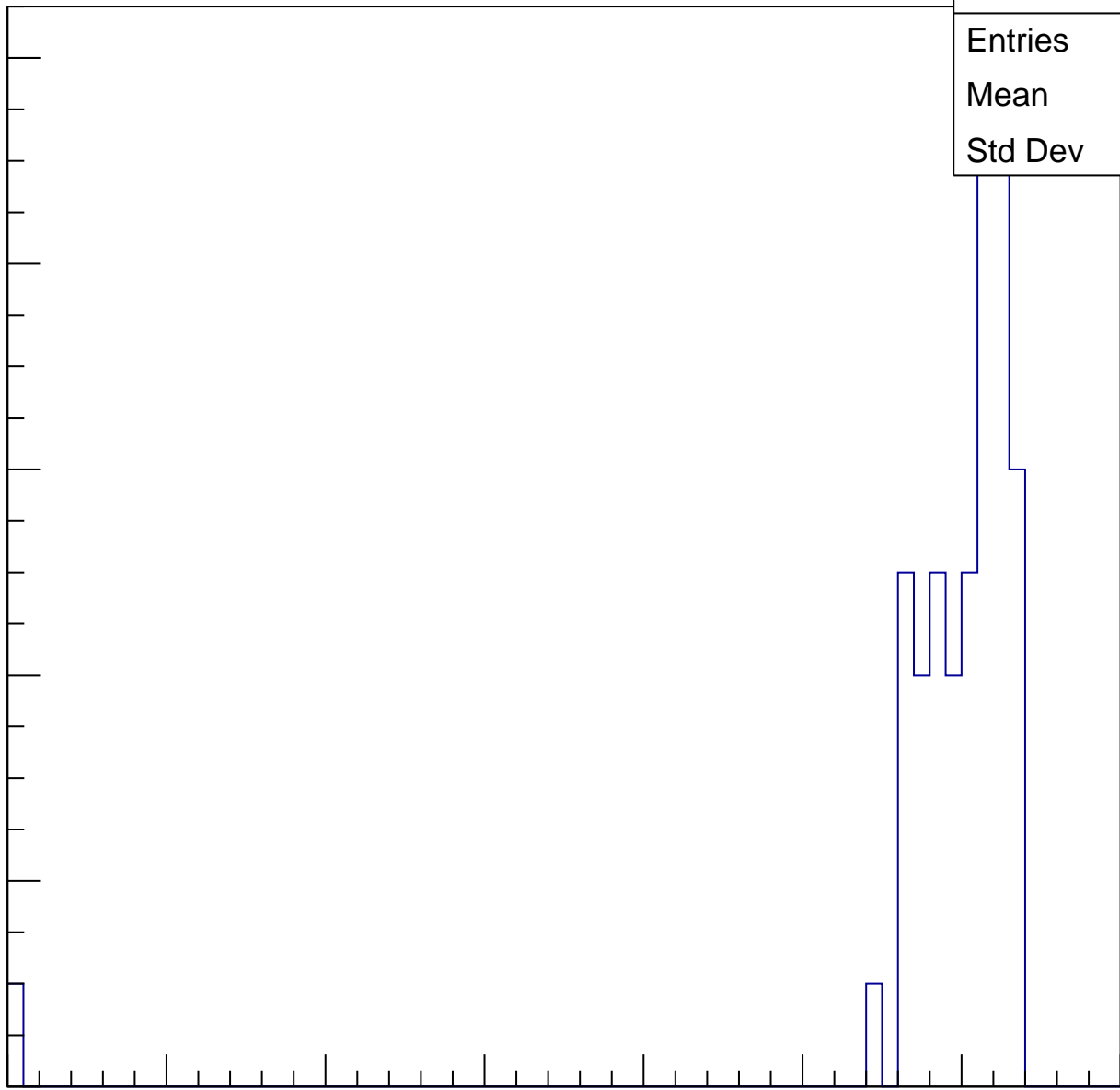
40

50

60

70

ampl



B1L103S, U10-ch46, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch46, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch47, adc0

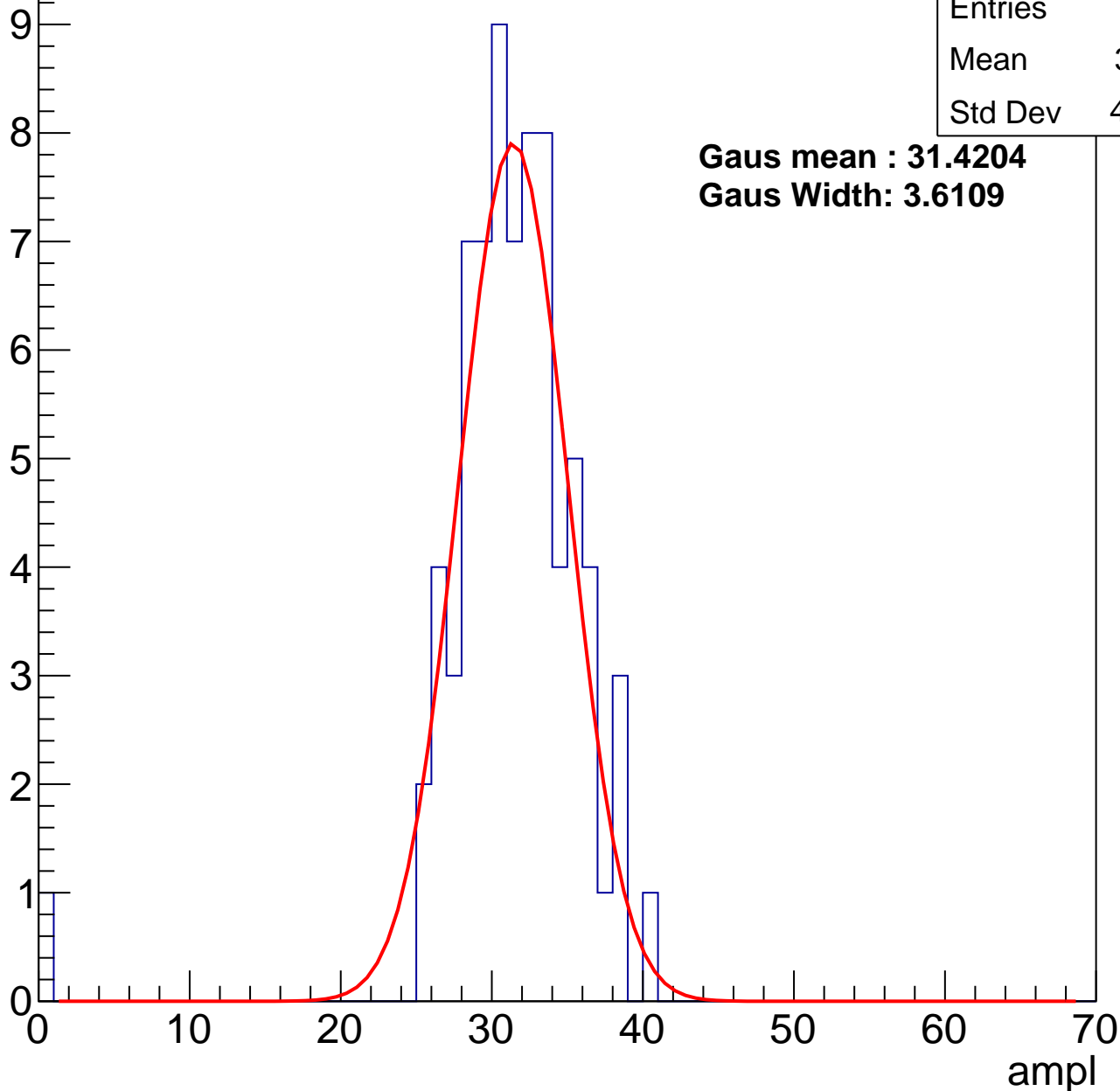
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	30.91
Std Dev	4.932

Gaus mean : 31.4204

Gaus Width: 3.6109



B1L103S, U10-ch47, adc1

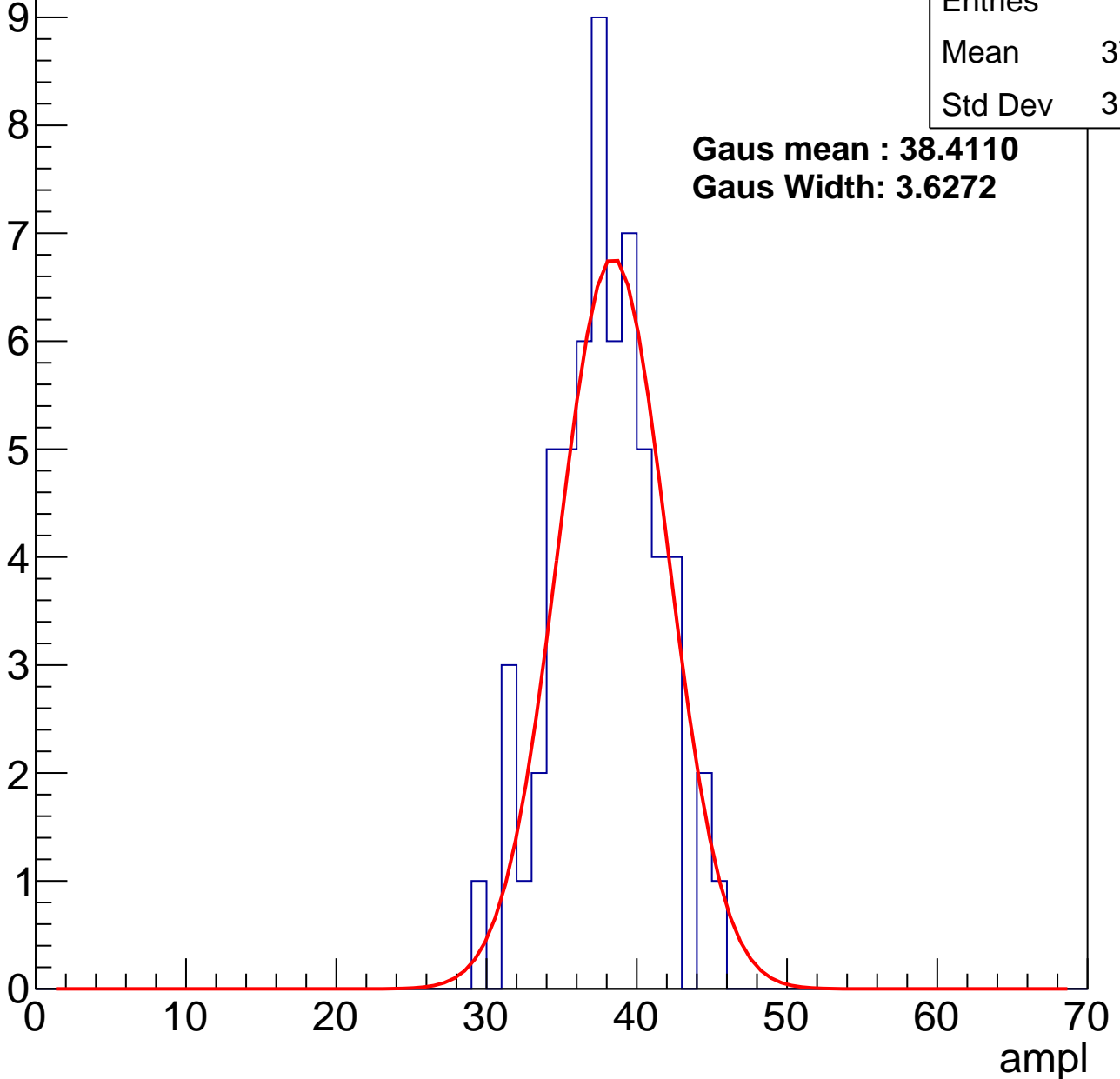
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	37.38
Std Dev	3.379

Gaus mean : 38.4110

Gaus Width: 3.6272



B1L103S, U10-ch47, adc2

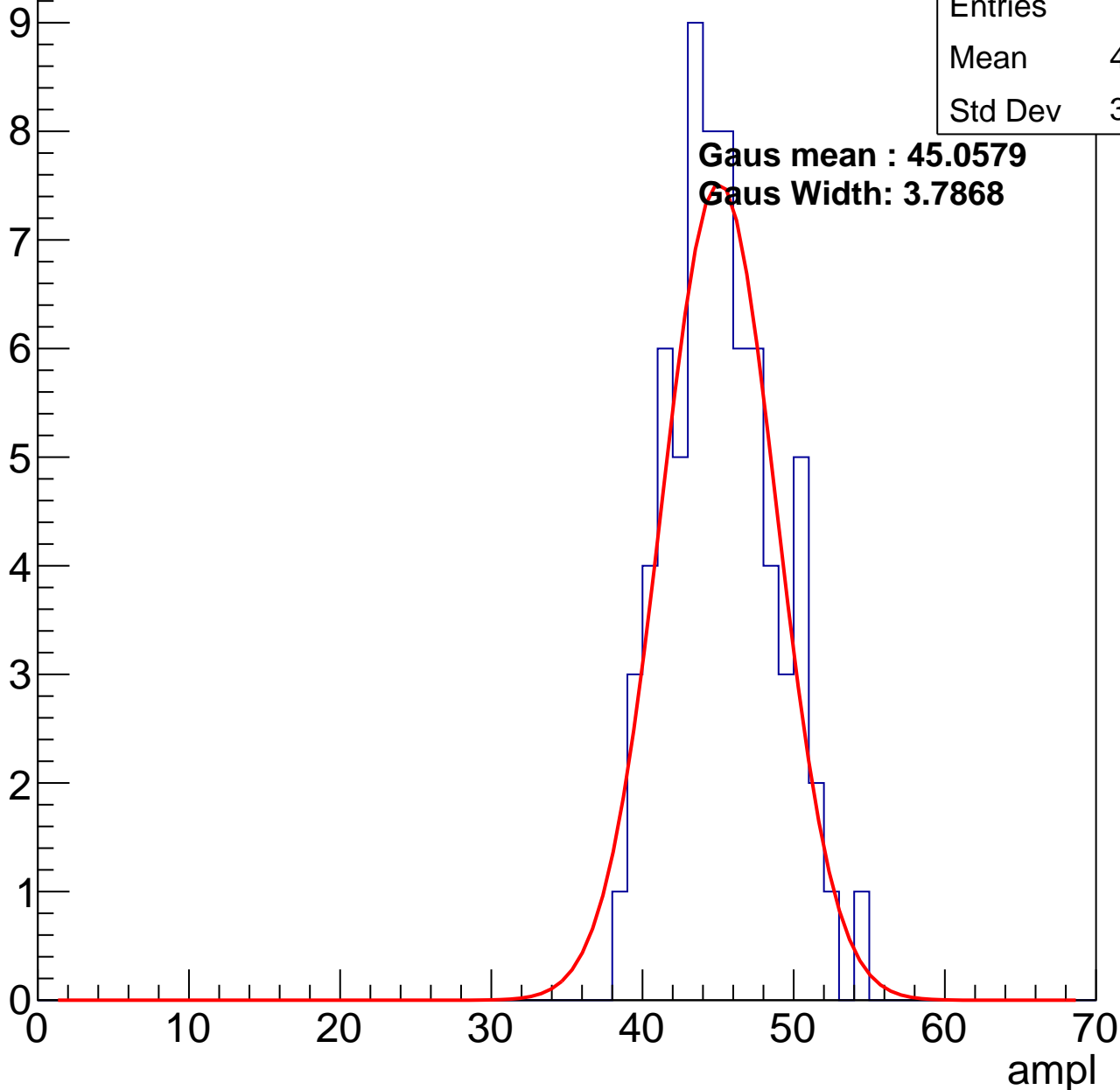
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	44.79
Std Dev	3.496

Gaus mean : 45.0579

Gaus Width: 3.7868

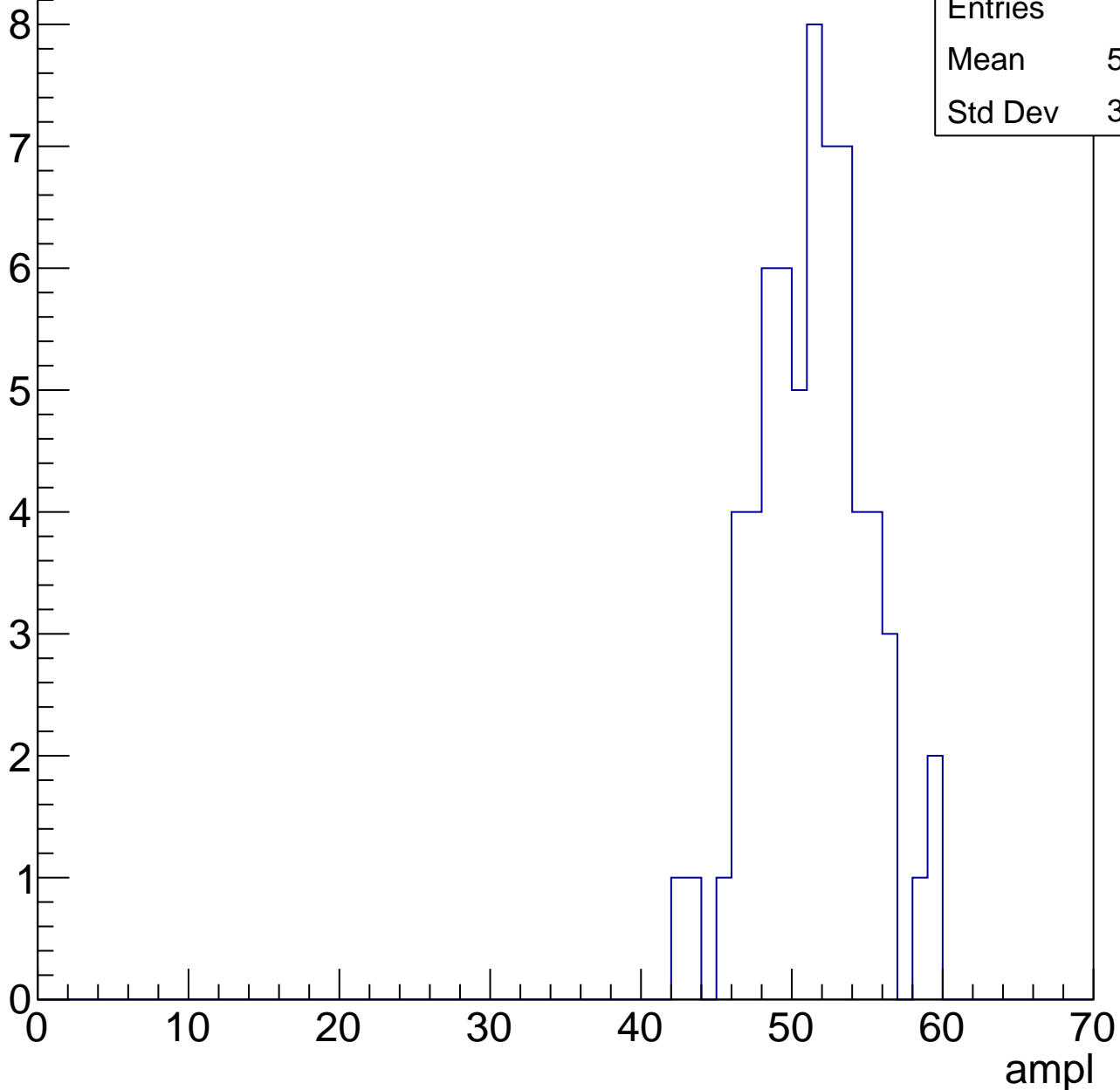


B1L103S, U10-ch47, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	50.89
Std Dev	3.567

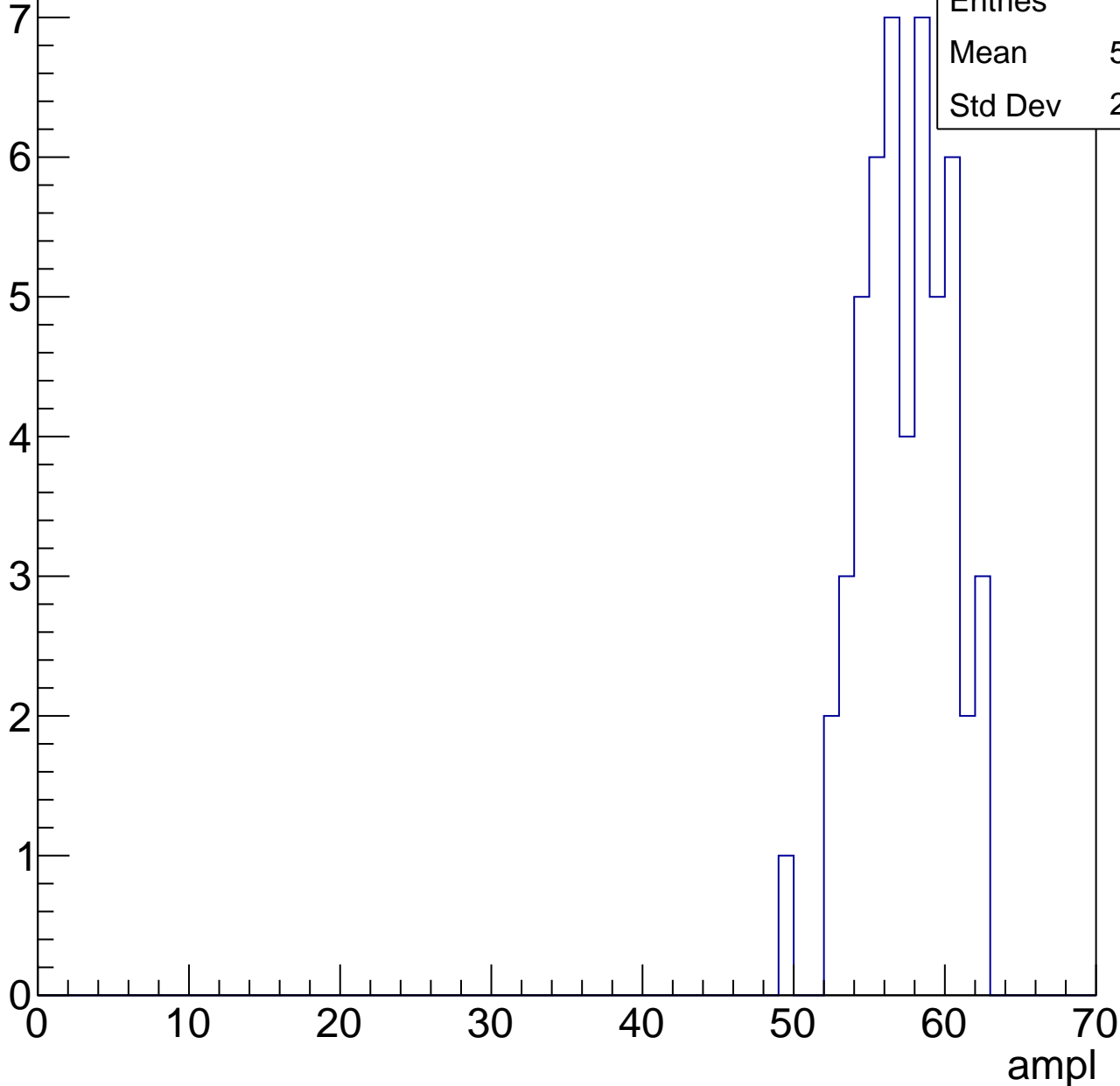


B1L103S, U10-ch47, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	56.88
Std Dev	2.888

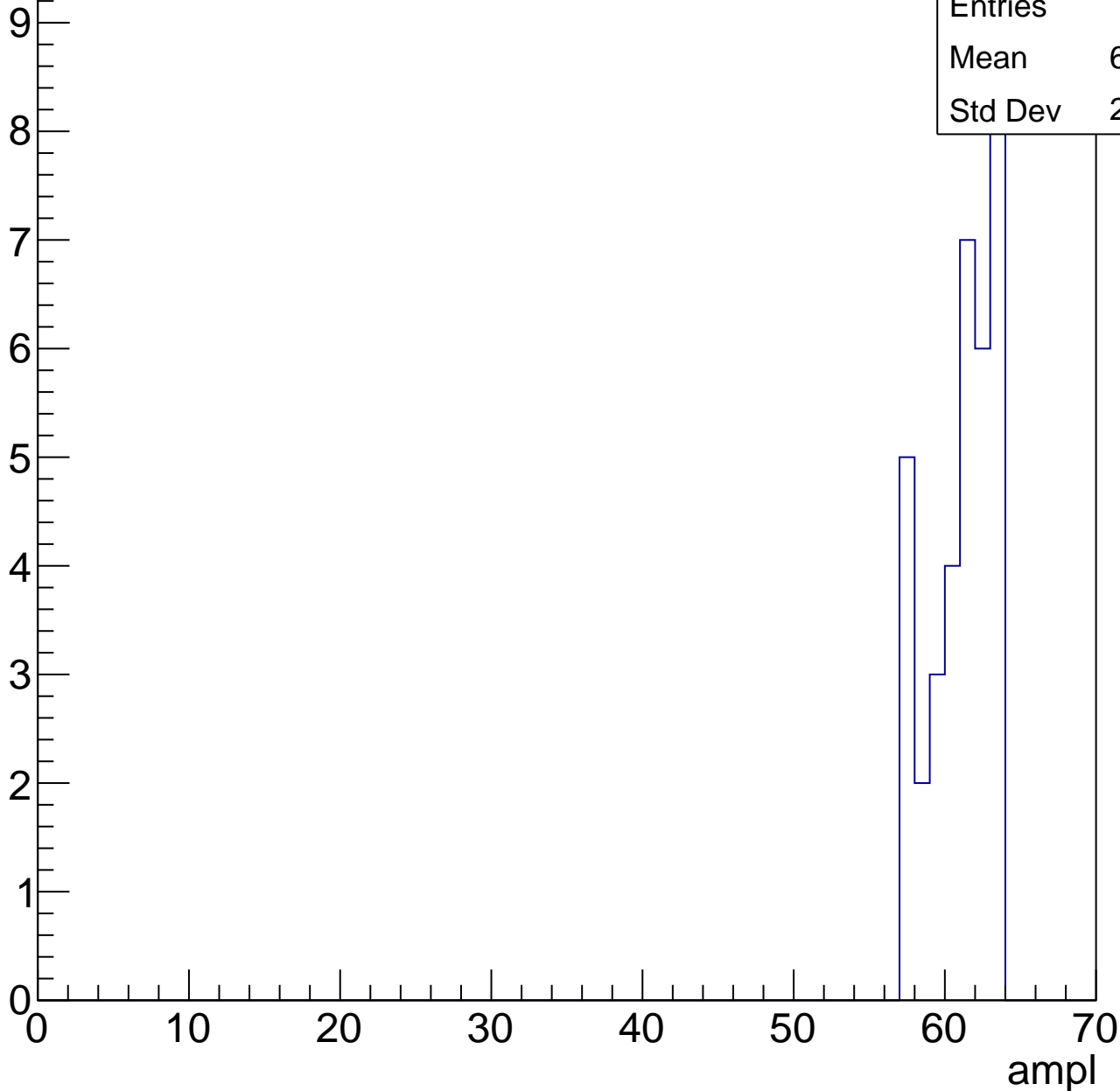


B1L103S, U10-ch47, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

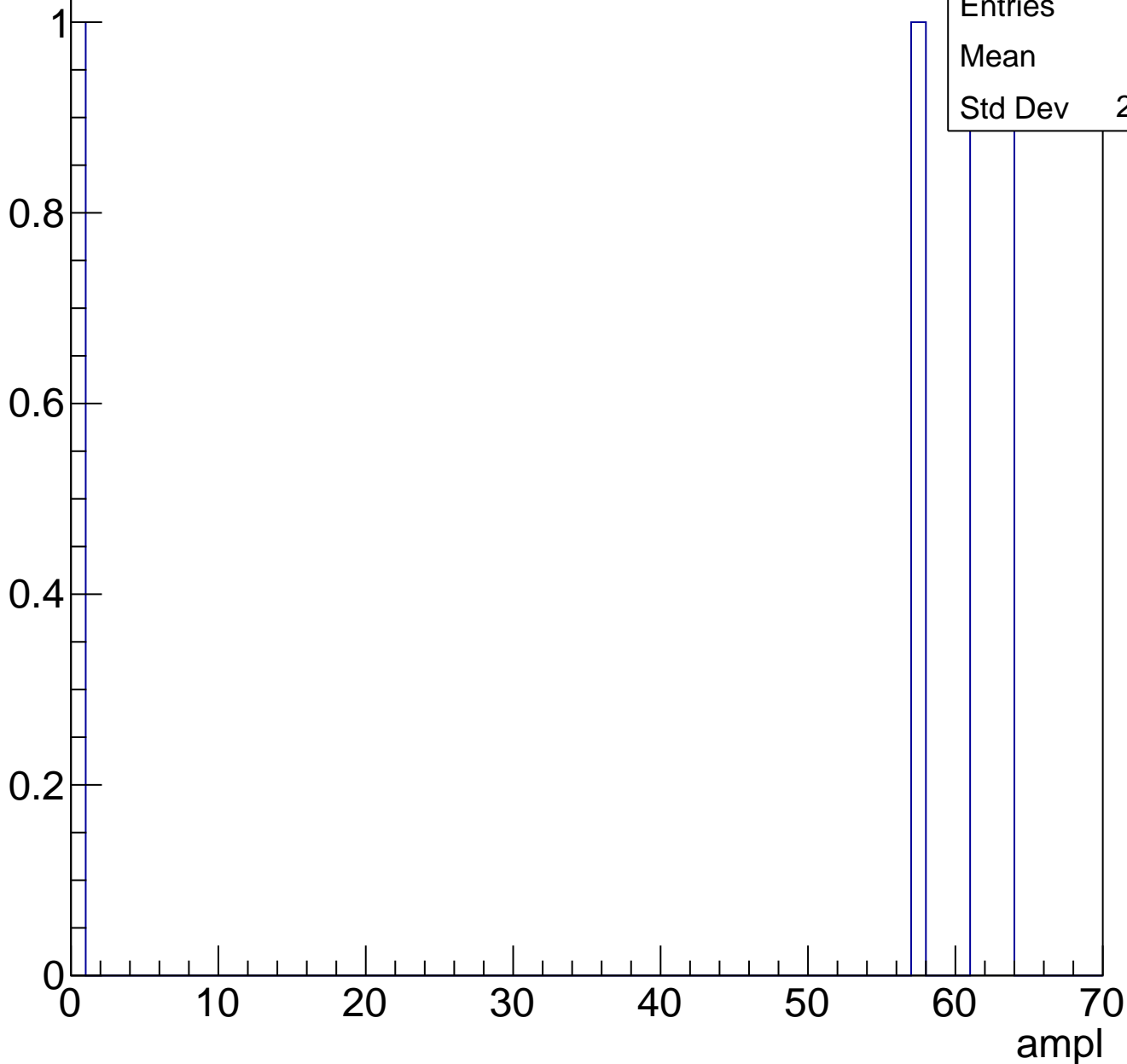
Entries	36
Mean	60.67
Std Dev	2.055



B1L103S, U10-ch47, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch47, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch48, adc0

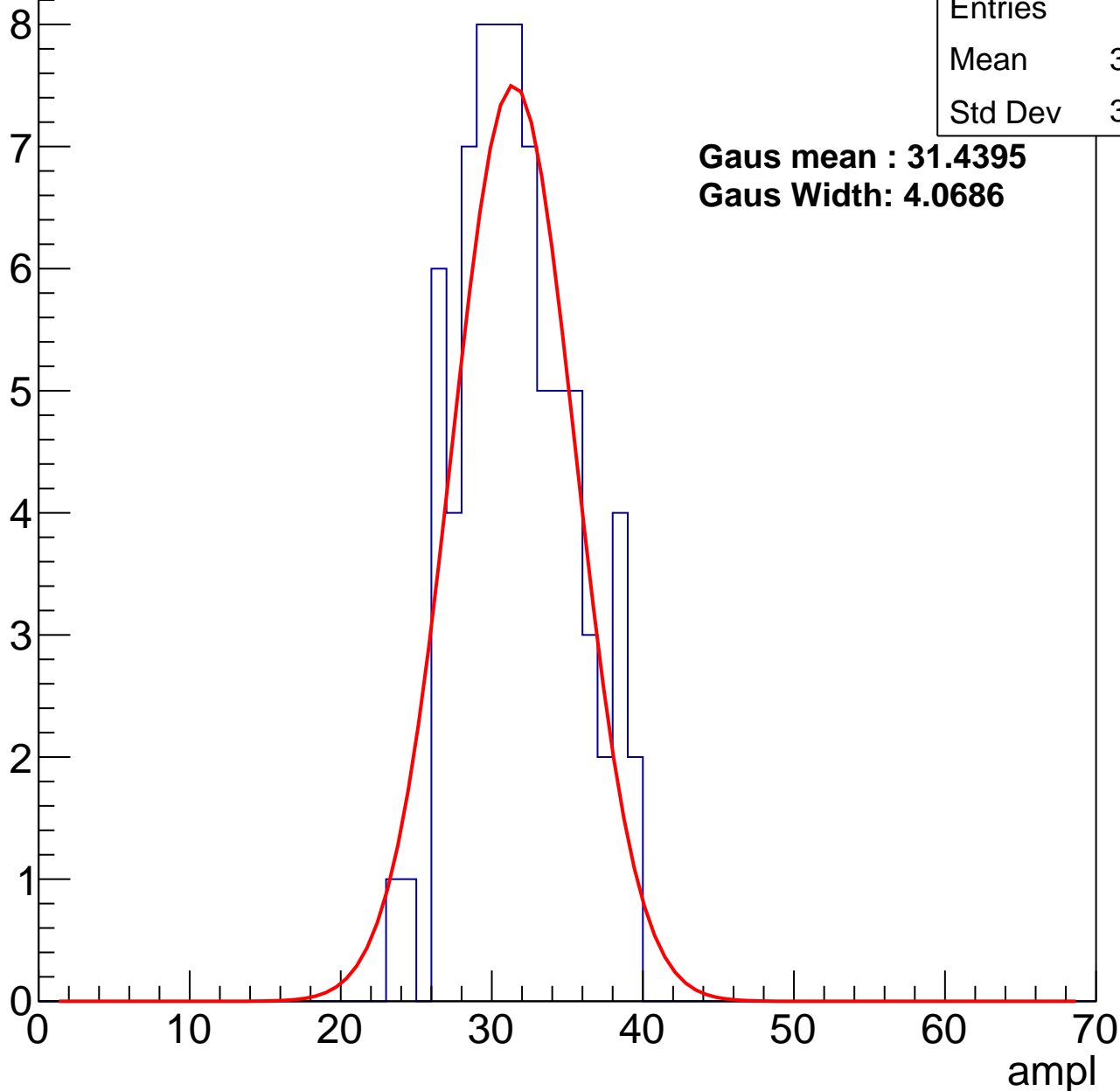
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	31.22
Std Dev	3.716

Gaus mean : 31.4395

Gaus Width: 4.0686



B1L103S, U10-ch48, adc1

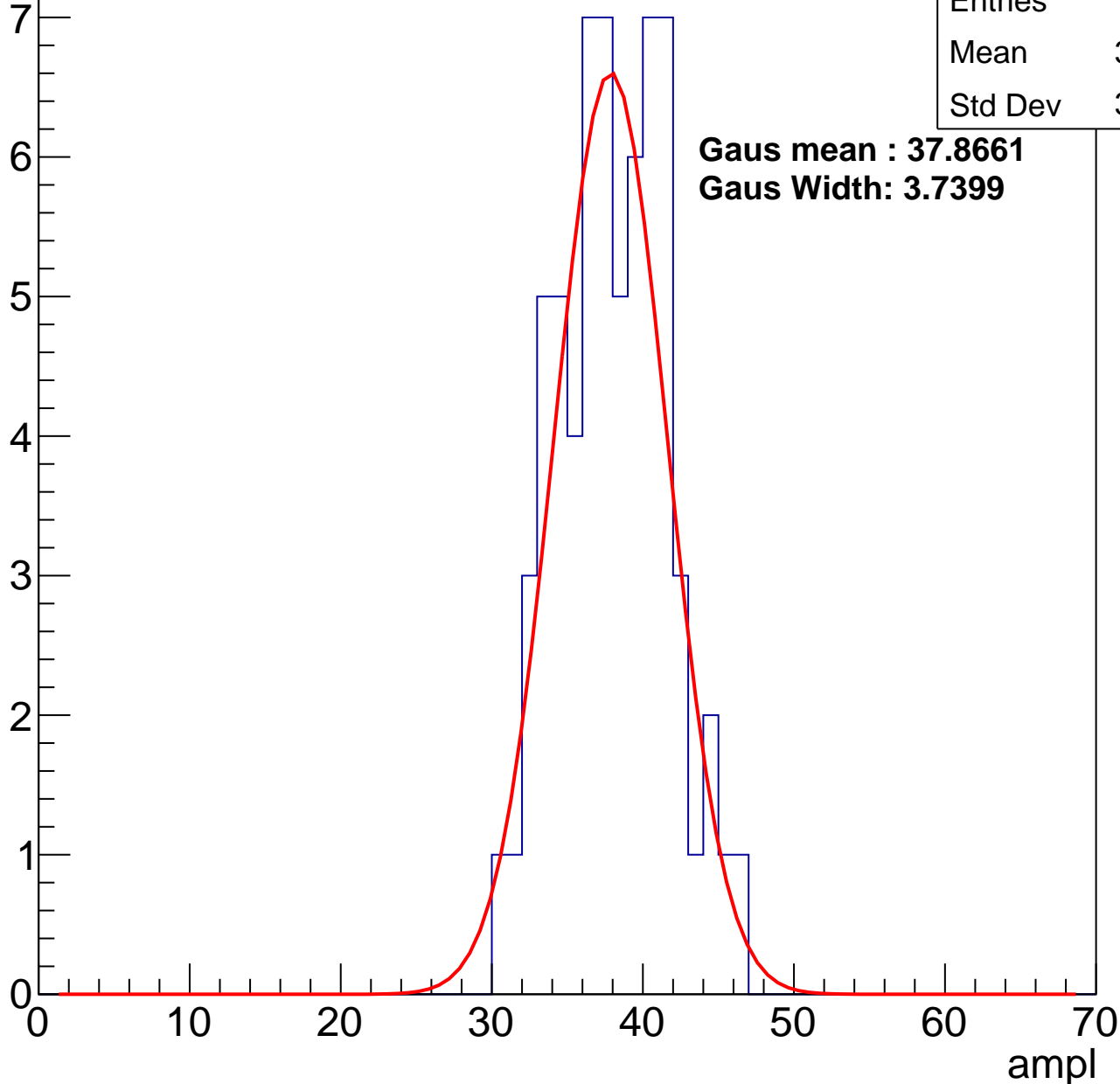
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	37.61
Std Dev	3.571

Gaus mean : 37.8661

Gaus Width: 3.7399



B1L103S, U10-ch48, adc2

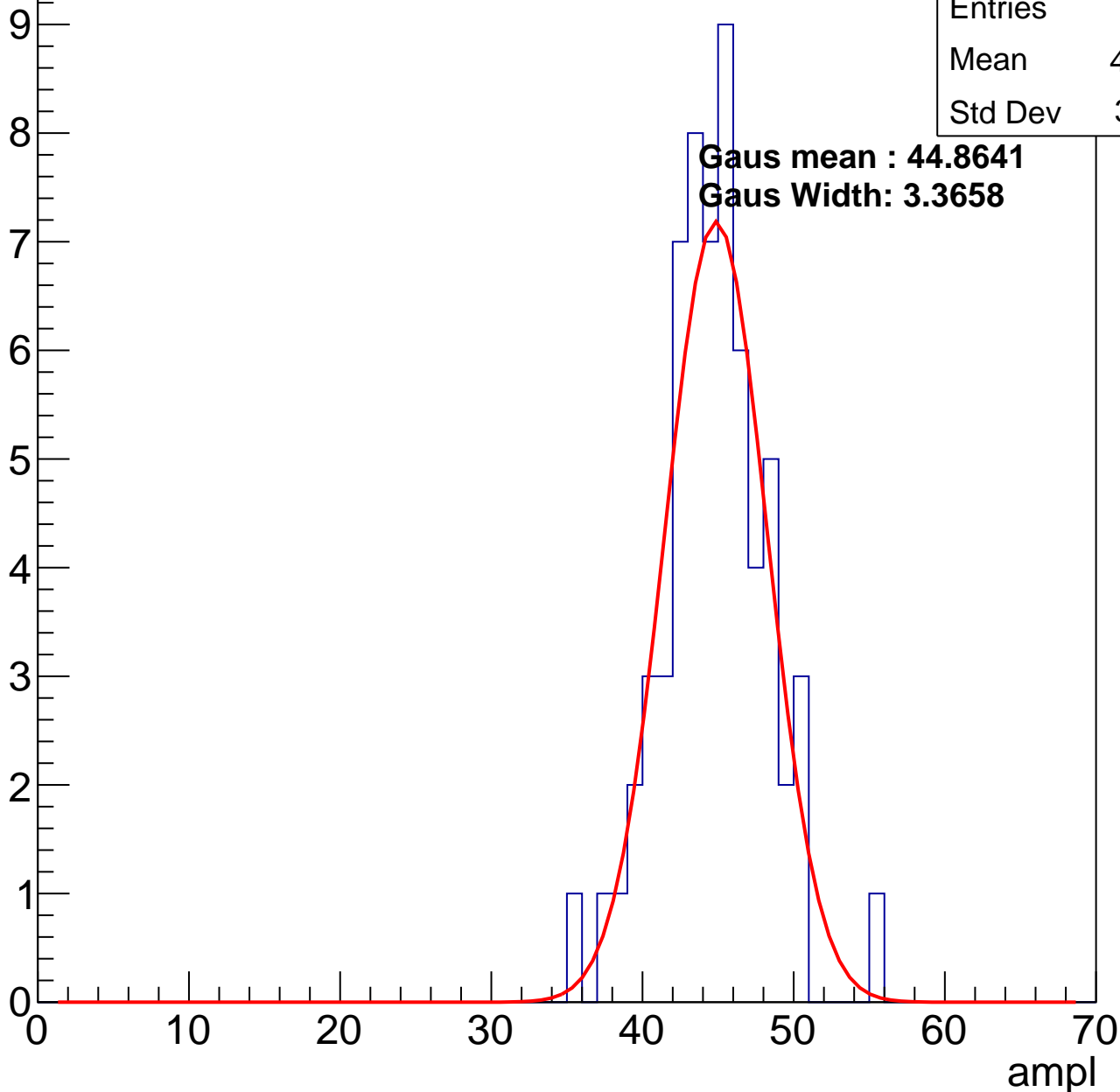
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.27
Std Dev	3.451

Gaus mean : 44.8641

Gaus Width: 3.3658



B1L103S, U10-ch48, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	77
Mean	51.47
Std Dev	3.451

Entry

10

8

6

4

2

0

0

10

20

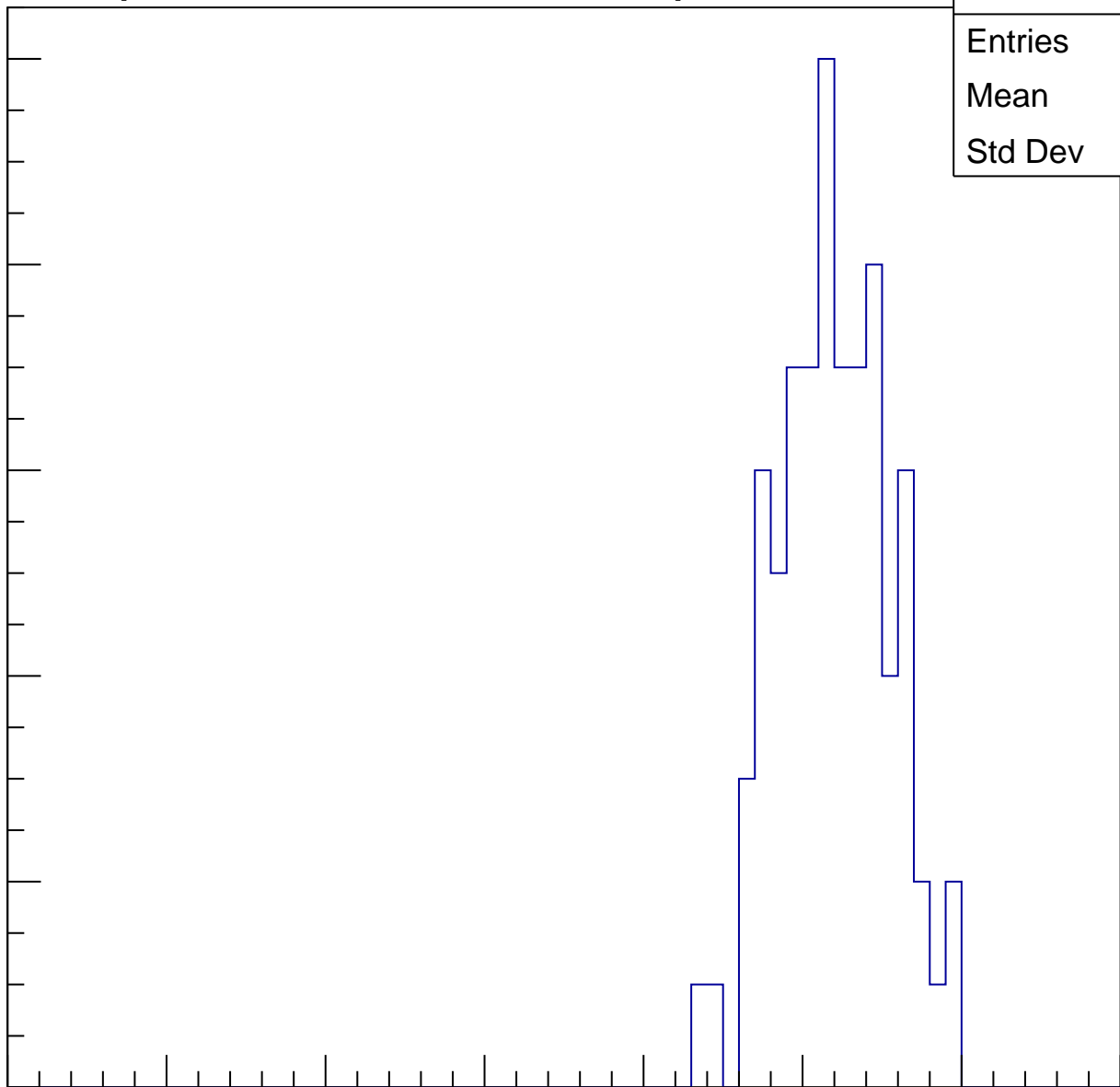
30

40

50

60

ampl

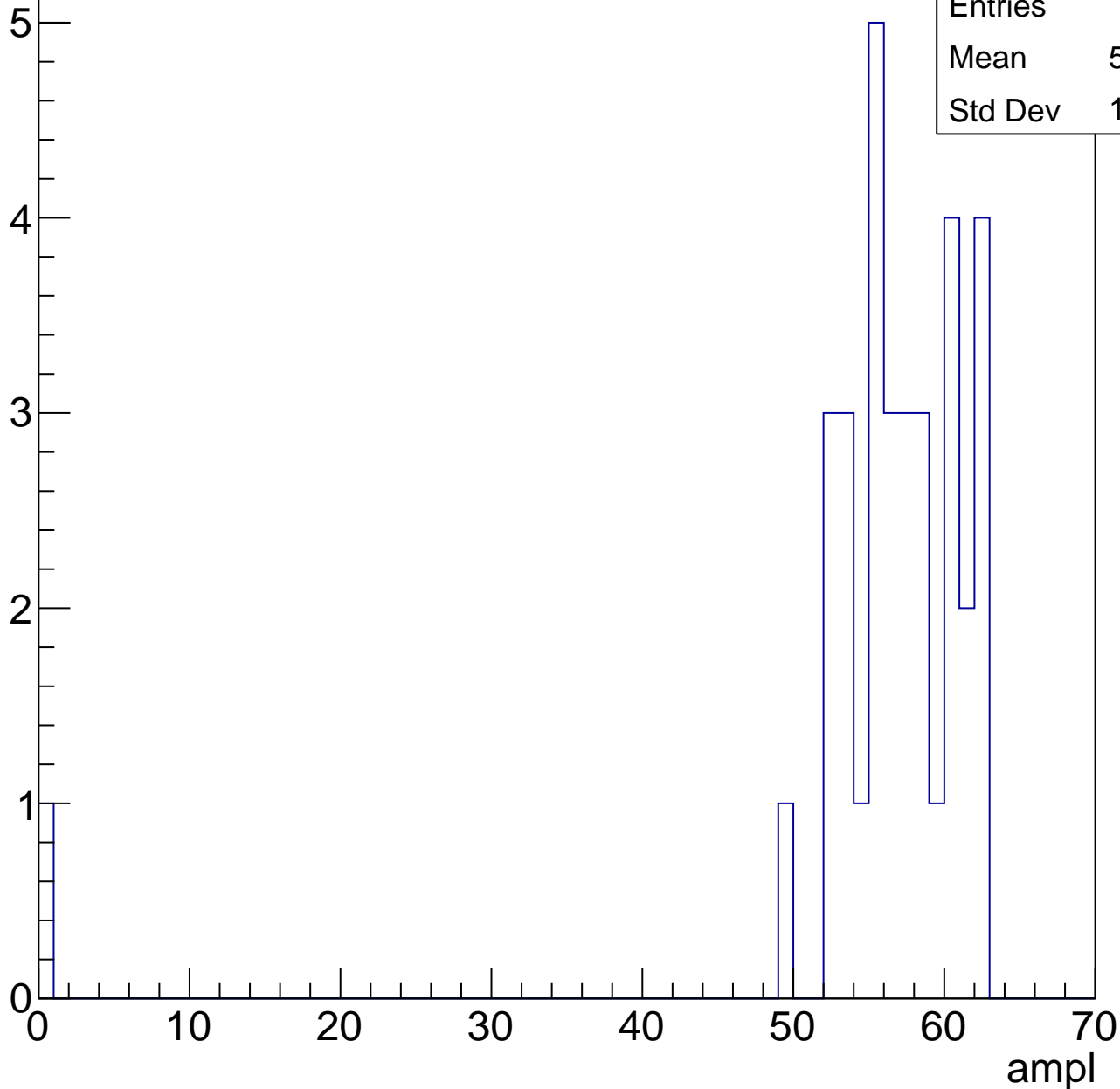


B1L103S, U10-ch48, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	55.15
Std Dev	10.18

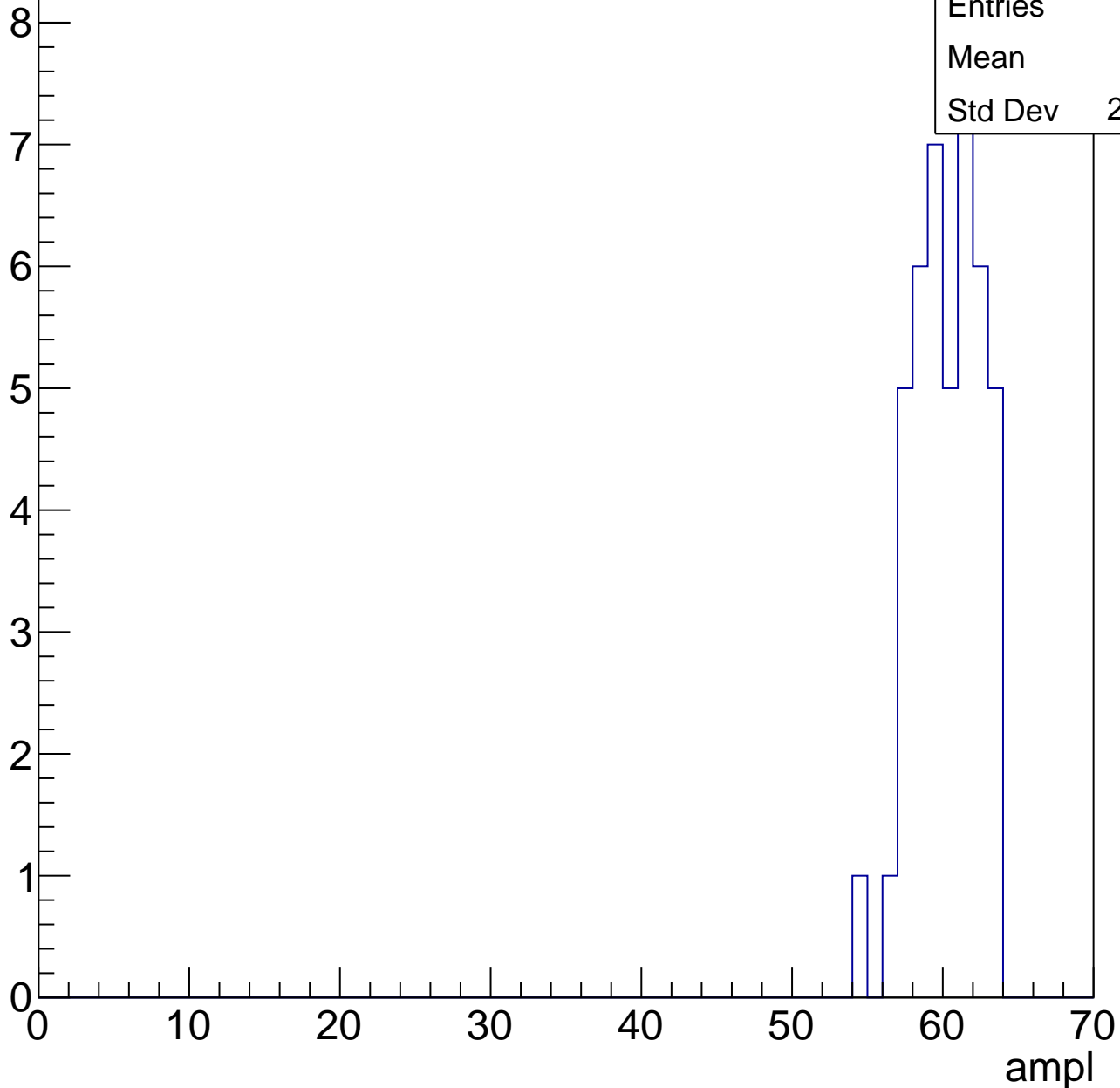


B1L103S, U10-ch48, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.8
Std Dev	2.149

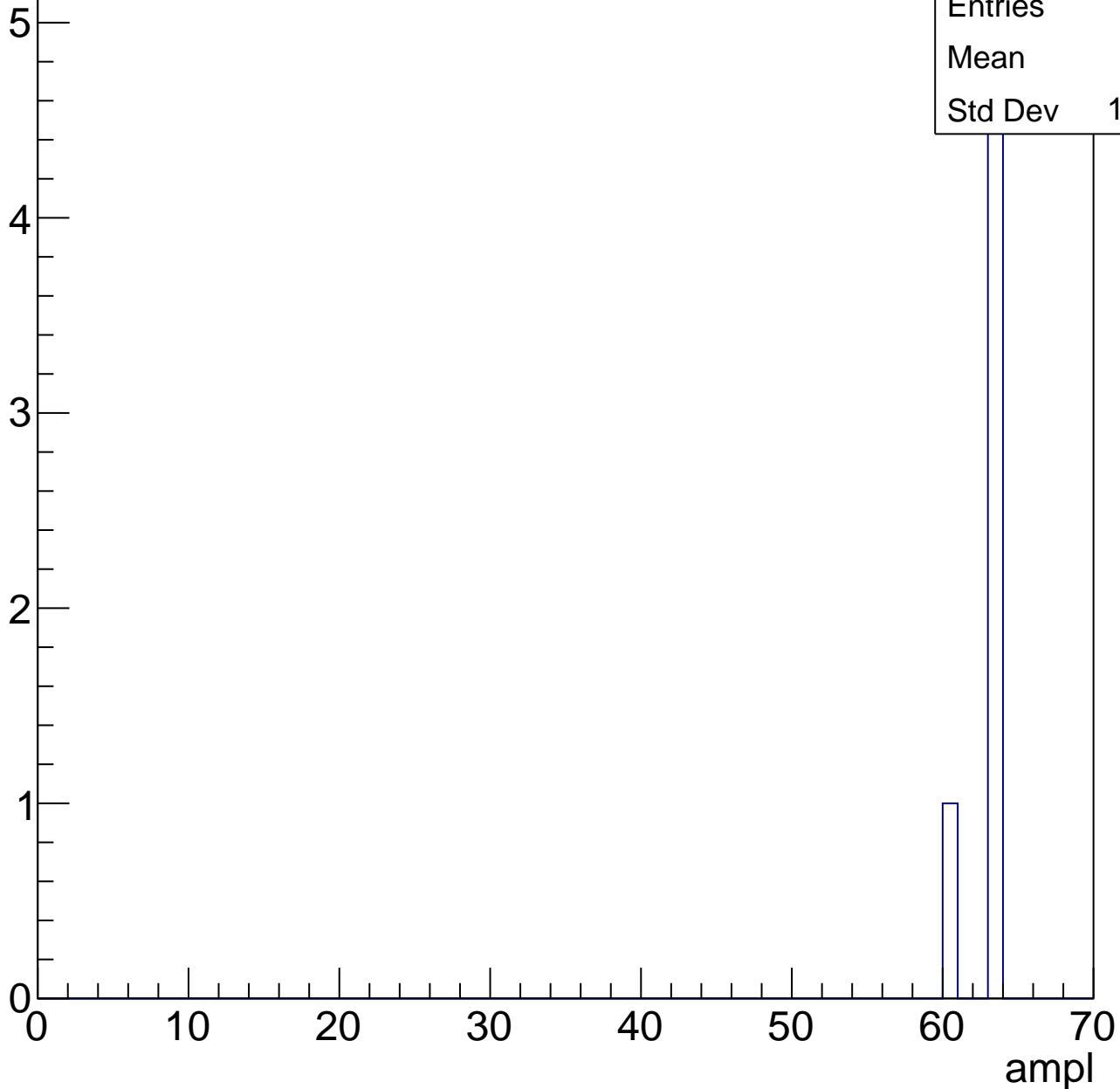


B1L103S, U10-ch48, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	6
Mean	62.5
Std Dev	1.118



B1L103S, U10-ch48, adc7

calib_packv5_042523_0143.root, FC#7, port C2

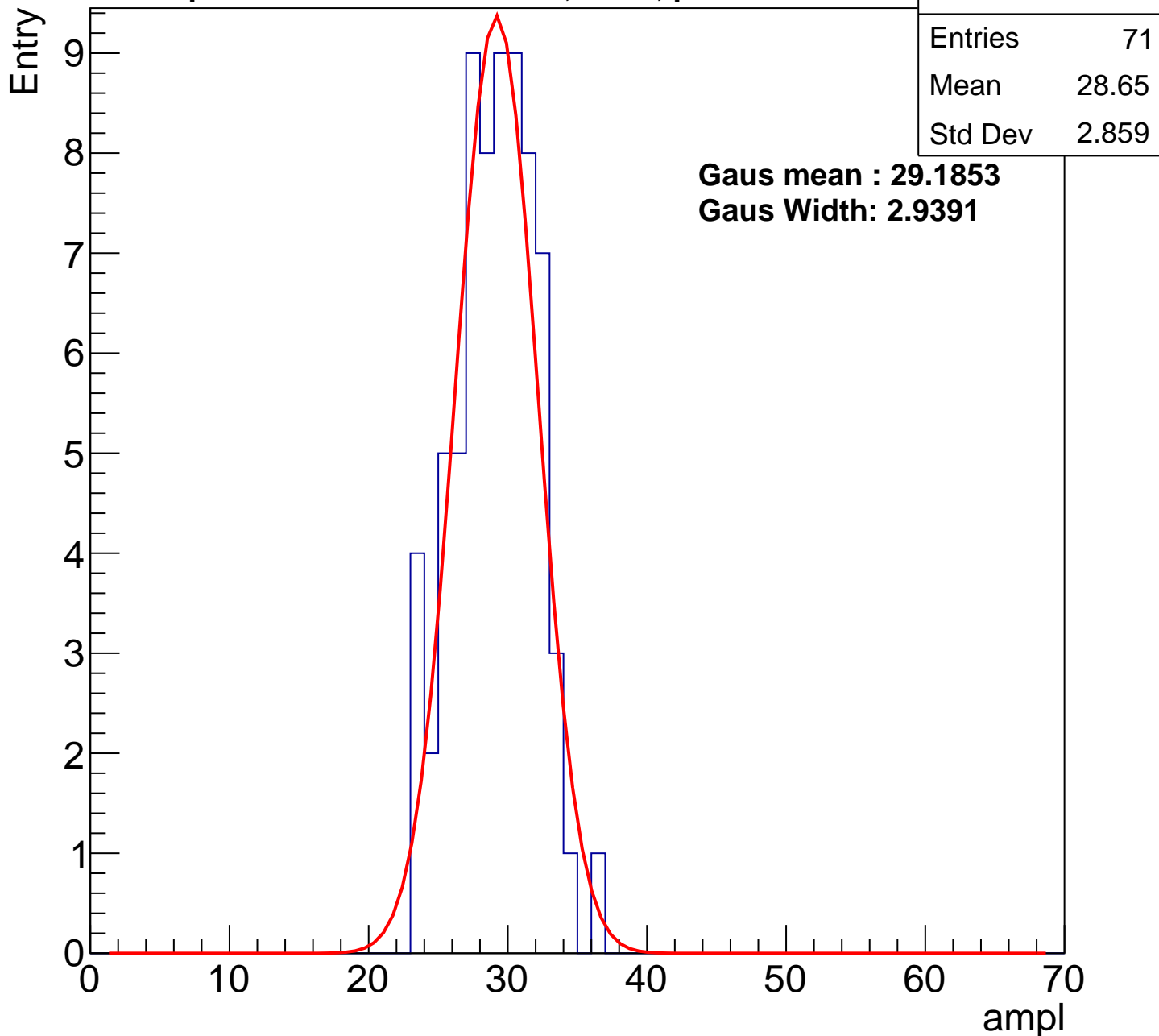
Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch49, adc0

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U10-ch49, adc1

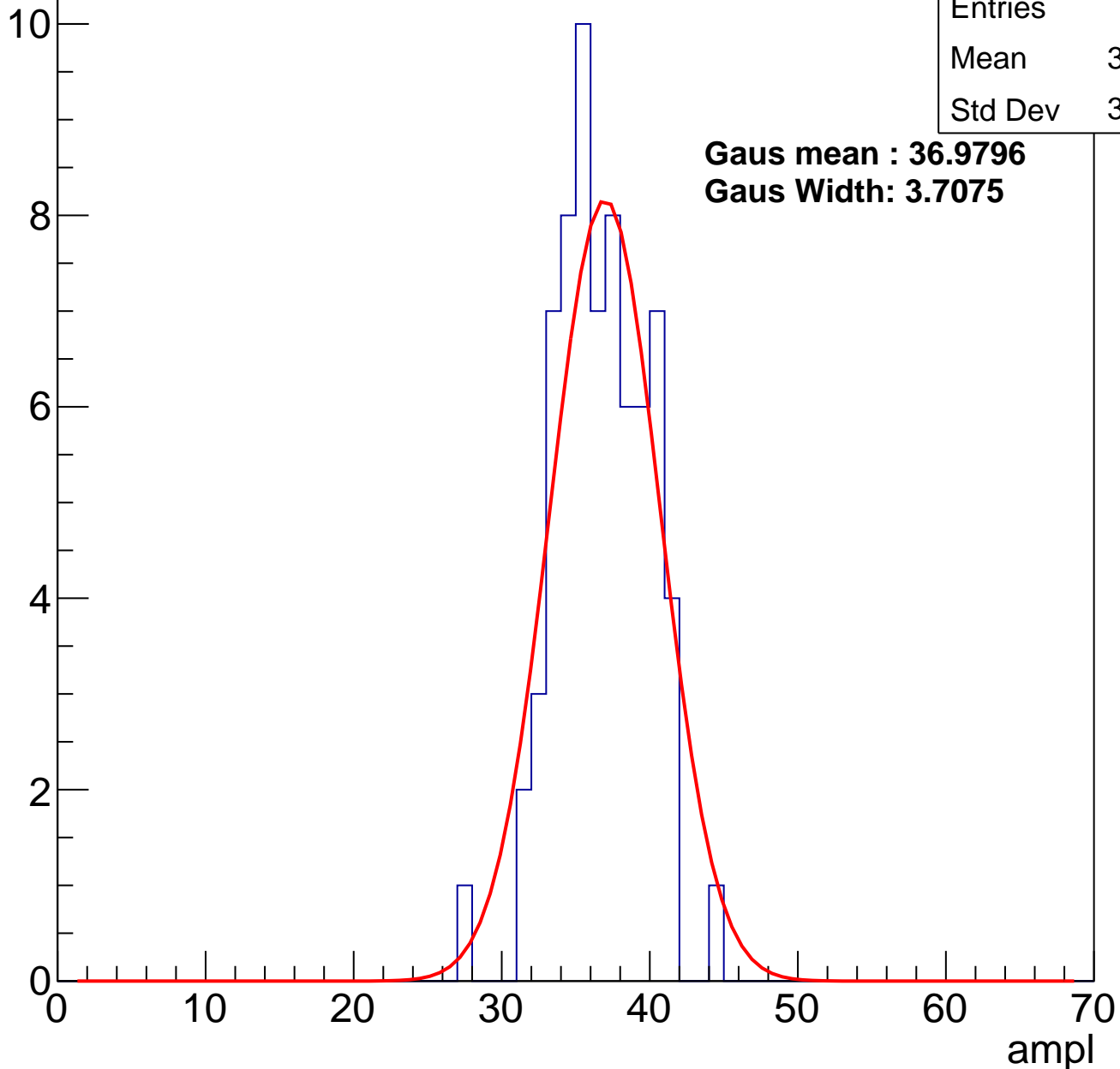
calib_packv5_042523_0143.root, FC#7, port C2

Entries	70
Mean	36.23
Std Dev	3.029

Gaus mean : 36.9796

Gaus Width: 3.7075

Entry



B1L103S, U10-ch49, adc2

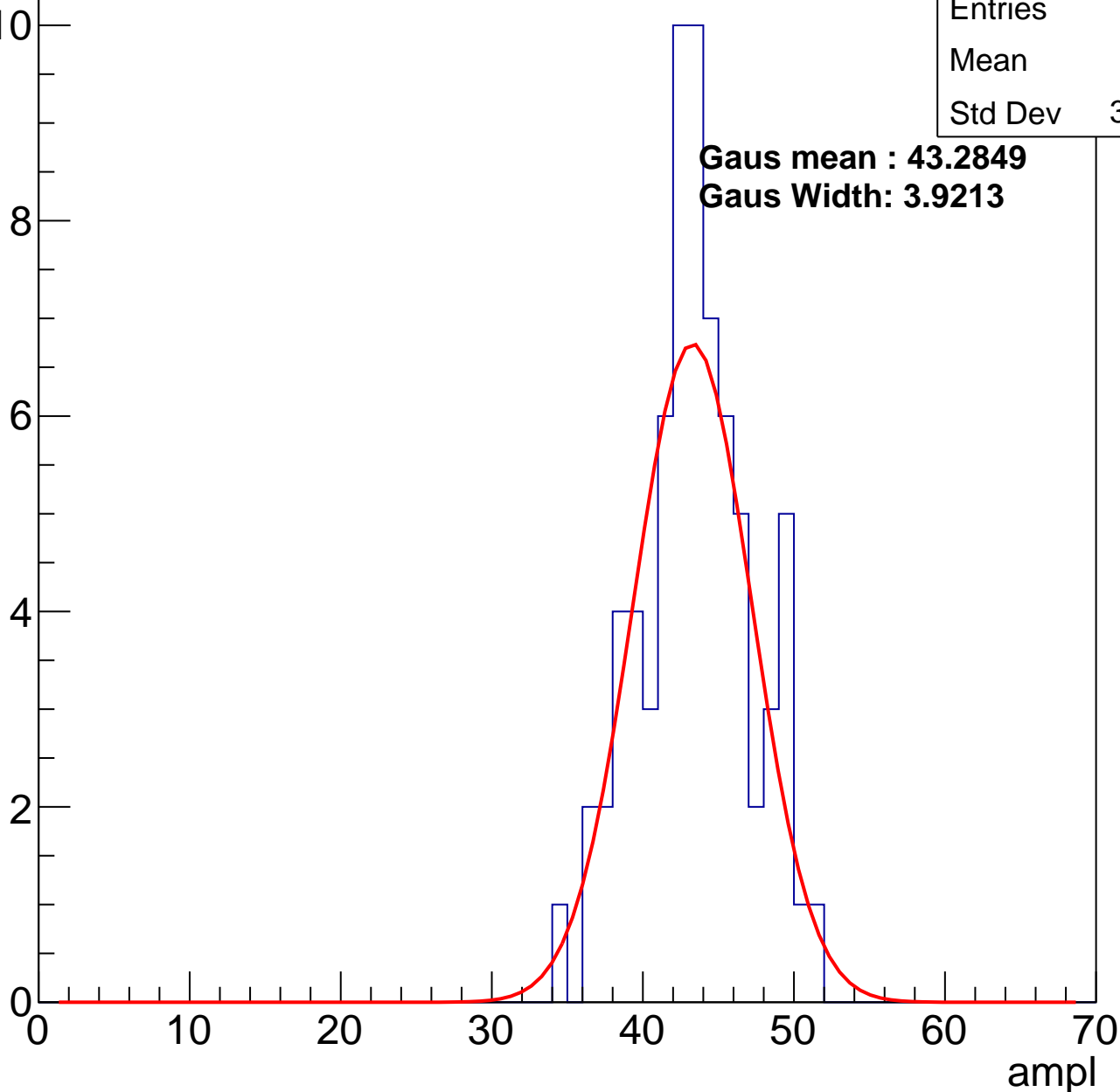
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	43
Std Dev	3.636

Gaus mean : 43.2849

Gaus Width: 3.9213

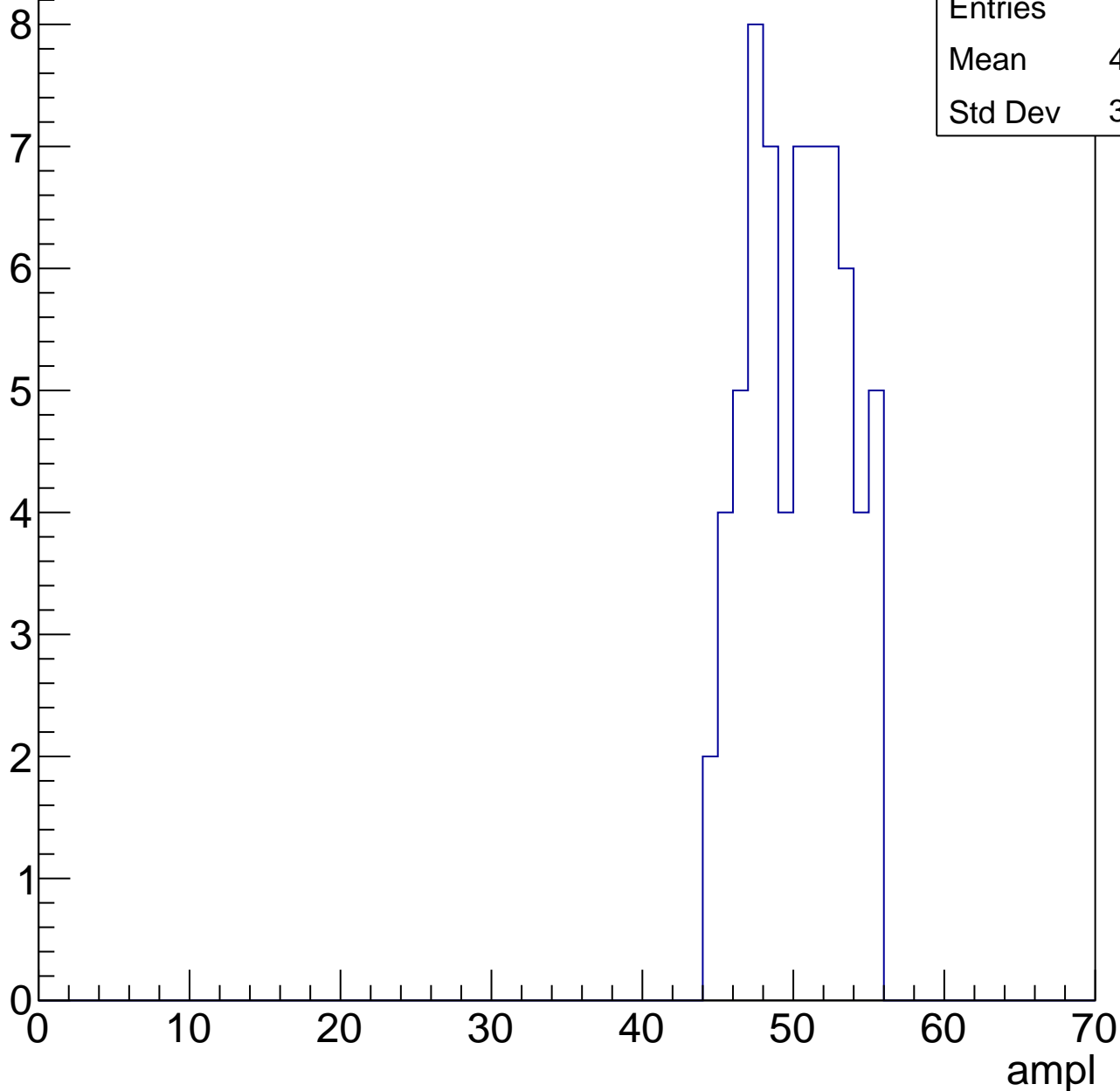


B1L103S, U10-ch49, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.79
Std Dev	3.092

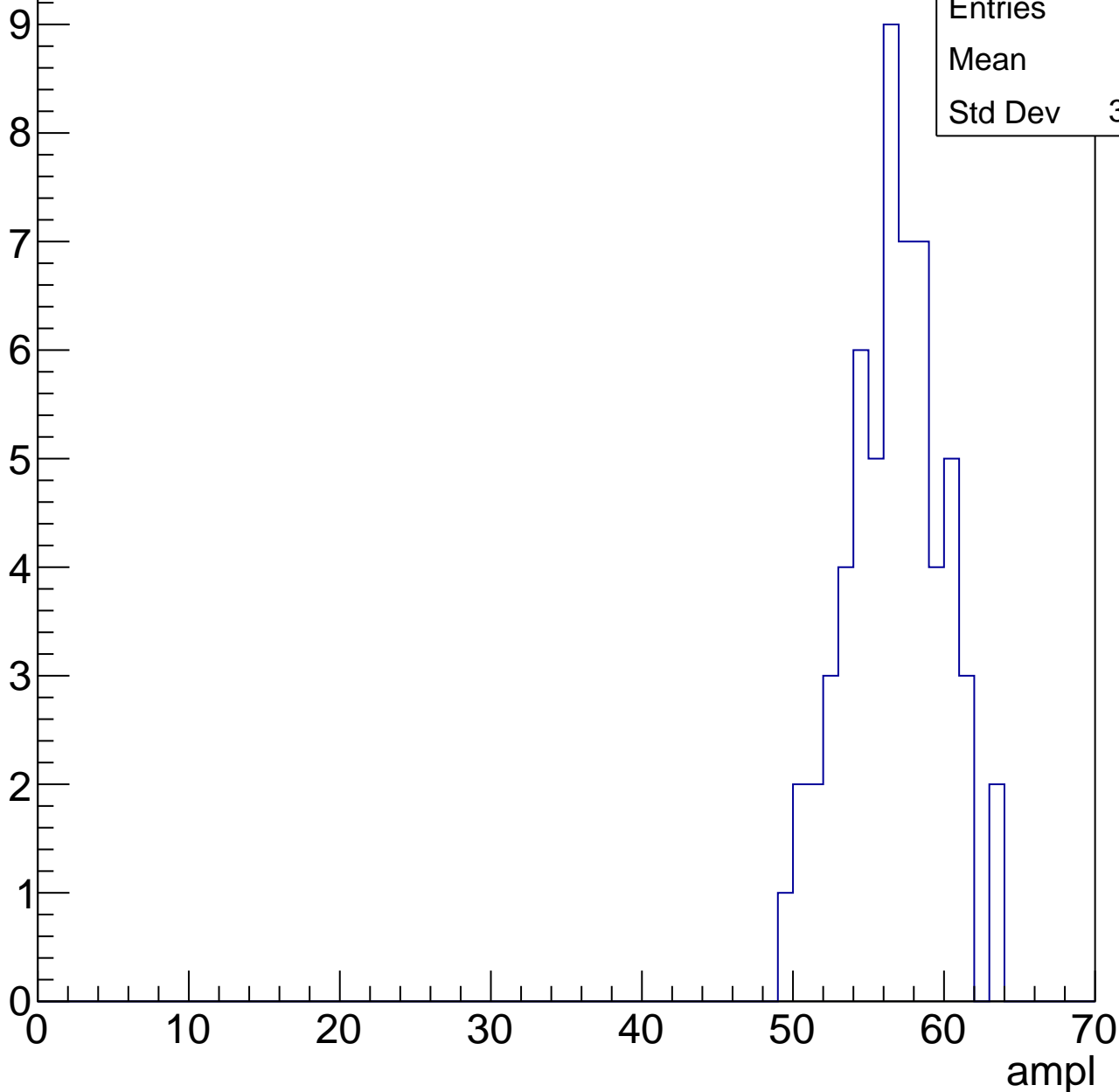


B1L103S, U10-ch49, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.2
Std Dev	3.177



B1L103S, U10-ch49, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries

36

Mean

60.25

Std Dev

1.935

ampl

0

10

20

30

40

50

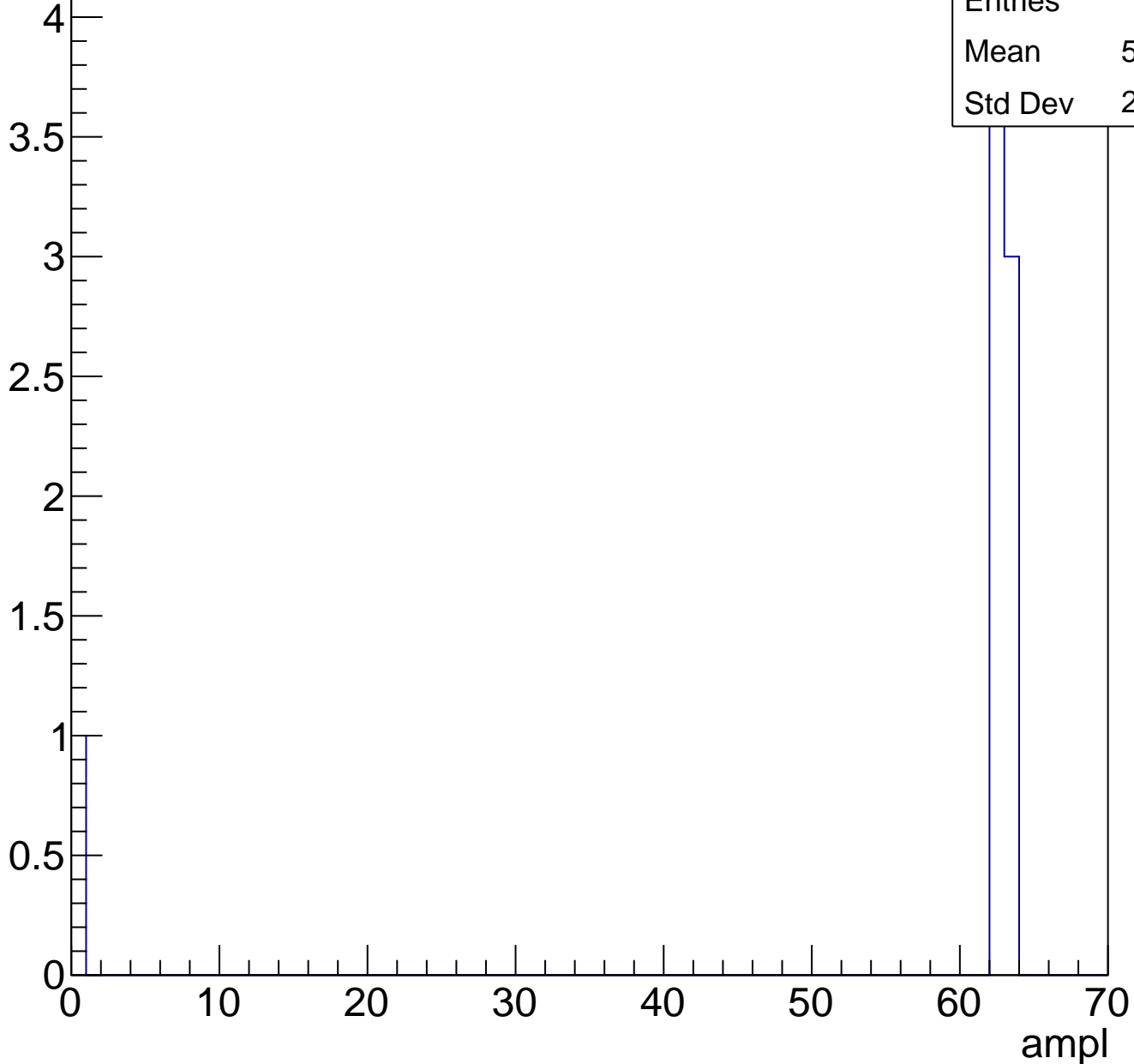
60

70

B1L103S, U10-ch49, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch49, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch50, adc0

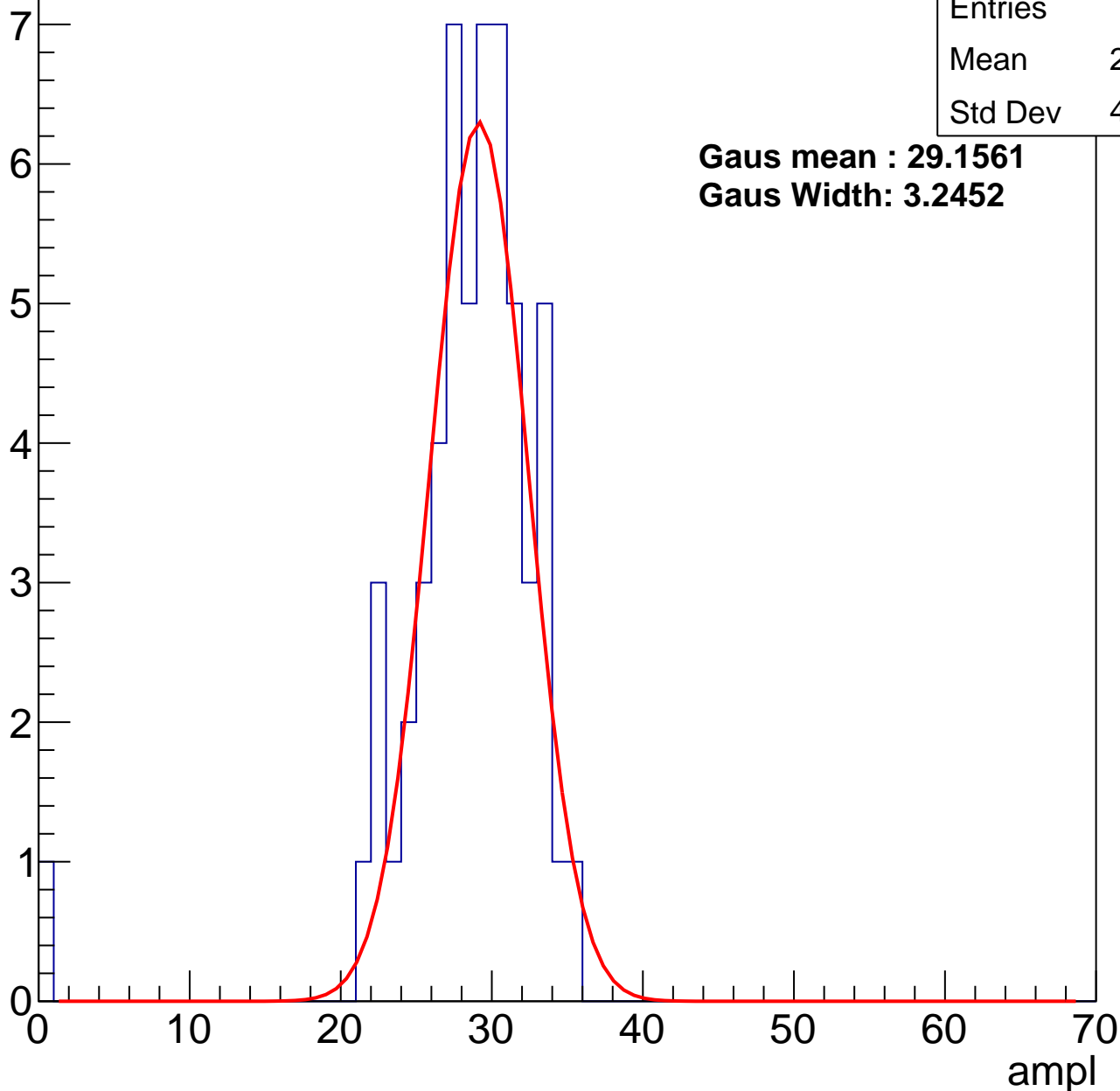
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	27.93
Std Dev	4.974

Gaus mean : 29.1561

Gaus Width: 3.2452



B1L103S, U10-ch50, adc1

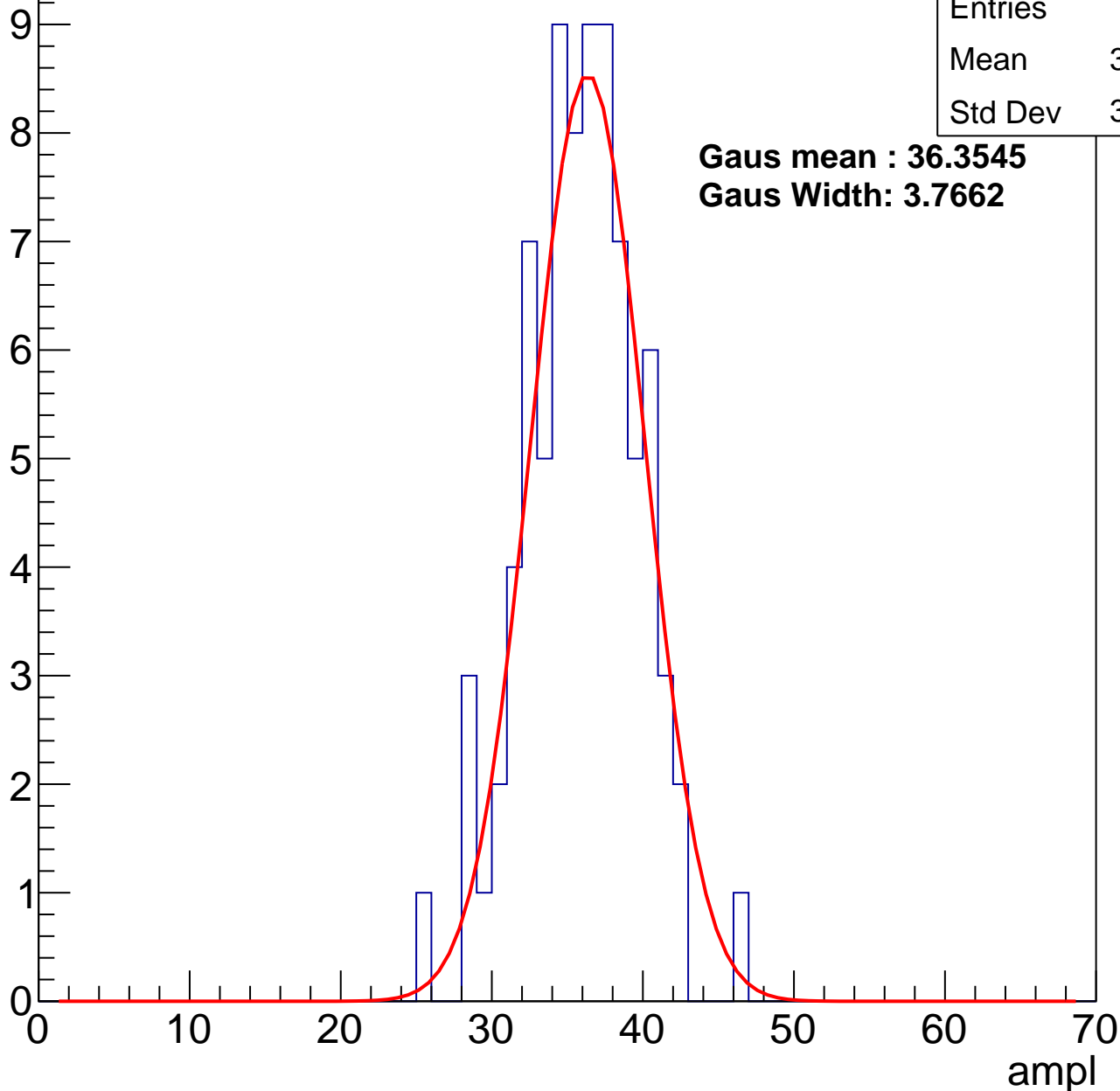
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	35.46
Std Dev	3.723

Gaus mean : 36.3545

Gaus Width: 3.7662



B1L103S, U10-ch50, adc2

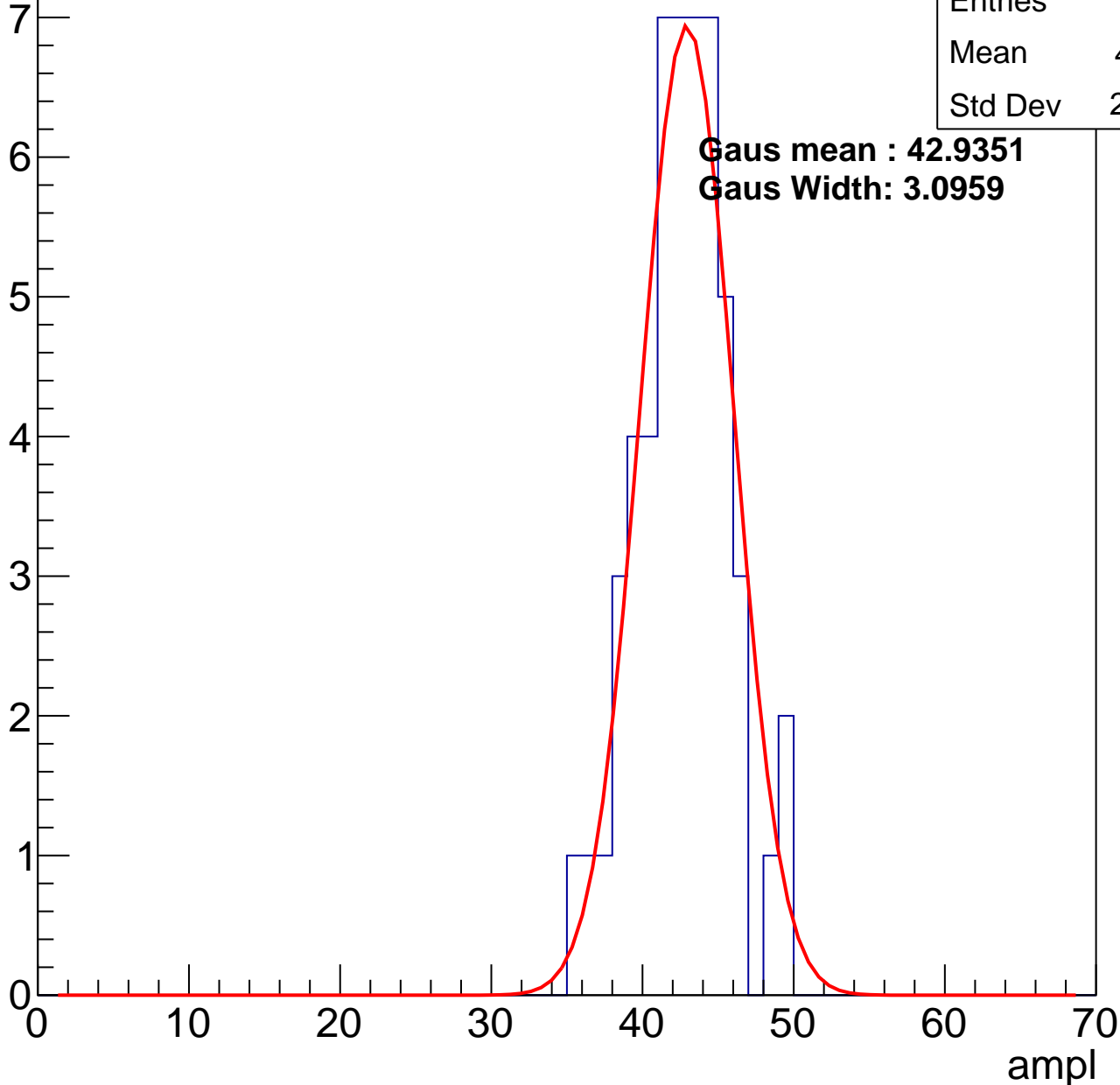
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	42.21
Std Dev	2.999

Gaus mean : 42.9351

Gaus Width: 3.0959



B1L103S, U10-ch50, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	49.05
Std Dev	3.44

Entry

10

8

6

4

2

0

0

10

20

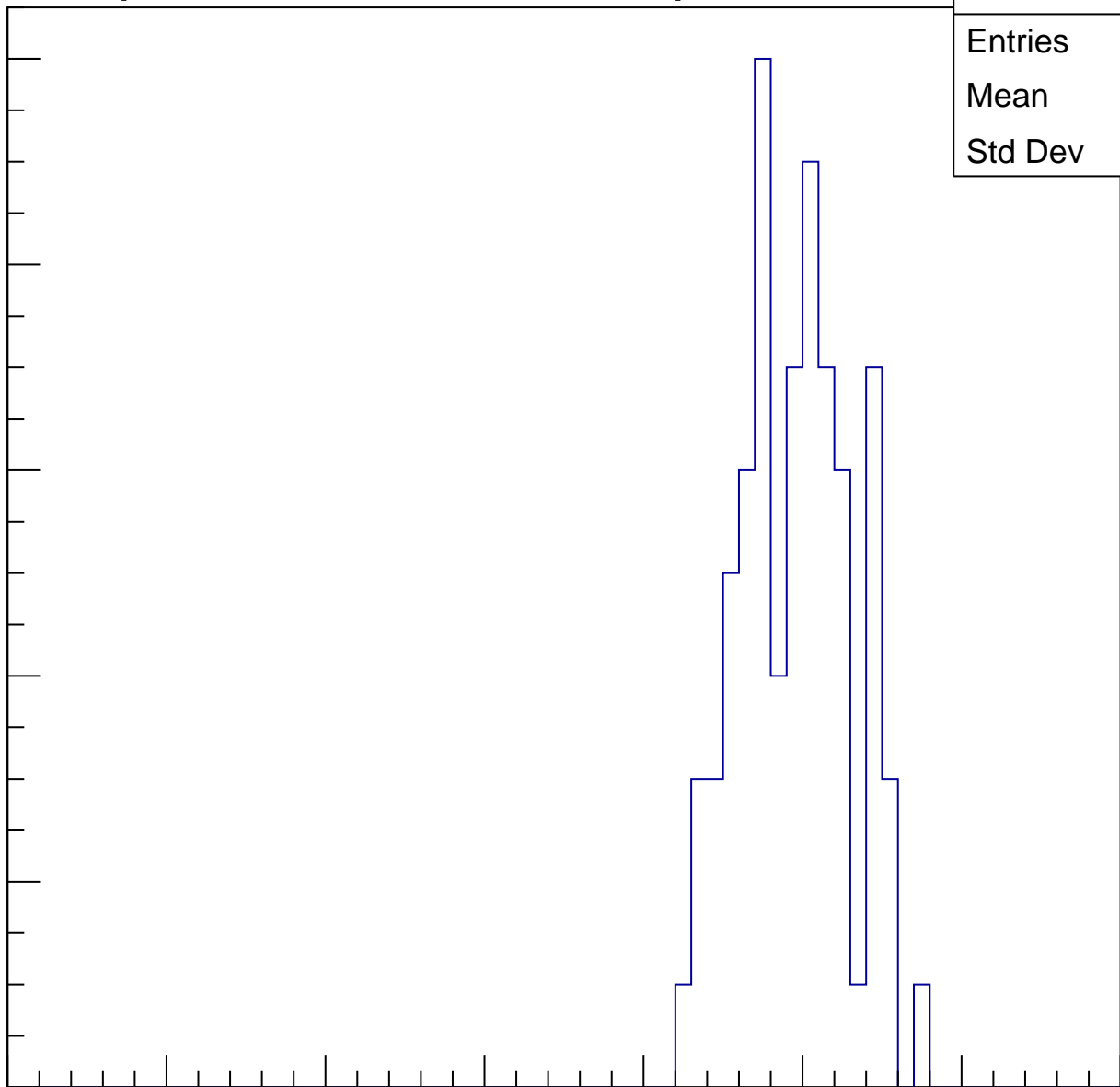
30

40

50

60

ampl

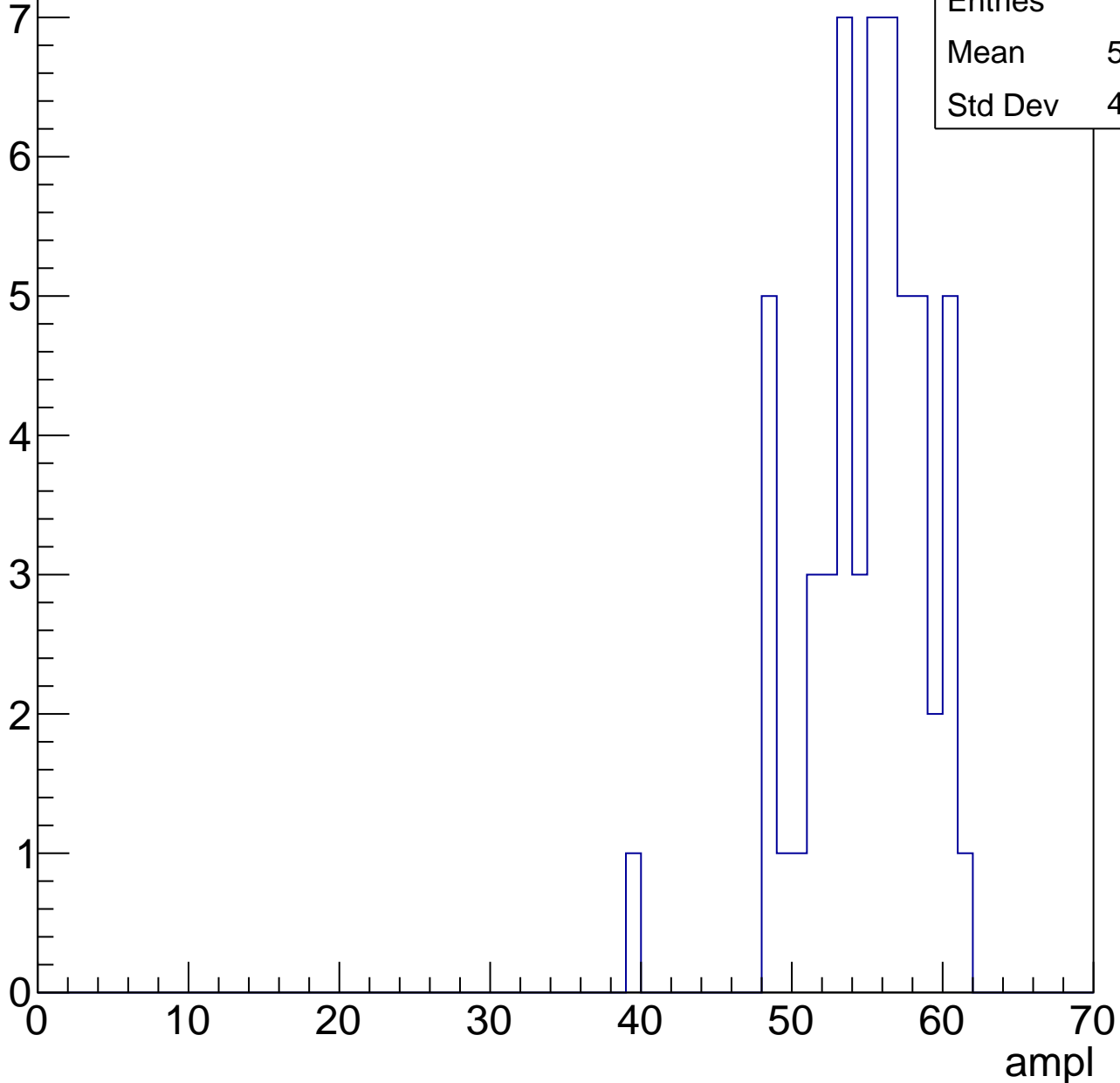


B1L103S, U10-ch50, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.48
Std Dev	4.049

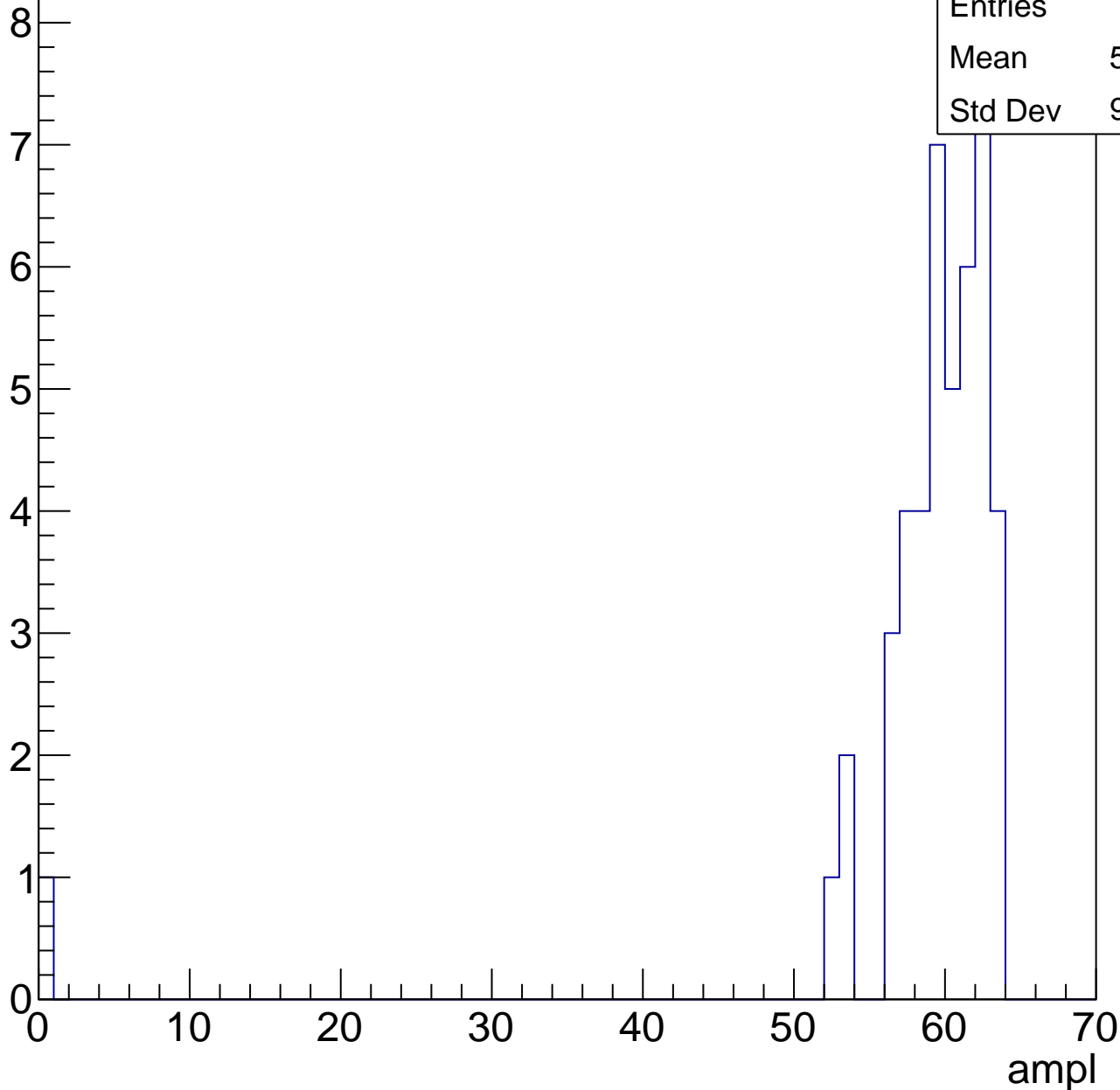


B1L103S, U10-ch50, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

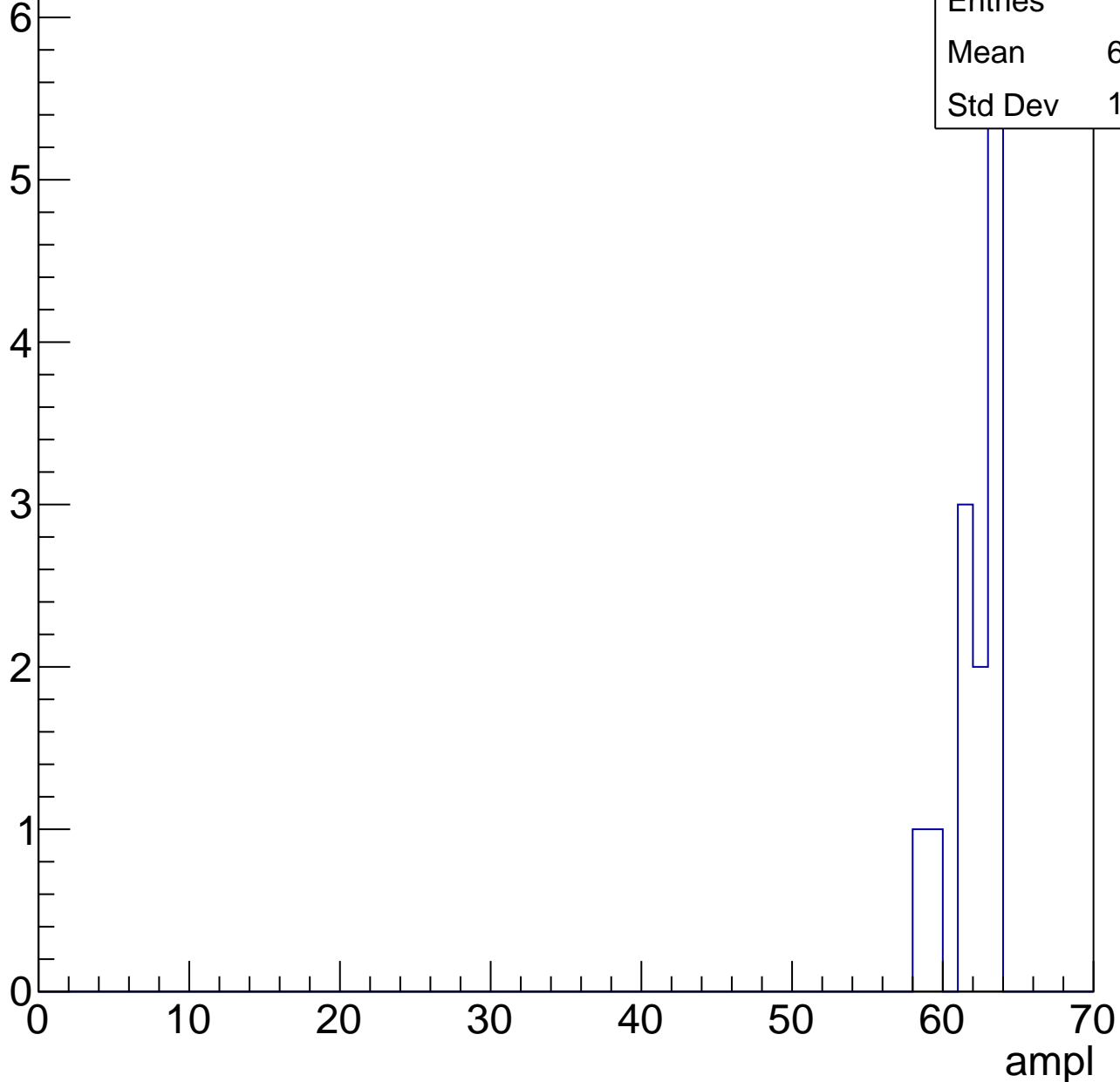
Entries	45
Mean	58.07
Std Dev	9.159



B1L103S, U10-ch50, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch50, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	9
Std Dev	9

B1L103S, U10-ch51, adc0

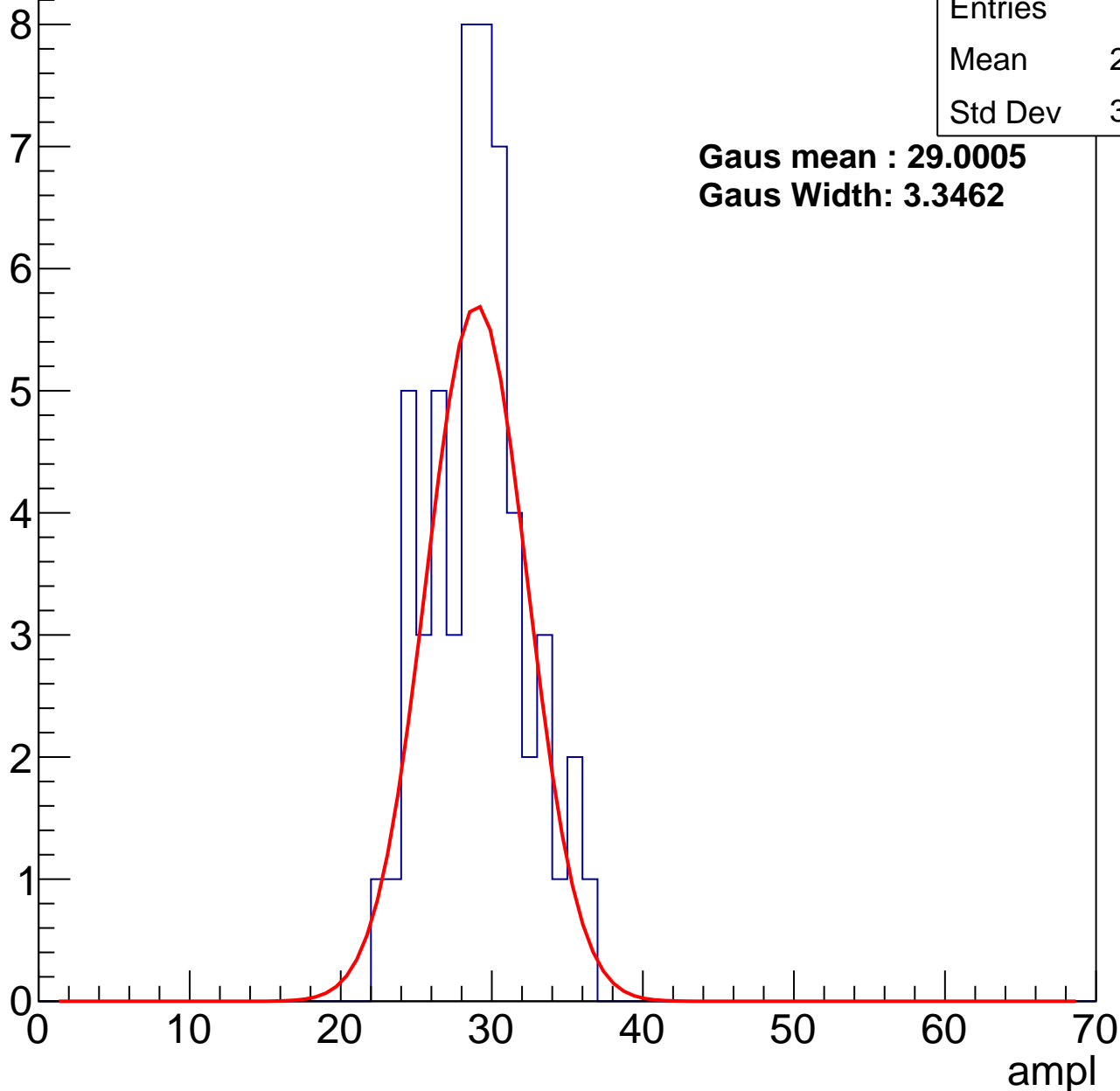
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	28.59
Std Dev	3.189

Gaus mean : 29.0005

Gaus Width: 3.3462



B1L103S, U10-ch51, adc1

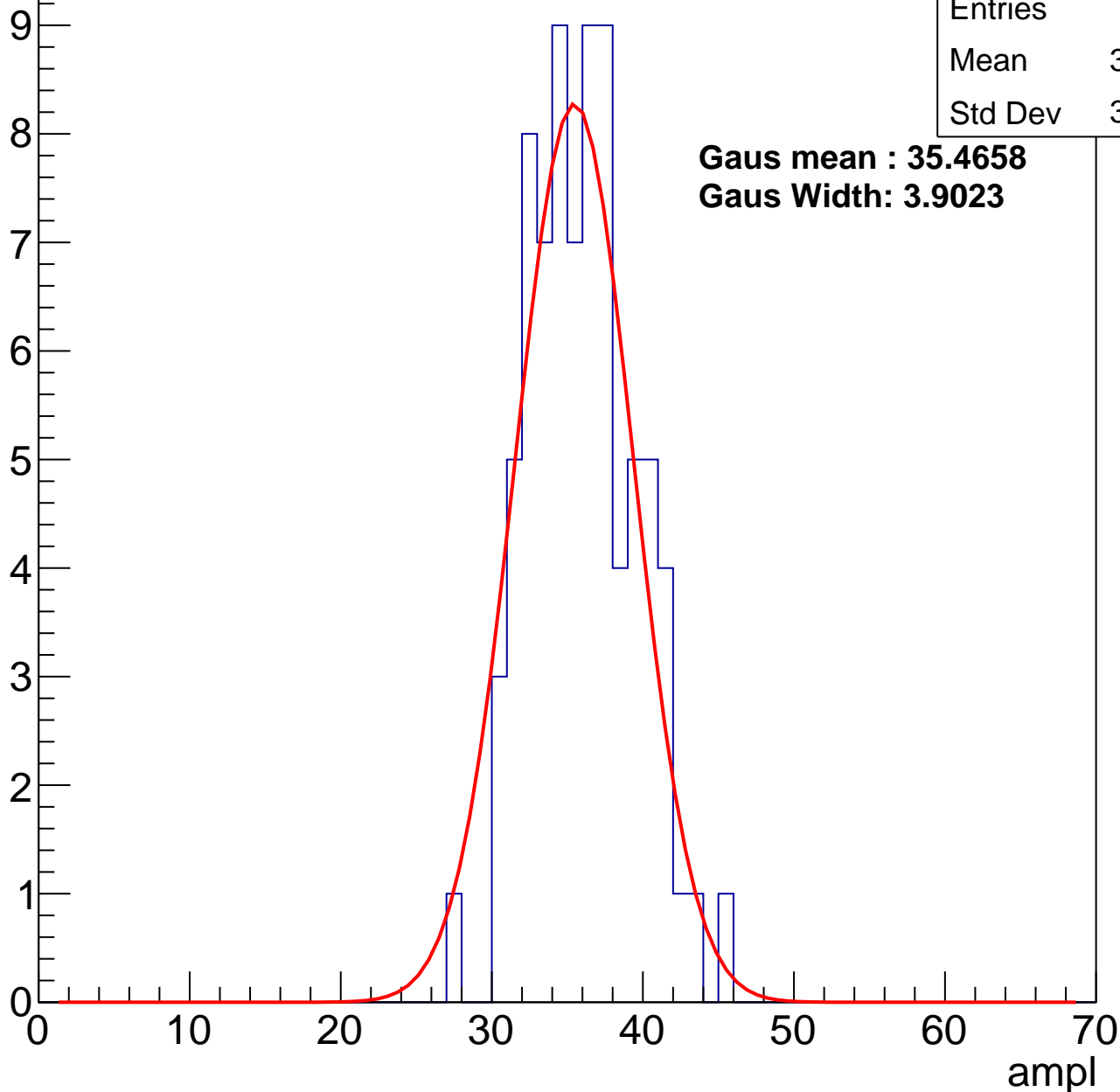
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	35.54
Std Dev	3.463

Gaus mean : 35.4658

Gaus Width: 3.9023



B1L103S, U10-ch51, adc2

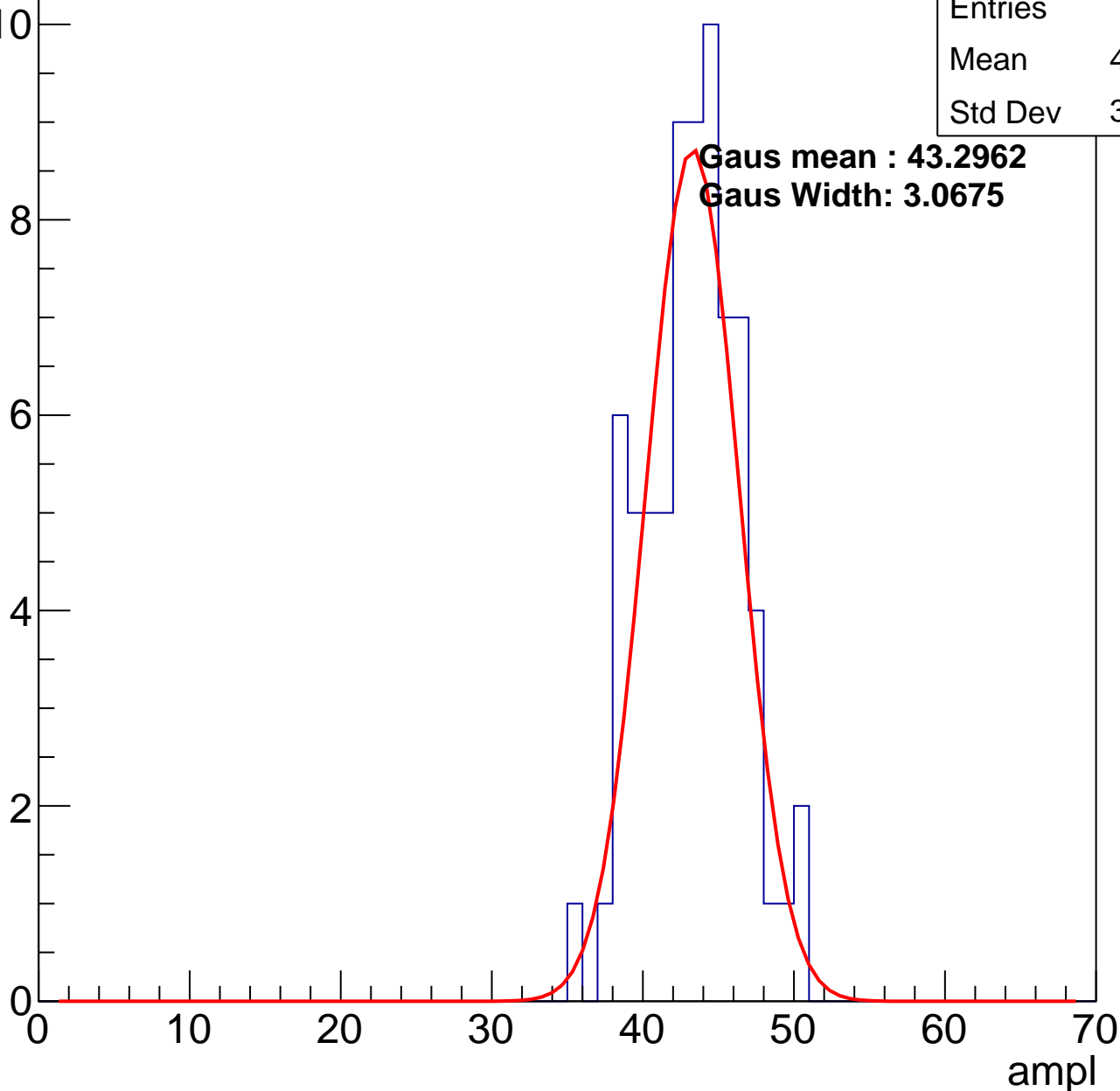
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	42.84
Std Dev	3.154

Gaus mean : 43.2962

Gaus Width: 3.0675

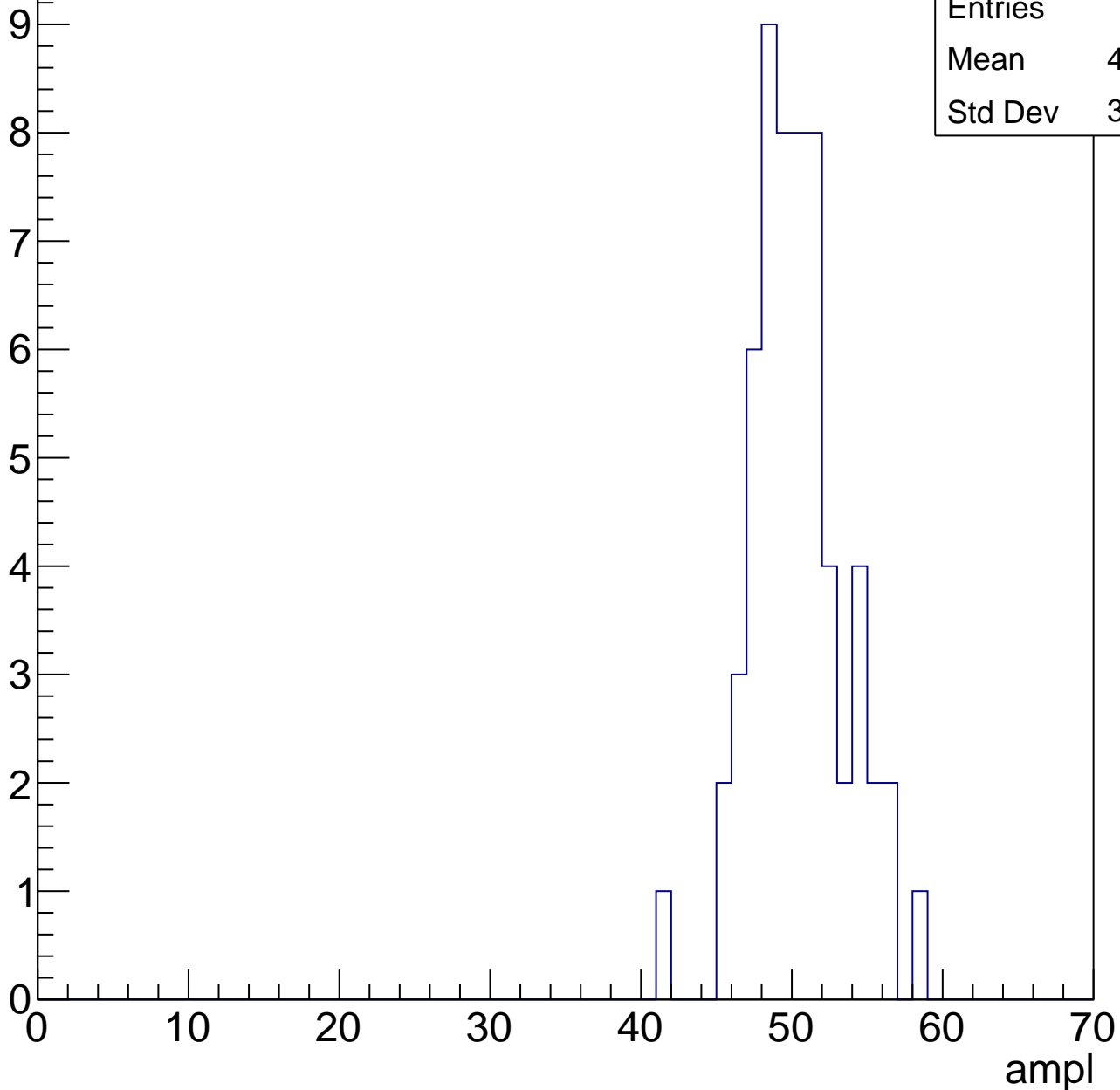


B1L103S, U10-ch51, adc3

calib_packv5_042523_0143.root, FC#7, port C2

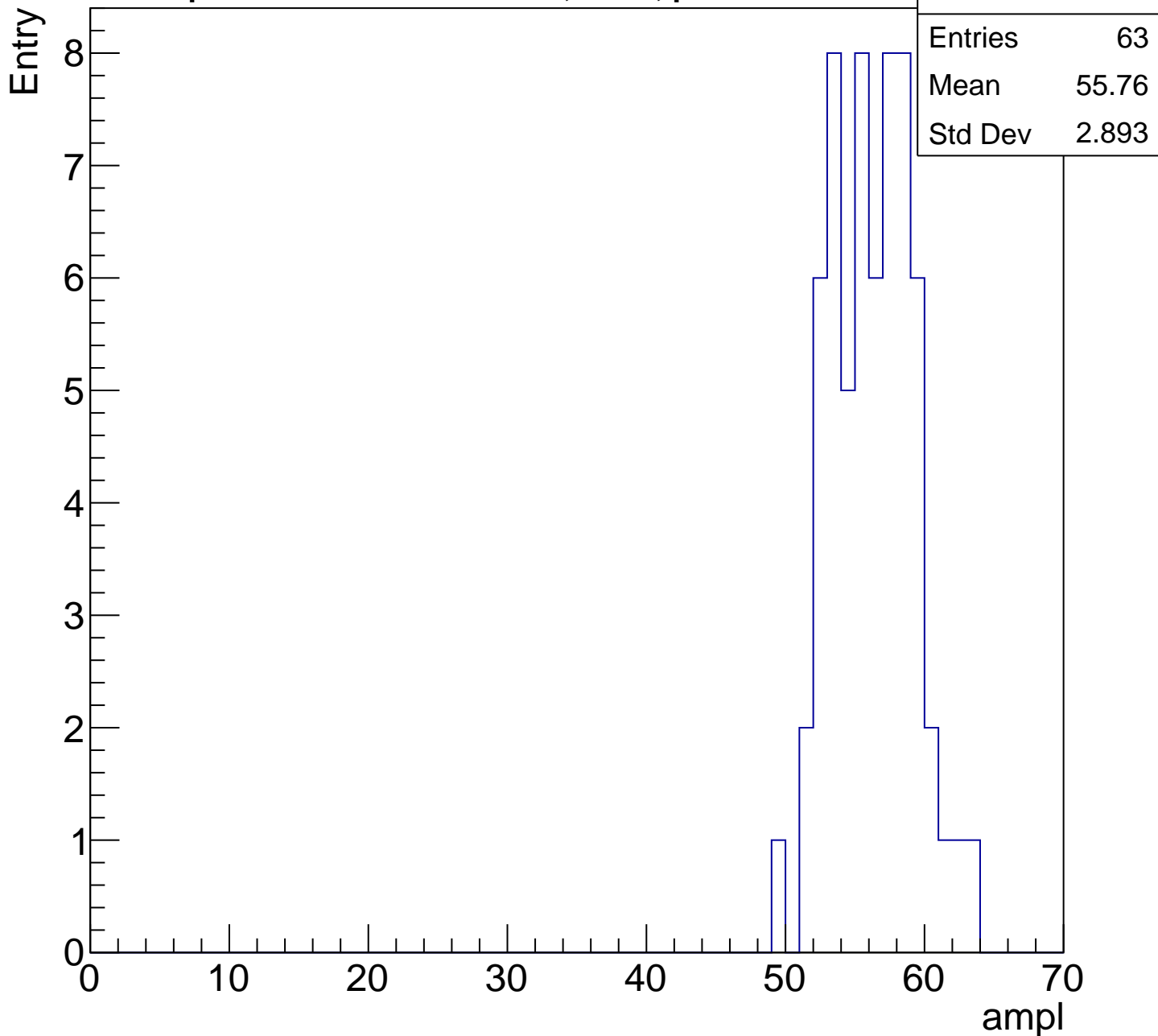
Entry

Entries	60
Mean	49.88
Std Dev	3.077



B1L103S, U10-ch51, adc4

calib_packv5_042523_0143.root, FC#7, port C2

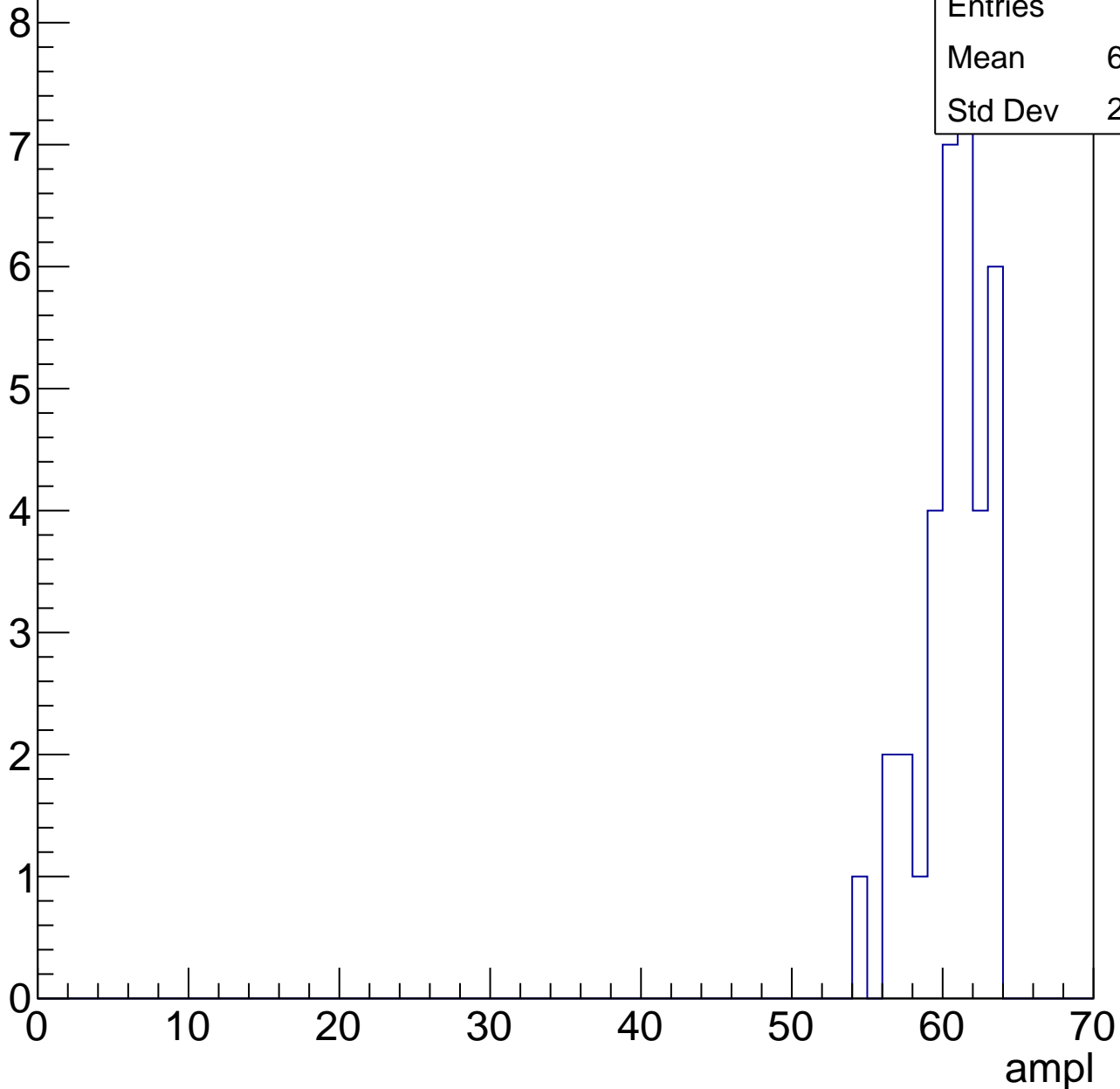


B1L103S, U10-ch51, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	60.23
Std Dev	2.205

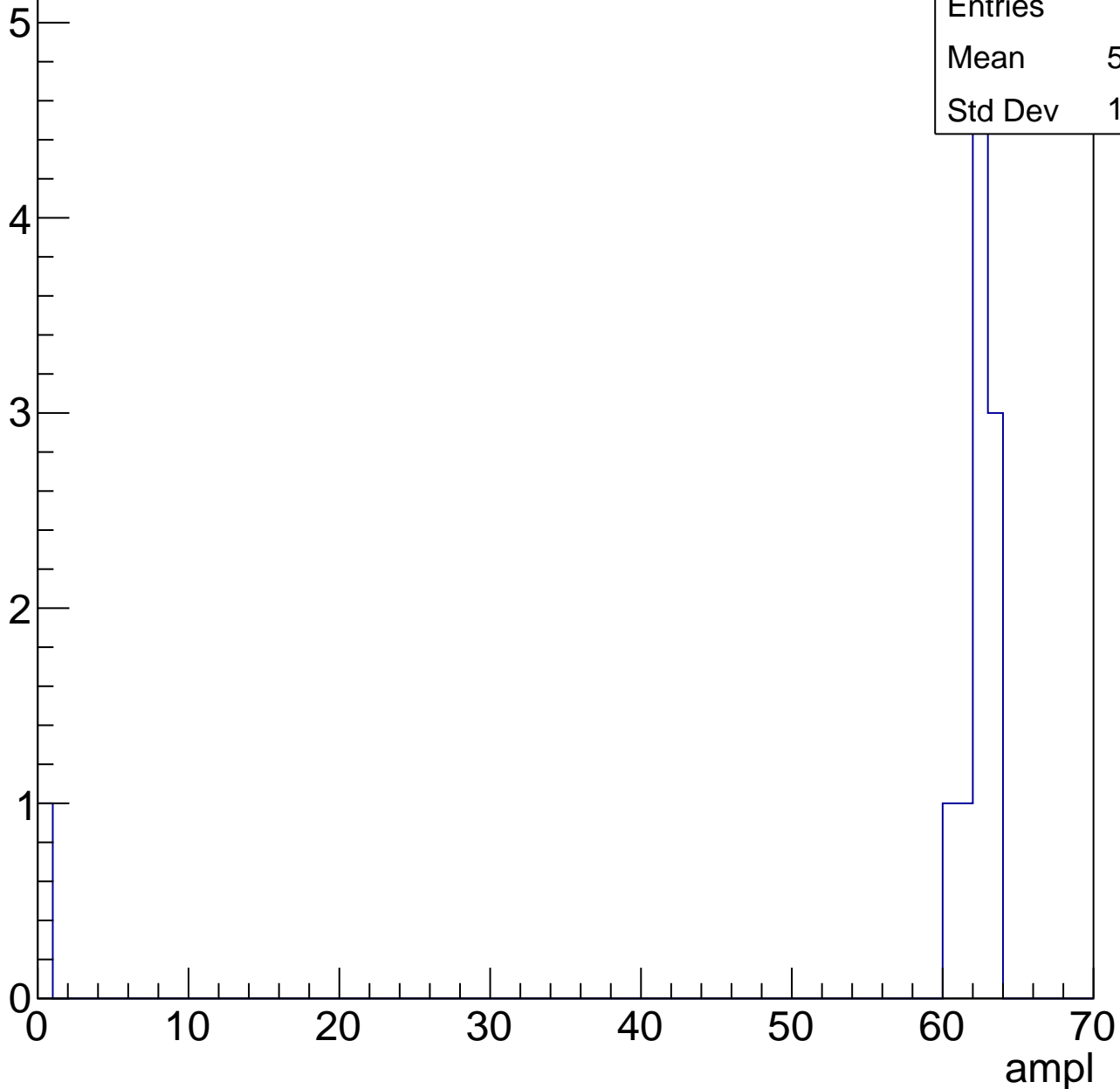


B1L103S, U10-ch51, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

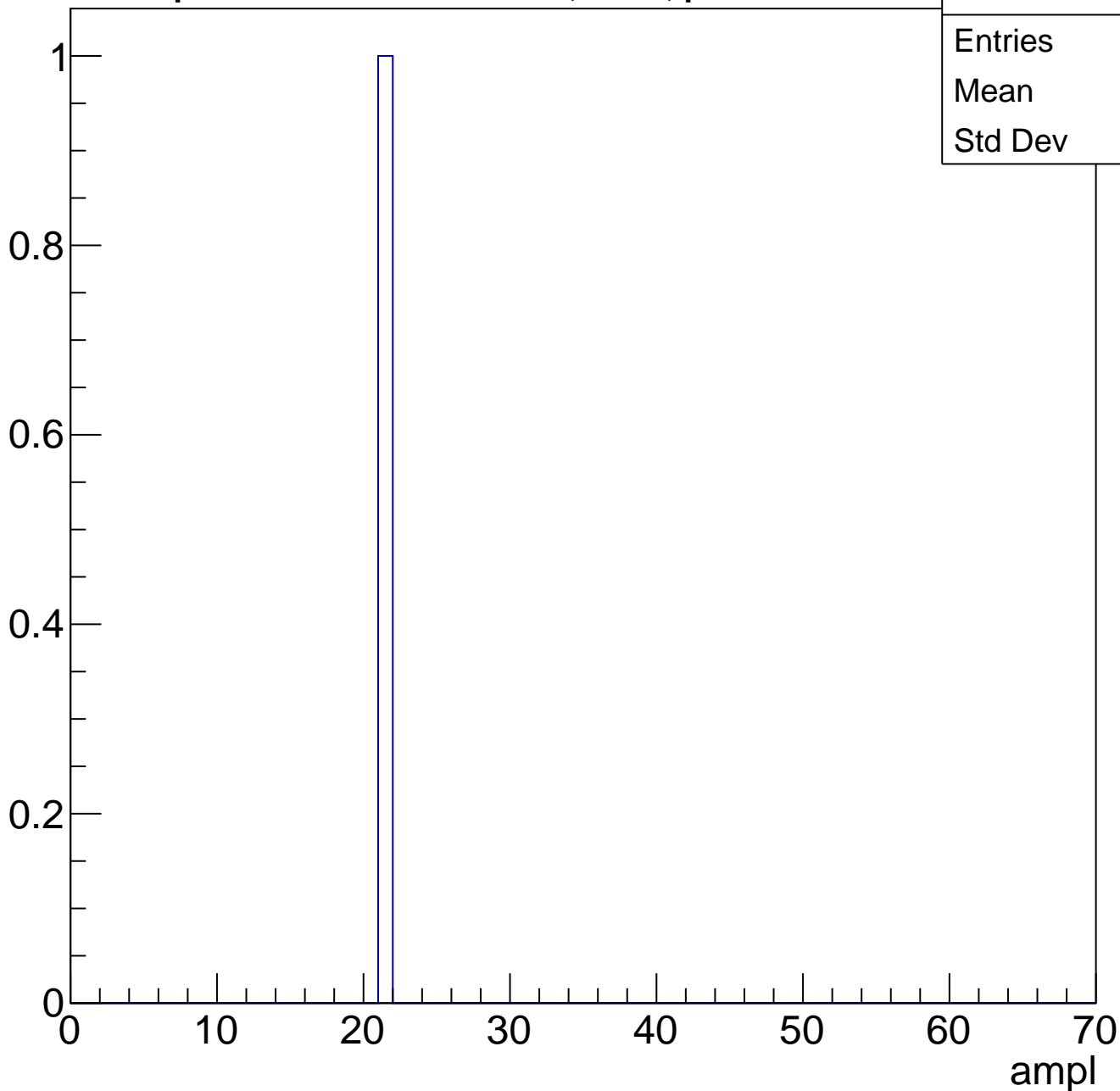
Entries	11
Mean	56.36
Std Dev	17.84



B1L103S, U10-ch51, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch52, adc0

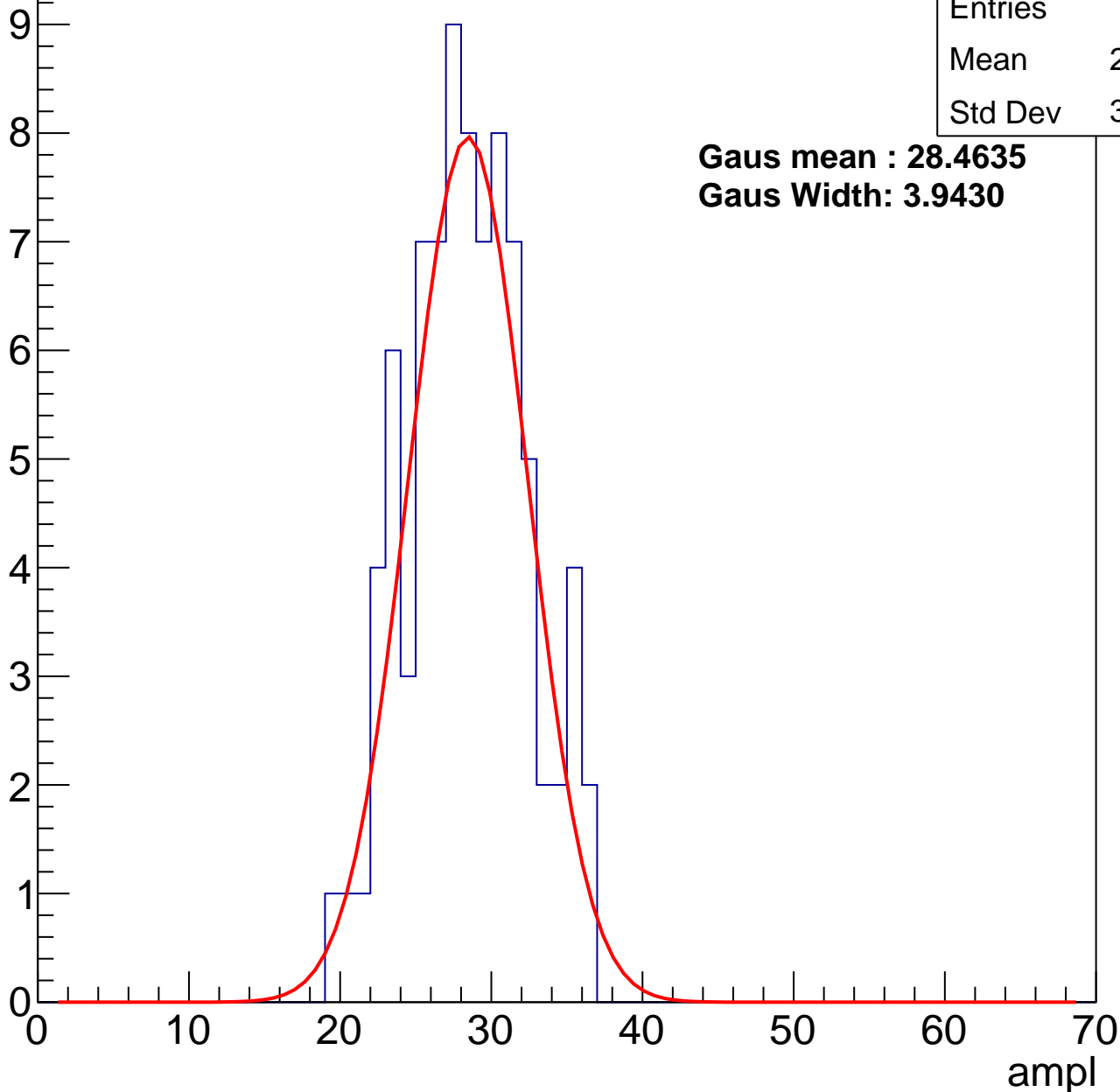
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	27.95
Std Dev	3.879

Gaus mean : 28.4635

Gaus Width: 3.9430



B1L103S, U10-ch52, adc1

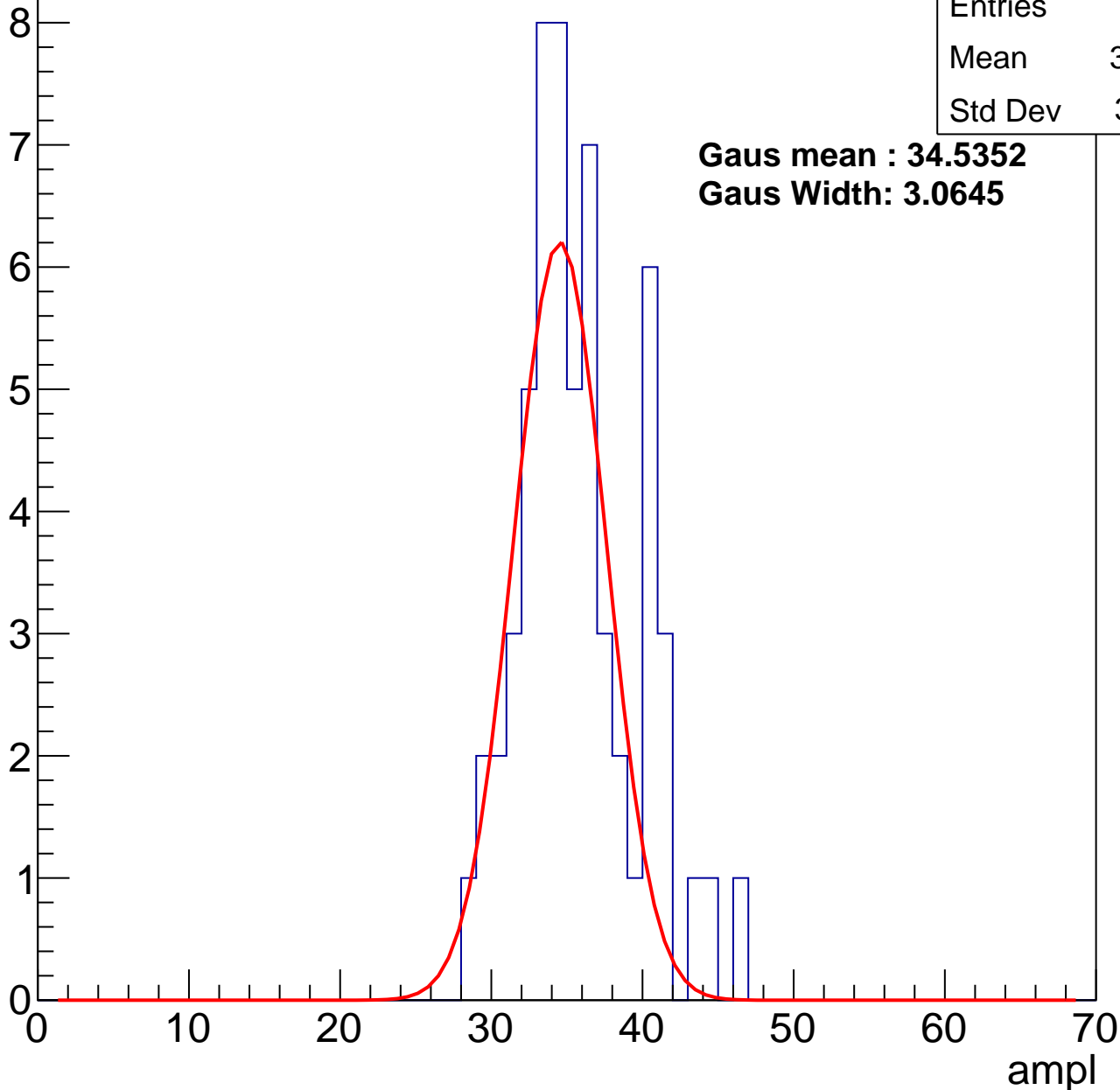
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	35.32
Std Dev	3.851

Gaus mean : 34.5352

Gaus Width: 3.0645



B1L103S, U10-ch52, adc2

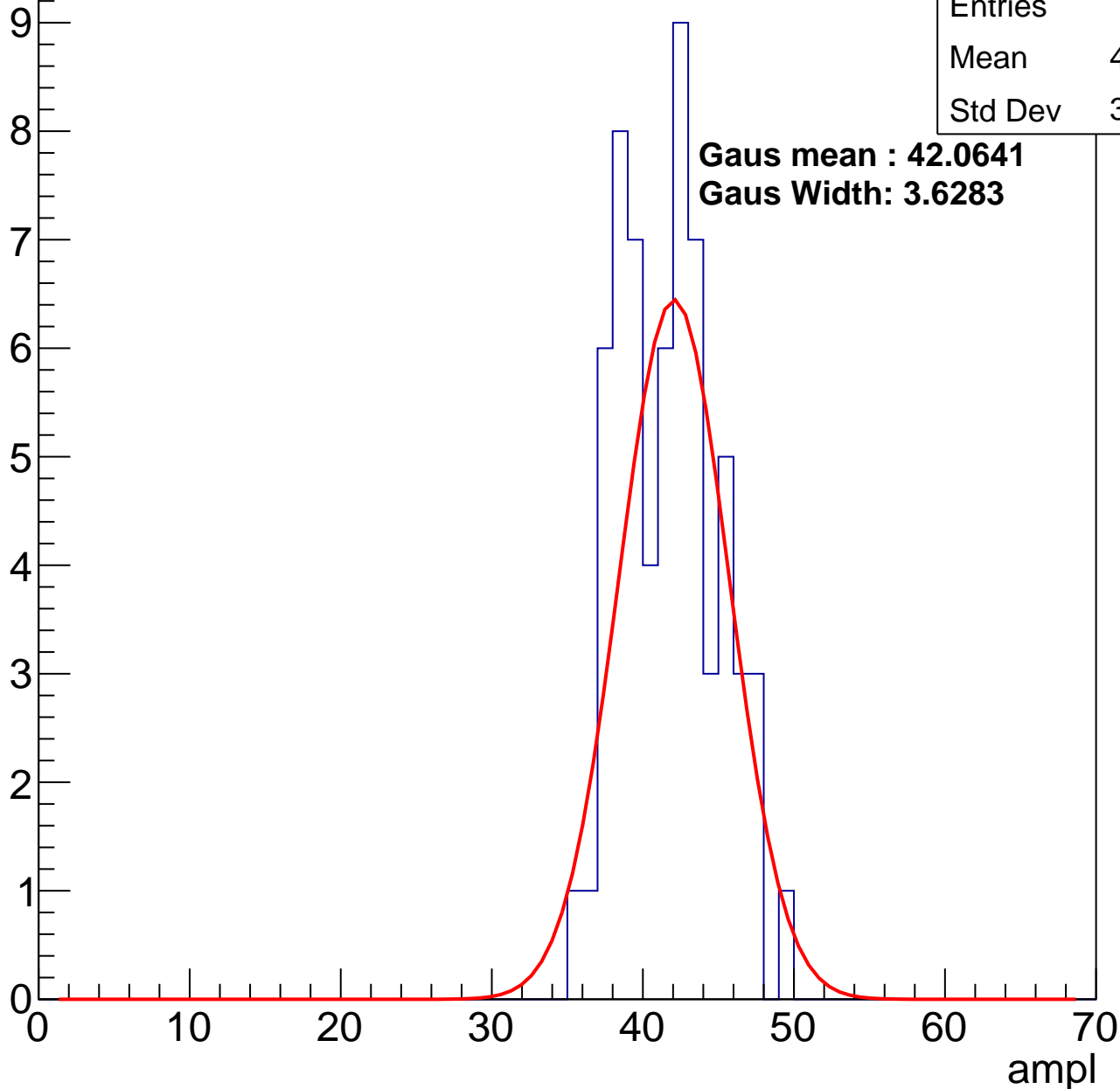
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	41.25
Std Dev	3.182

Gaus mean : 42.0641

Gaus Width: 3.6283

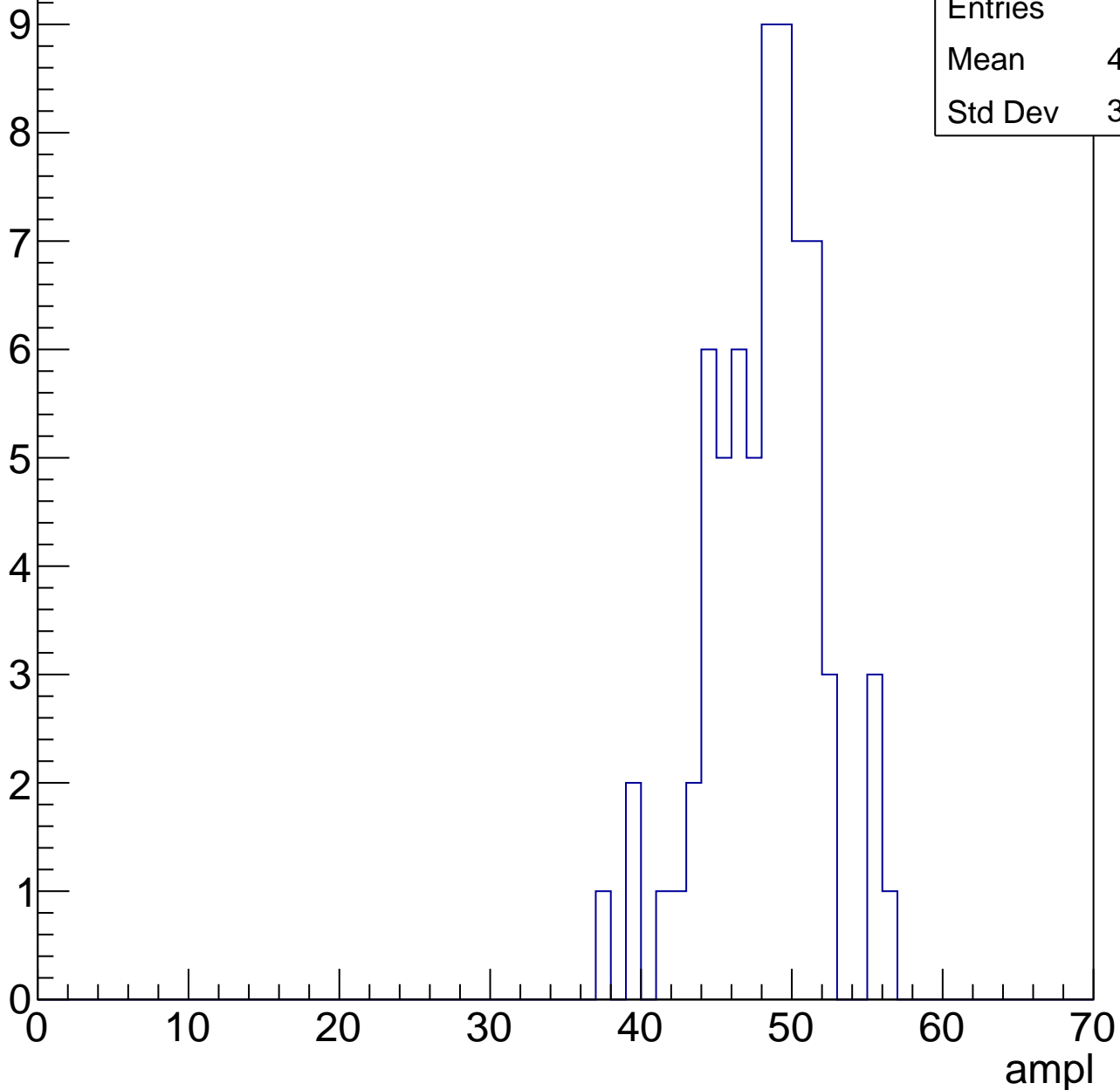


B1L103S, U10-ch52, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	47.66
Std Dev	3.716

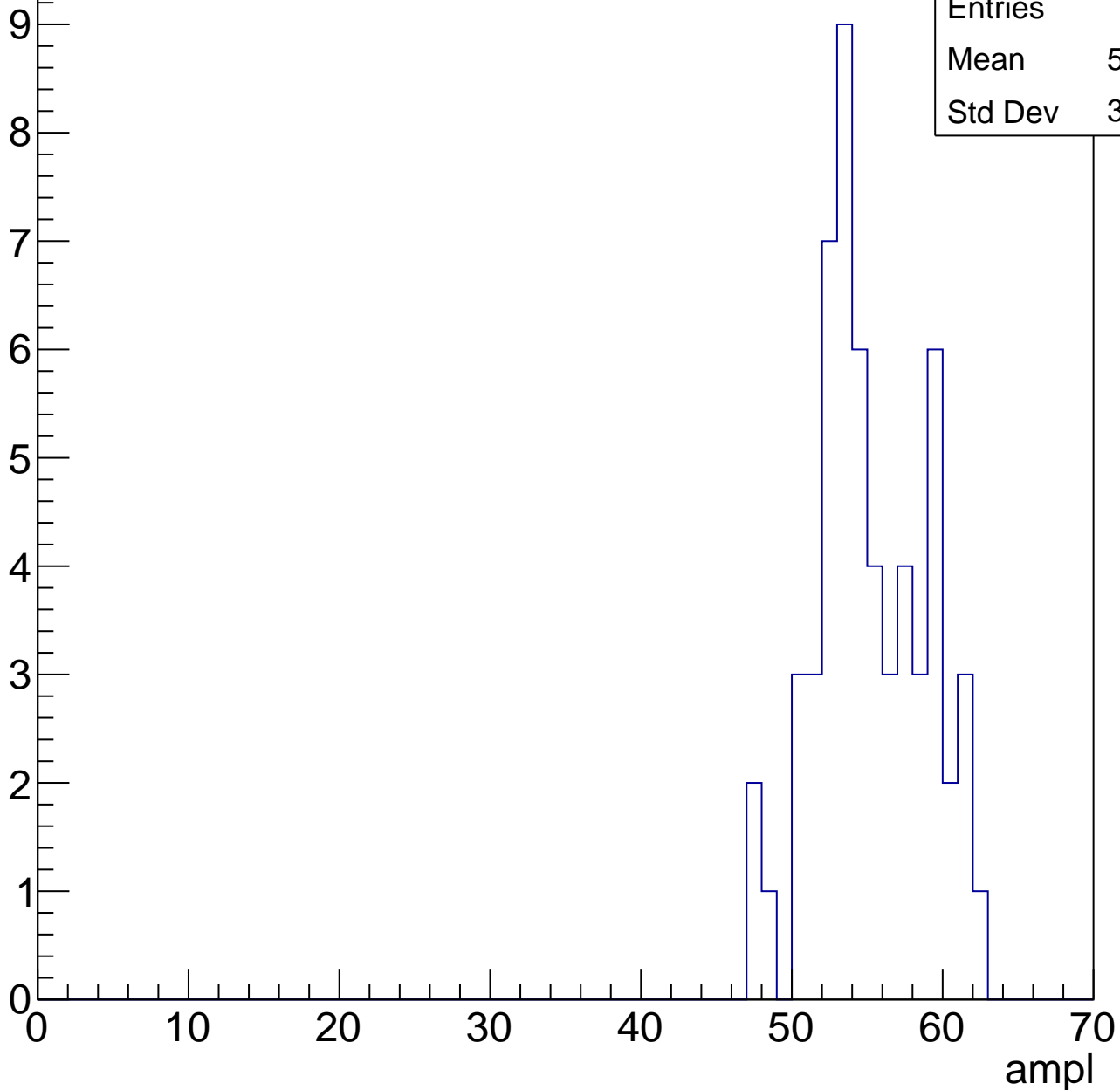


B1L103S, U10-ch52, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

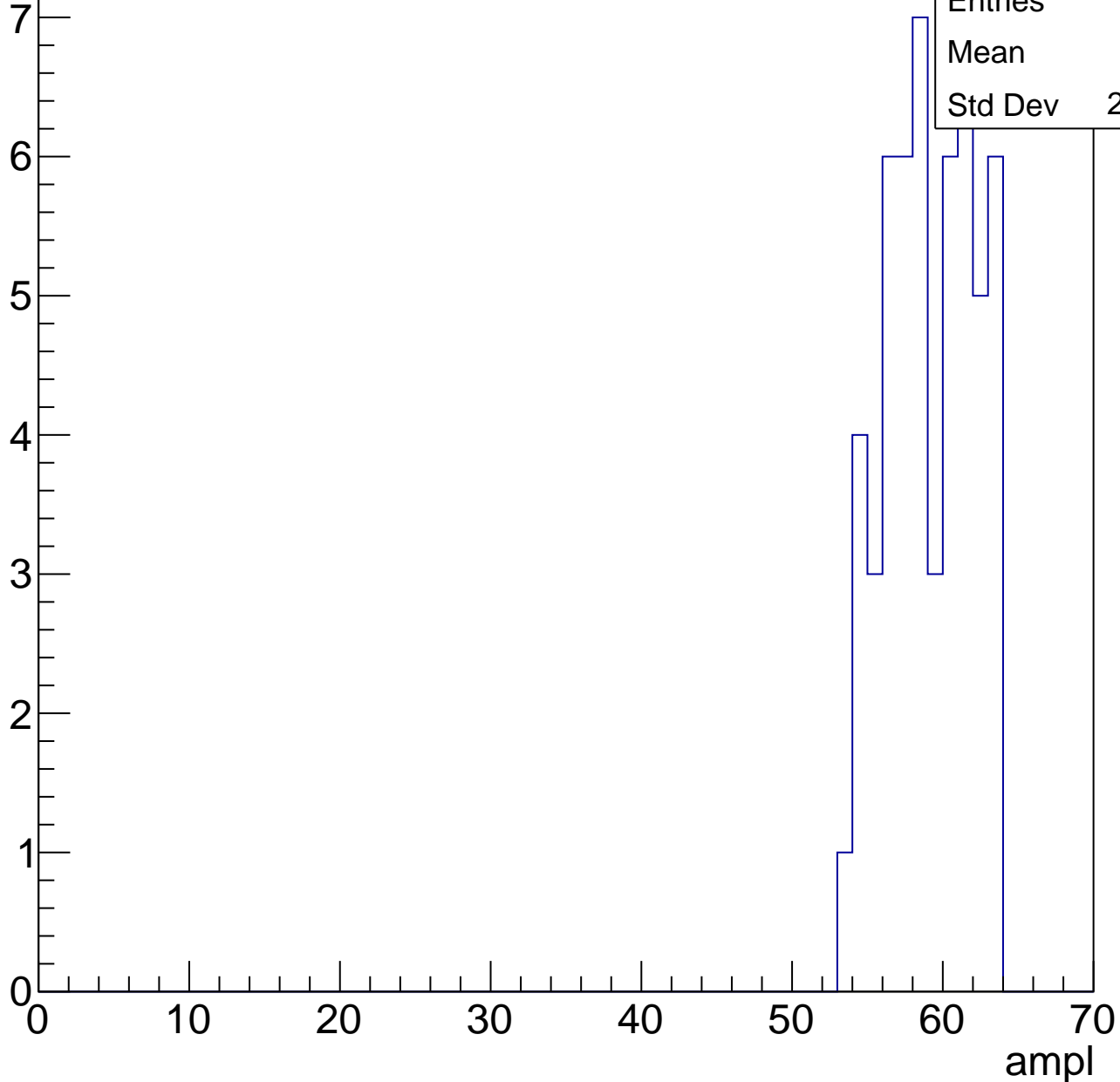
Entries	57
Mean	54.72
Std Dev	3.626



B1L103S, U10-ch52, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

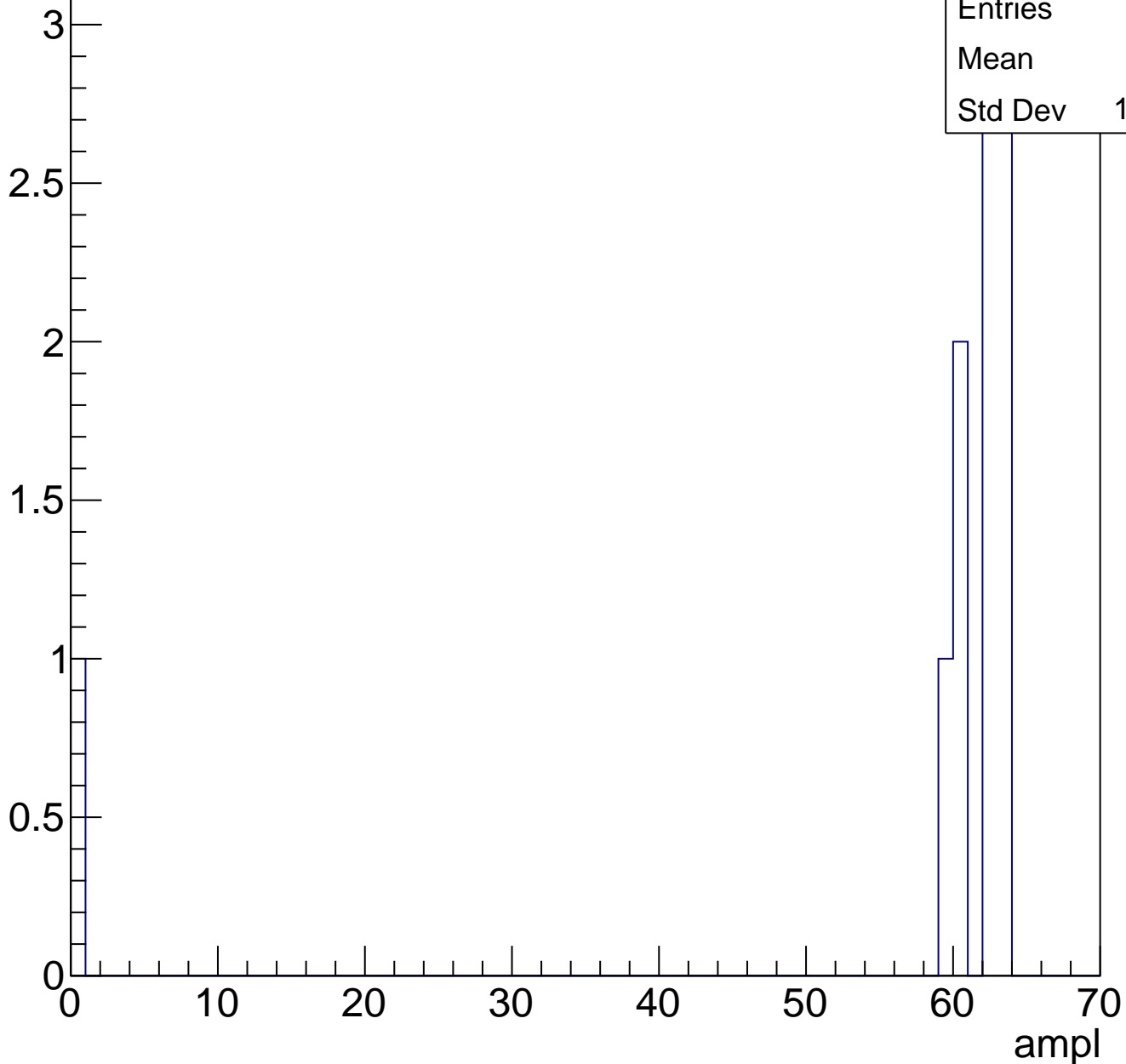


Entries	54
Mean	58.7
Std Dev	2.852

B1L103S, U10-ch52, adc6

calib_packv5_042523_0143.root, FC#7, port C2

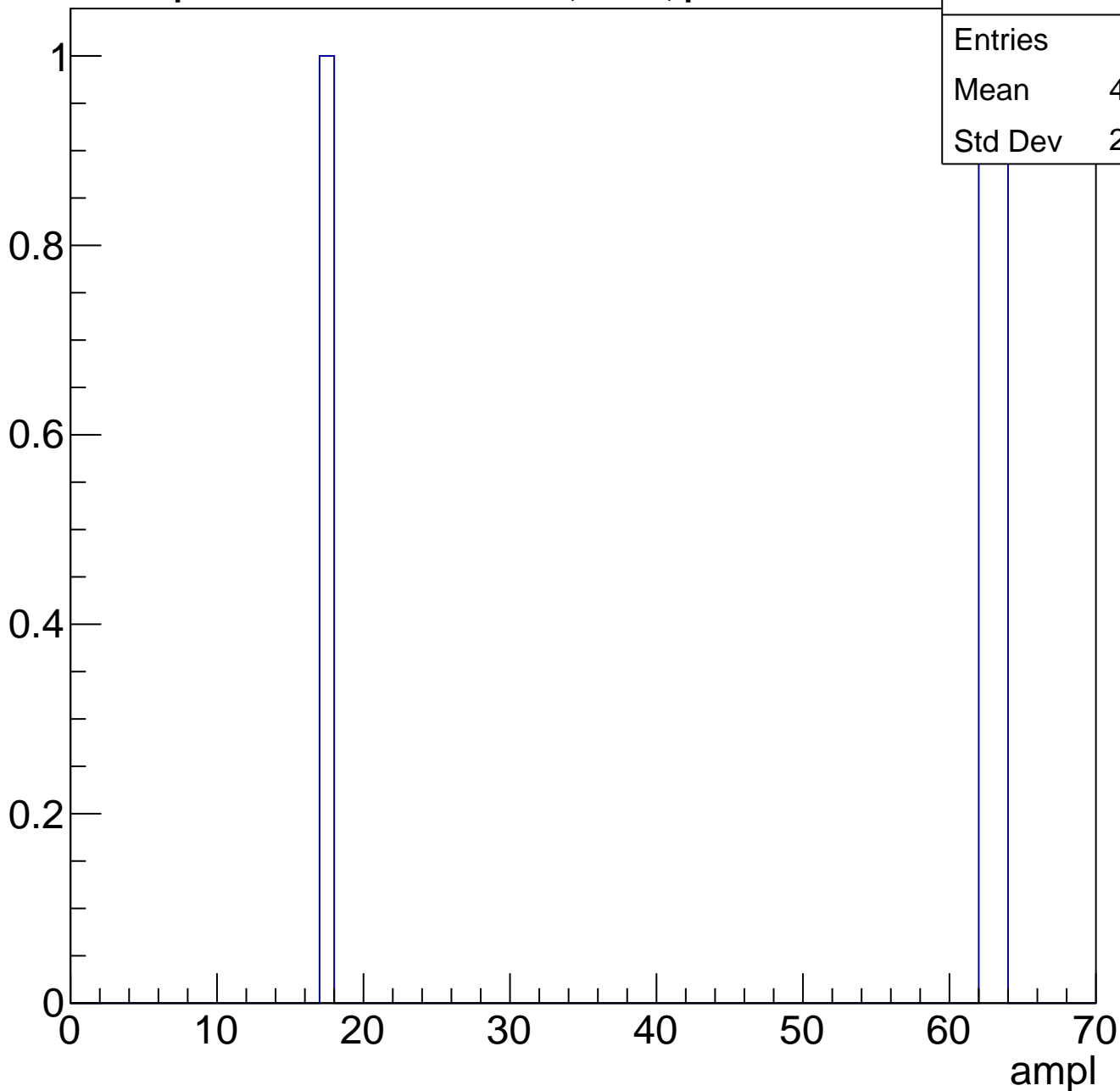
Entry



B1L103S, U10-ch52, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	47.33
Std Dev	21.45

B1L103S, U10-ch53, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	28.48
Std Dev	4.769

Gaus mean : 29.7268

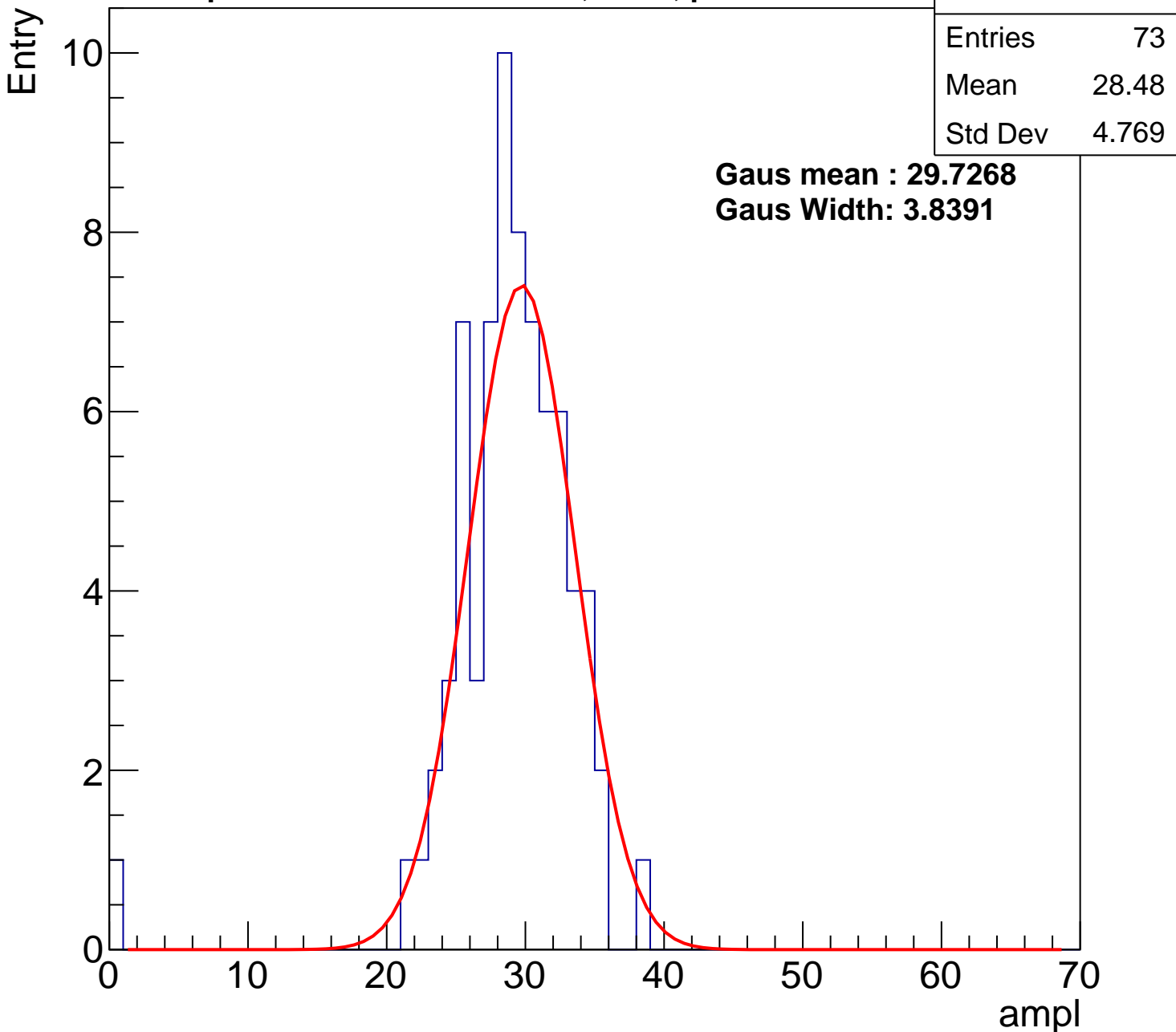
Gaus Width: 3.8391

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

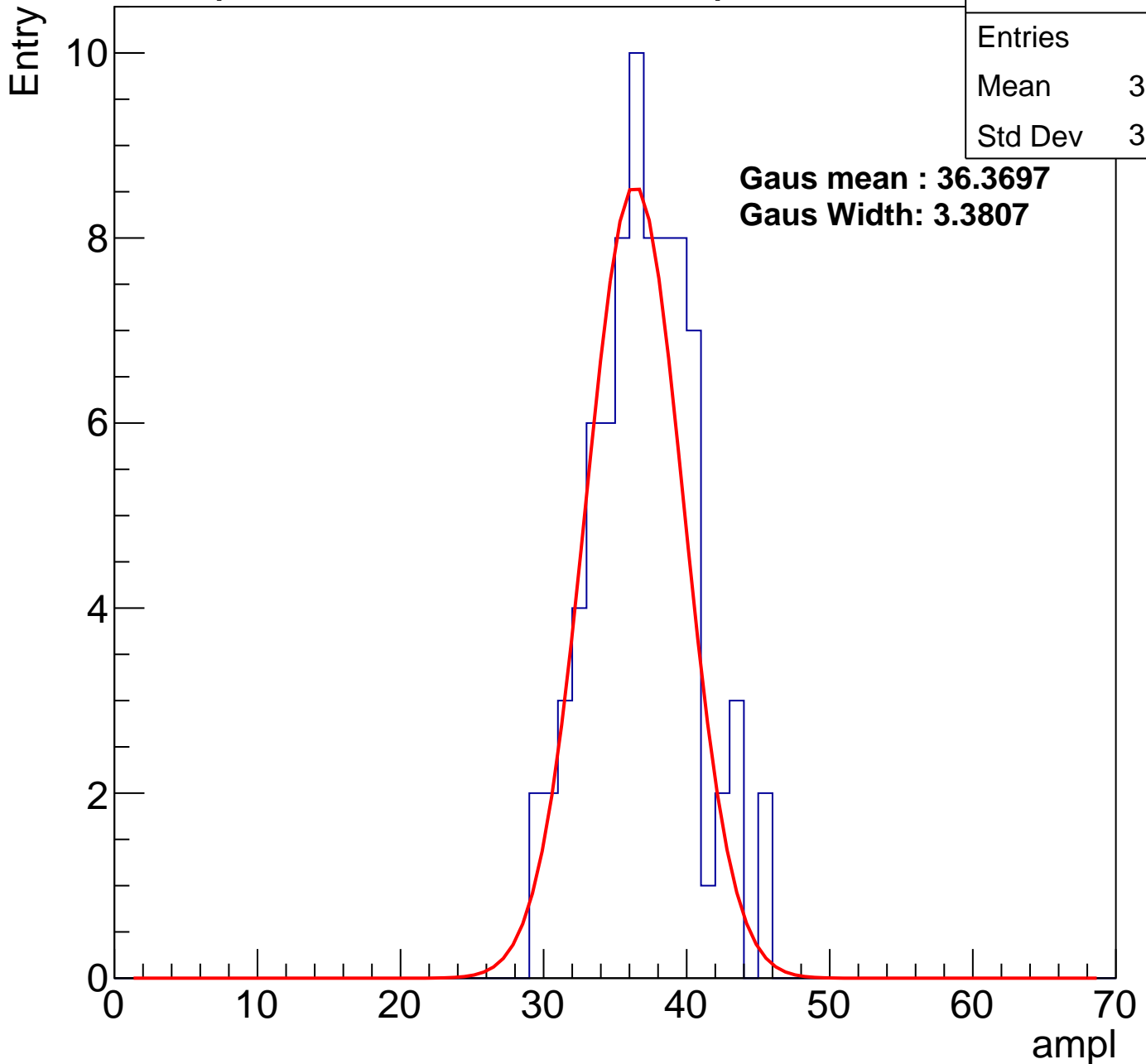


B1L103S, U10-ch53, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	80
Mean	36.46
Std Dev	3.549

Gaus mean : 36.3697
Gaus Width: 3.3807



B1L103S, U10-ch53, adc2

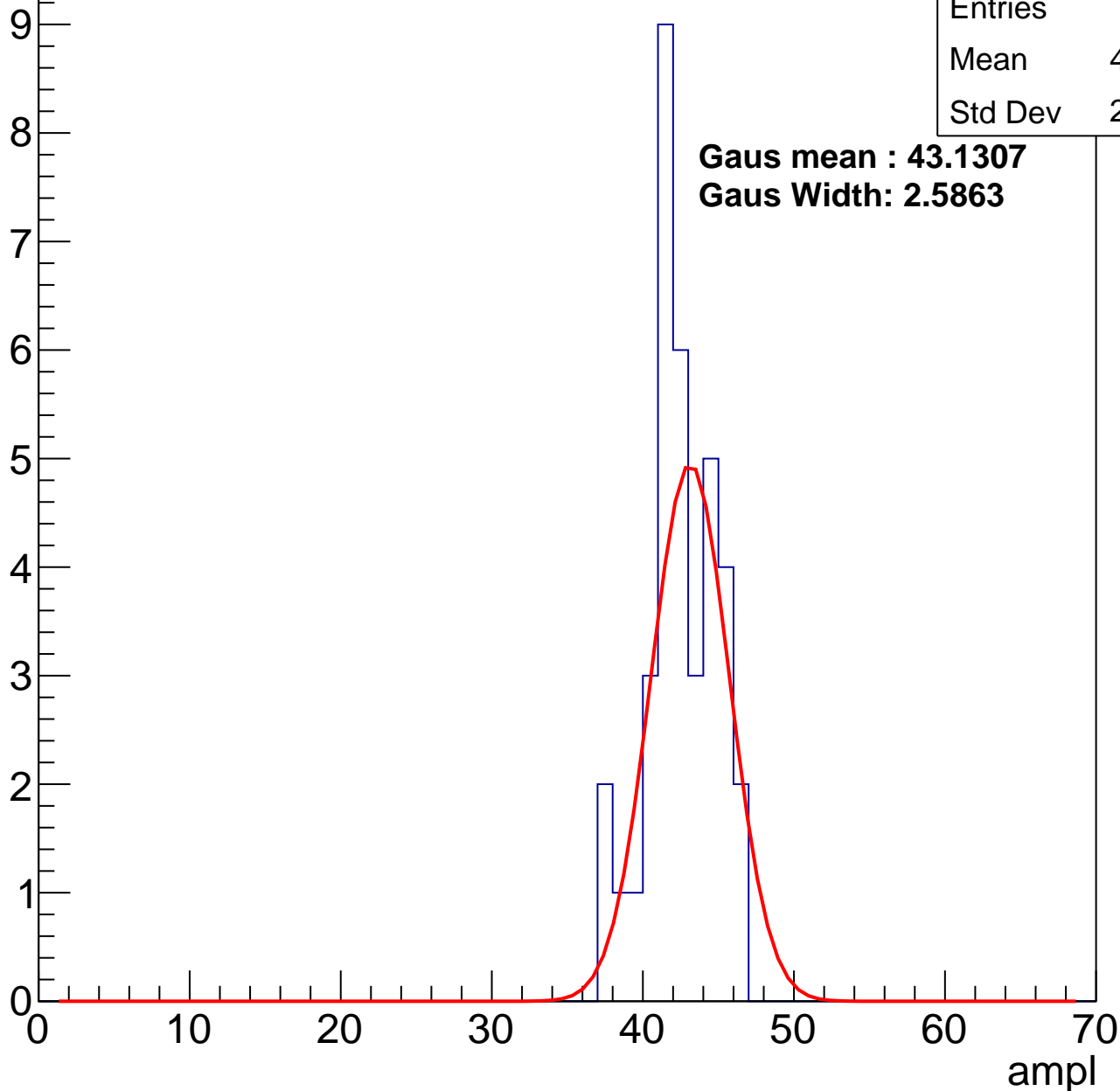
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	42.03
Std Dev	2.279

Gaus mean : 43.1307

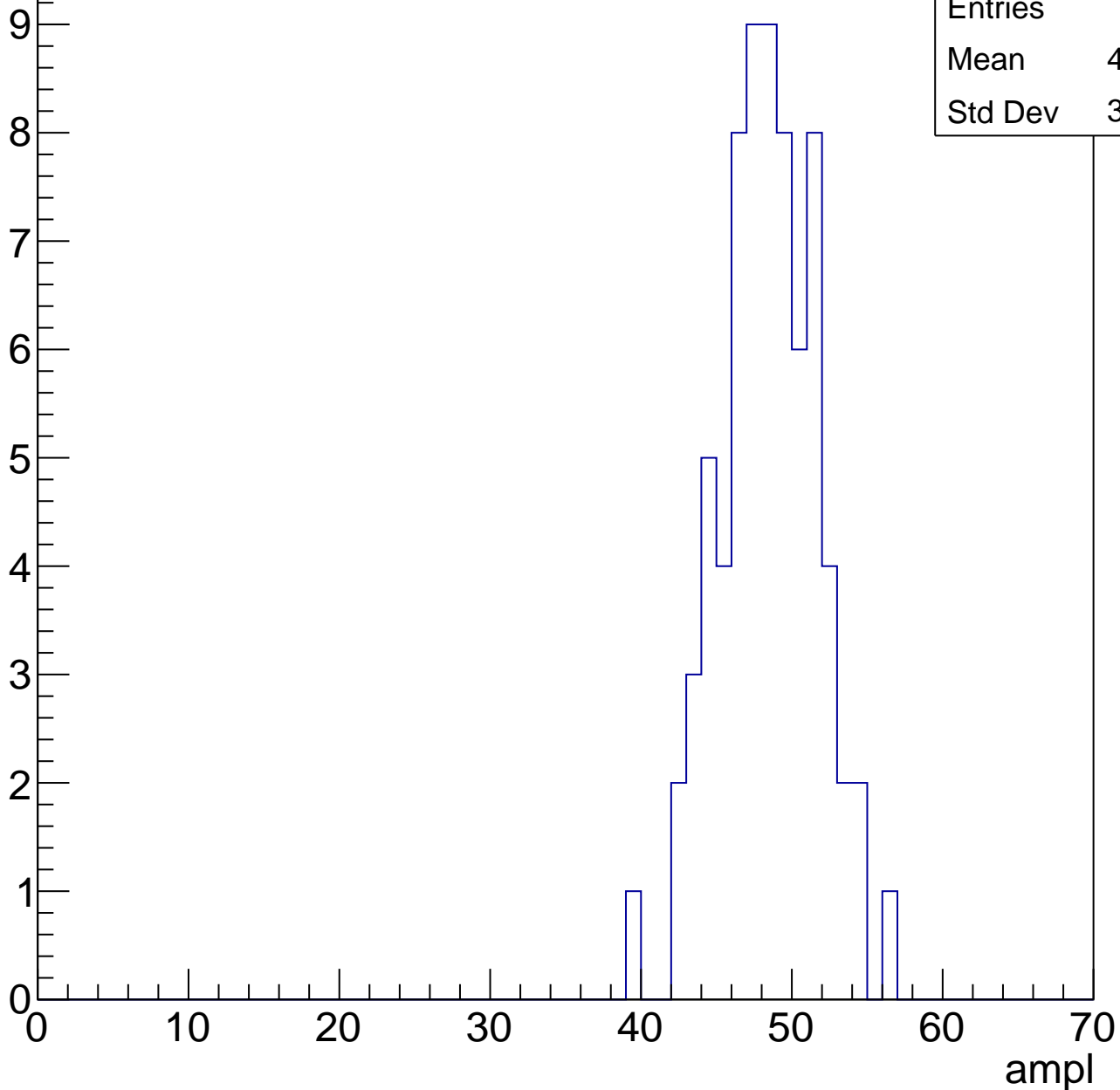
Gaus Width: 2.5863



B1L103S, U10-ch53, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



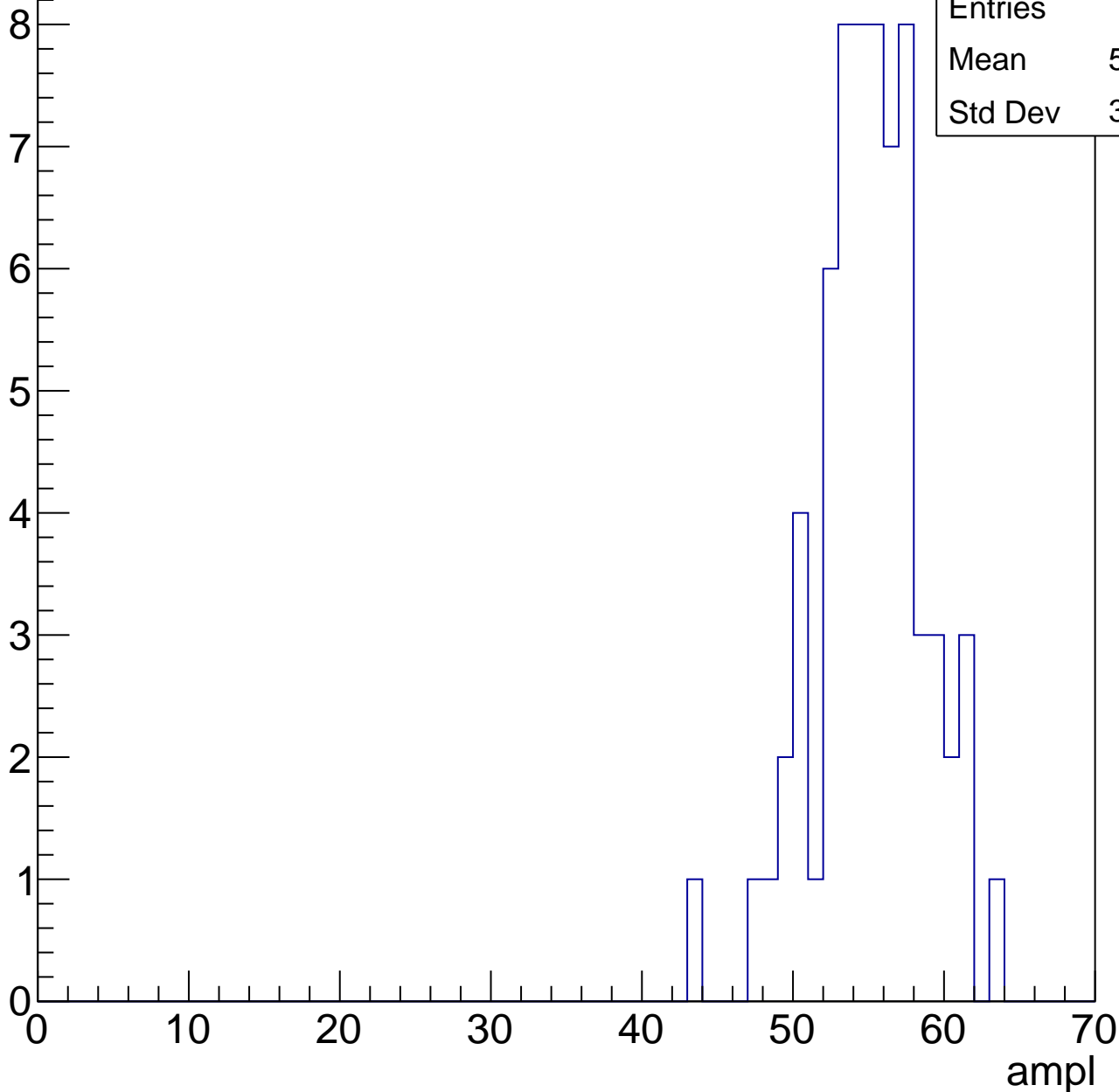
Entries	72
Mean	47.96
Std Dev	3.203

B1L103S, U10-ch53, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	54.63
Std Dev	3.595

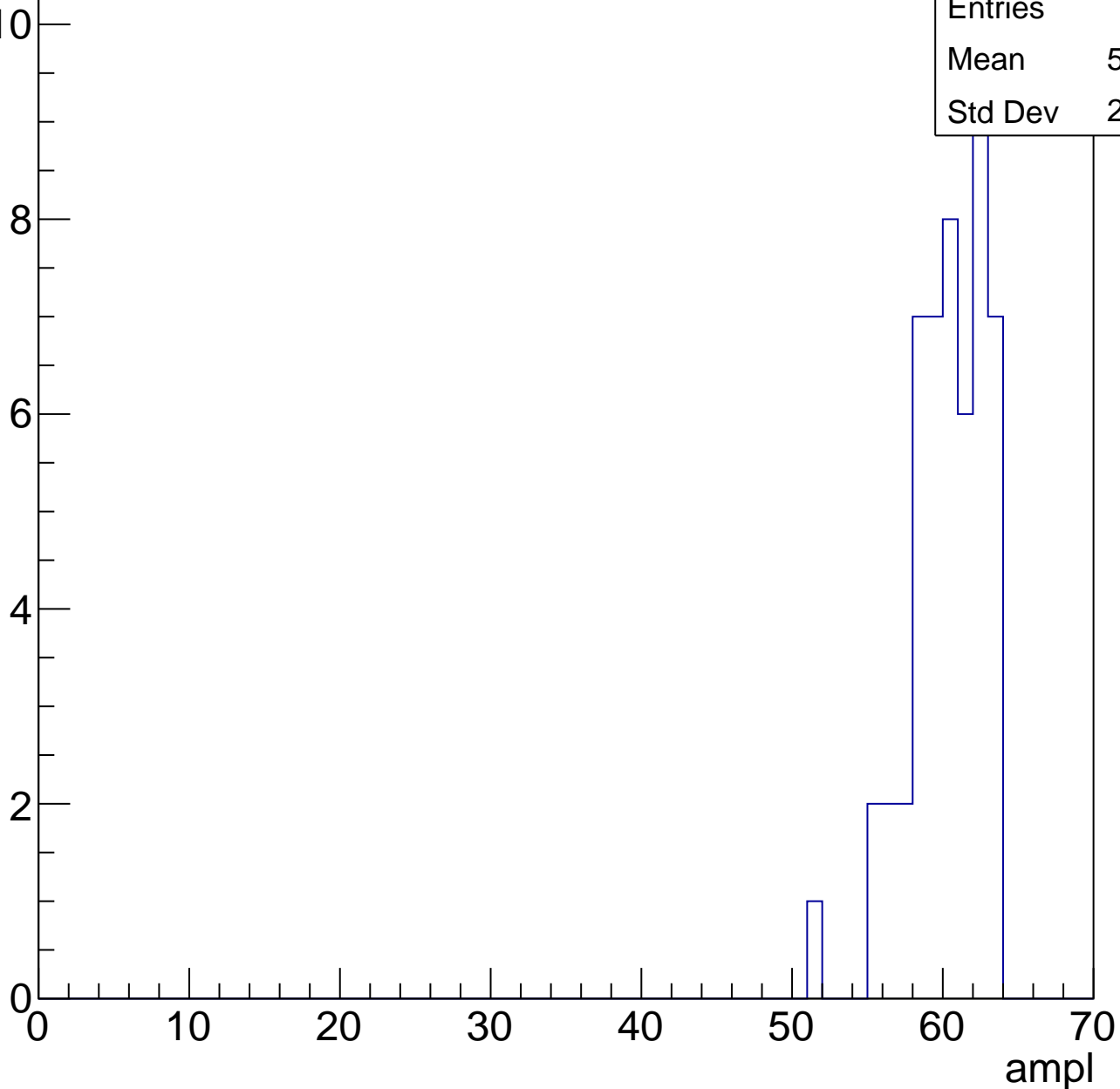


B1L103S, U10-ch53, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	59.87
Std Dev	2.496



B1L103S, U10-ch53, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	41.33
Std Dev	29.24

B1L103S, U10-ch53, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U10-ch54, adc0

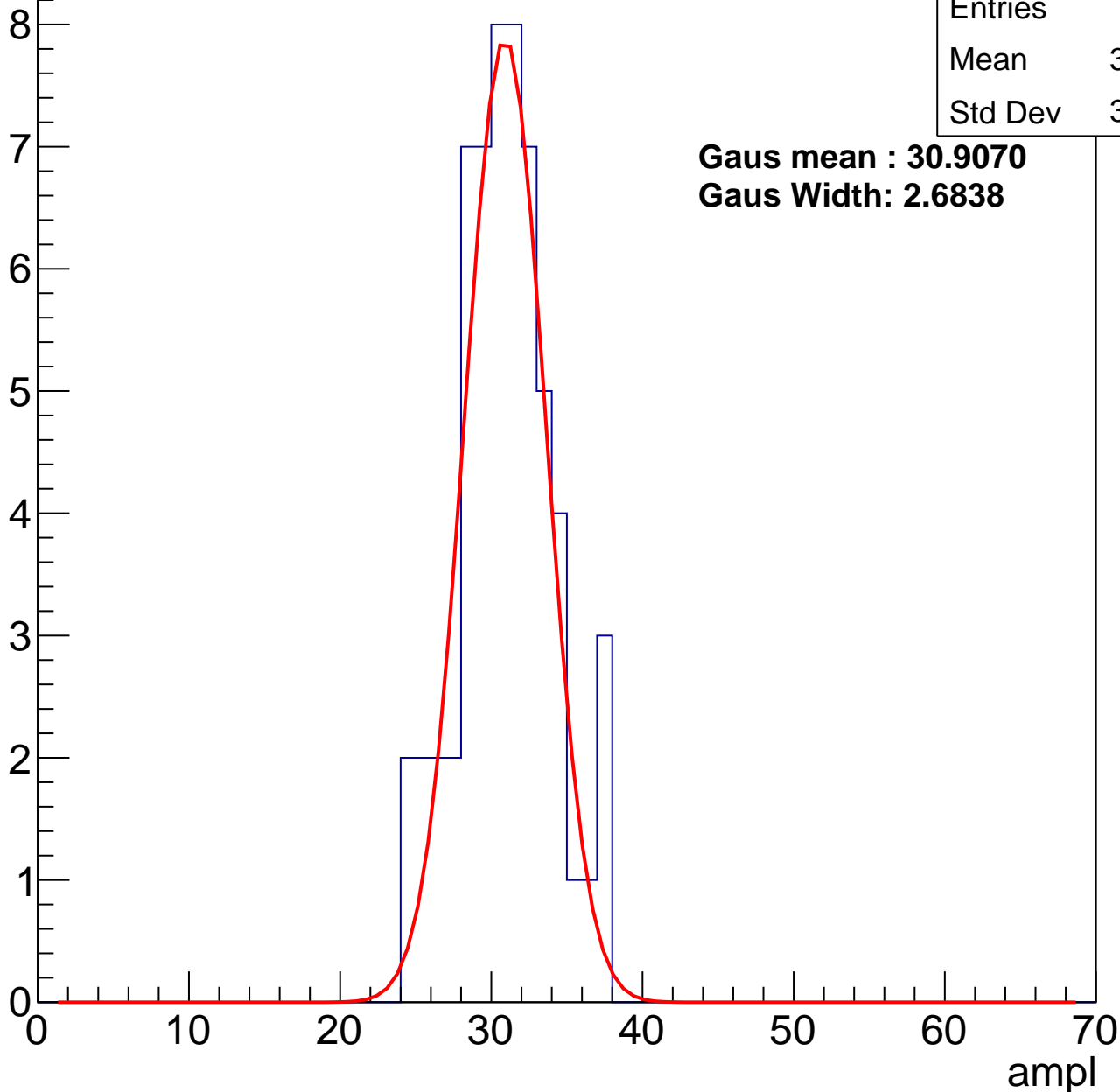
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	30.47
Std Dev	3.044

Gaus mean : 30.9070

Gaus Width: 2.6838



B1L103S, U10-ch54, adc1

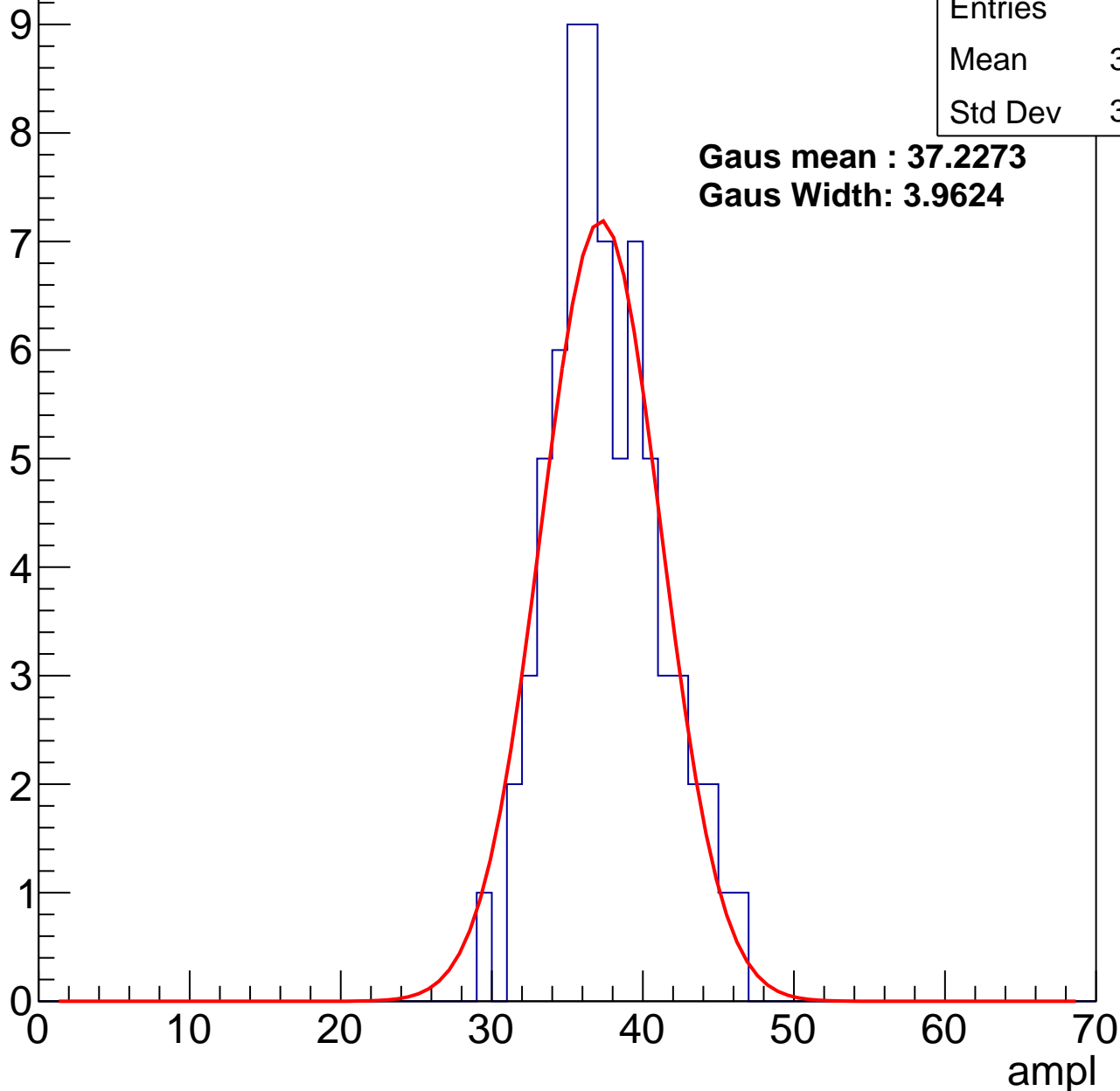
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	37.06
Std Dev	3.572

Gaus mean : 37.2273

Gaus Width: 3.9624



B1L103S, U10-ch54, adc2

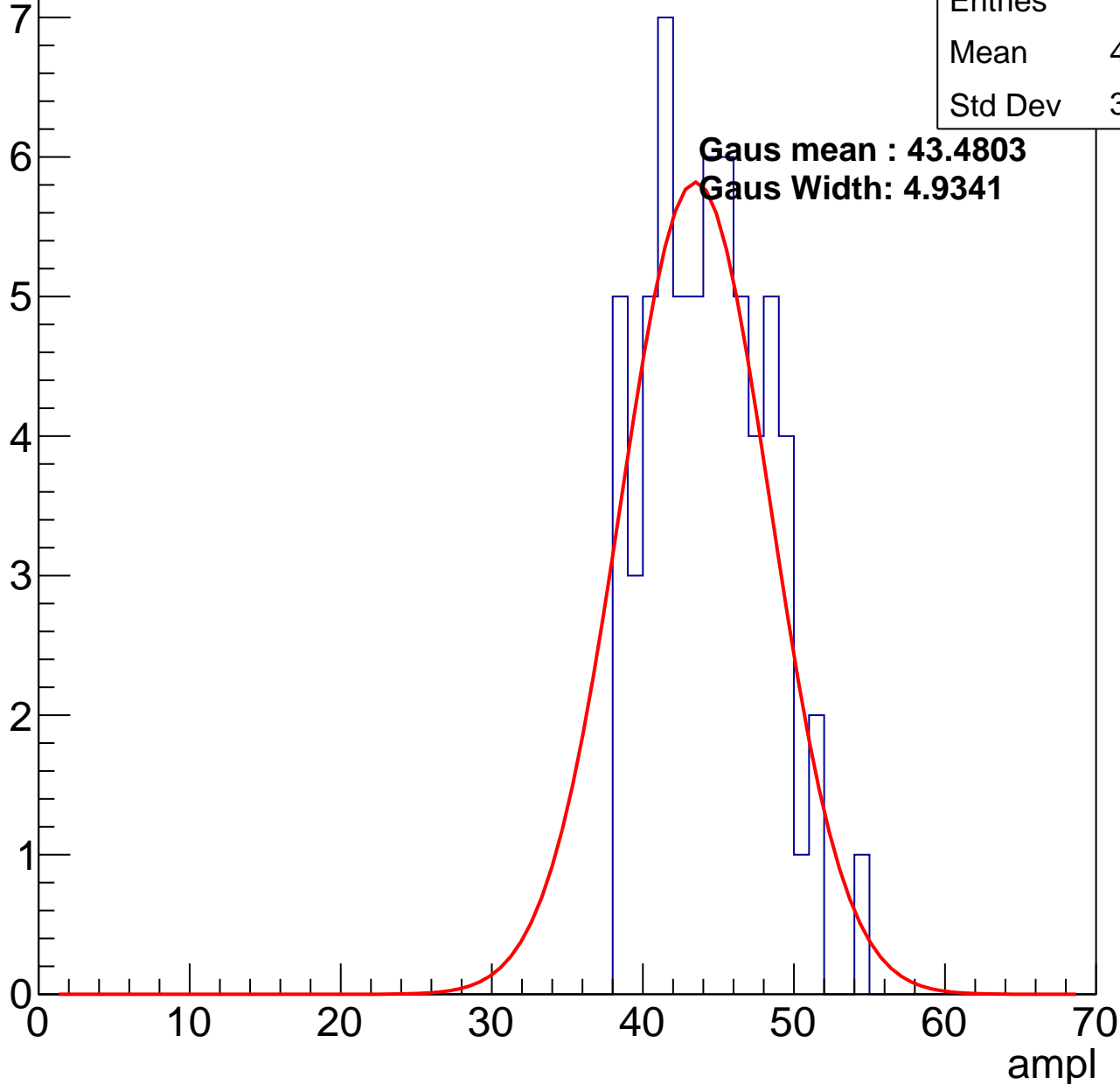
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.95
Std Dev	3.748

Gaus mean : 43.4803

Gaus Width: 4.9341

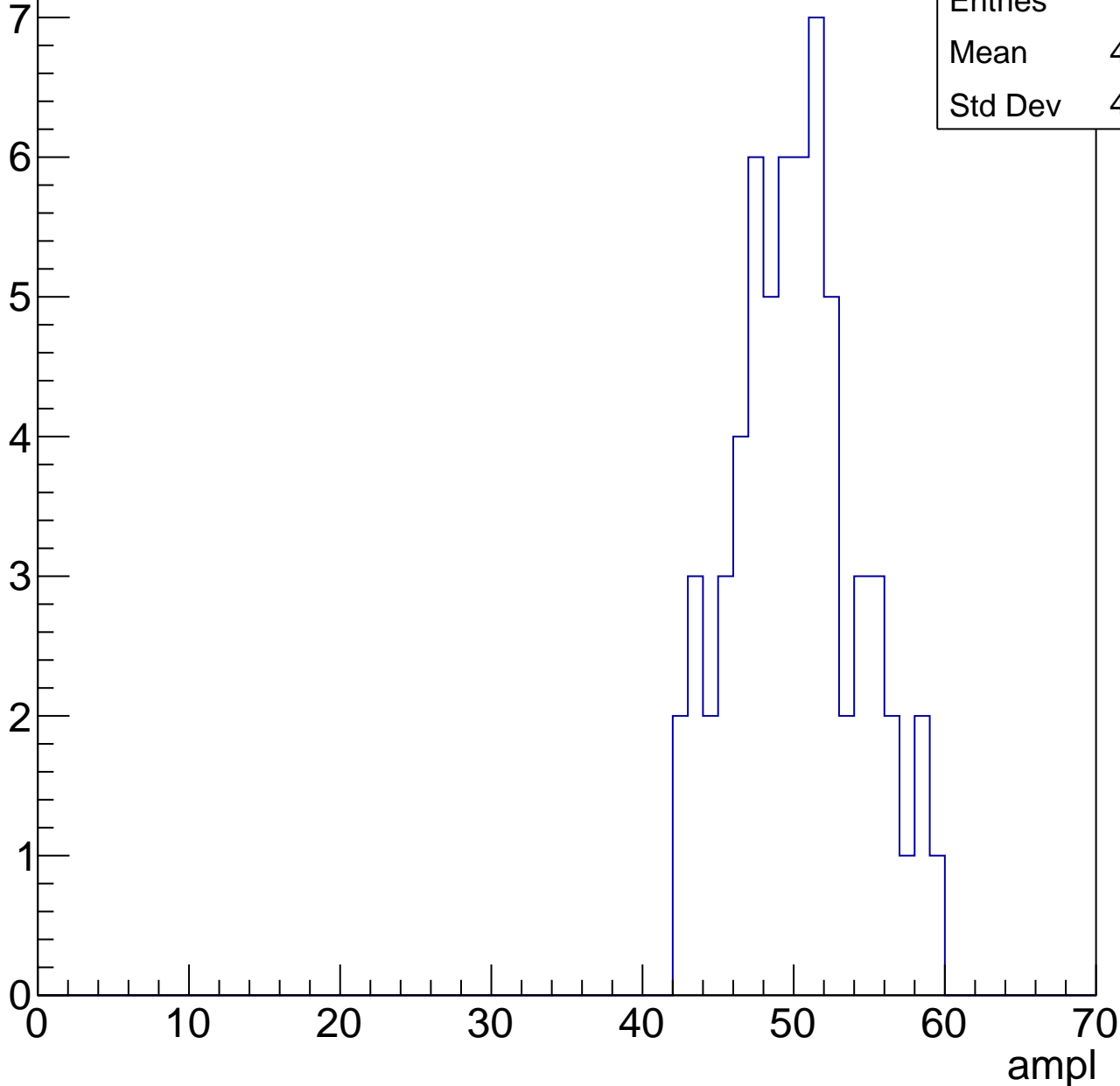


B1L103S, U10-ch54, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

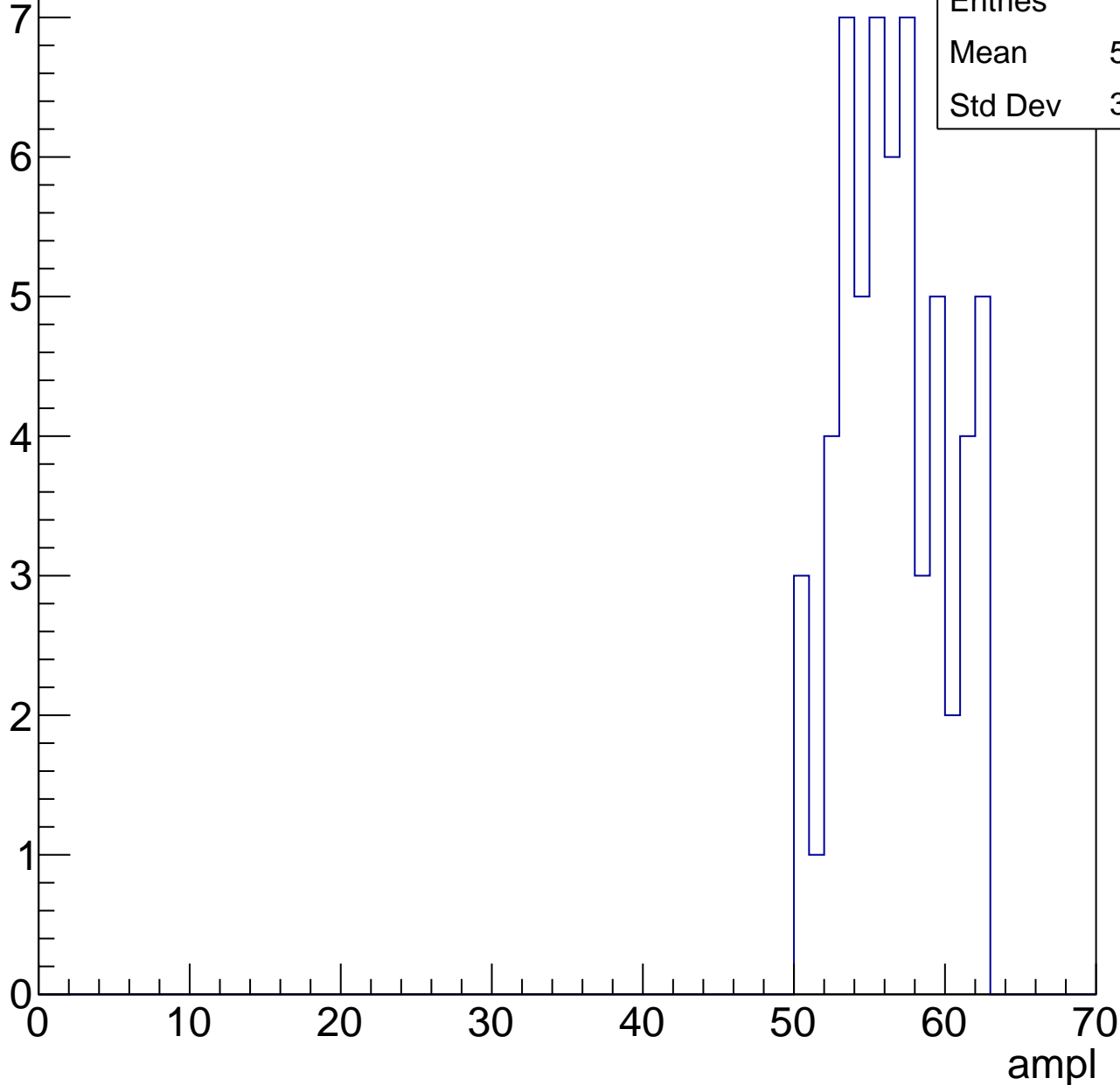
Entries	63
Mean	49.68
Std Dev	4.097



B1L103S, U10-ch54, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

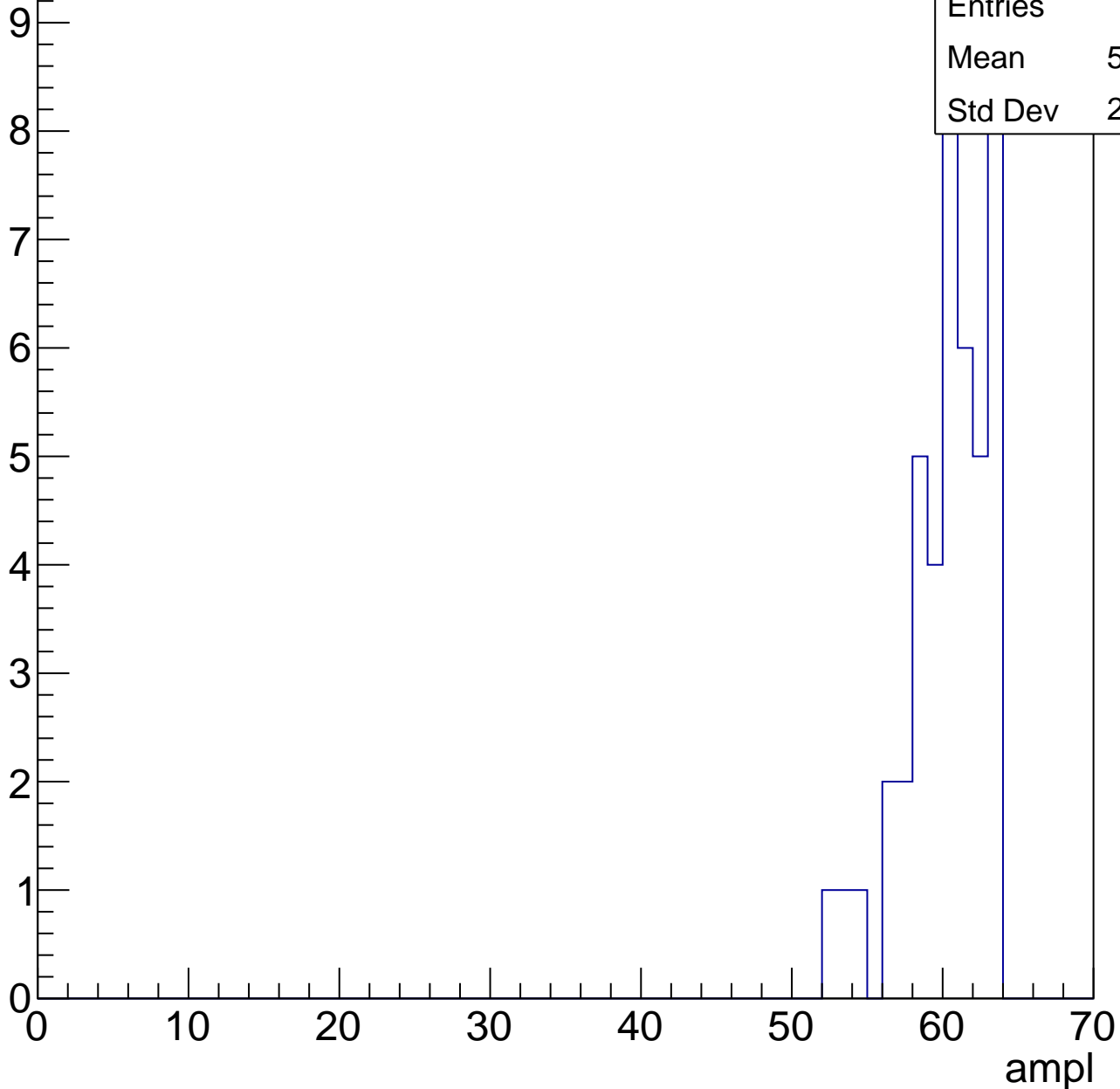


B1L103S, U10-ch54, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.86
Std Dev	2.735



B1L103S, U10-ch54, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch54, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch55, adc0

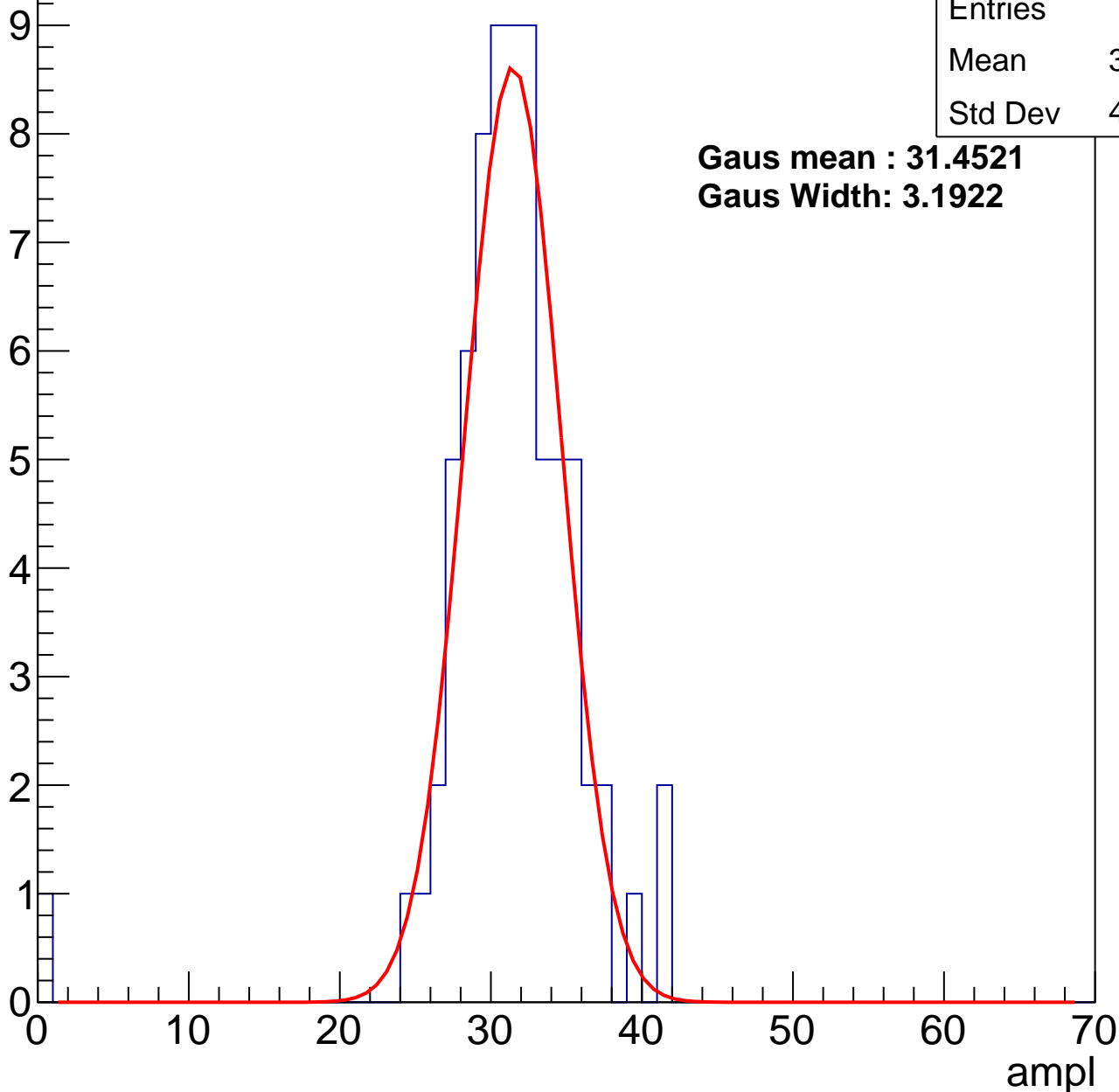
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	30.82
Std Dev	4.978

Gaus mean : 31.4521

Gaus Width: 3.1922



B1L103S, U10-ch55, adc1

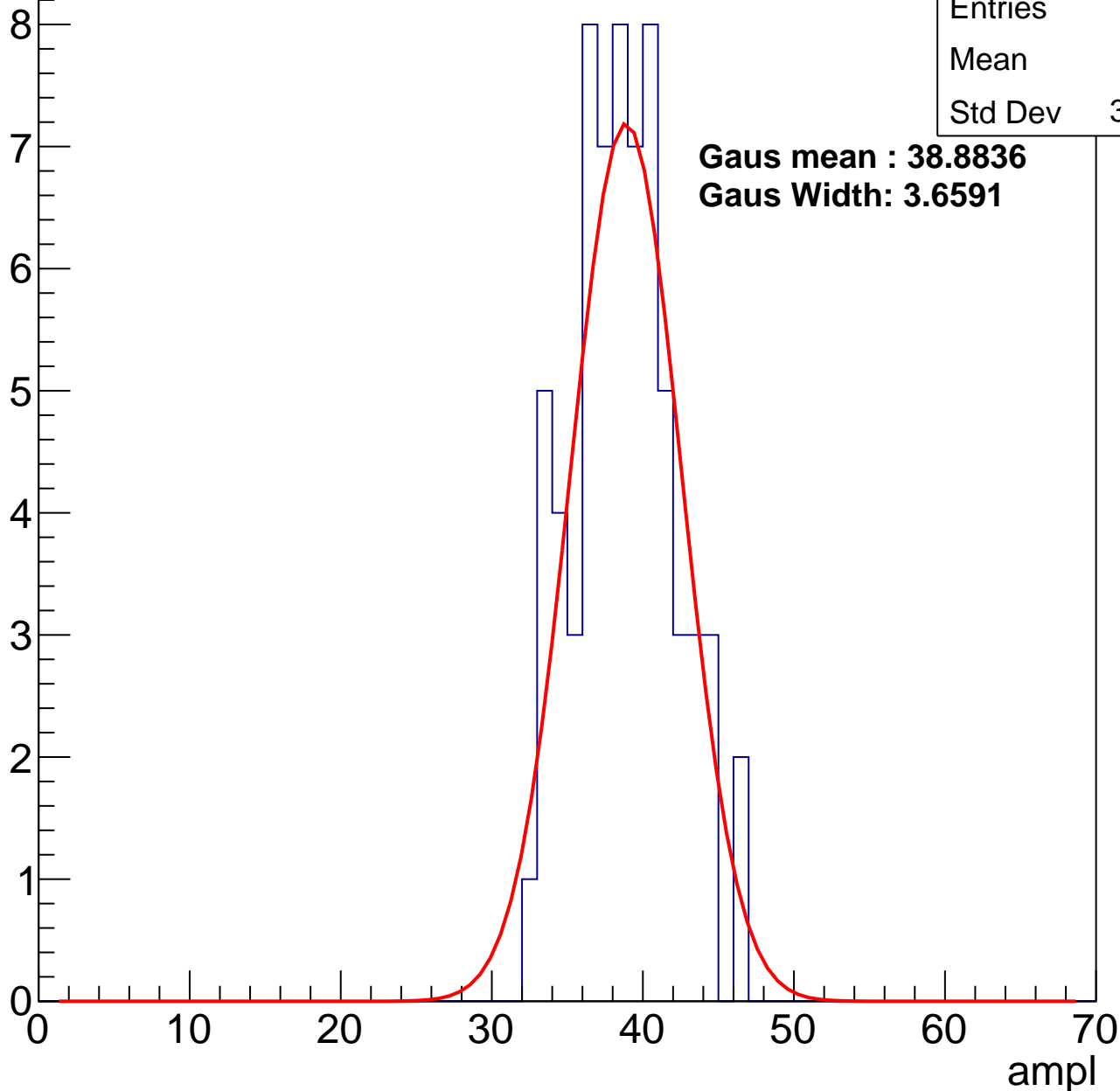
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	38.3
Std Dev	3.296

Gaus mean : 38.8836

Gaus Width: 3.6591



B1L103S, U10-ch55, adc2

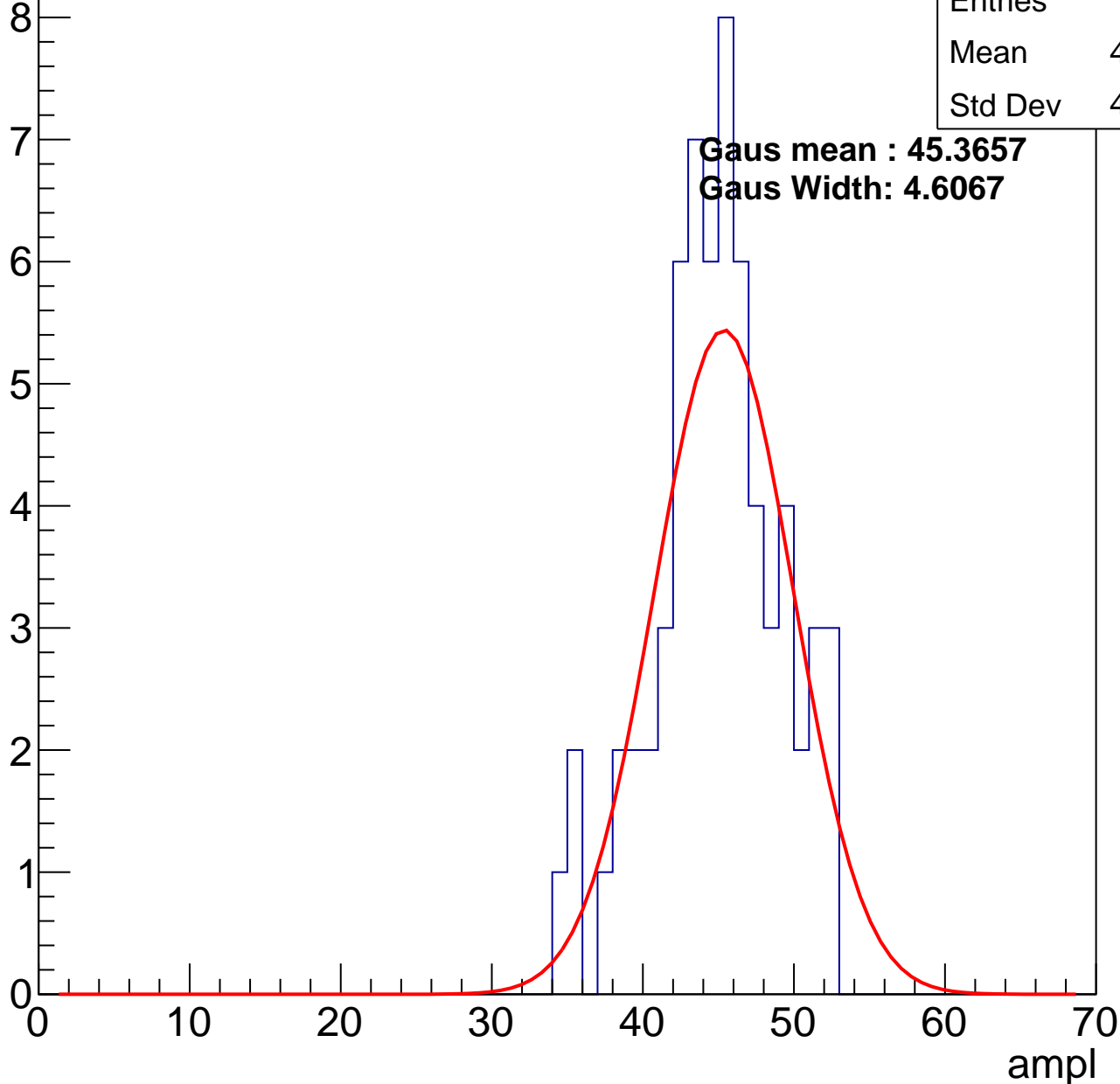
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.43
Std Dev	4.184

Gaus mean : 45.3657

Gaus Width: 4.6067

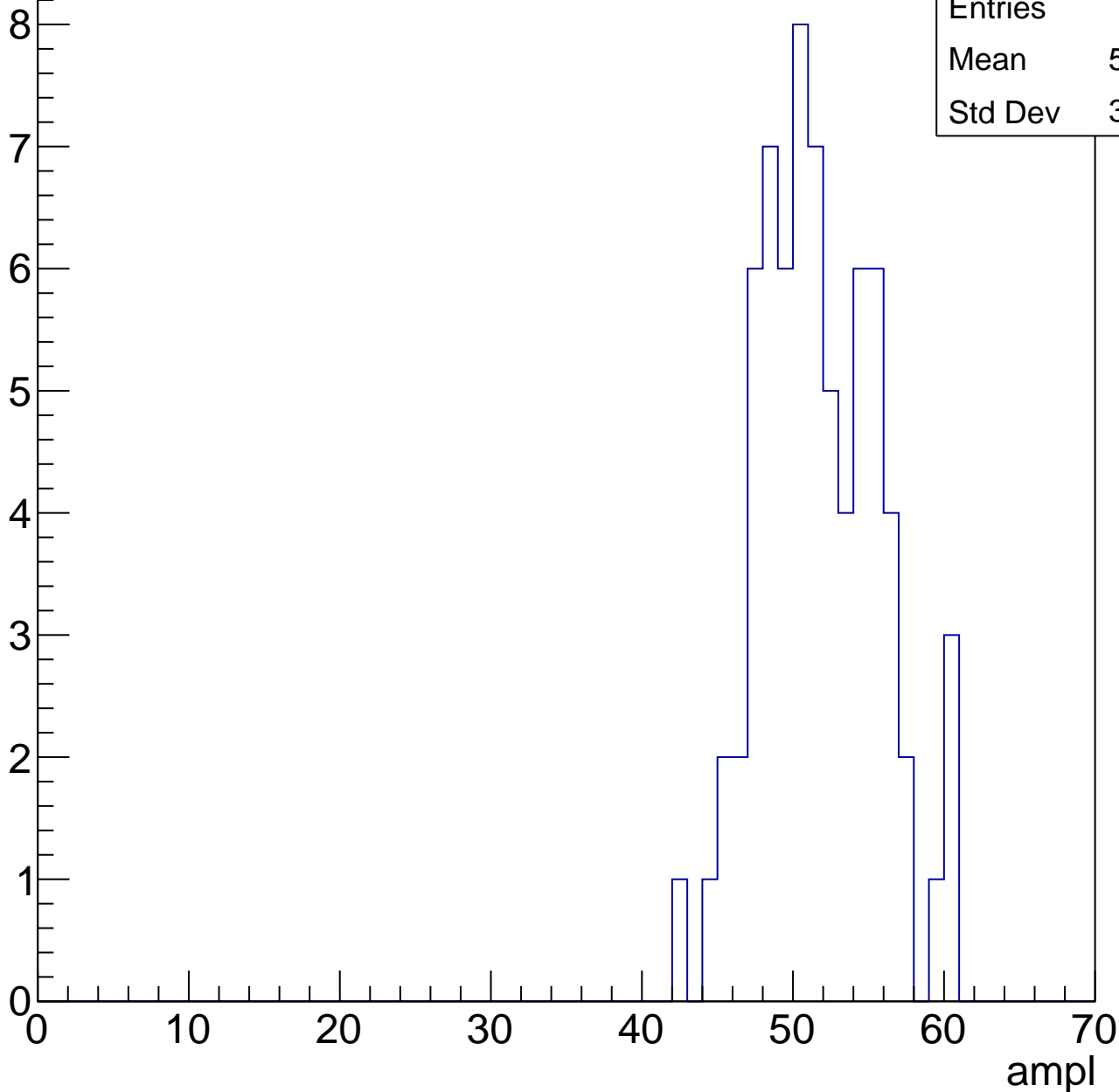


B1L103S, U10-ch55, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	51.27
Std Dev	3.914

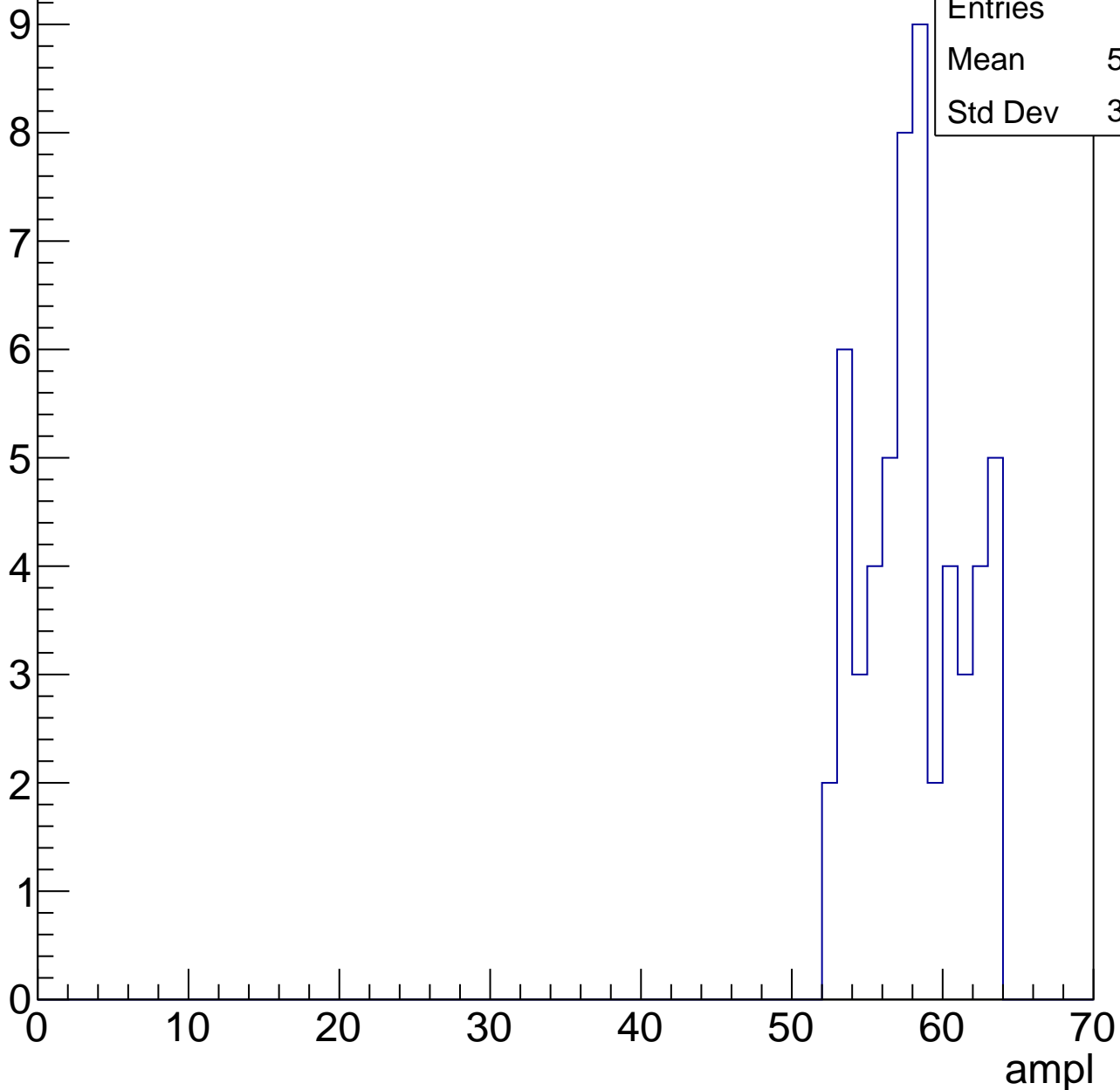


B1L103S, U10-ch55, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	57.56
Std Dev	3.184



B1L103S, U10-ch55, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 31

Mean 58.48

Std Dev 10.87

ampl

0

10

20

30

40

50

60

70

B1L103S, U10-ch55, adc6

calib_packv5_042523_0143.root, FC#7, port C2

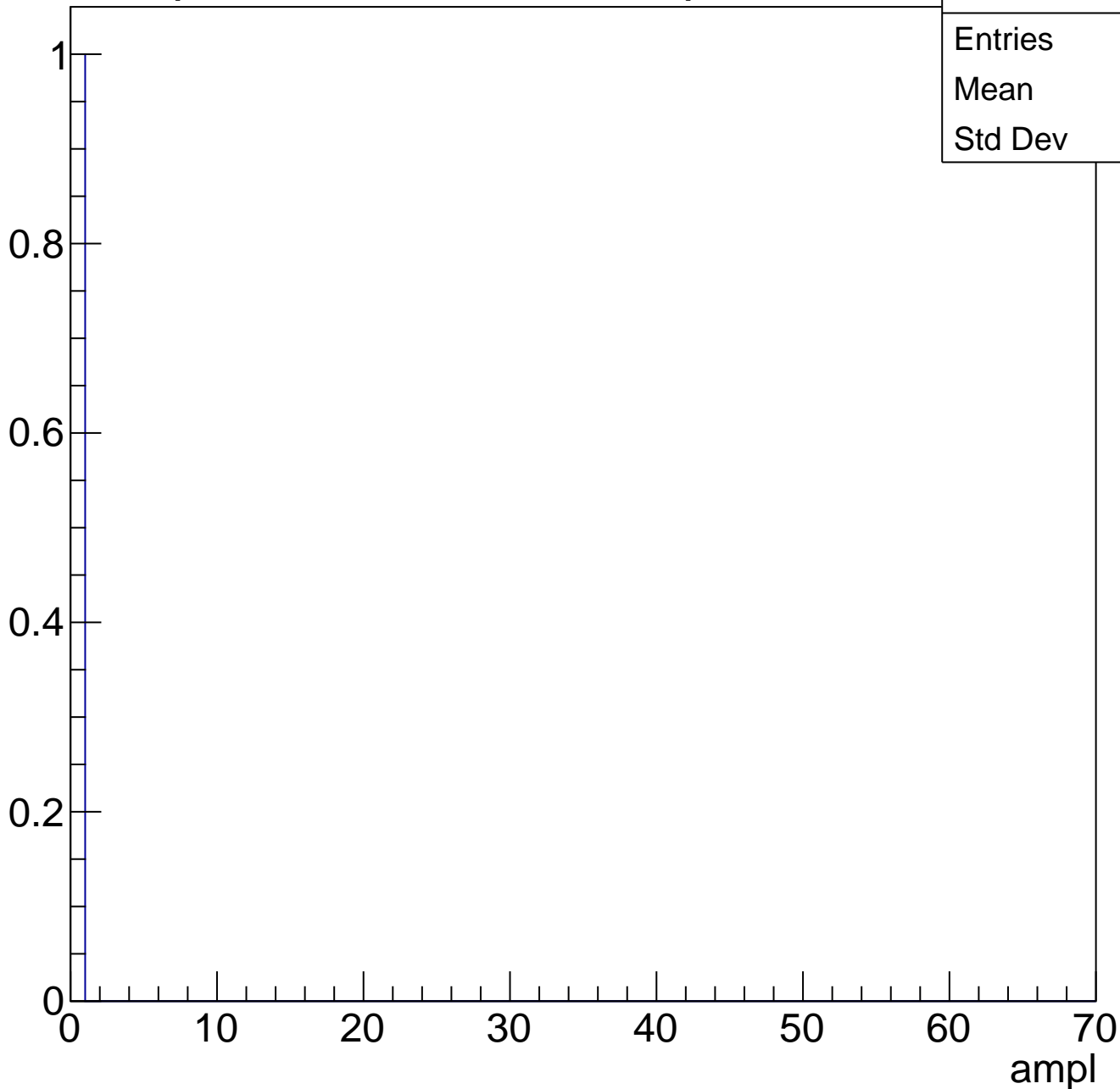
Entry



B1L103S, U10-ch55, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch56, adc0

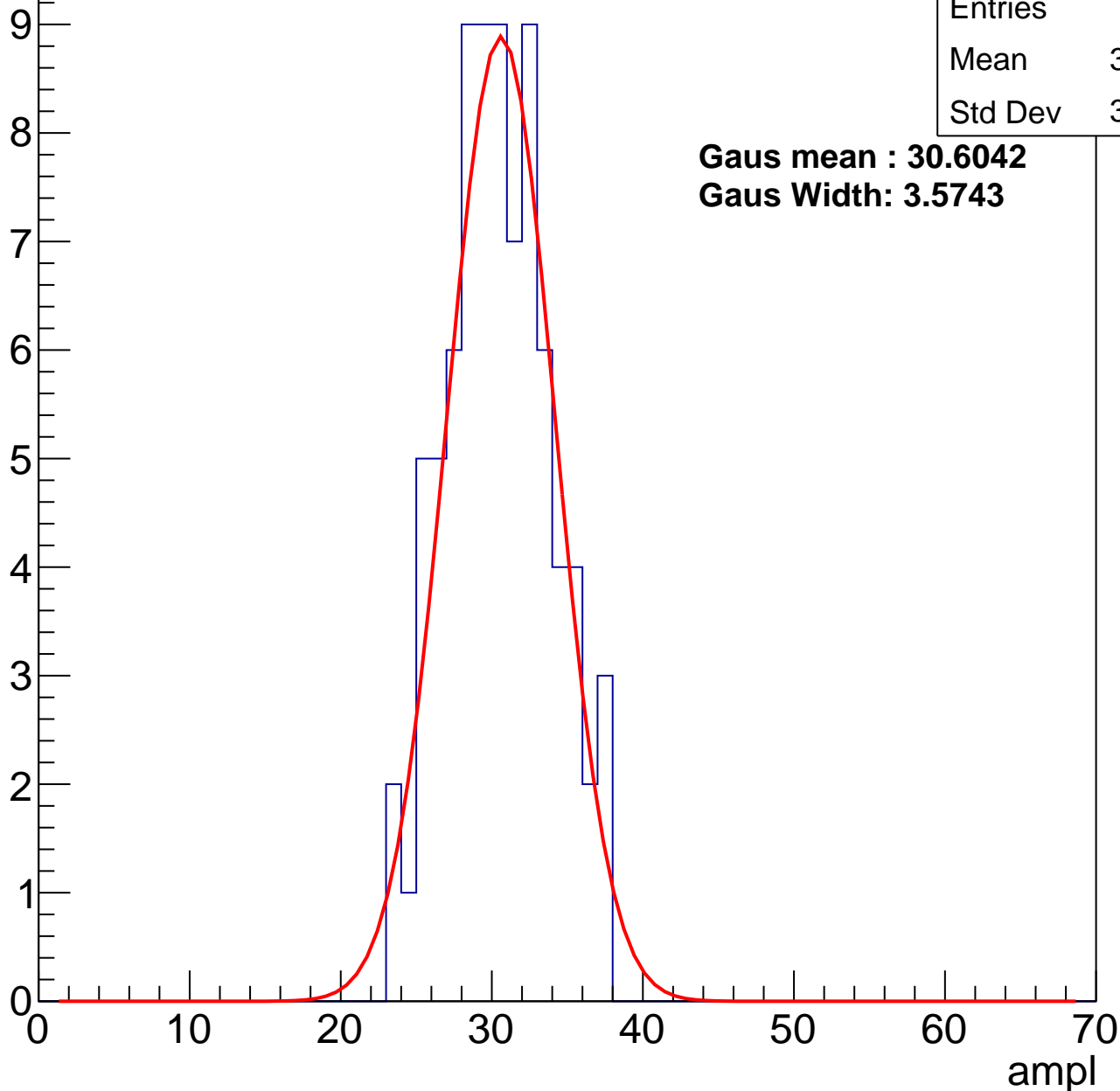
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	30.02
Std Dev	3.366

Gaus mean : 30.6042

Gaus Width: 3.5743



B1L103S, U10-ch56, adc1

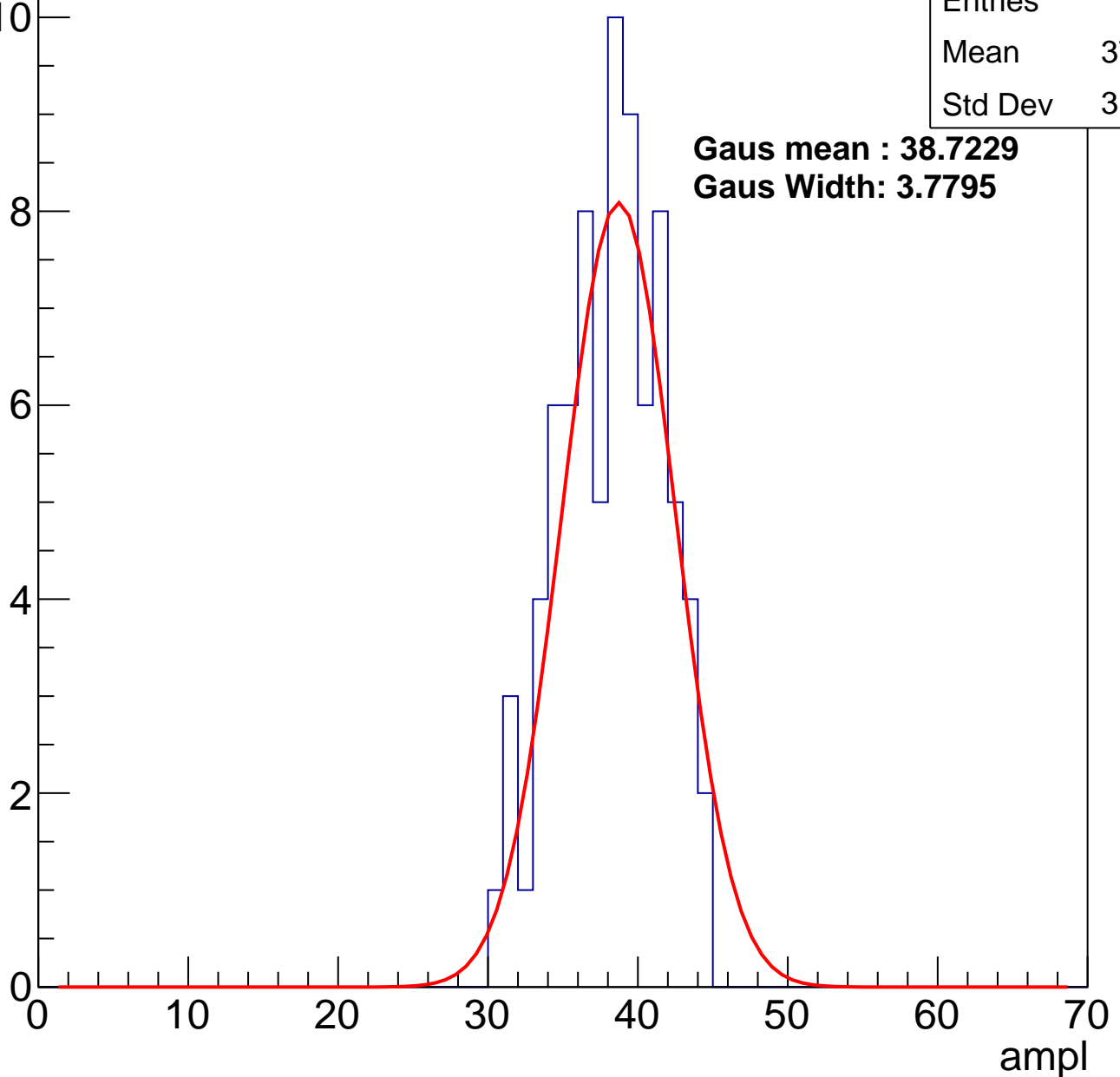
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	37.73
Std Dev	3.369

Gaus mean : 38.7229

Gaus Width: 3.7795



B1L103S, U10-ch56, adc2

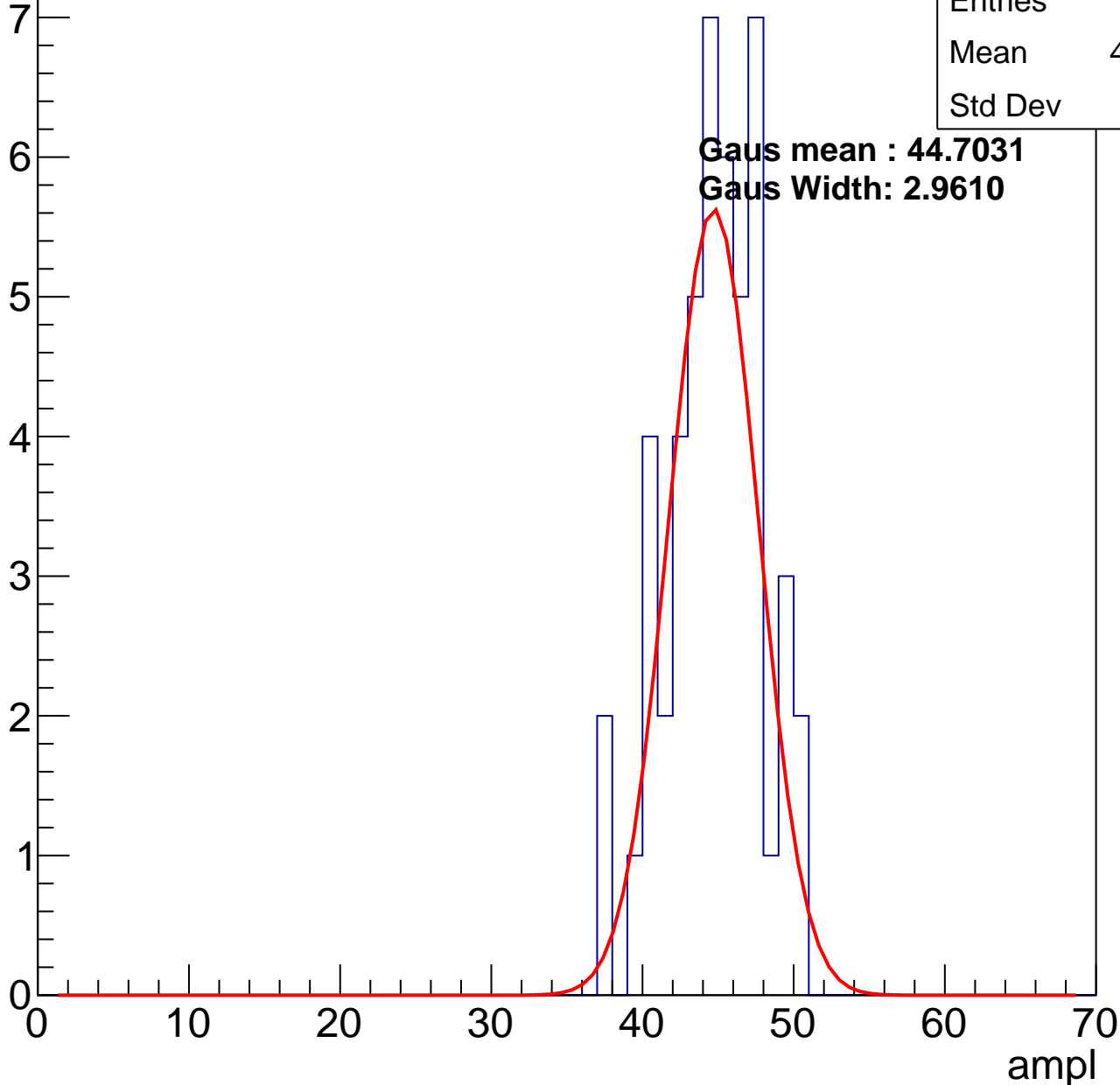
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	44.29
Std Dev	3.11

Gaus mean : 44.7031

Gaus Width: 2.9610

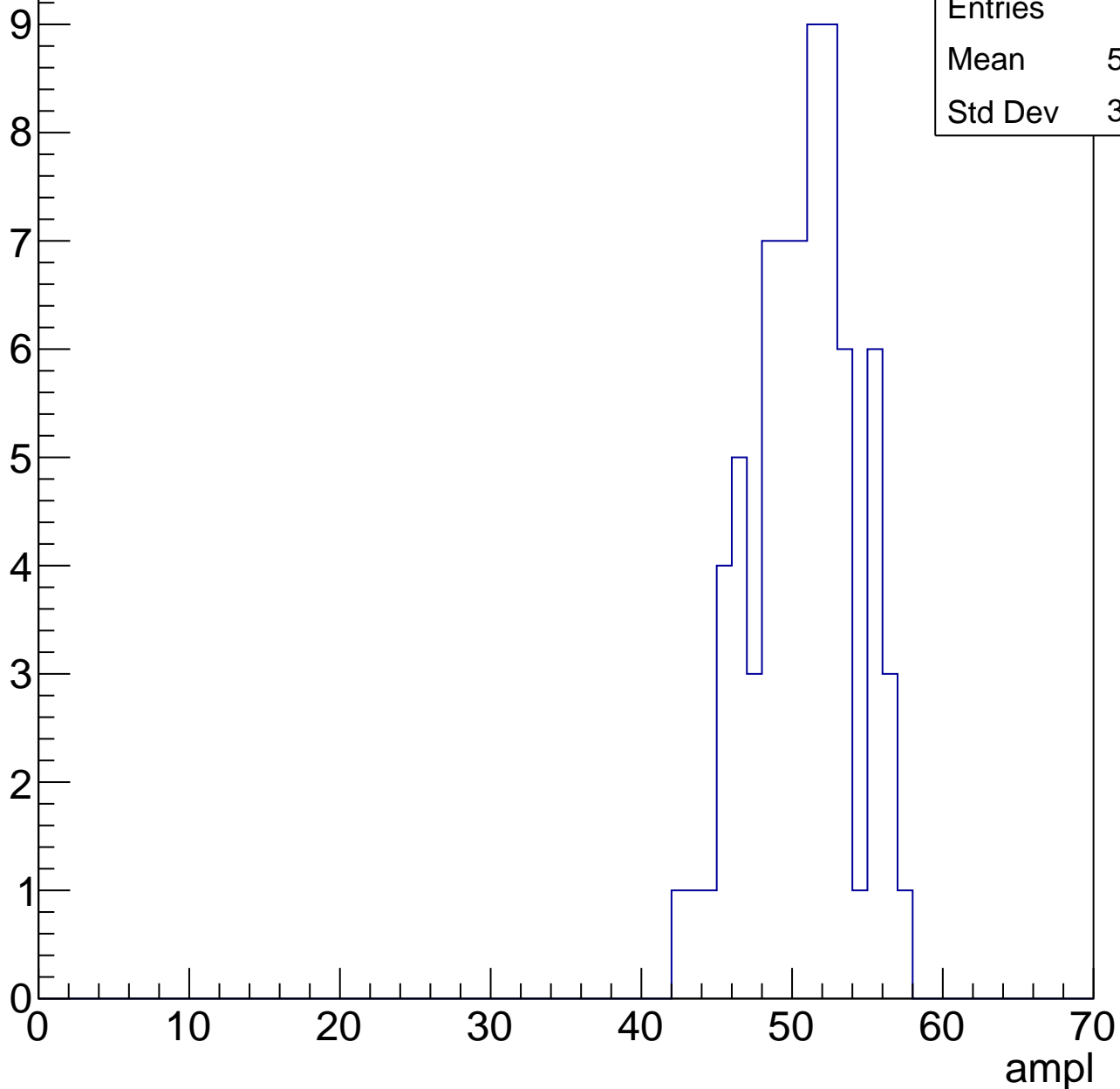


B1L103S, U10-ch56, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.18
Std Dev	3.379

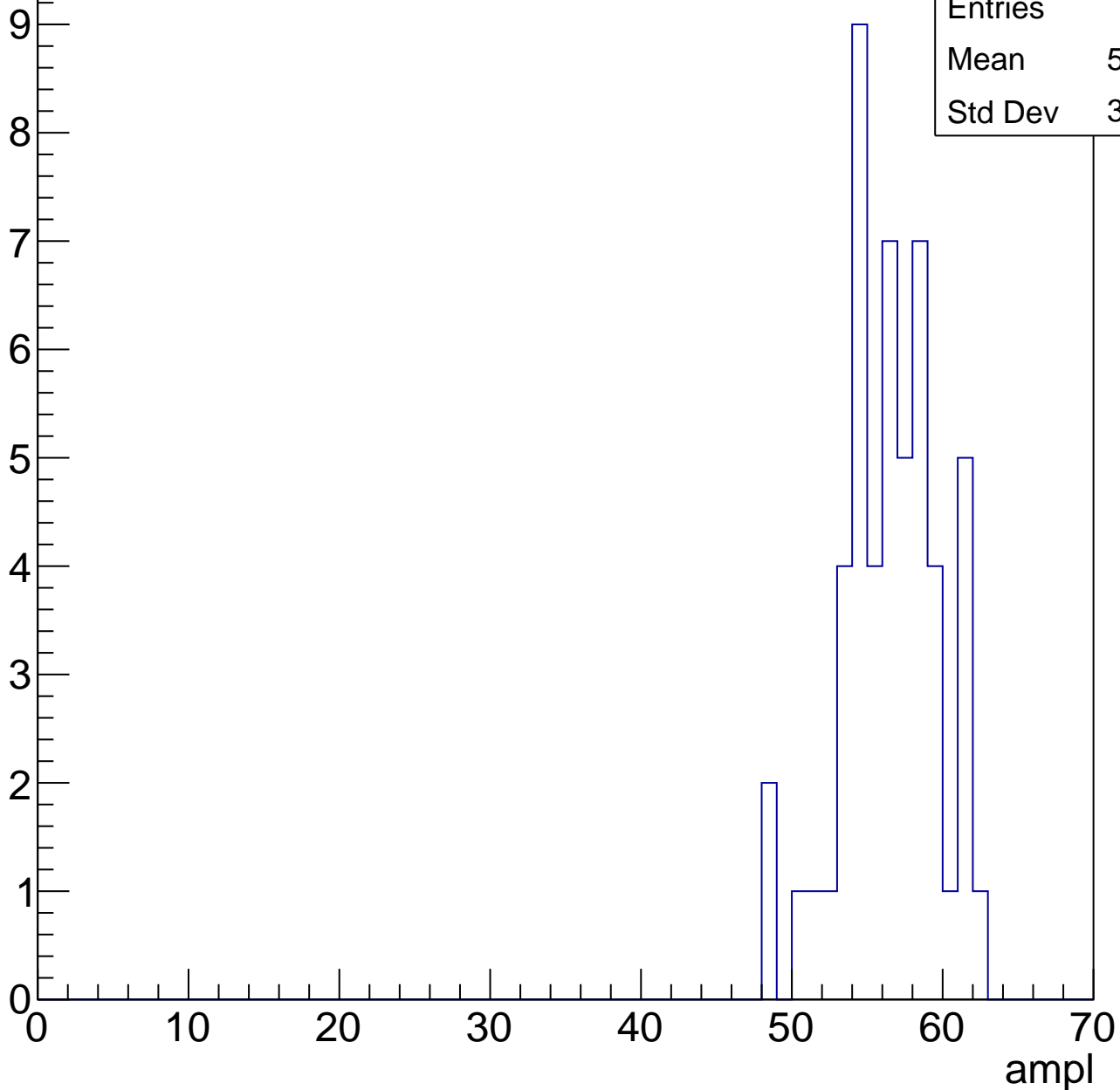


B1L103S, U10-ch56, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	56.02
Std Dev	3.183

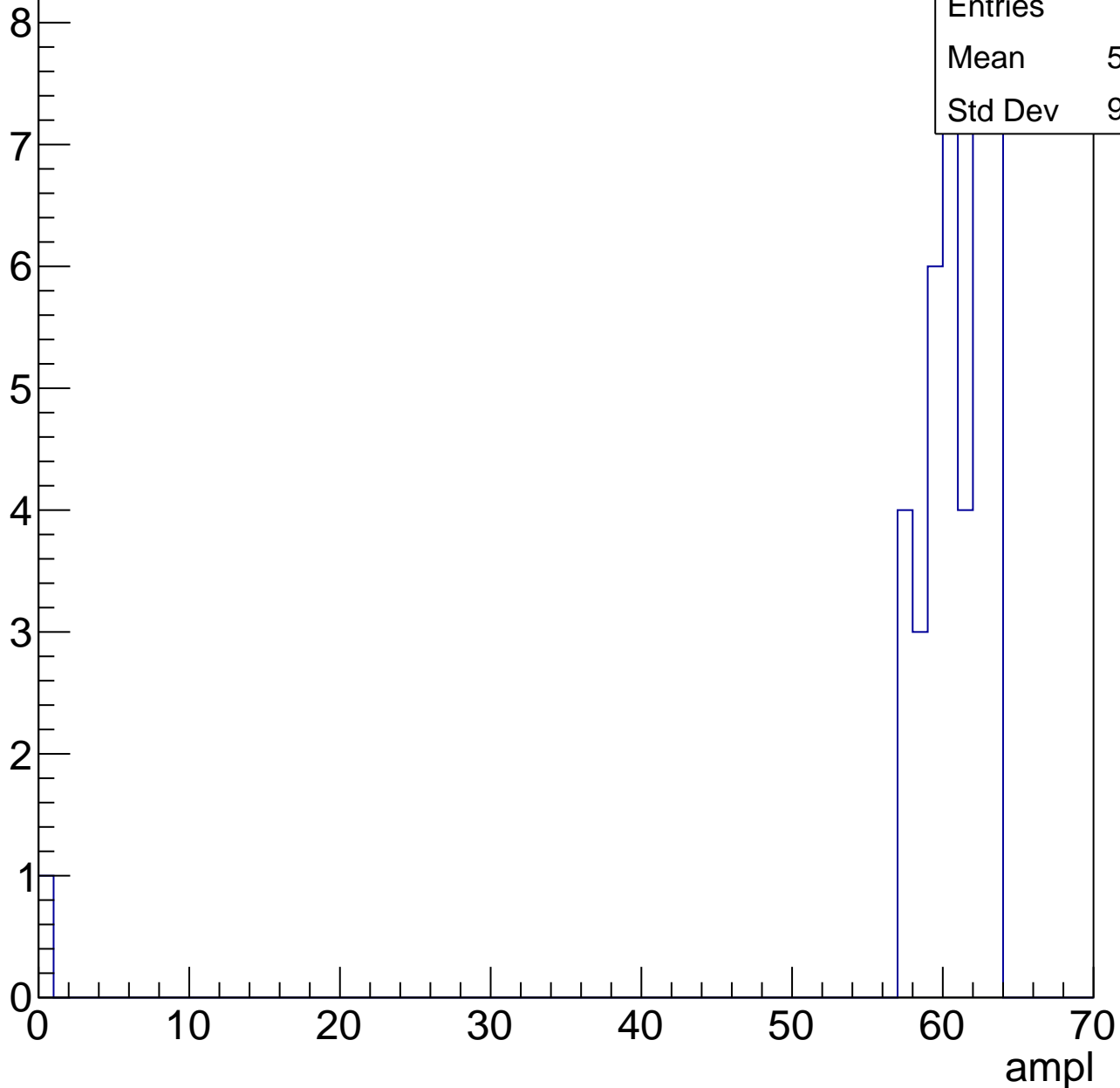


B1L103S, U10-ch56, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.05
Std Dev	9.416



B1L103S, U10-ch56, adc6

calib_packv5_042523_0143.root, FC#7, port C2

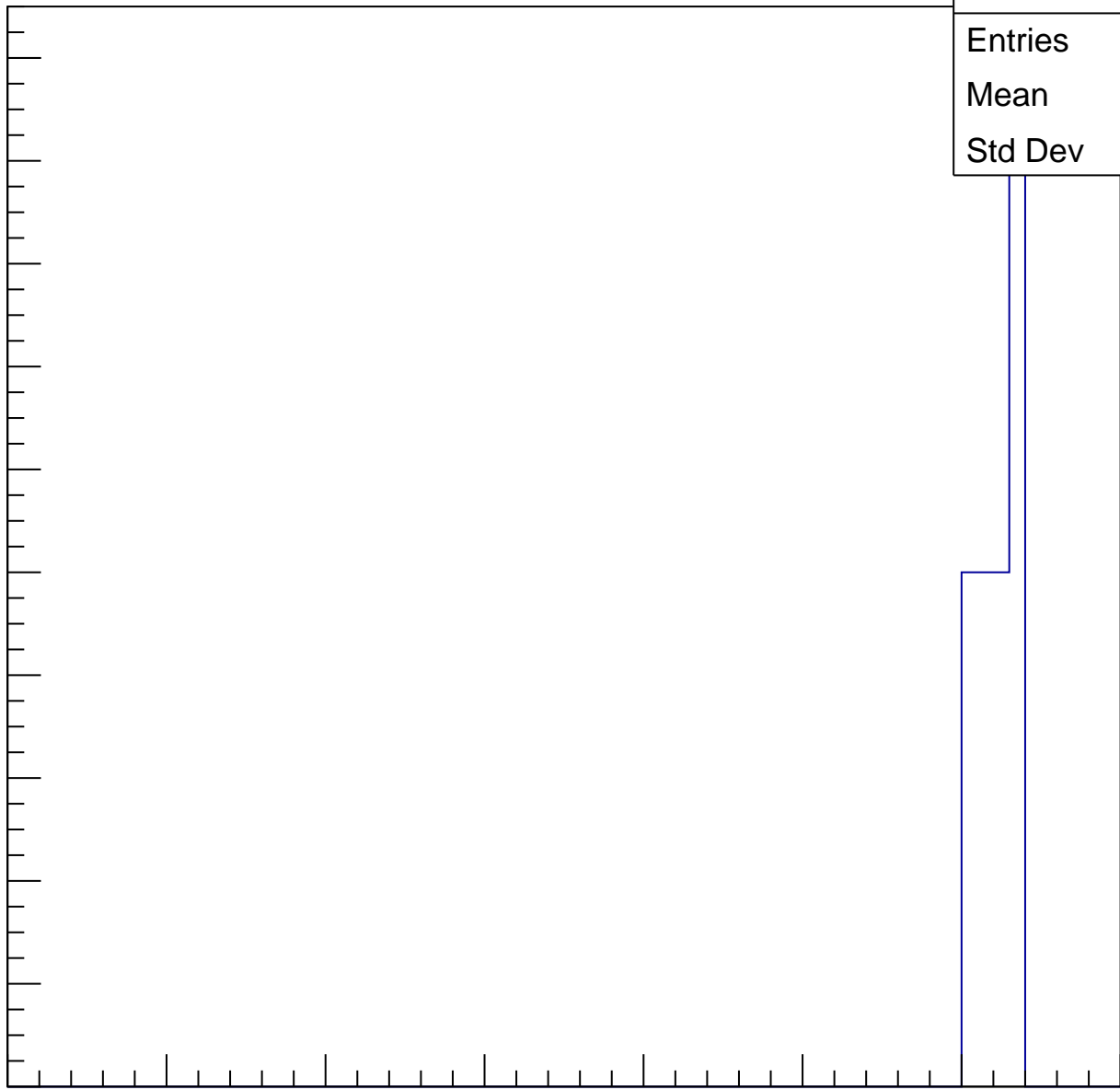
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch56, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch57, adc0

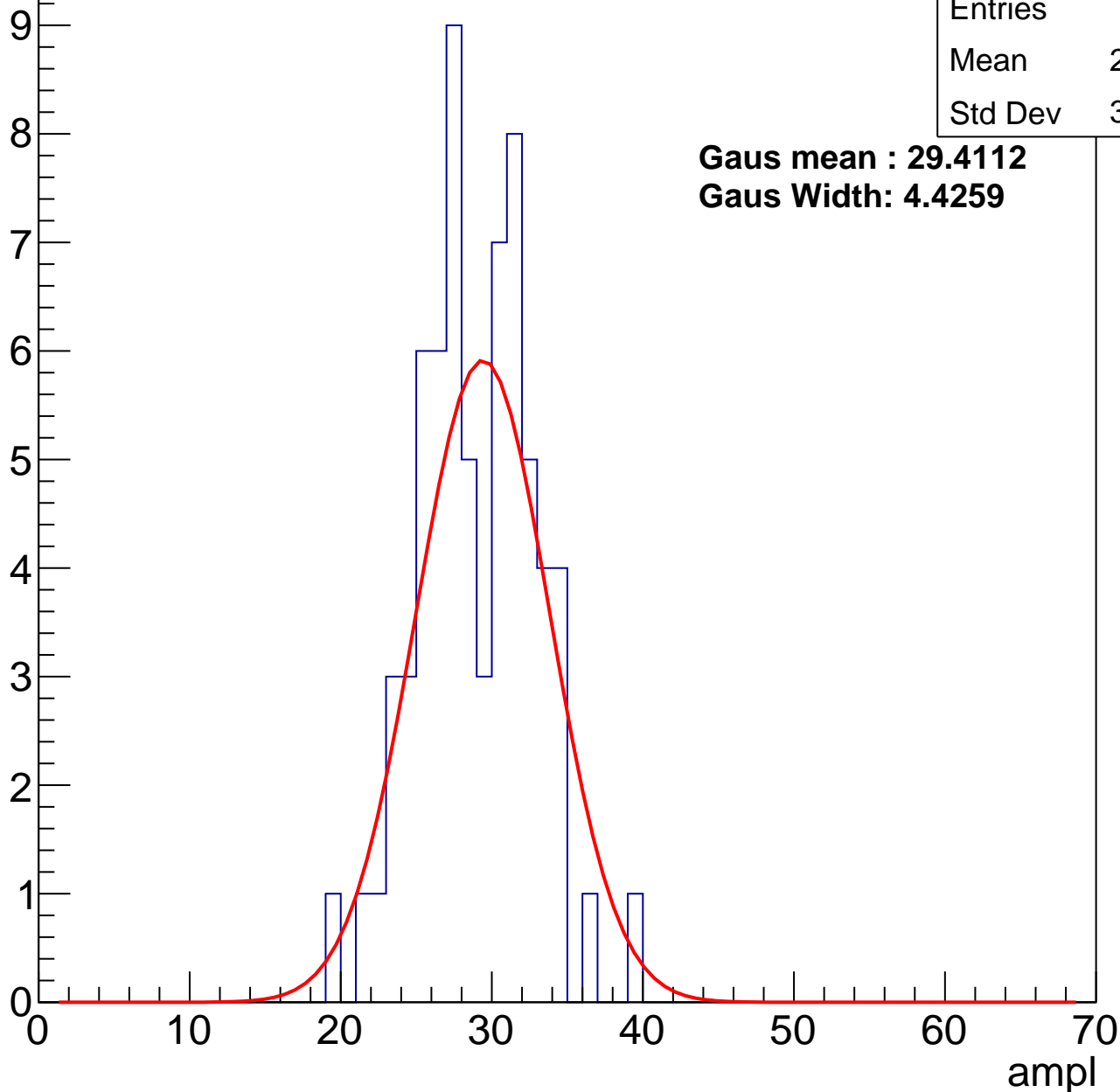
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.53
Std Dev	3.775

Gaus mean : 29.4112

Gaus Width: 4.4259



B1L103S, U10-ch57, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	35.71
Std Dev	3.68

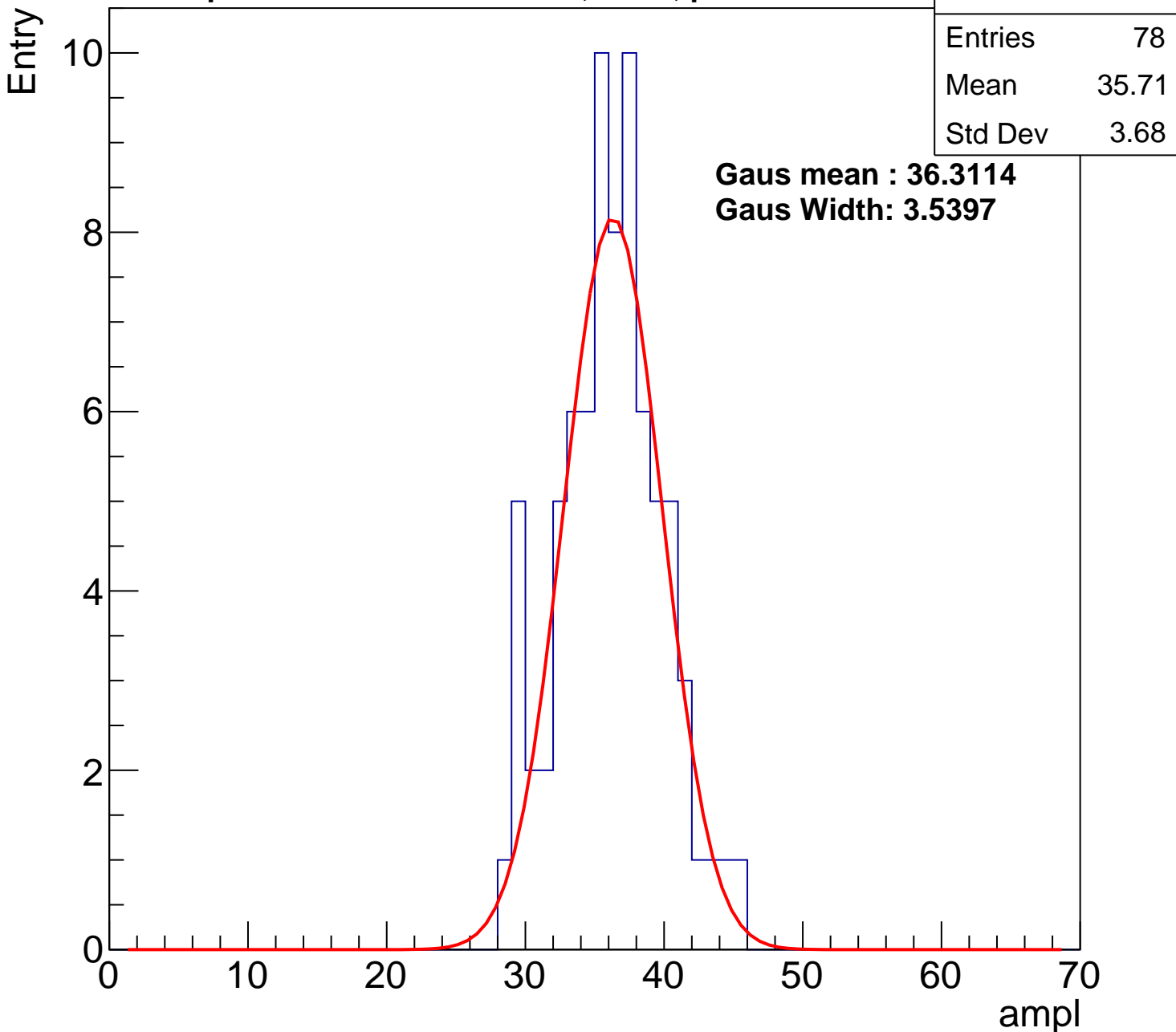
Gaus mean : 36.3114

Gaus Width: 3.5397

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch57, adc2

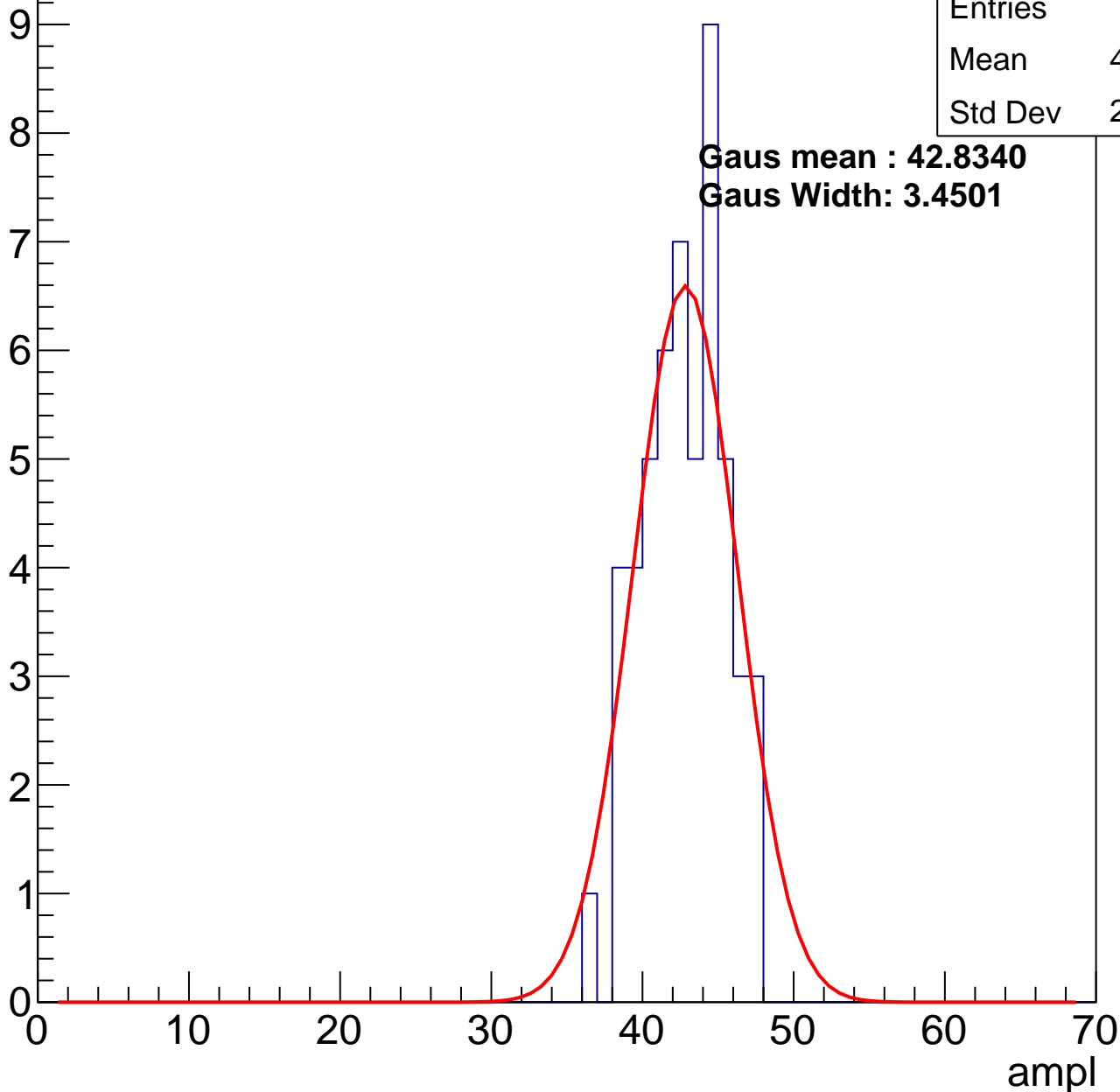
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	42.29
Std Dev	2.655

Gaus mean : 42.8340

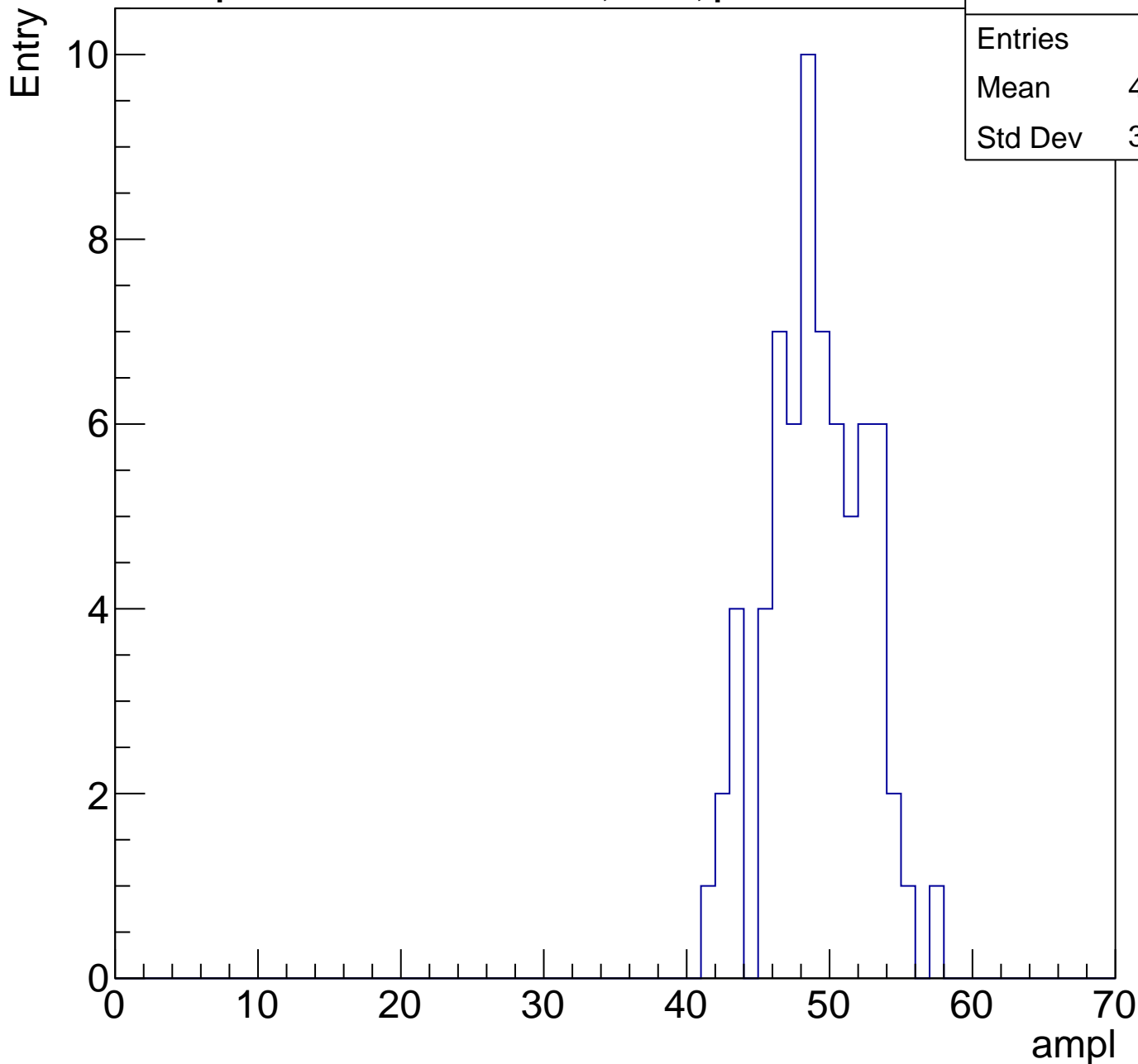
Gaus Width: 3.4501



B1L103S, U10-ch57, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	68
Mean	48.66
Std Dev	3.398

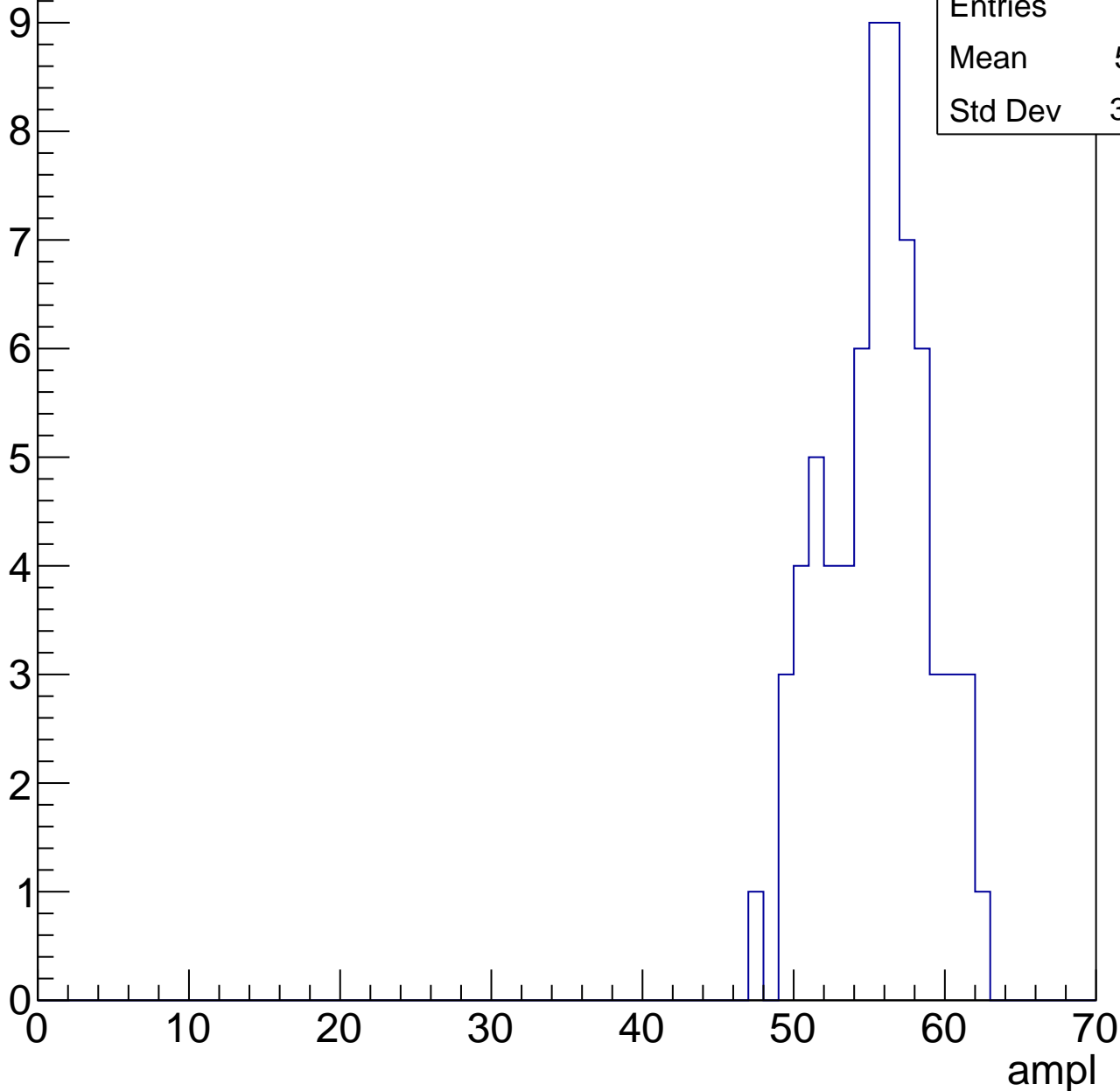


B1L103S, U10-ch57, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.01
Std Dev	3.389

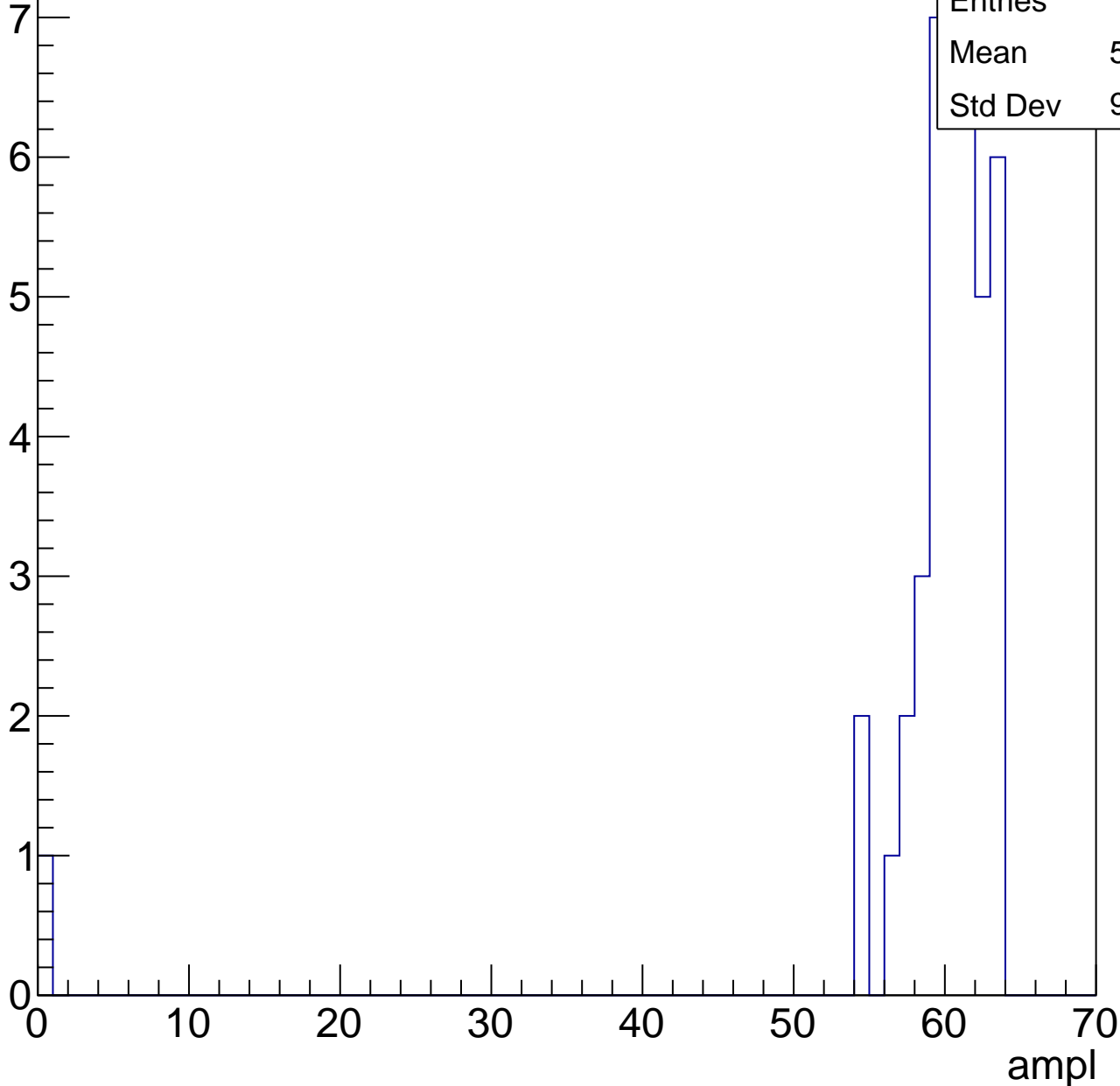


B1L103S, U10-ch57, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

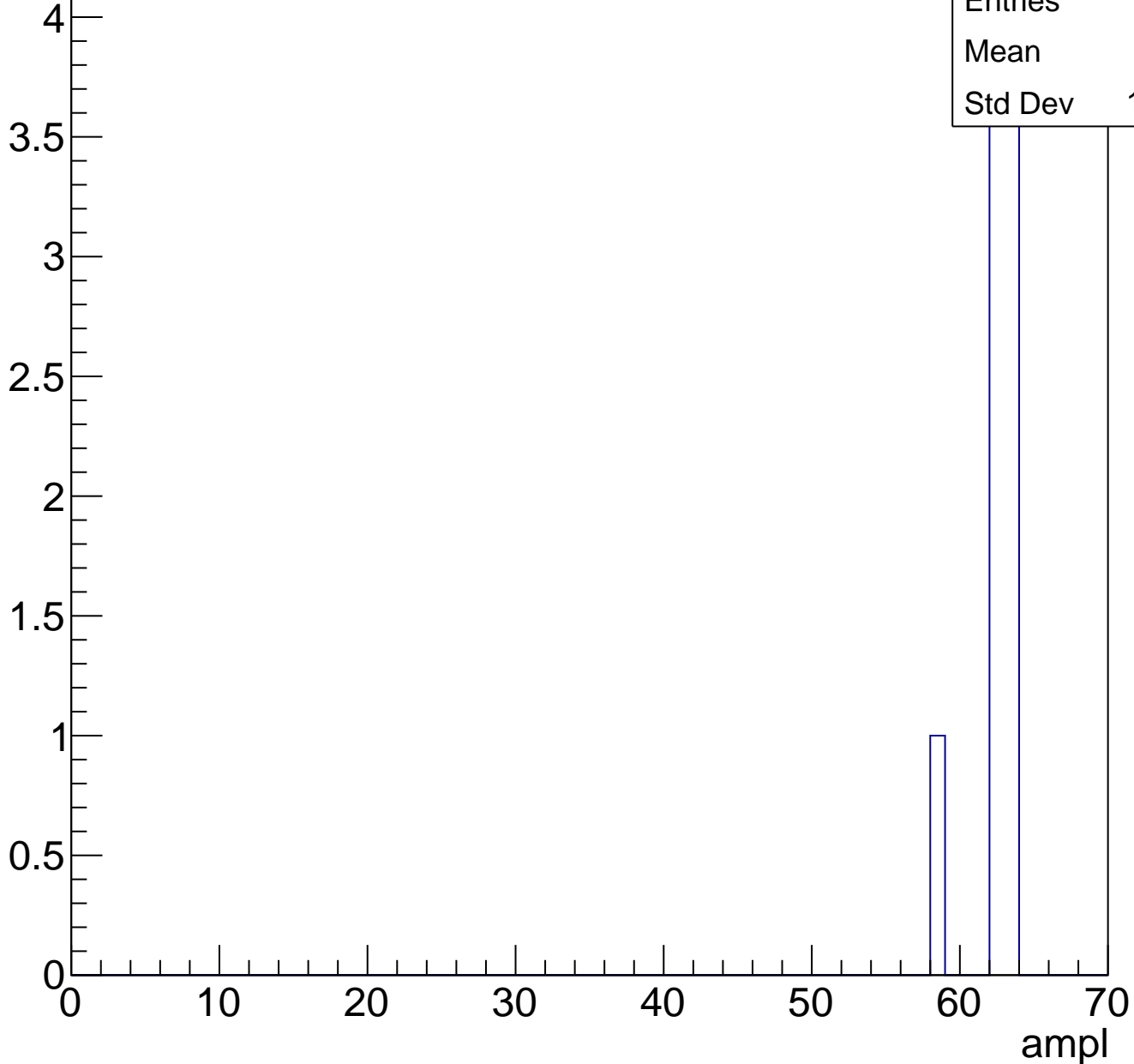
Entries	41
Mean	58.54
Std Dev	9.523



B1L103S, U10-ch57, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch57, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch58, adc0

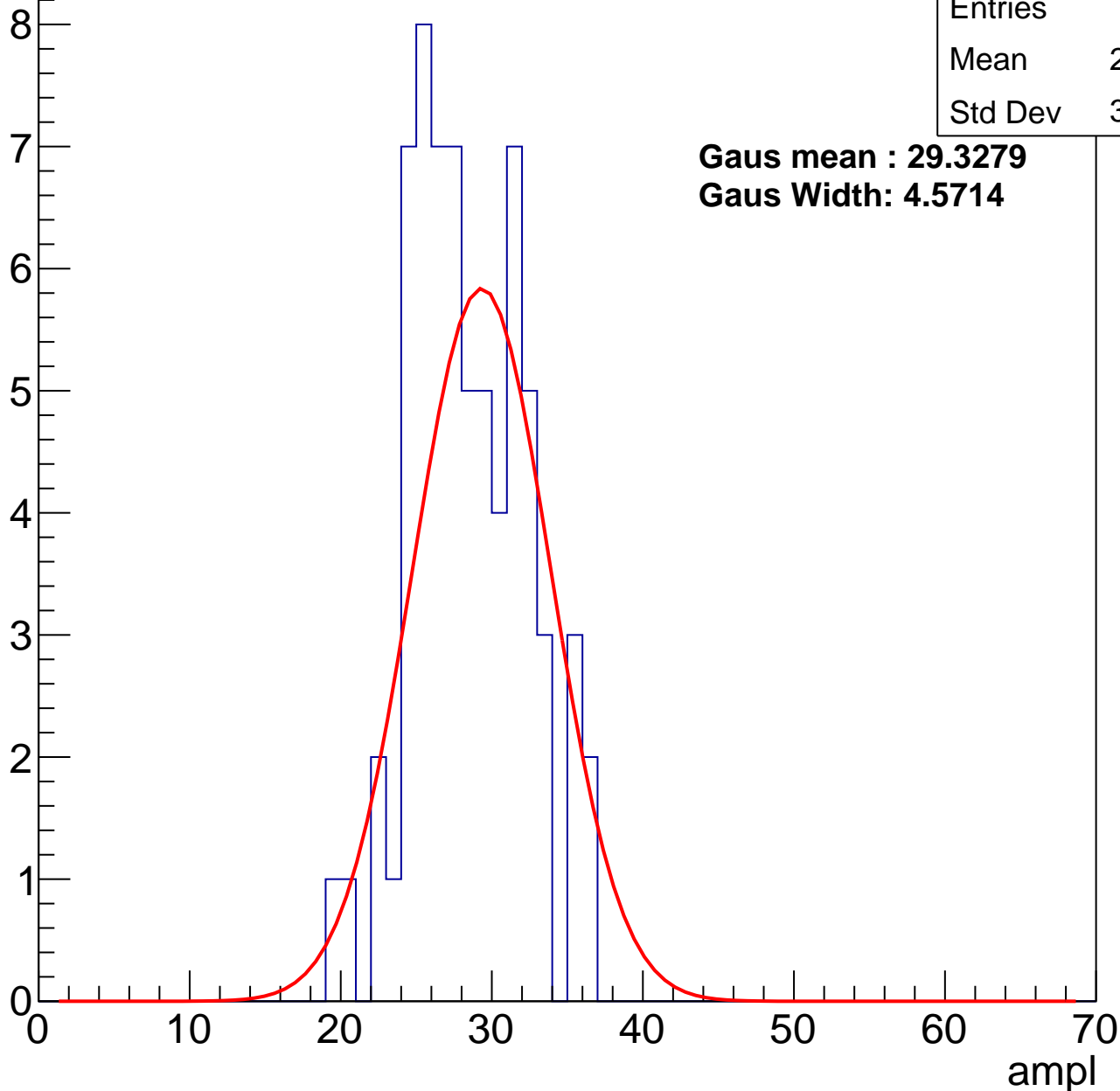
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	27.99
Std Dev	3.787

Gaus mean : 29.3279

Gaus Width: 4.5714



B1L103S, U10-ch58, adc1

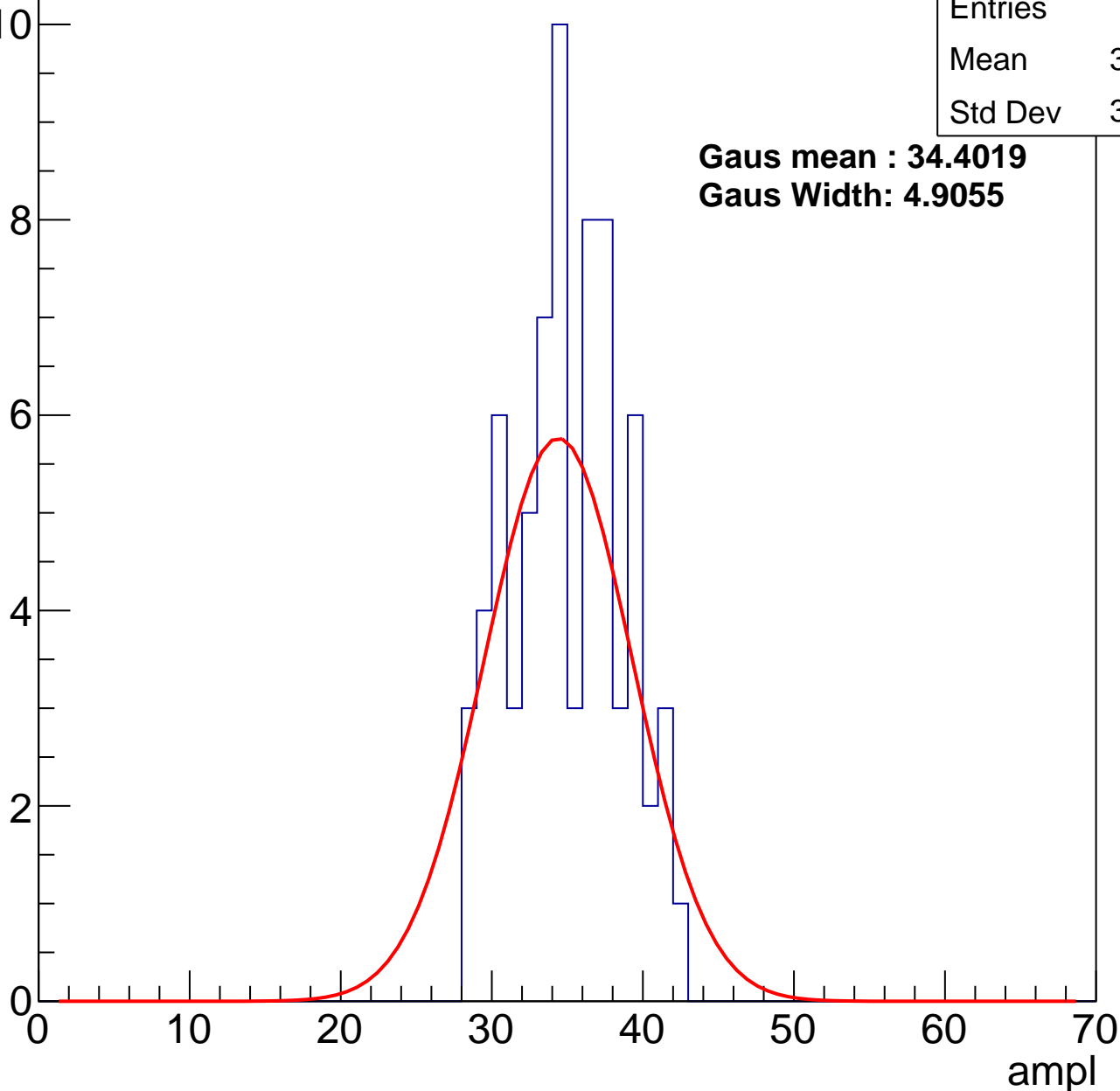
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	34.53
Std Dev	3.586

Gaus mean : 34.4019

Gaus Width: 4.9055



B1L103S, U10-ch58, adc2

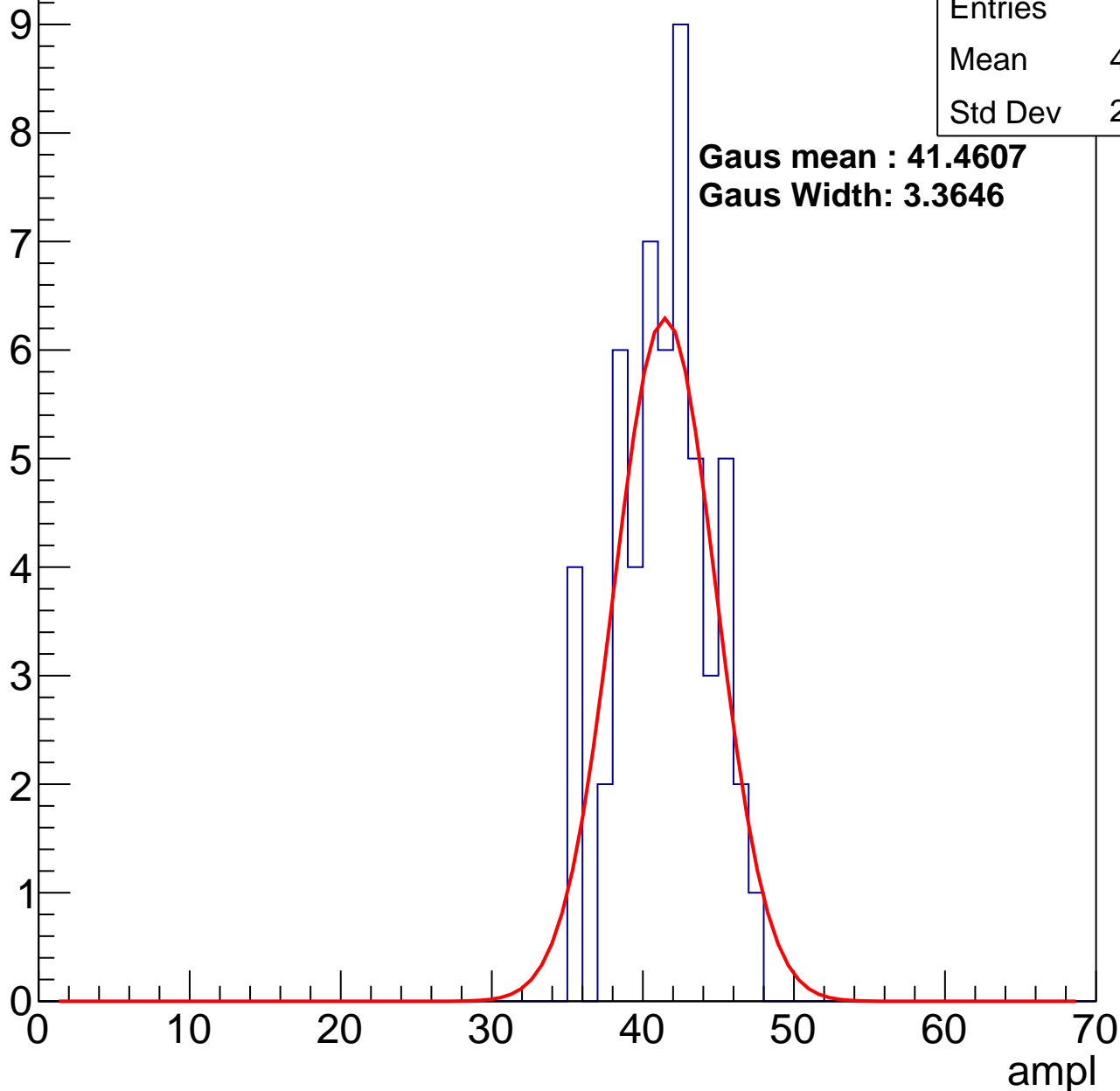
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	40.98
Std Dev	2.966

Gaus mean : 41.4607

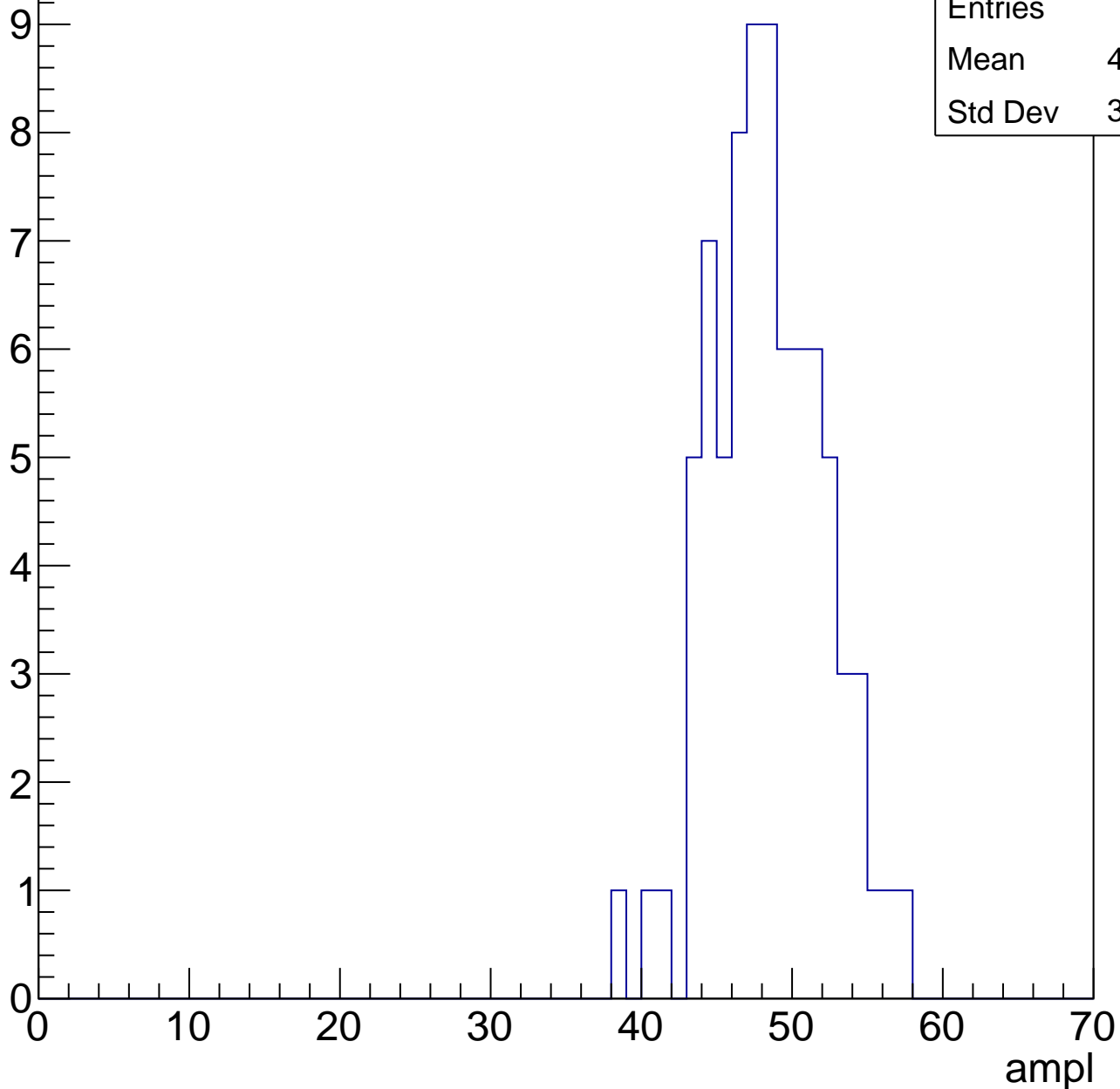
Gaus Width: 3.3646



B1L103S, U10-ch58, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

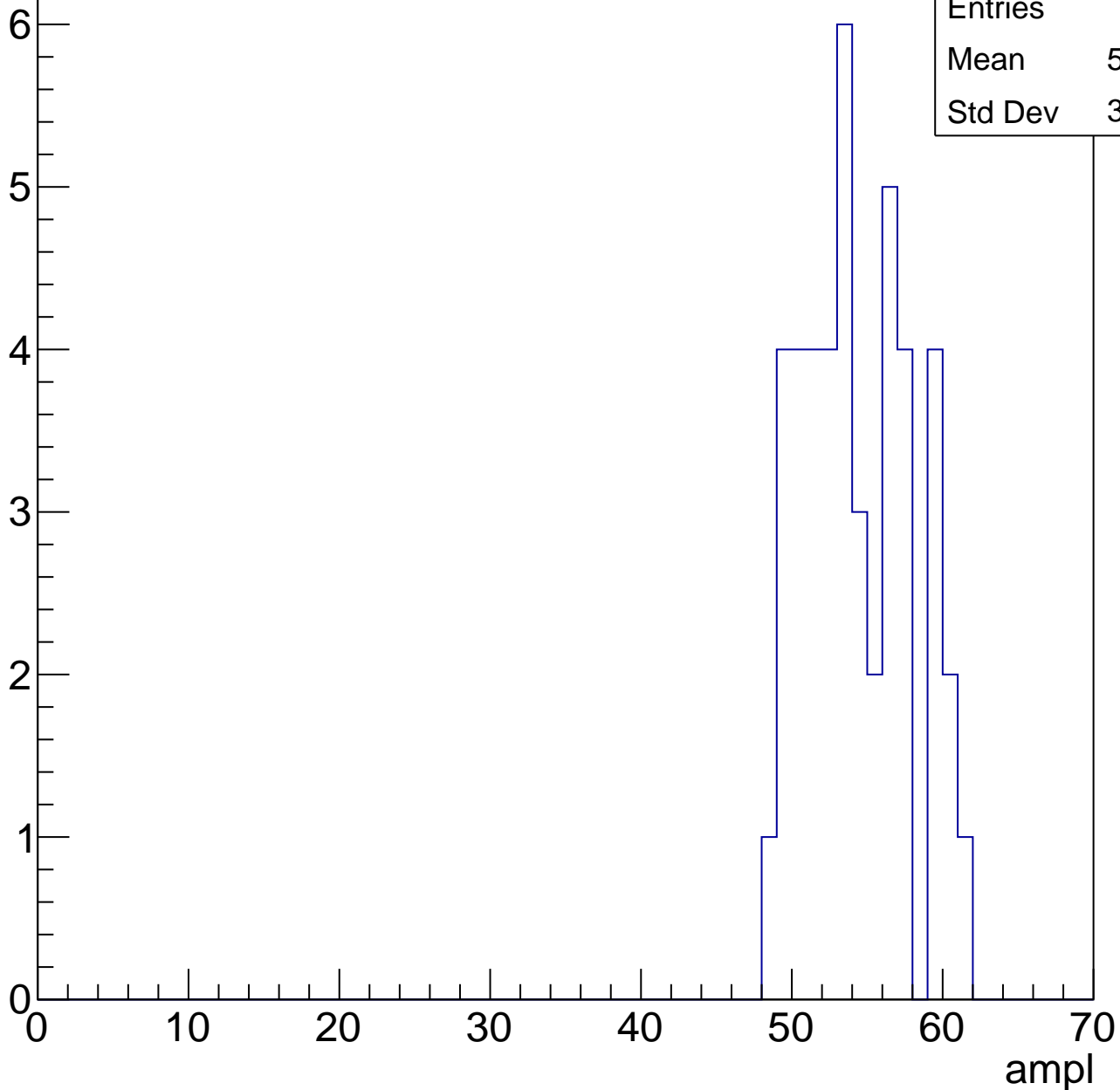


B1L103S, U10-ch58, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	53.89
Std Dev	3.492



B1L103S, U10-ch58, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	67
Mean	58.6
Std Dev	2.818

Entry

10

8

6

4

2

0

0

10

20

30

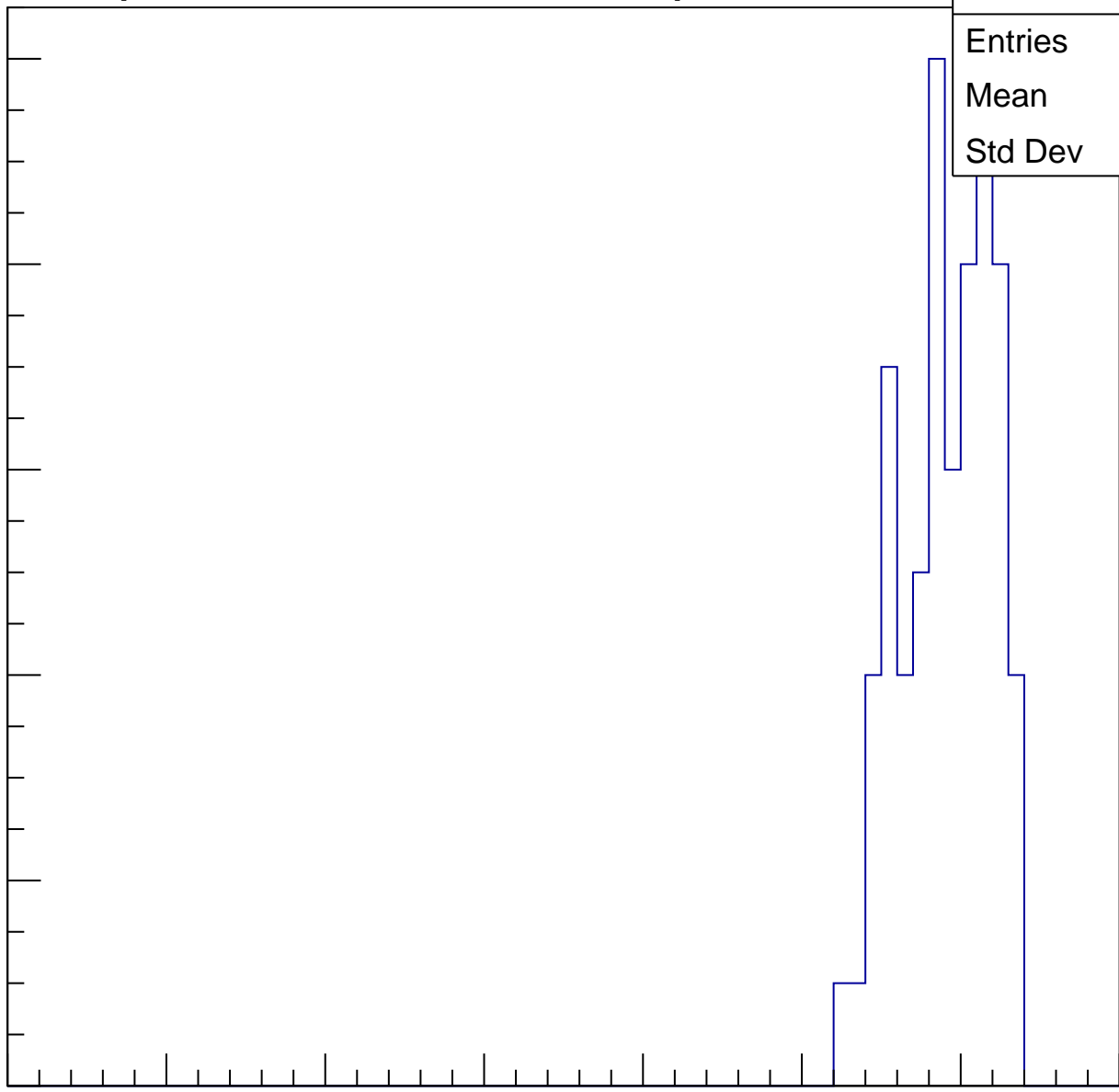
40

50

60

70

ampl

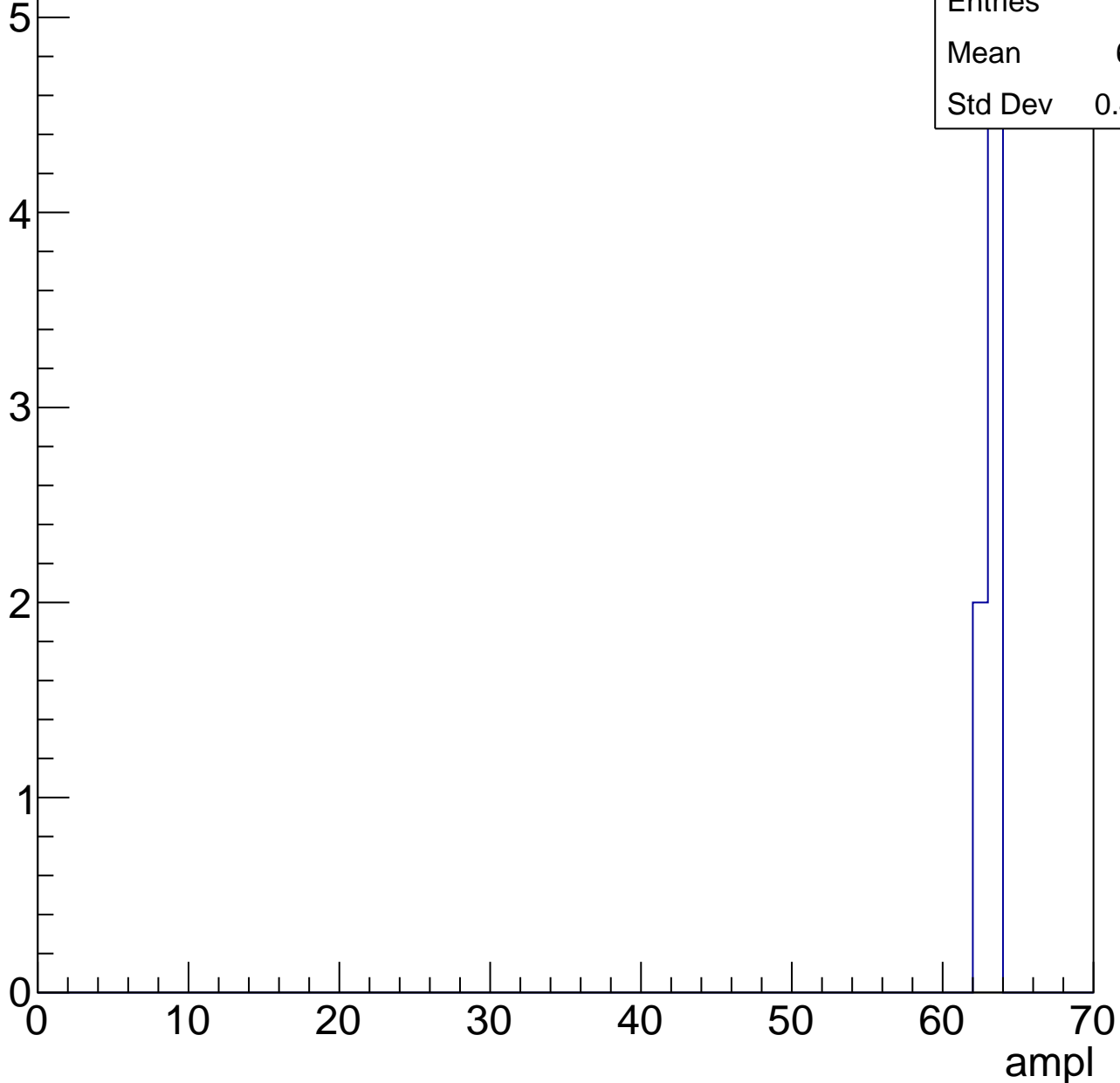


B1L103S, U10-ch58, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	7
Mean	62.71
Std Dev	0.4518



B1L103S, U10-ch58, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch59, adc0

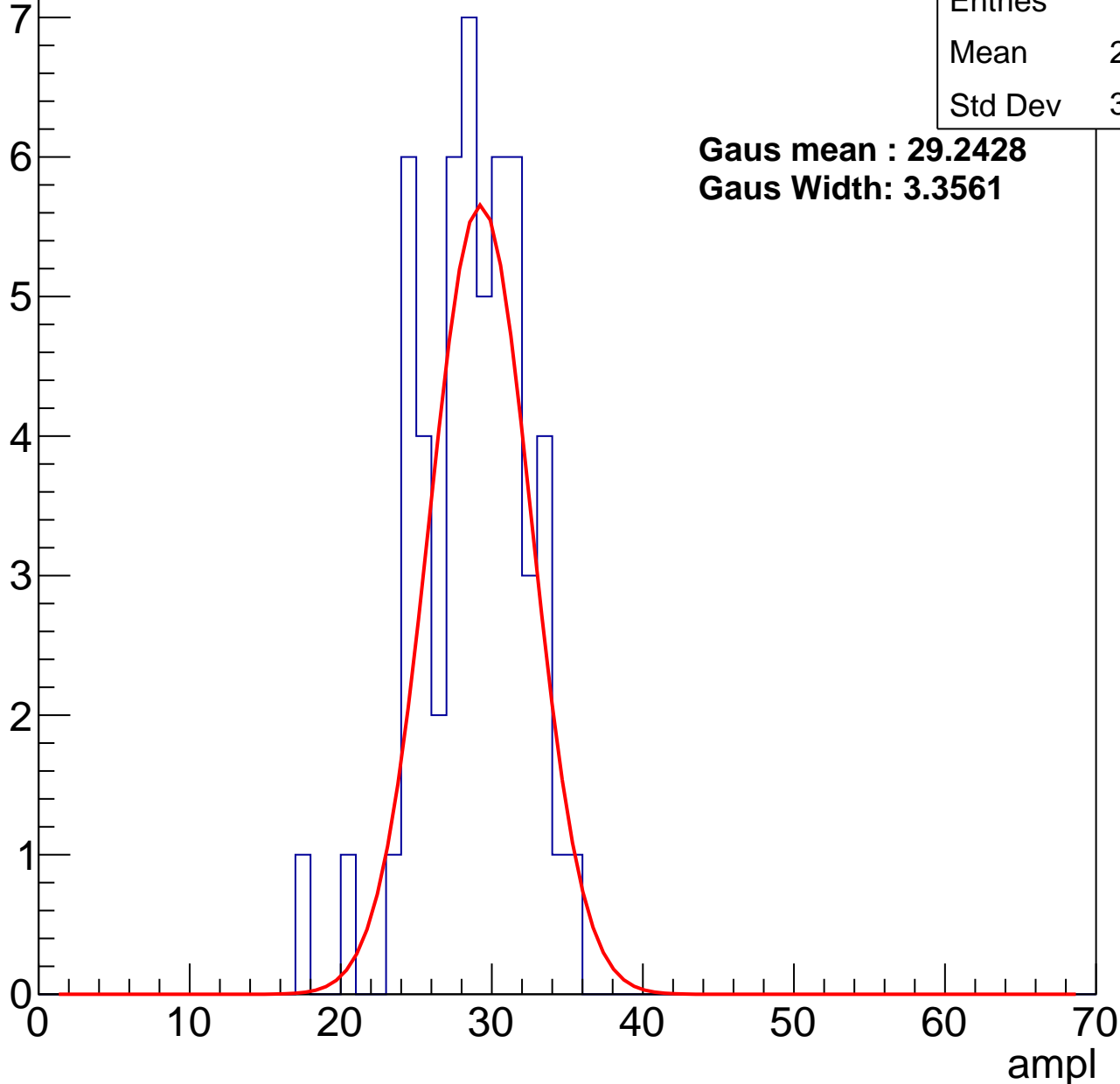
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	28.19
Std Dev	3.523

Gaus mean : 29.2428

Gaus Width: 3.3561



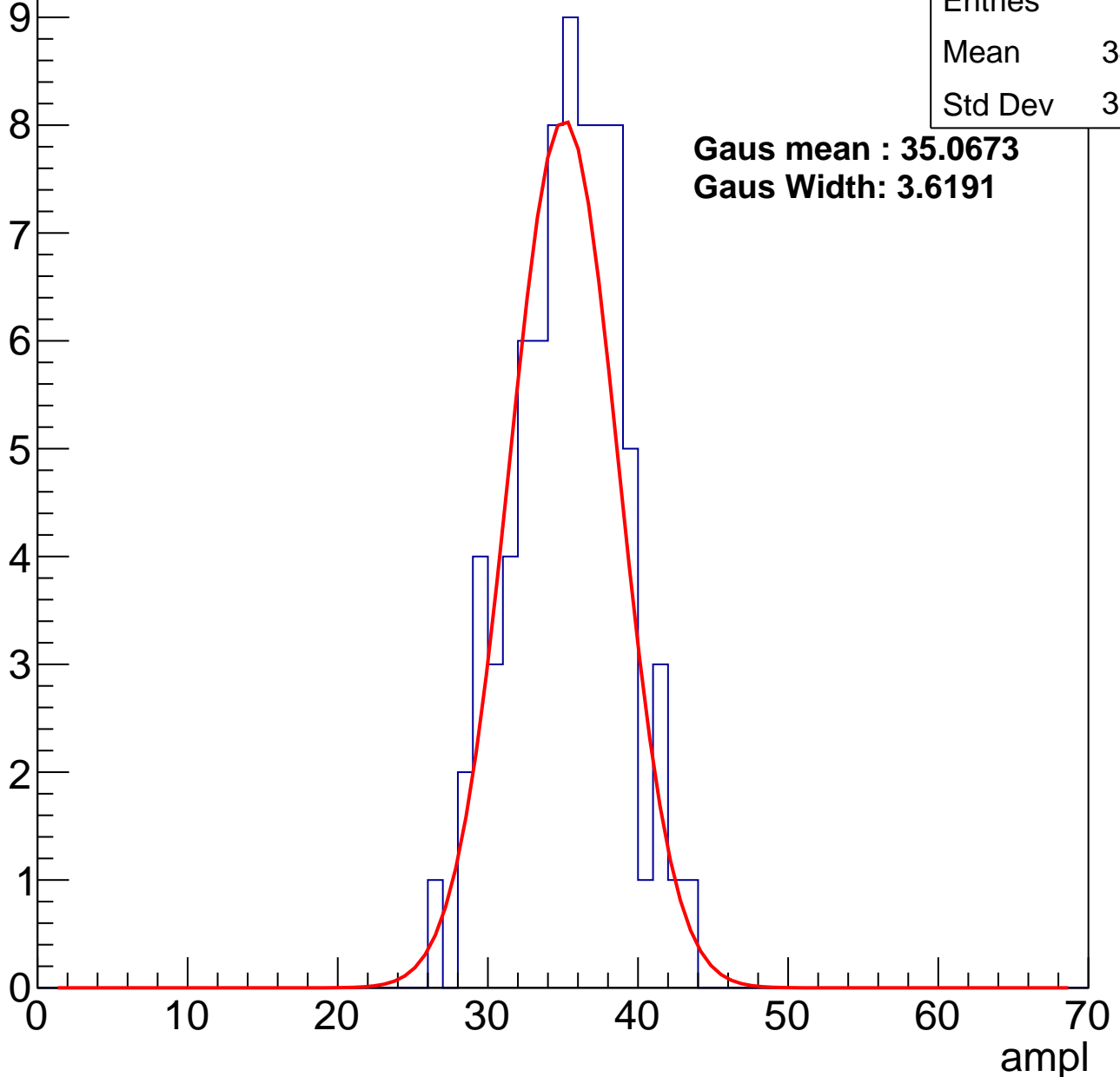
B1L103S, U10-ch59, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	34.87
Std Dev	3.553

Gaus mean : 35.0673
Gaus Width: 3.6191



B1L103S, U10-ch59, adc2

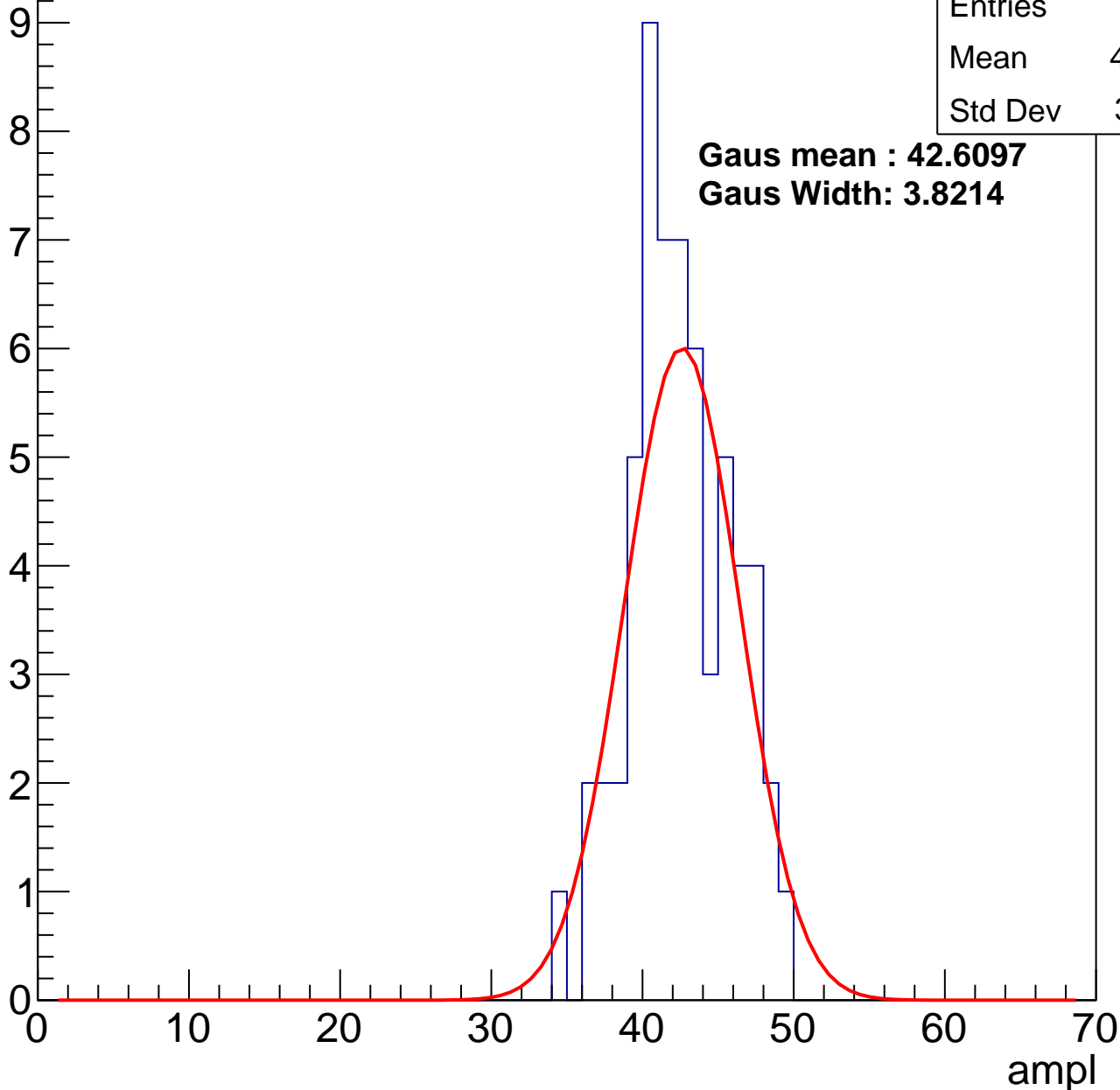
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	42.07
Std Dev	3.301

Gaus mean : 42.6097

Gaus Width: 3.8214

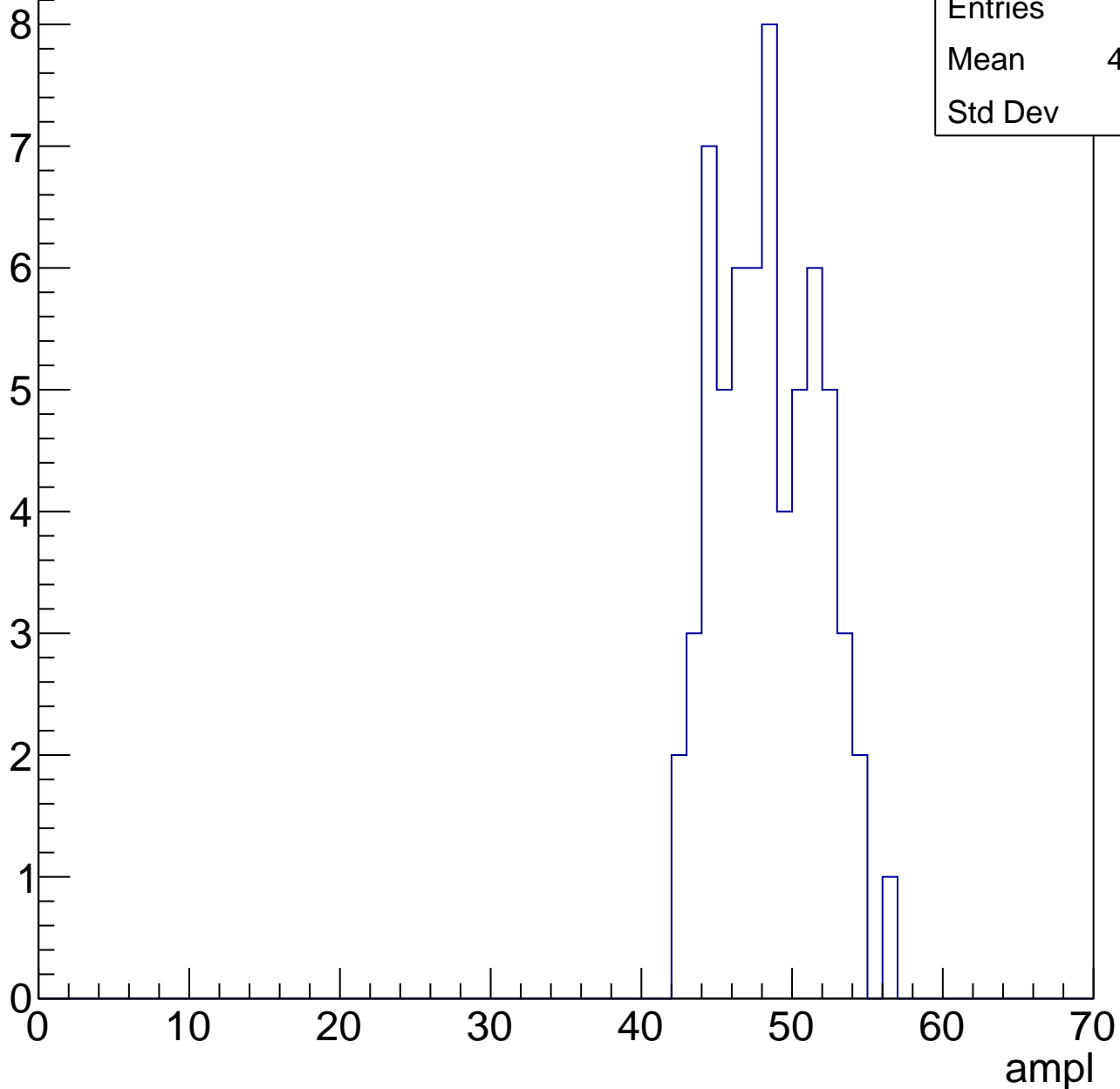


B1L103S, U10-ch59, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	47.98
Std Dev	3.34

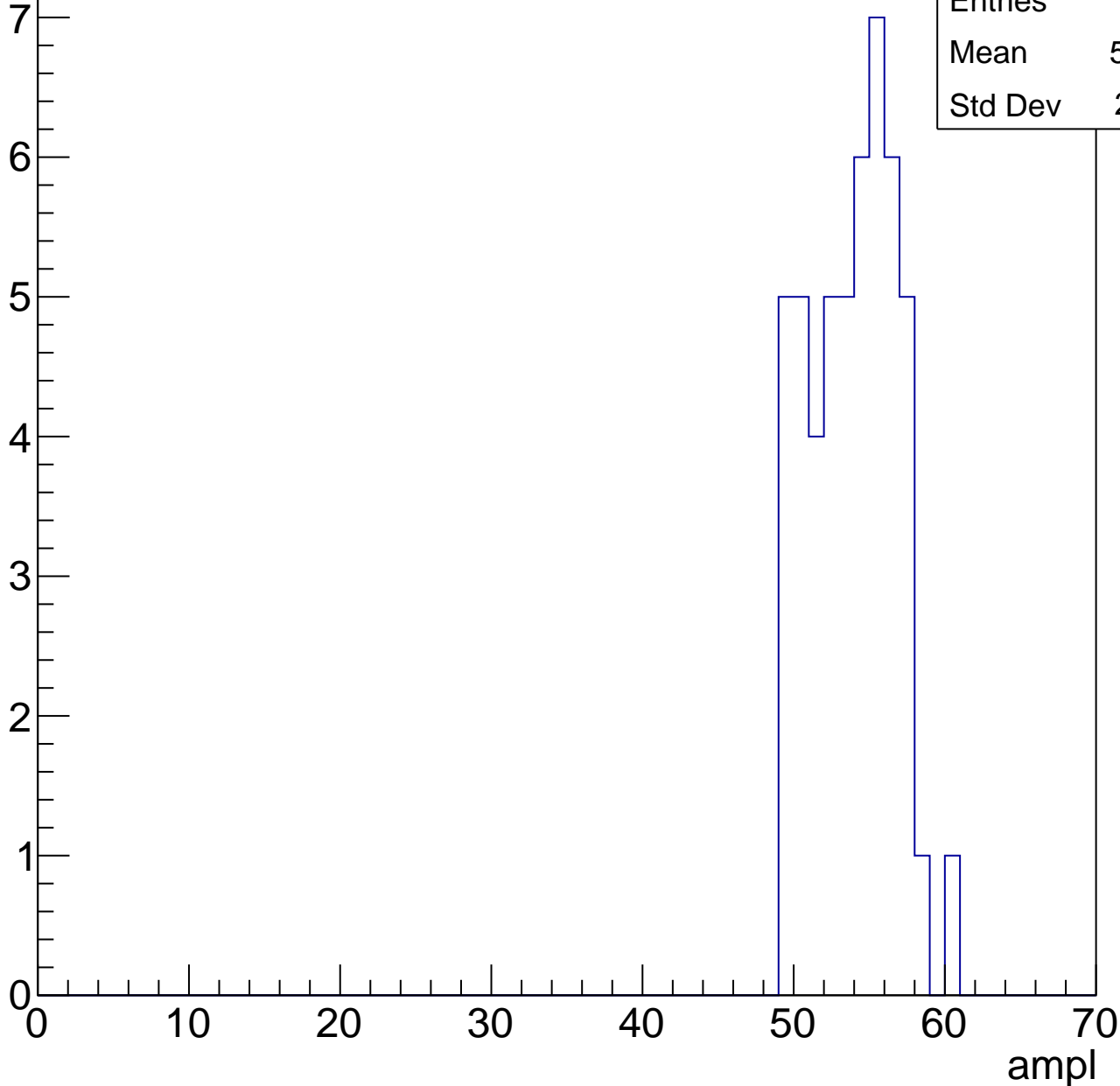


B1L103S, U10-ch59, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.44
Std Dev	2.751



B1L103S, U10-ch59, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

ampl

Entries	61
Mean	58.98
Std Dev	2.538

10

8

6

4

2

0

0

10

20

30

40

50

60

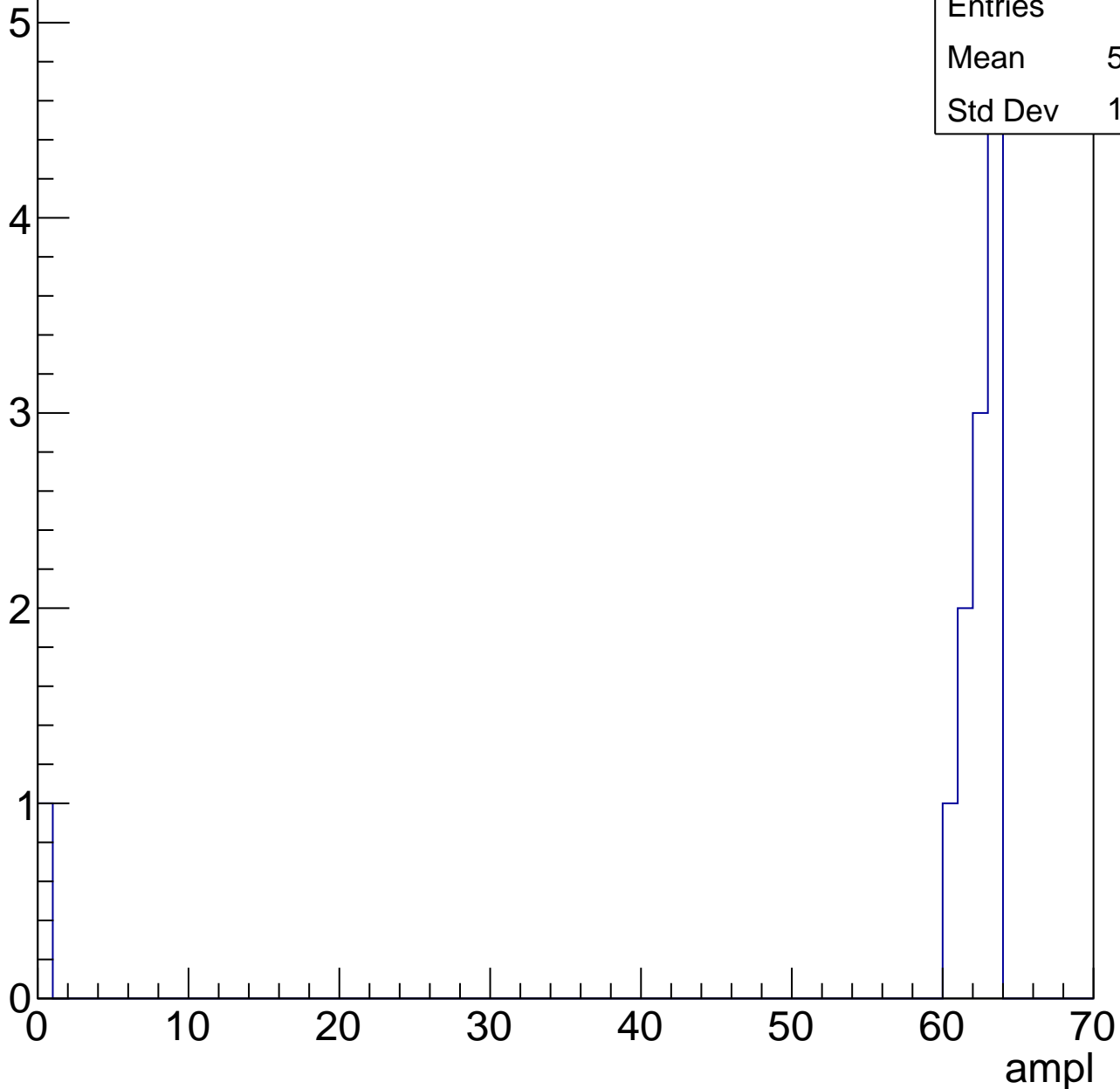
ampl

B1L103S, U10-ch59, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	56.92
Std Dev	17.19



B1L103S, U10-ch59, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	63
Std Dev	0

B1L103S, U10-ch60, adc0

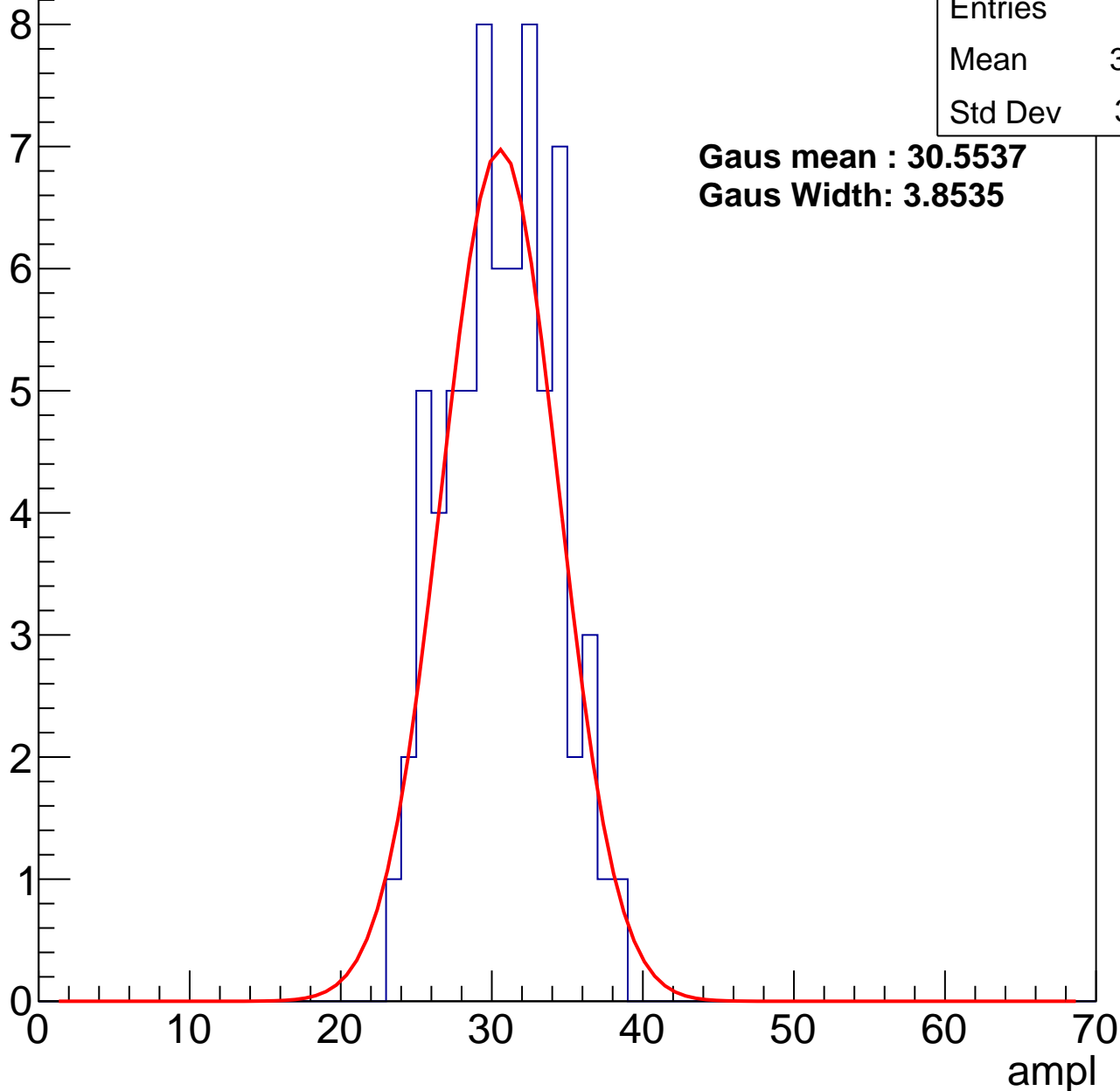
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	30.22
Std Dev	3.501

Gaus mean : 30.5537

Gaus Width: 3.8535



B1L103S, U10-ch60, adc1

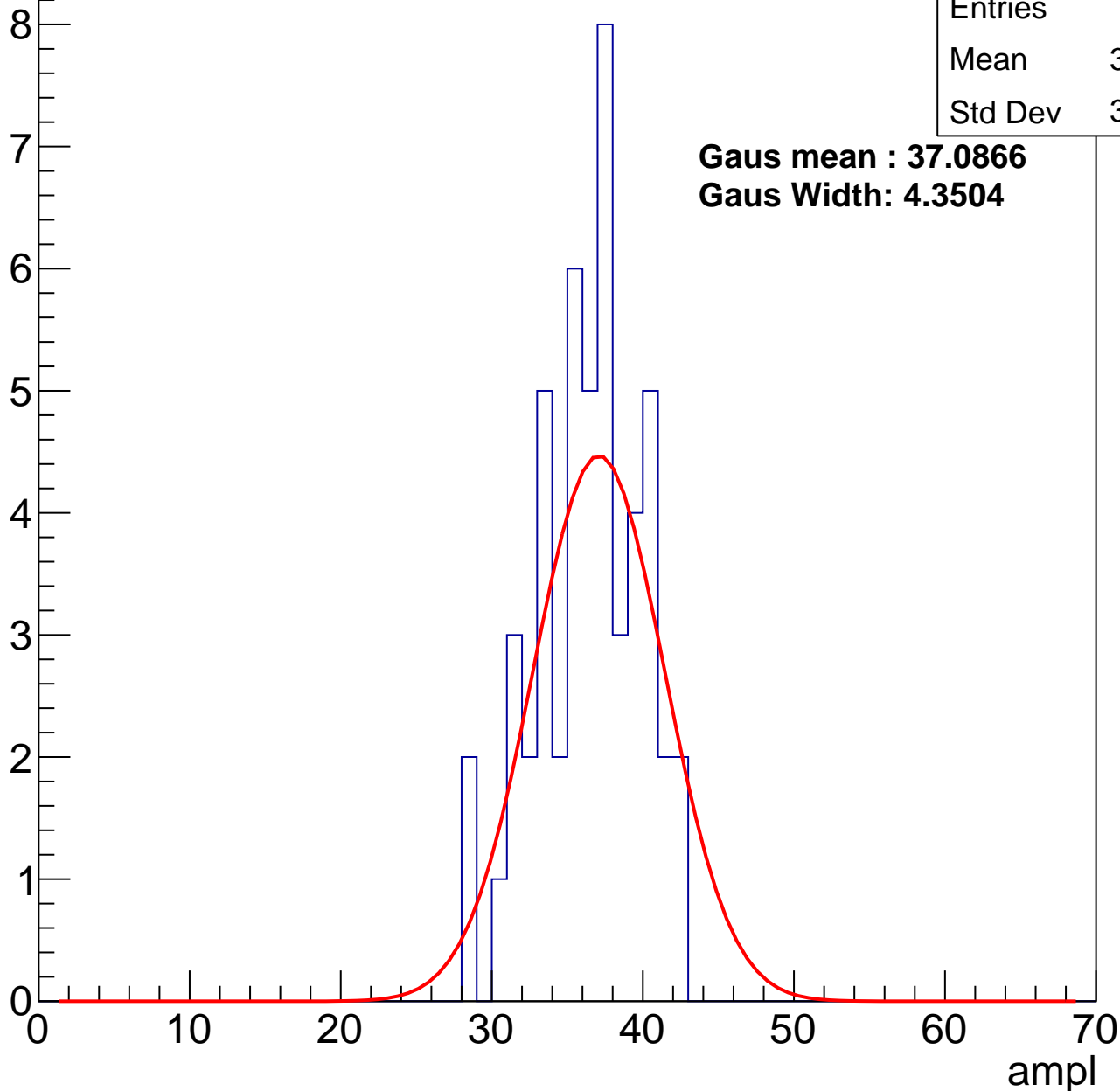
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	35.96
Std Dev	3.429

Gaus mean : 37.0866

Gaus Width: 4.3504



B1L103S, U10-ch60, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

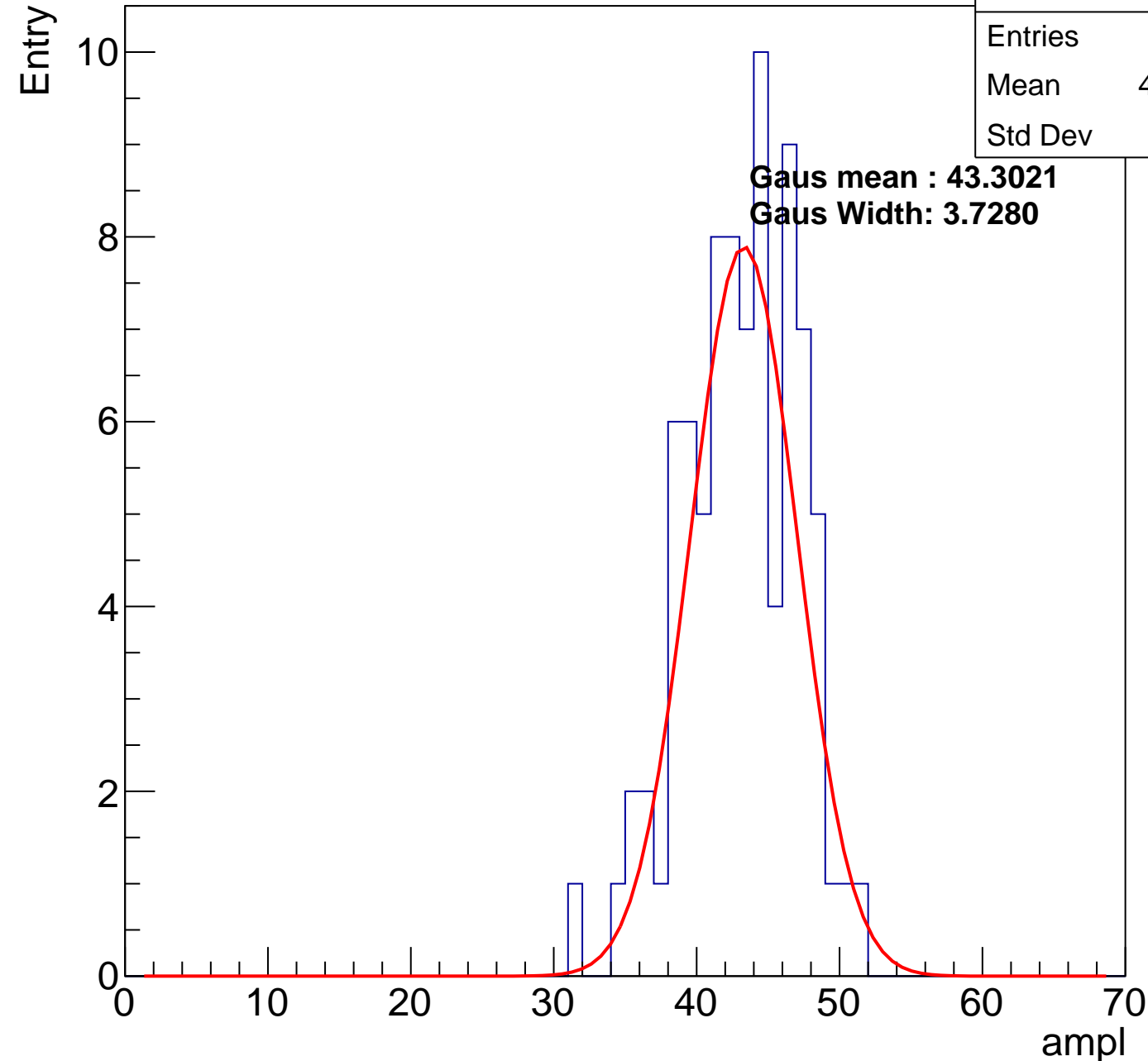
Entries	85
Mean	42.64
Std Dev	3.91

Gaus mean : 43.3021

Gaus Width: 3.7280

10
8
6
4
2
0

ampl

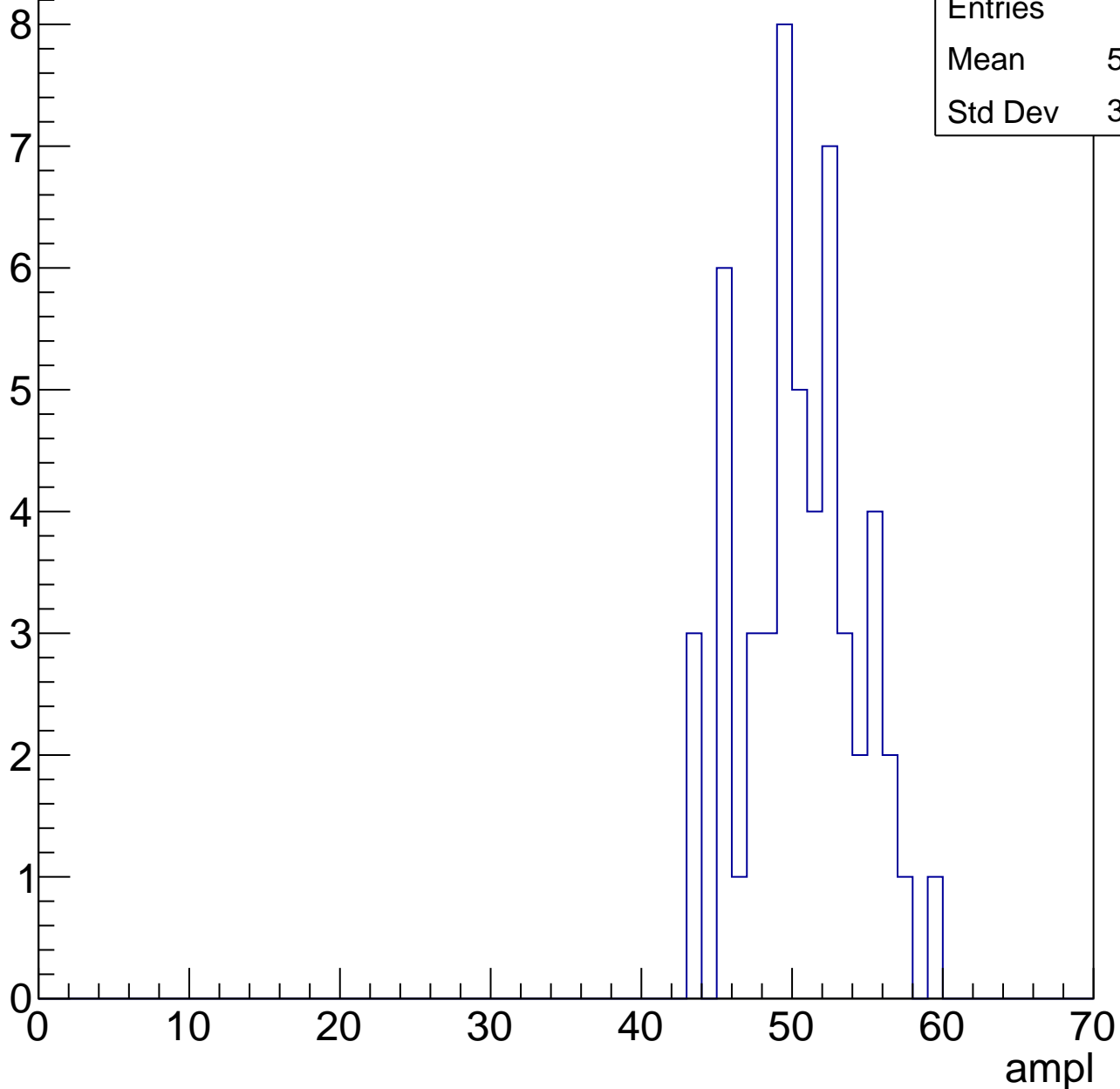


B1L103S, U10-ch60, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	50.09
Std Dev	3.768

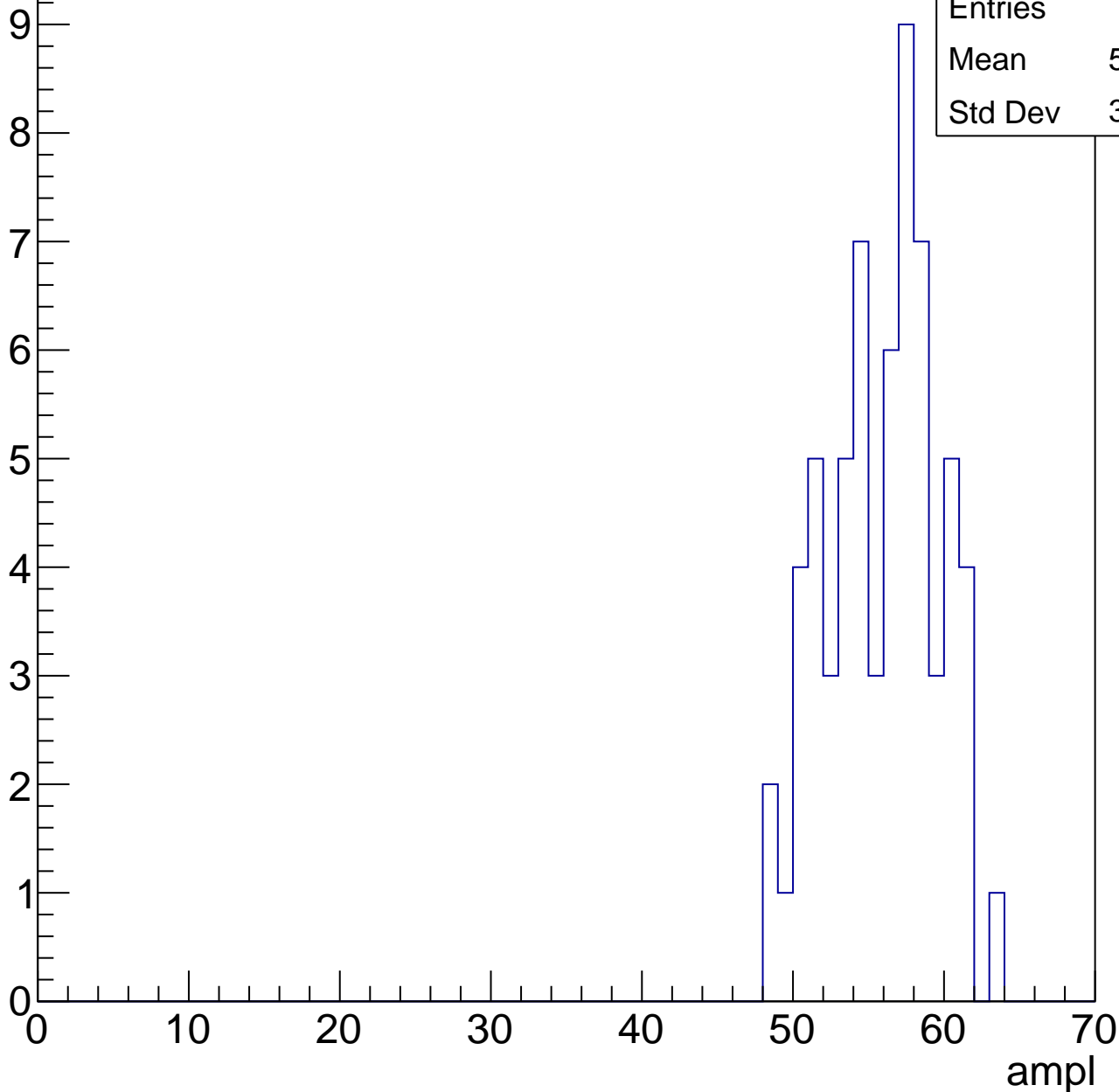


B1L103S, U10-ch60, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

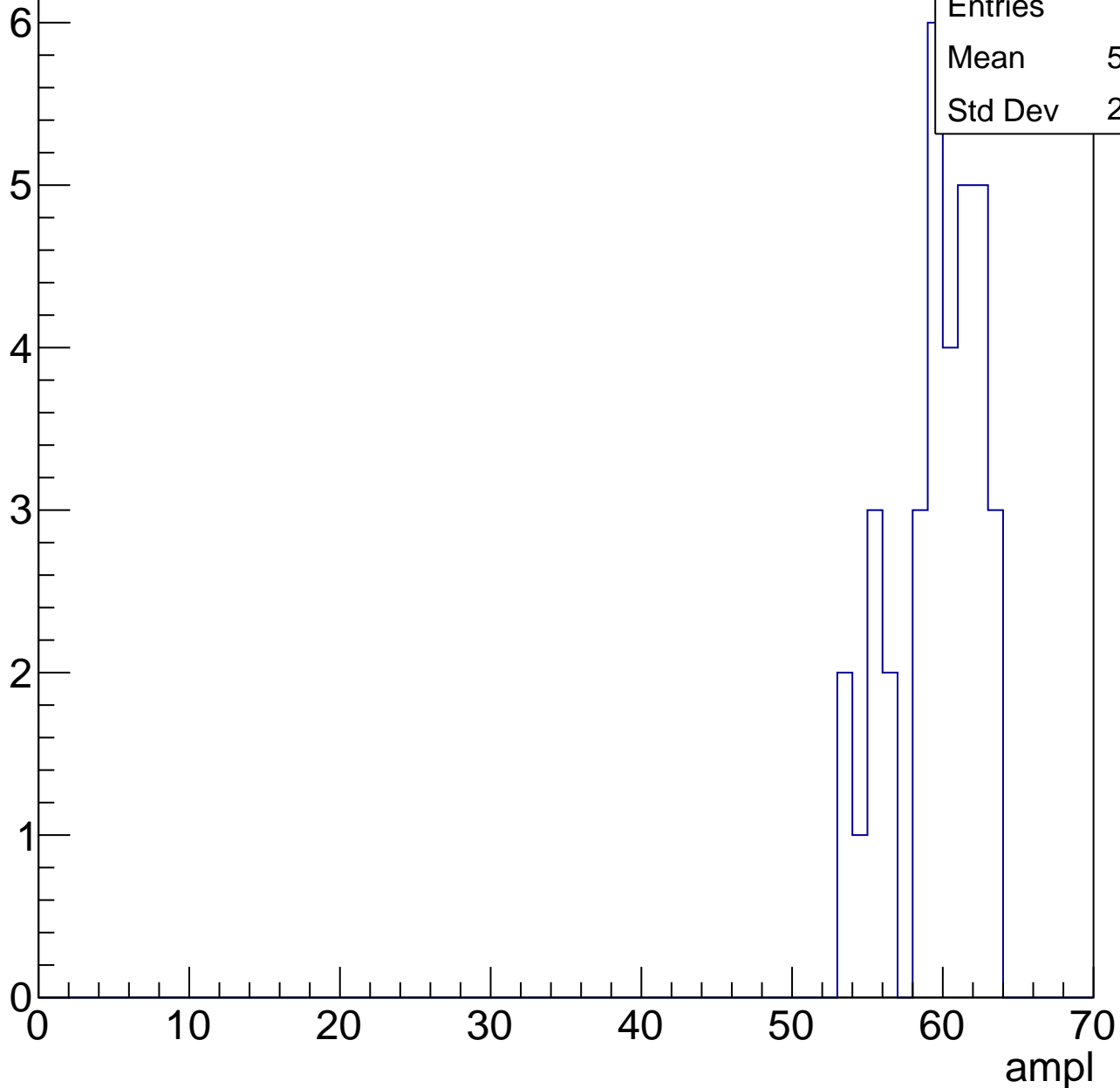
Entries	65
Mean	55.43
Std Dev	3.599



B1L103S, U10-ch60, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

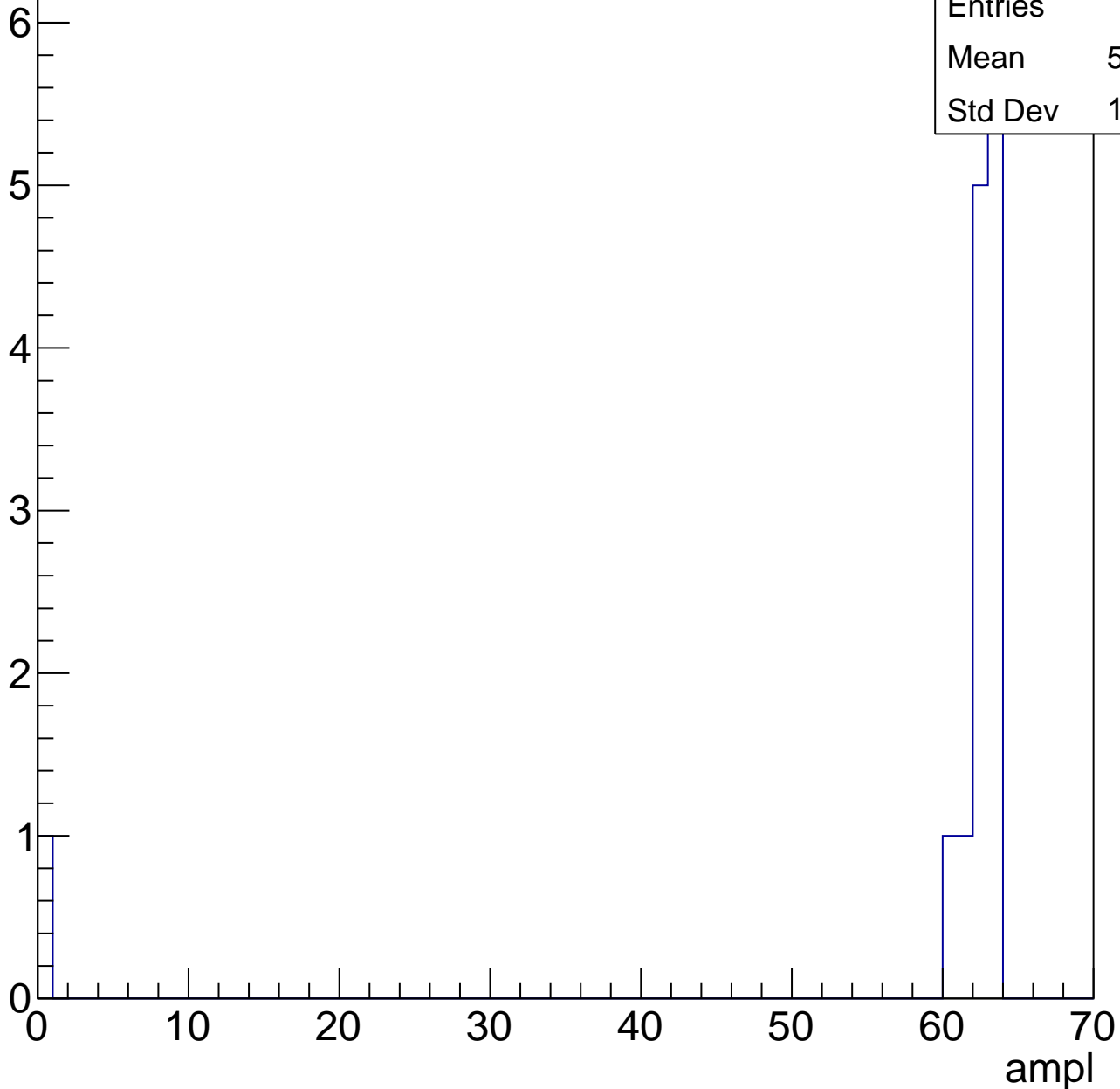


B1L103S, U10-ch60, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.79
Std Dev	16.05



B1L103S, U10-ch60, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch61, adc0

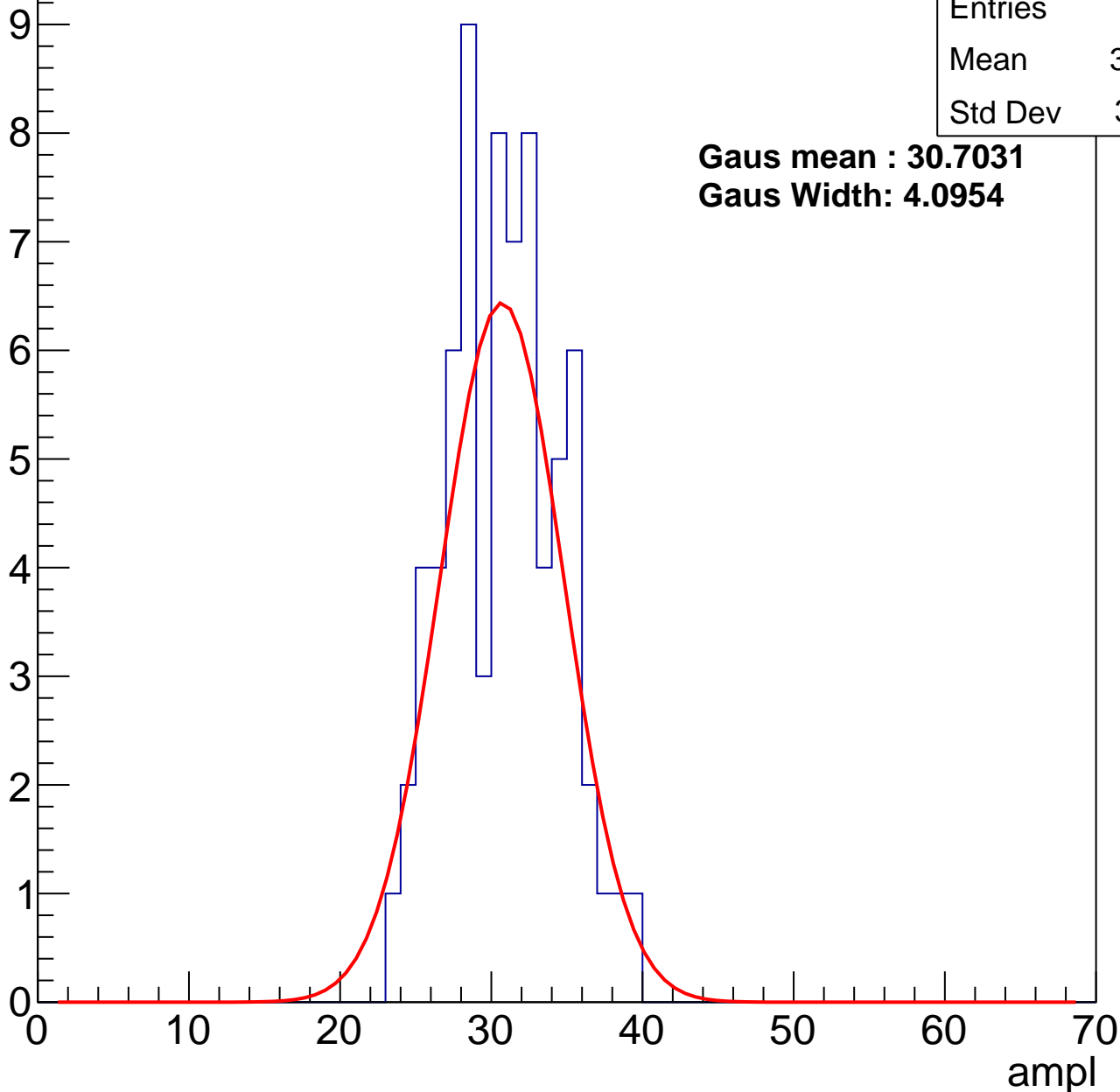
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.38
Std Dev	3.611

Gaus mean : 30.7031

Gaus Width: 4.0954



B1L103S, U10-ch61, adc1

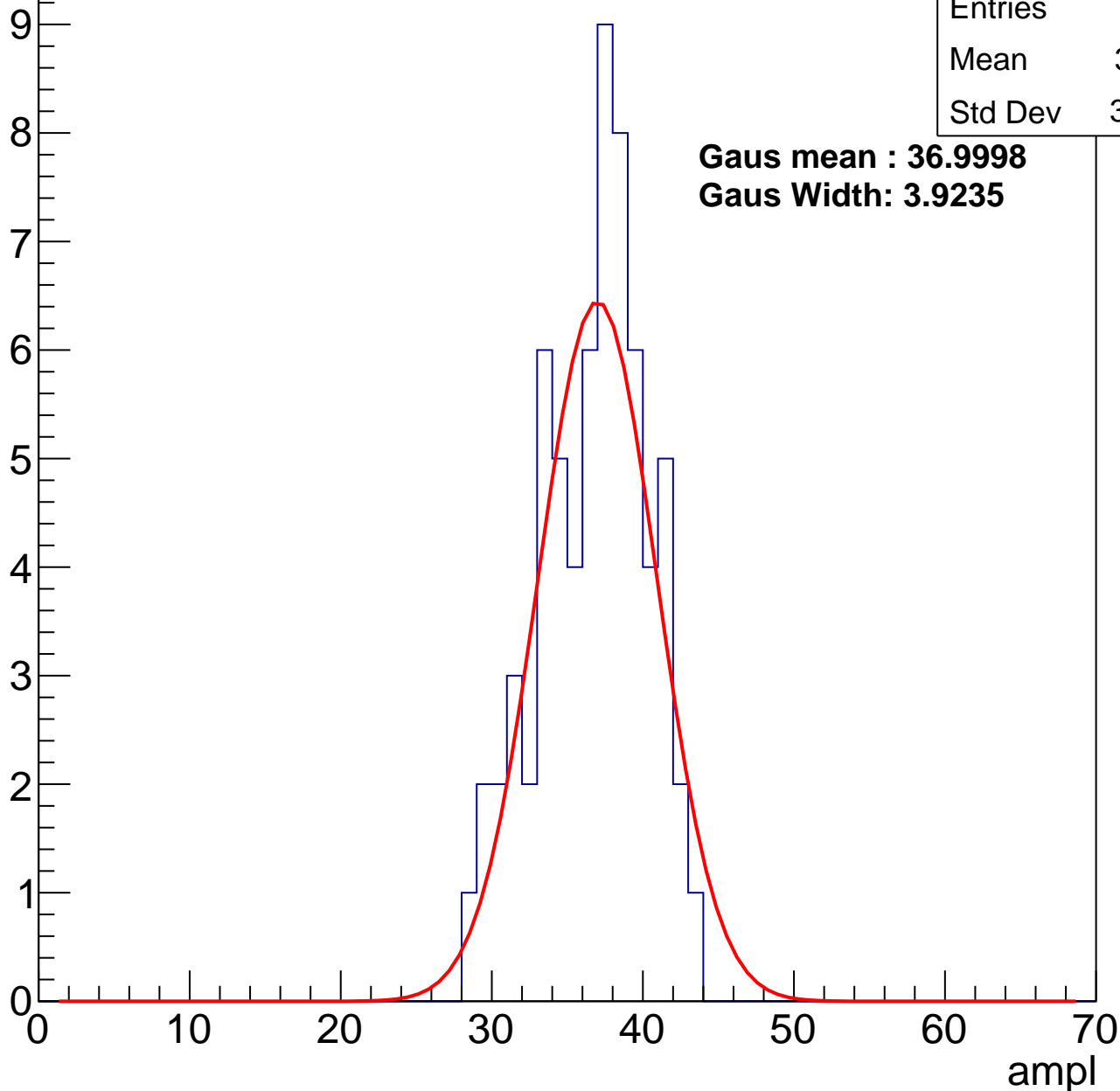
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.21
Std Dev	3.527

Gaus mean : 36.9998

Gaus Width: 3.9235



B1L103S, U10-ch61, adc2

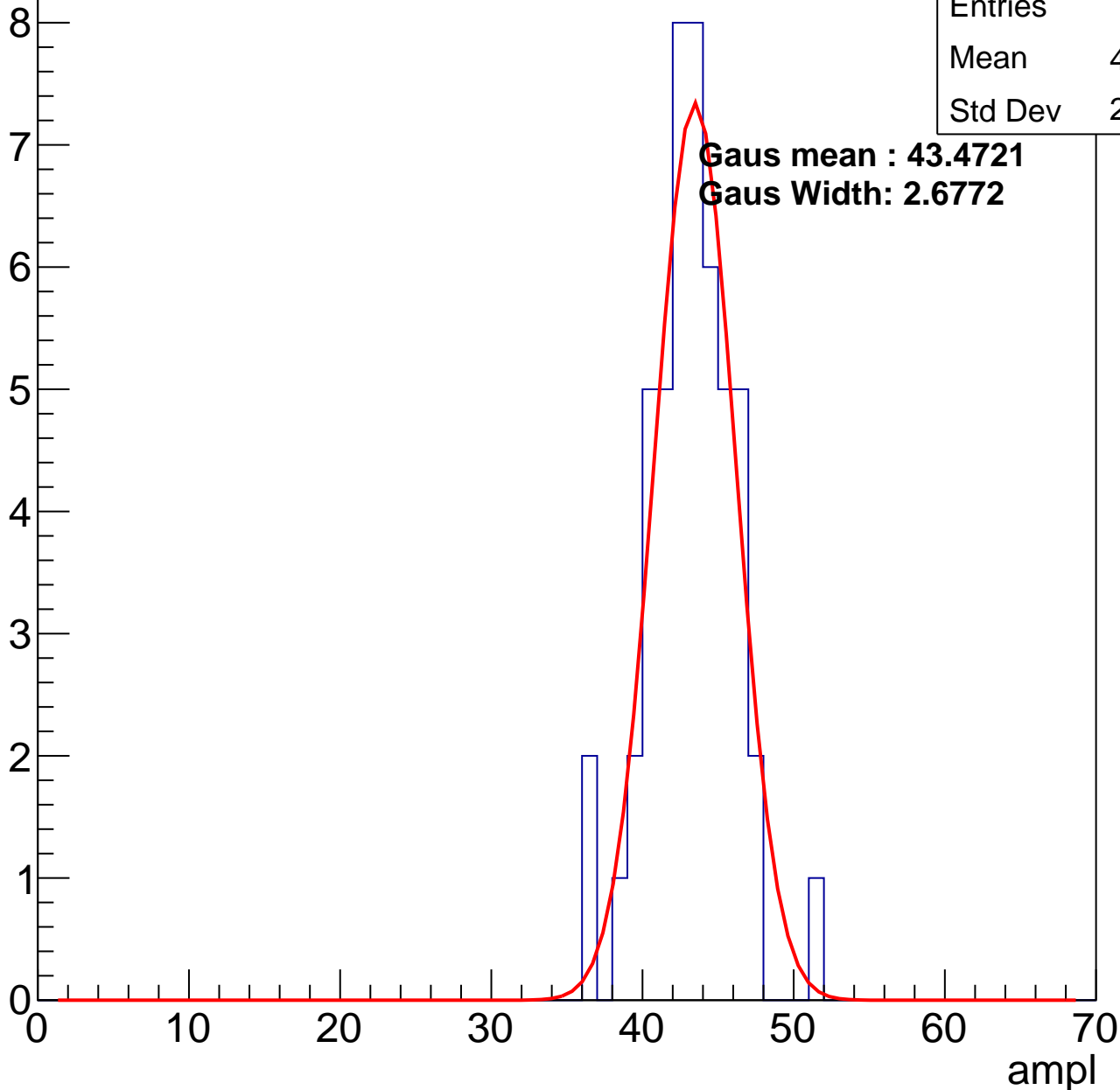
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.74
Std Dev	2.799

Gaus mean : 43.4721

Gaus Width: 2.6772



B1L103S, U10-ch61, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	48.62
Std Dev	3.208

Entry

10

8

6

4

2

0

0

10

20

30

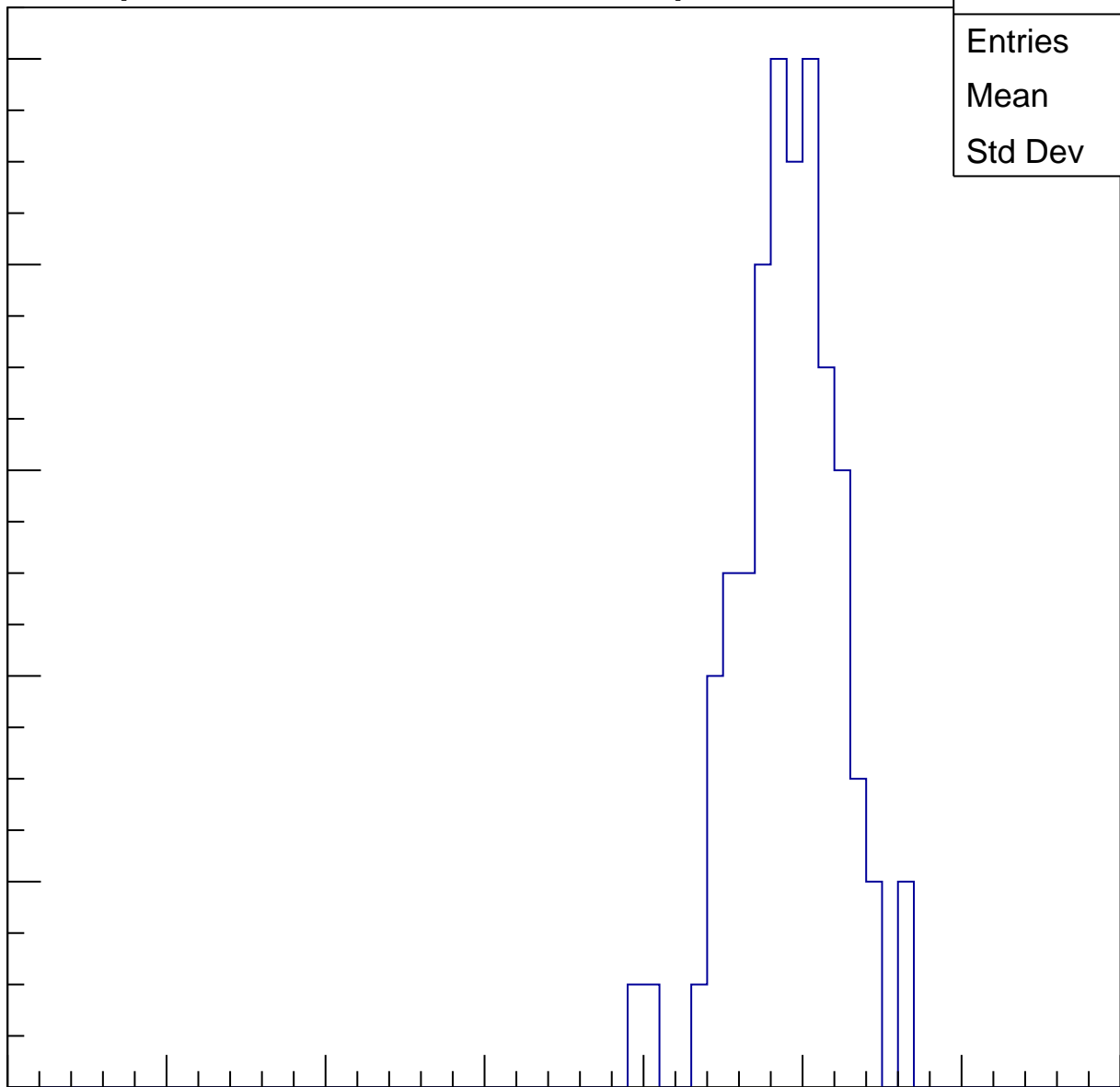
40

50

60

70

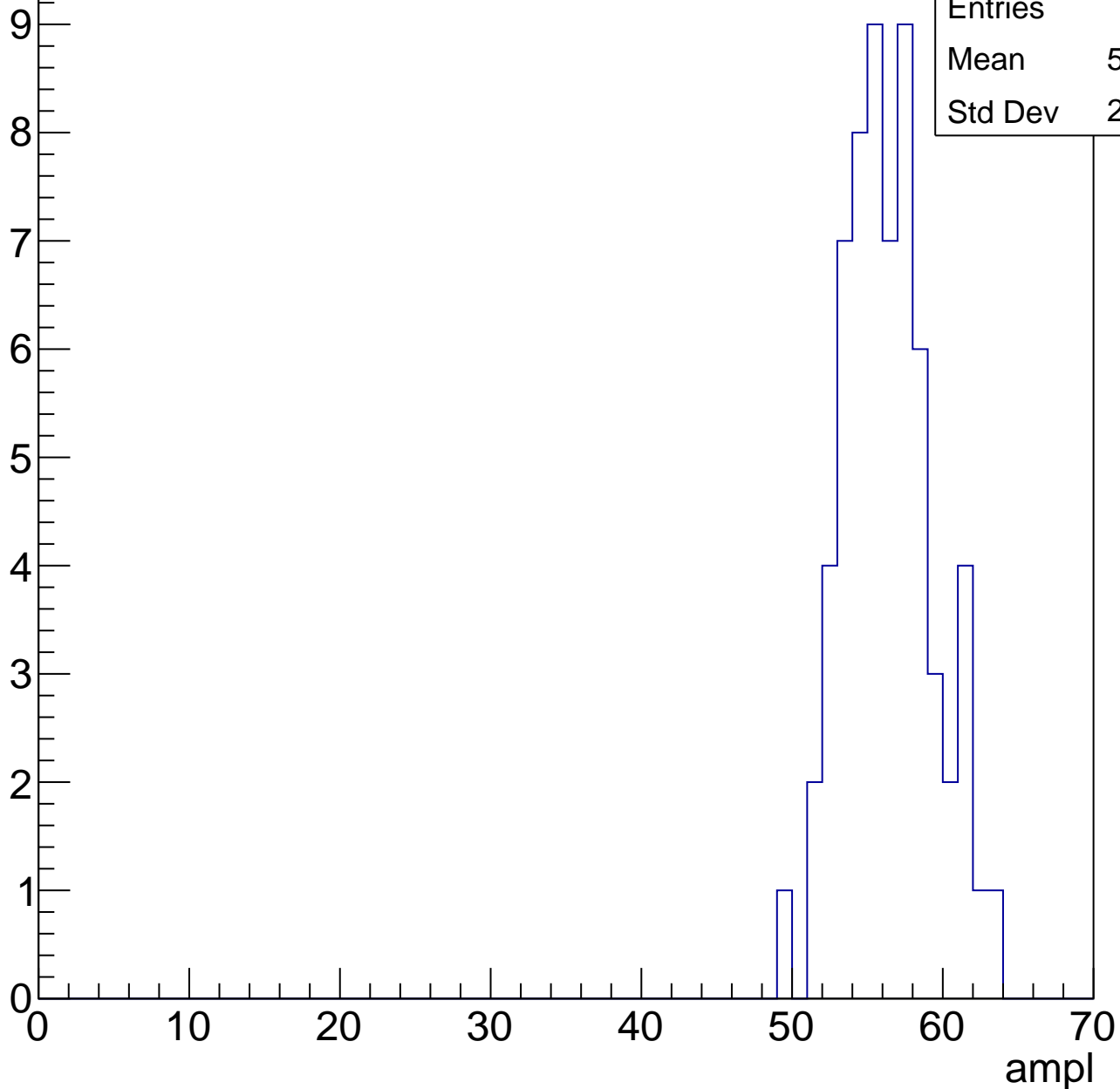
ampl



B1L103S, U10-ch61, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch61, adc5

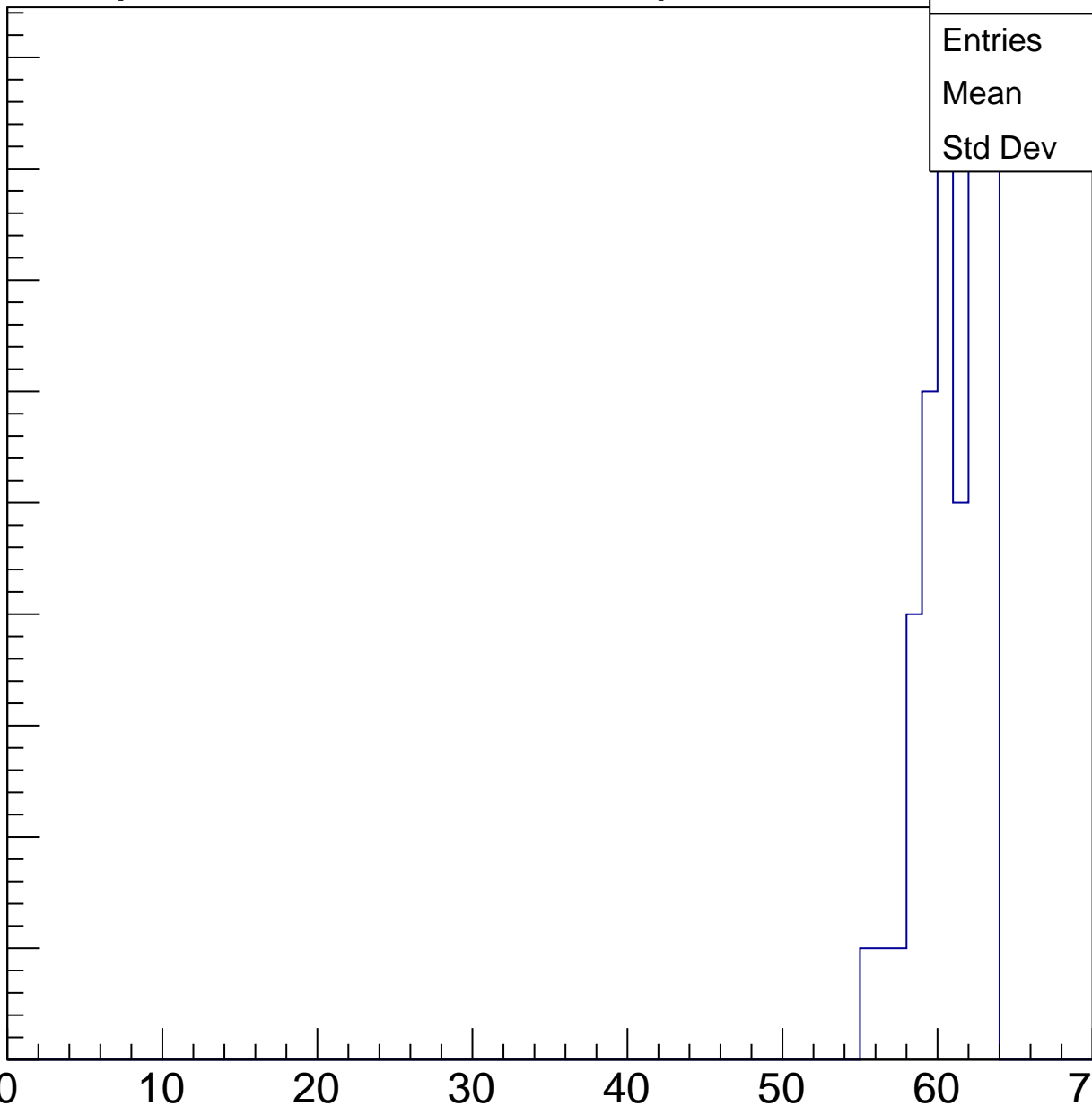
calib_packv5_042523_0143.root, FC#7, port C2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	43
Mean	60.51
Std Dev	2.039

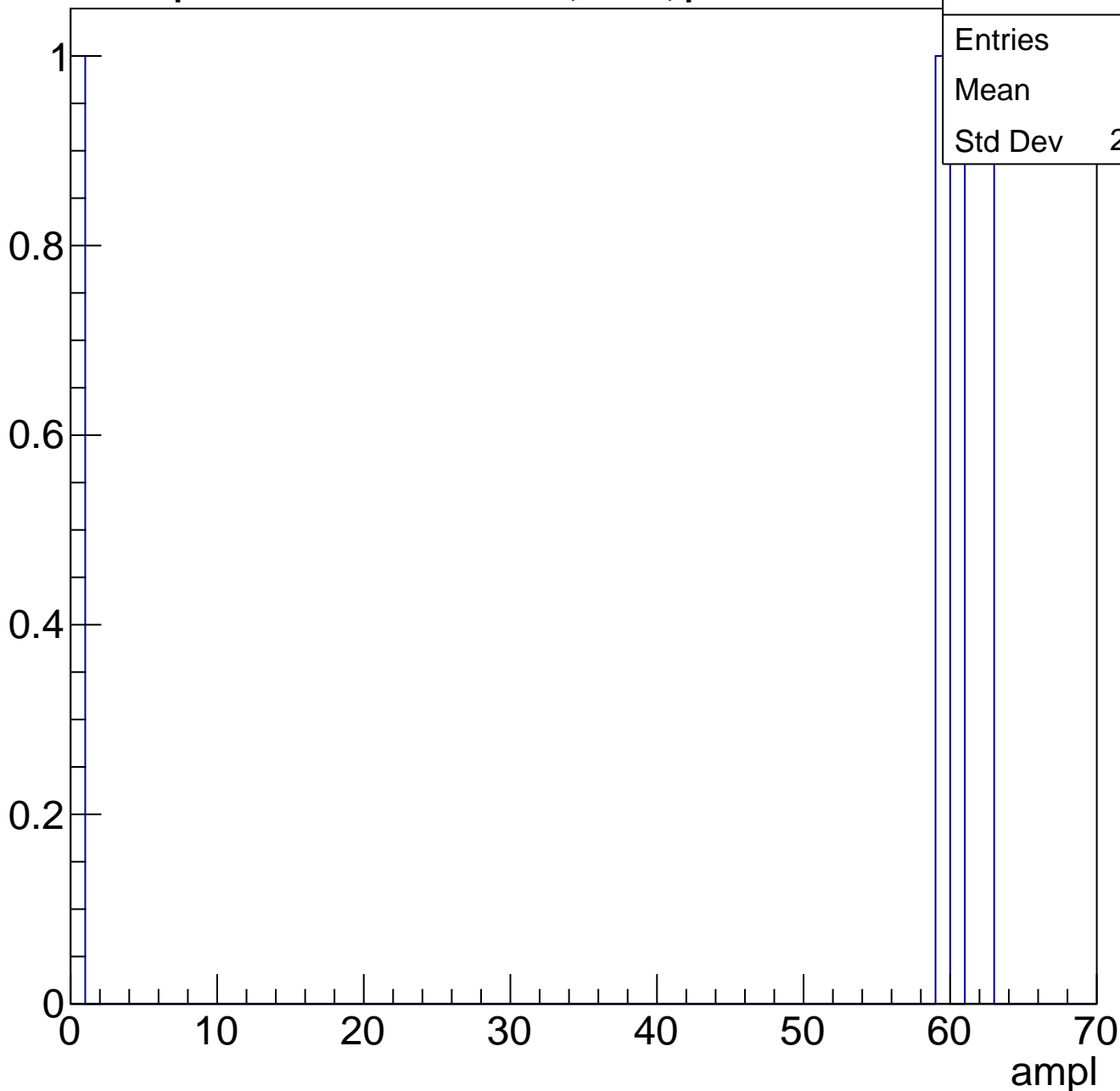
ampl



B1L103S, U10-ch61, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

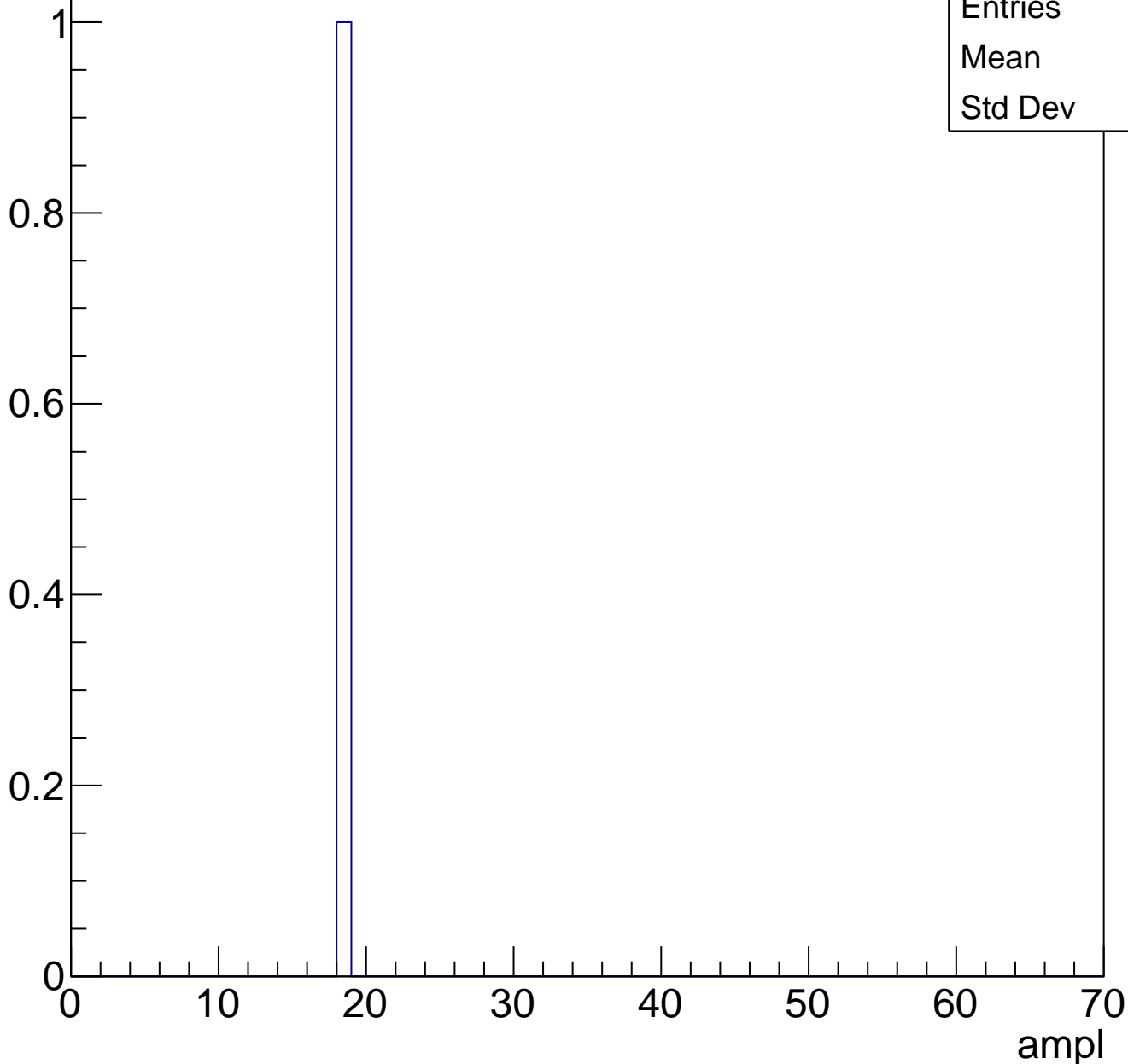


Entries	4
Mean	45.5
Std Dev	26.29

B1L103S, U10-ch61, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch62, adc0

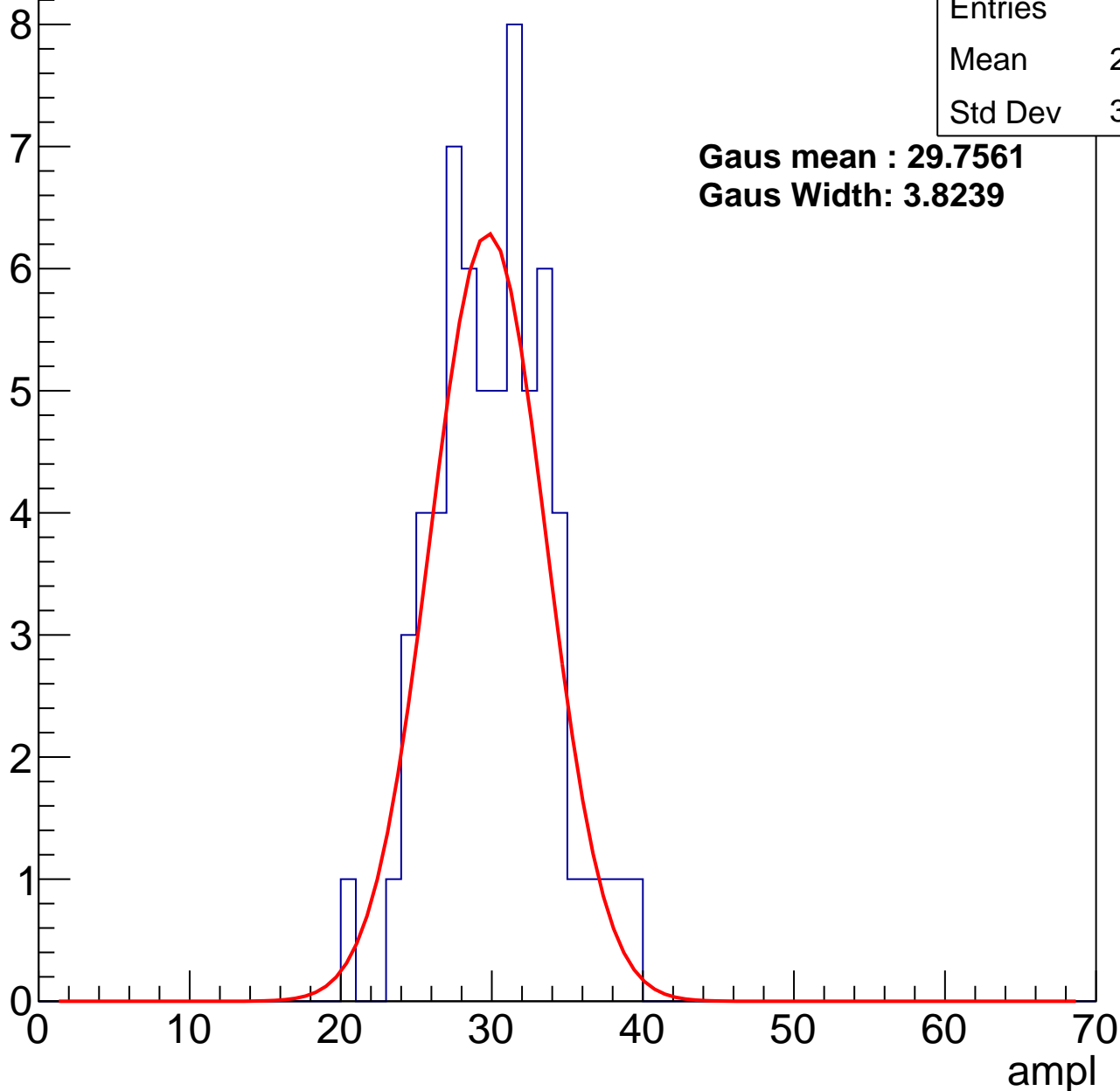
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.66
Std Dev	3.763

Gaus mean : 29.7561

Gaus Width: 3.8239



B1L103S, U10-ch62, adc1

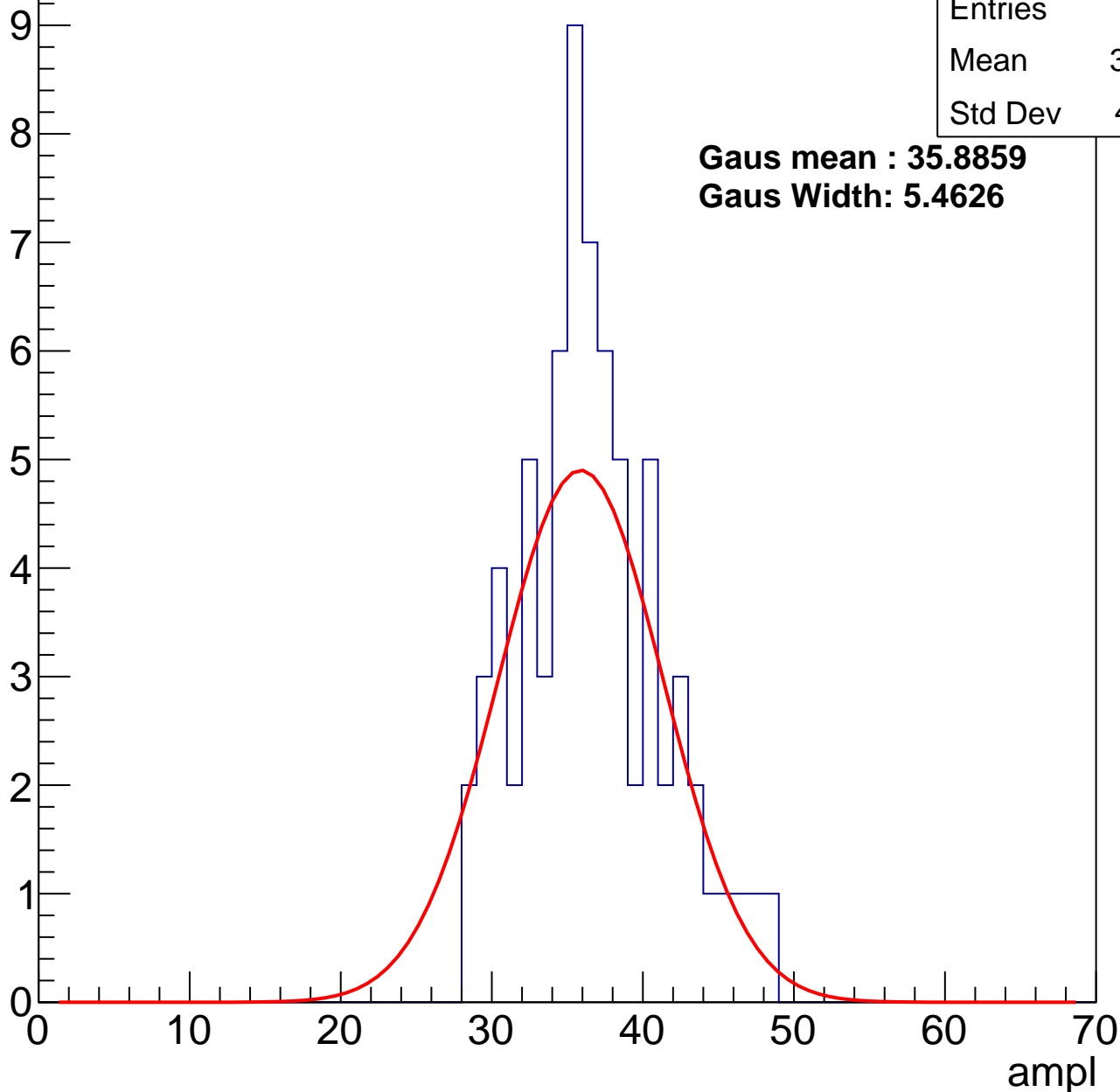
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	36.18
Std Dev	4.591

Gaus mean : 35.8859

Gaus Width: 5.4626



B1L103S, U10-ch62, adc2

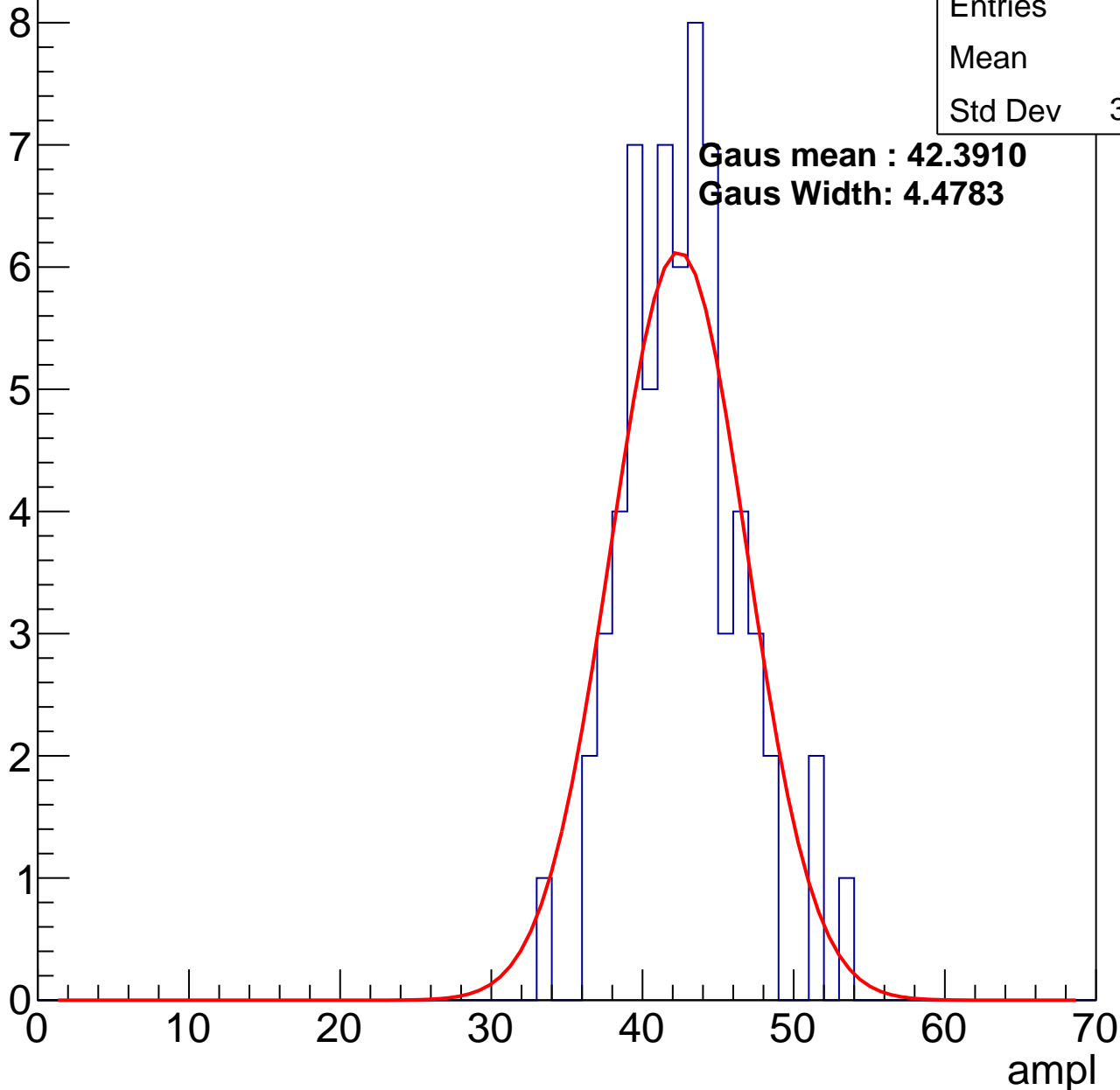
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.2
Std Dev	3.796

Gaus mean : 42.3910

Gaus Width: 4.4783

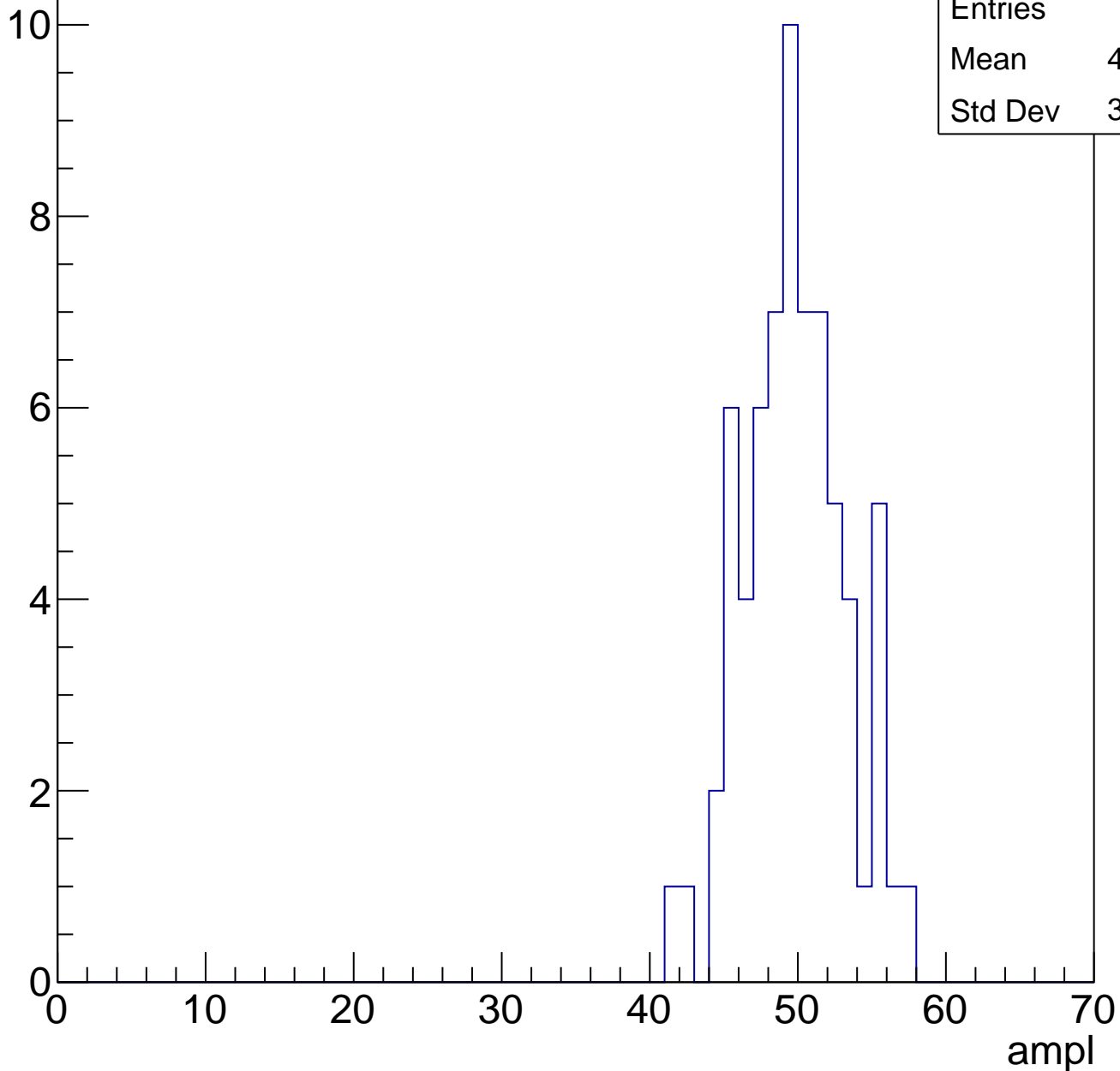


B1L103S, U10-ch62, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.32
Std Dev	3.389

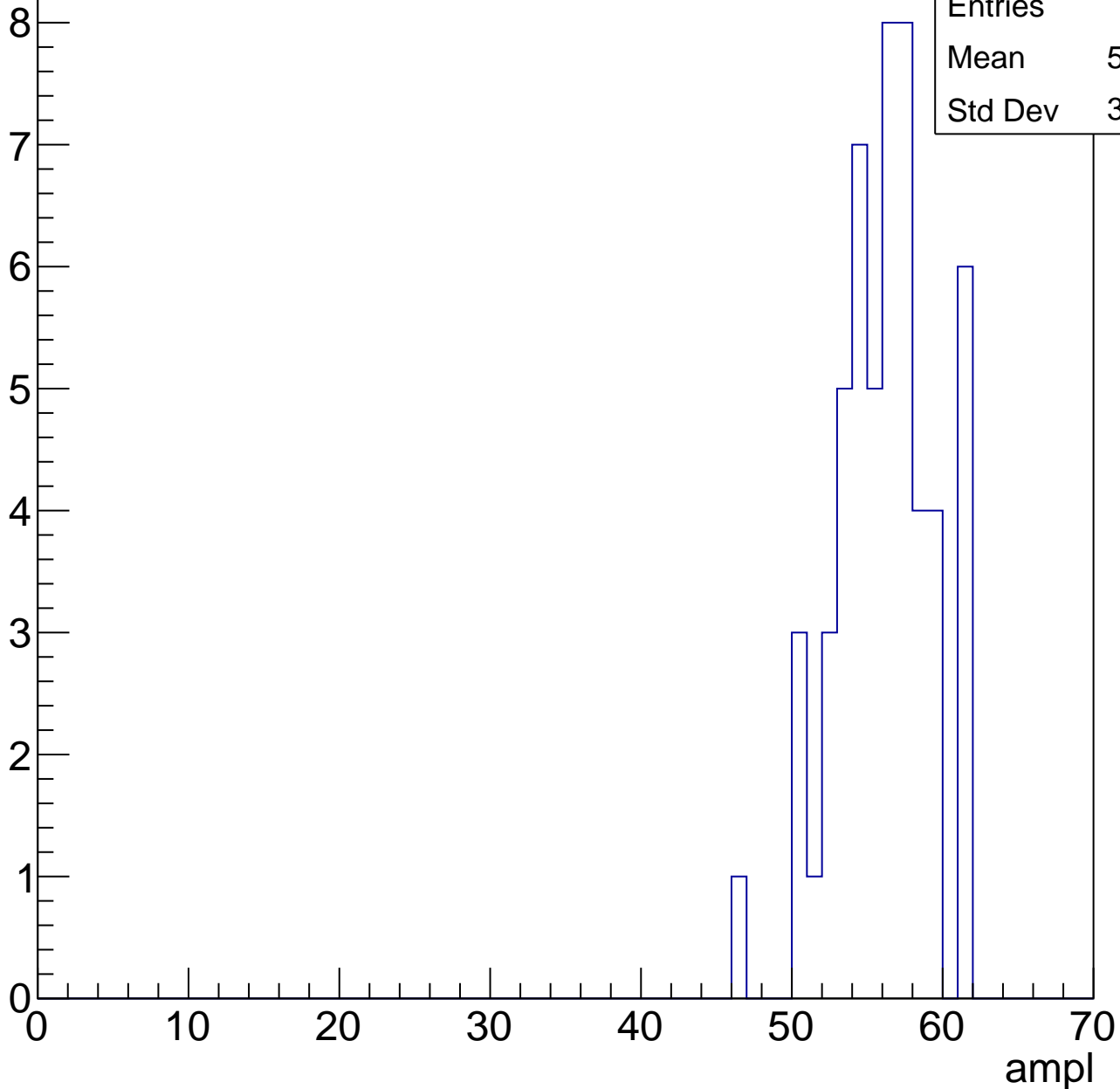


B1L103S, U10-ch62, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

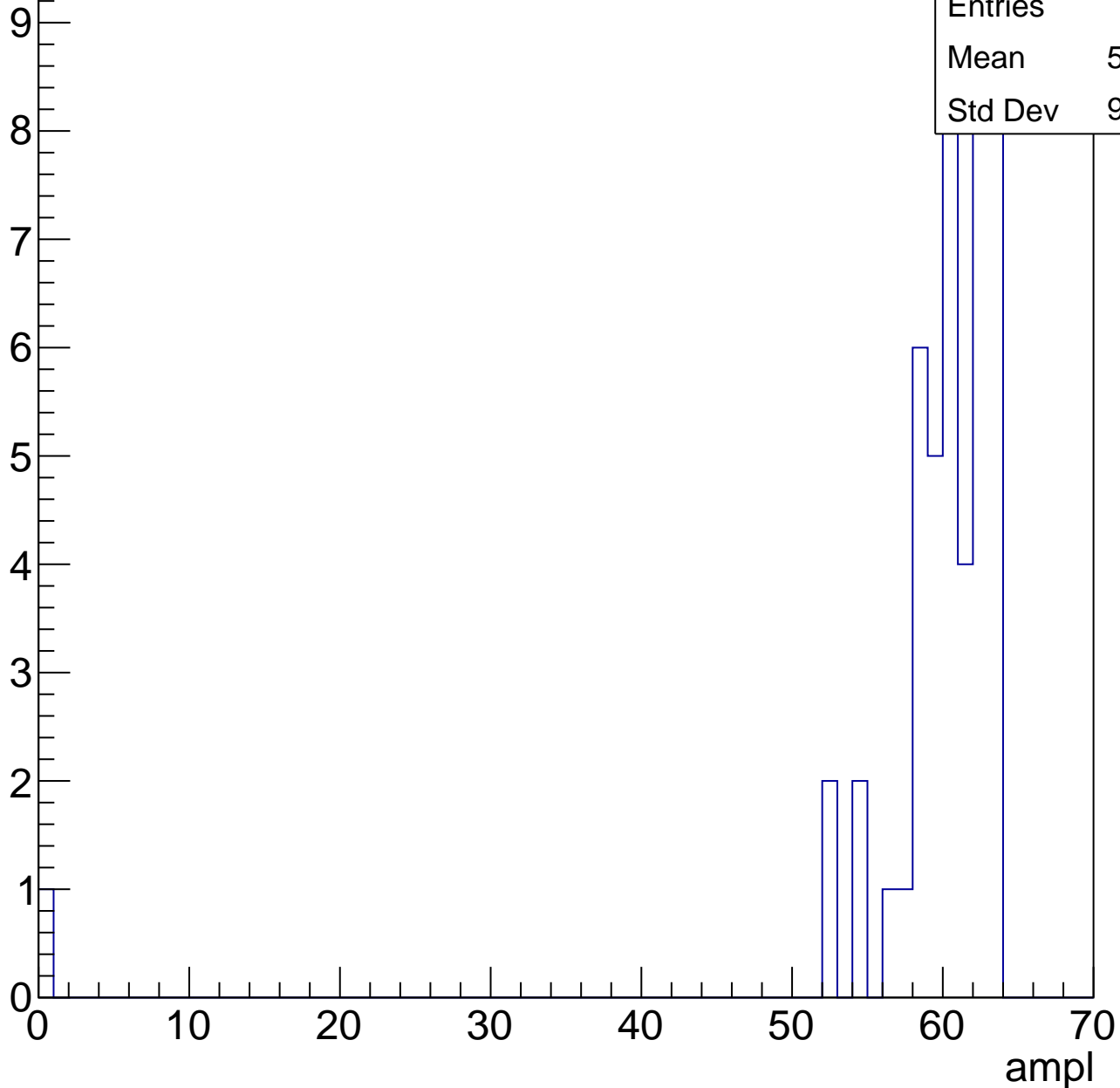
Entries	55
Mean	55.62
Std Dev	3.194



B1L103S, U10-ch62, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch62, adc6

calib_packv5_042523_0143.root, FC#7, port C2

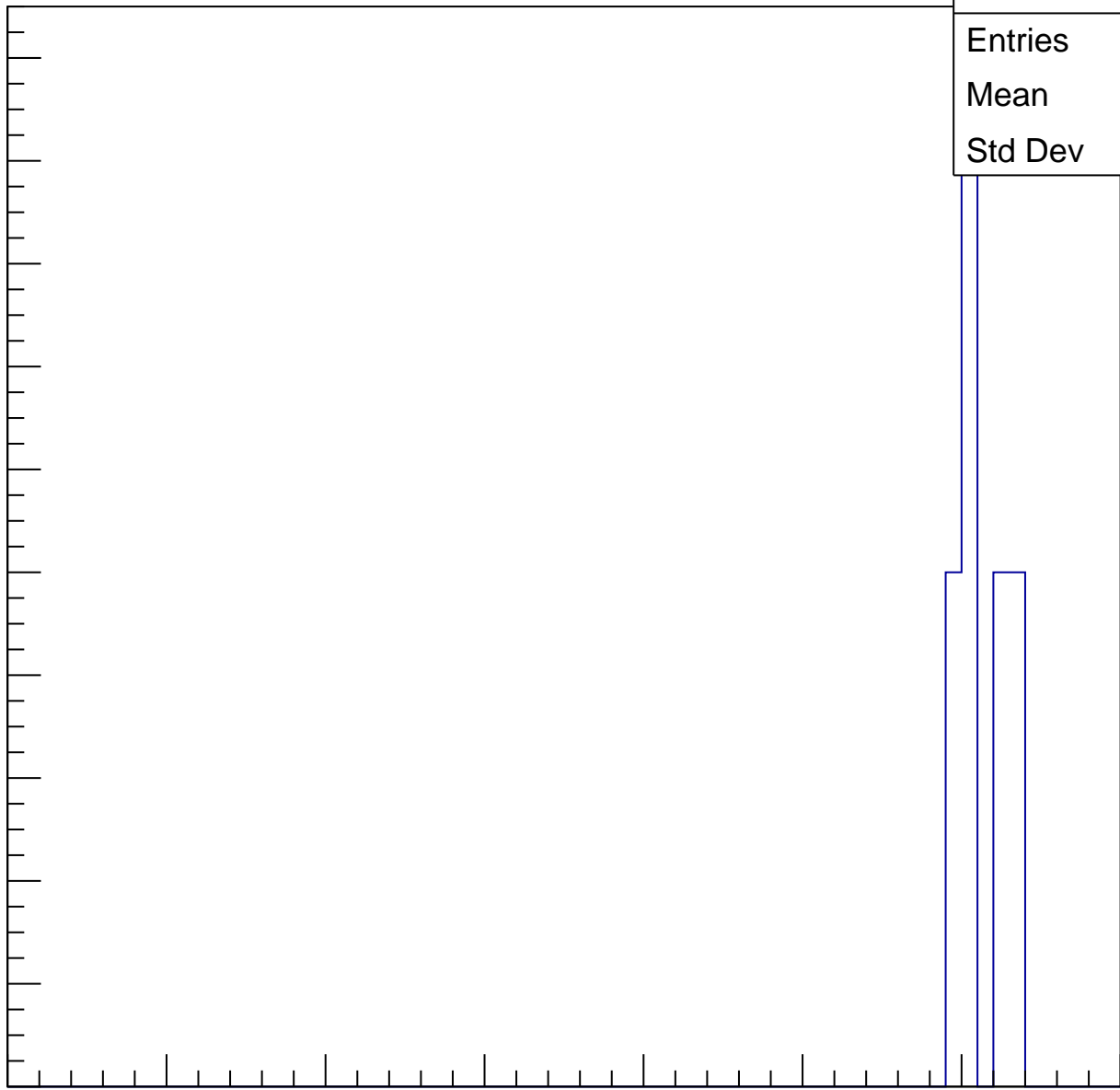
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.8
Std Dev	1.47

ampl

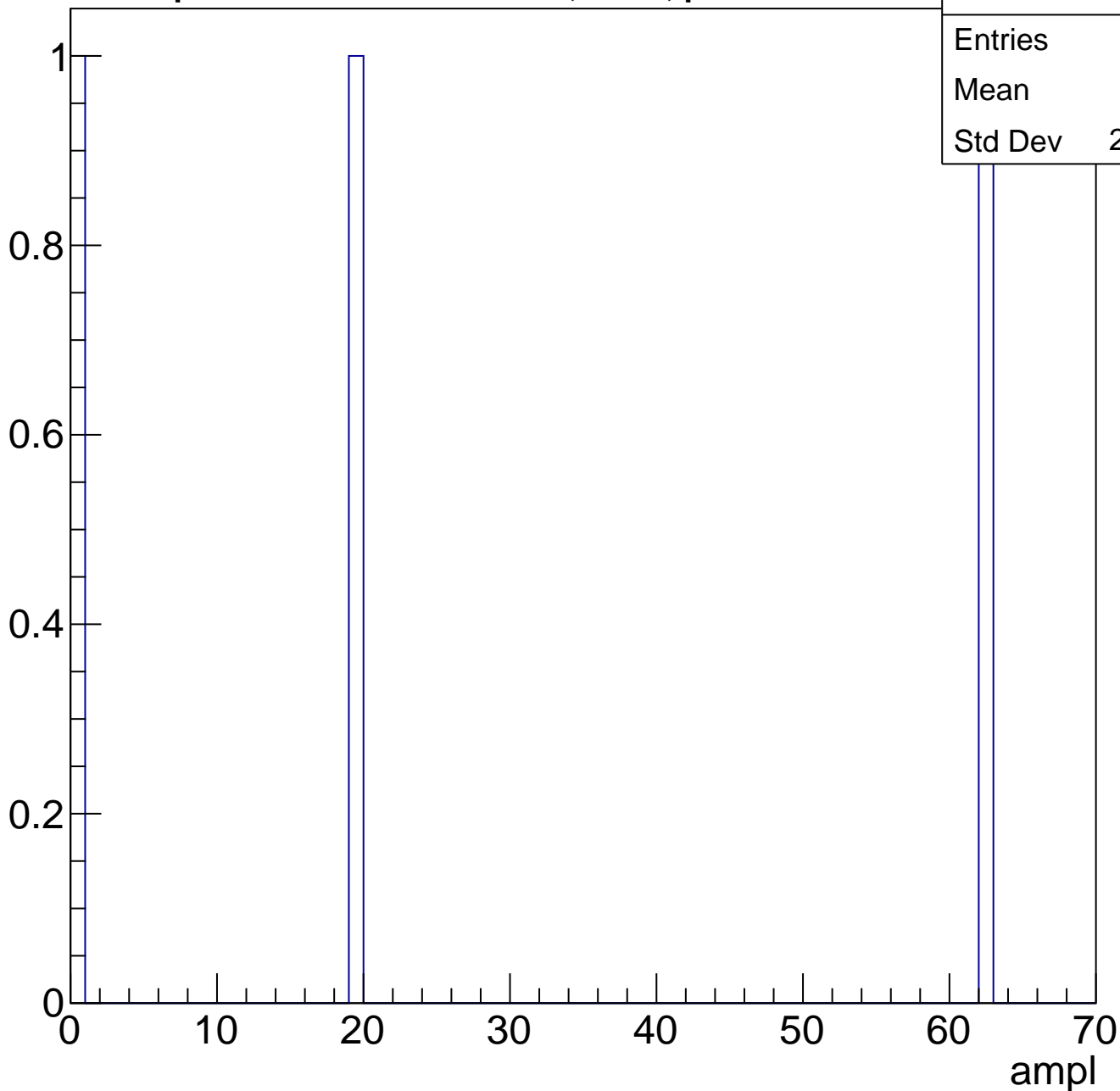
0 10 20 30 40 50 60 70



B1L103S, U10-ch62, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	27
Std Dev	25.94

B1L103S, U10-ch63, adc0

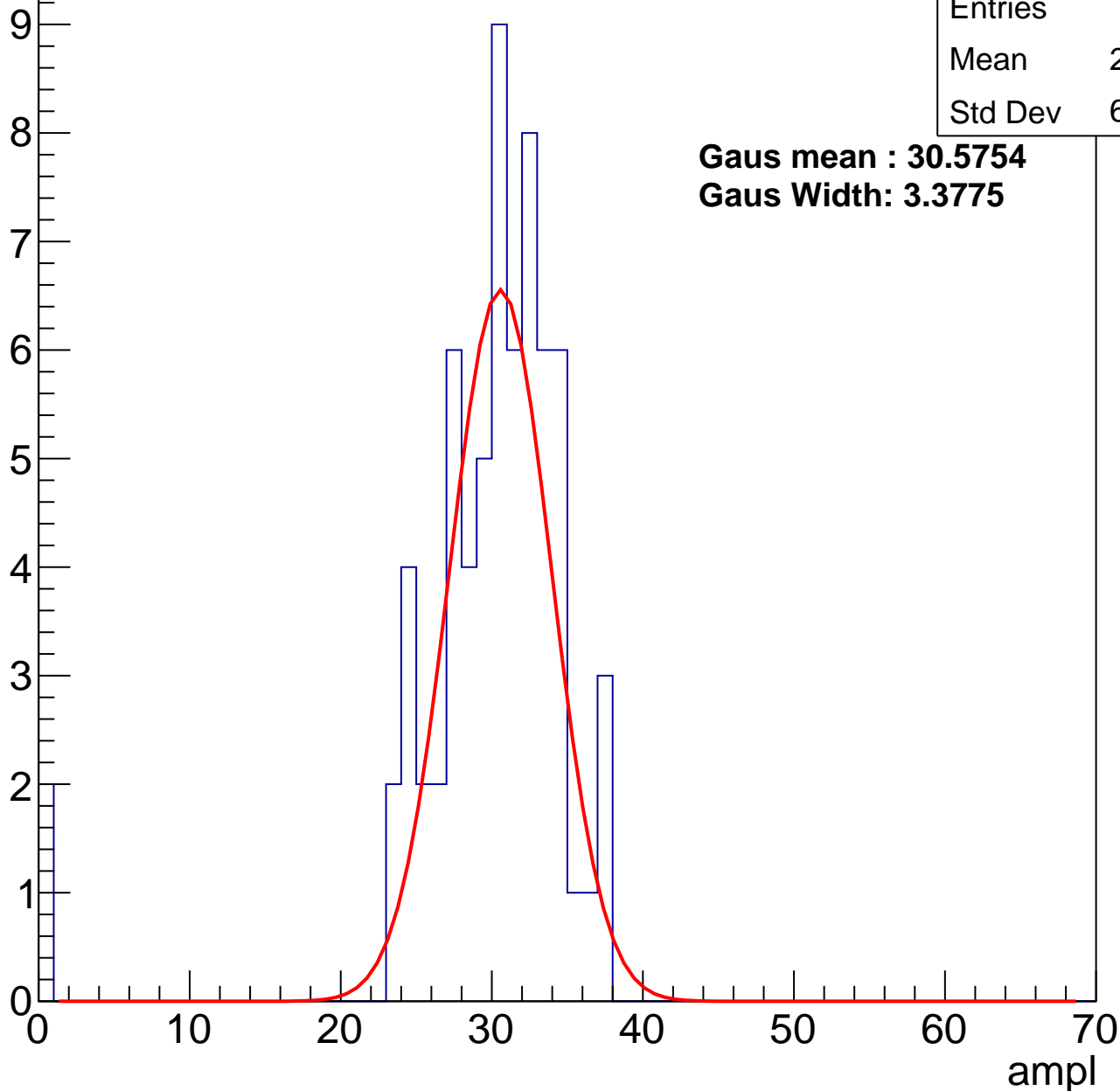
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	29.24
Std Dev	6.177

Gaus mean : 30.5754

Gaus Width: 3.3775



B1L103S, U10-ch63, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	37.27
Std Dev	3.3

Gaus mean : 37.9374

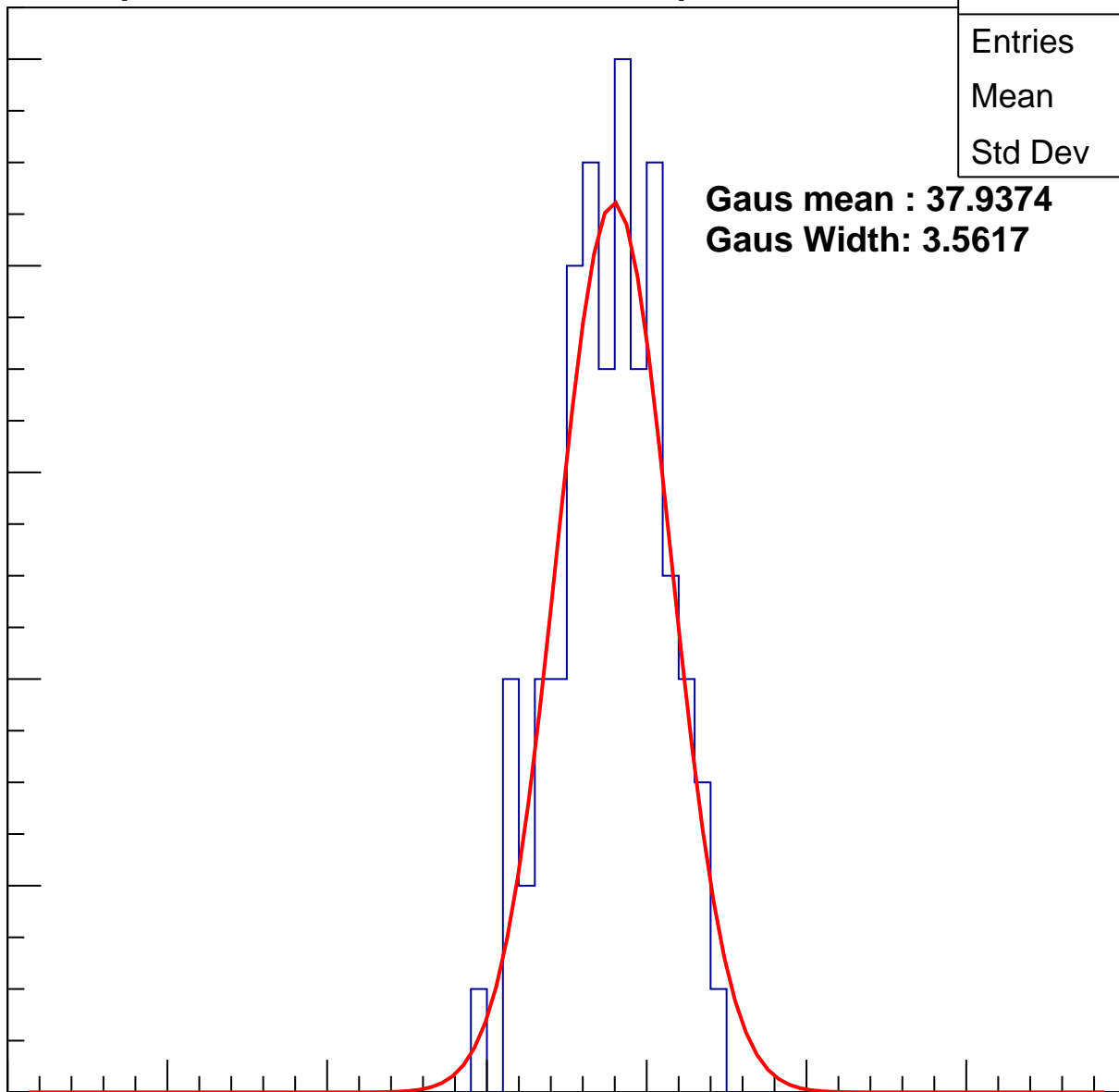
Gaus Width: 3.5617

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch63, adc2

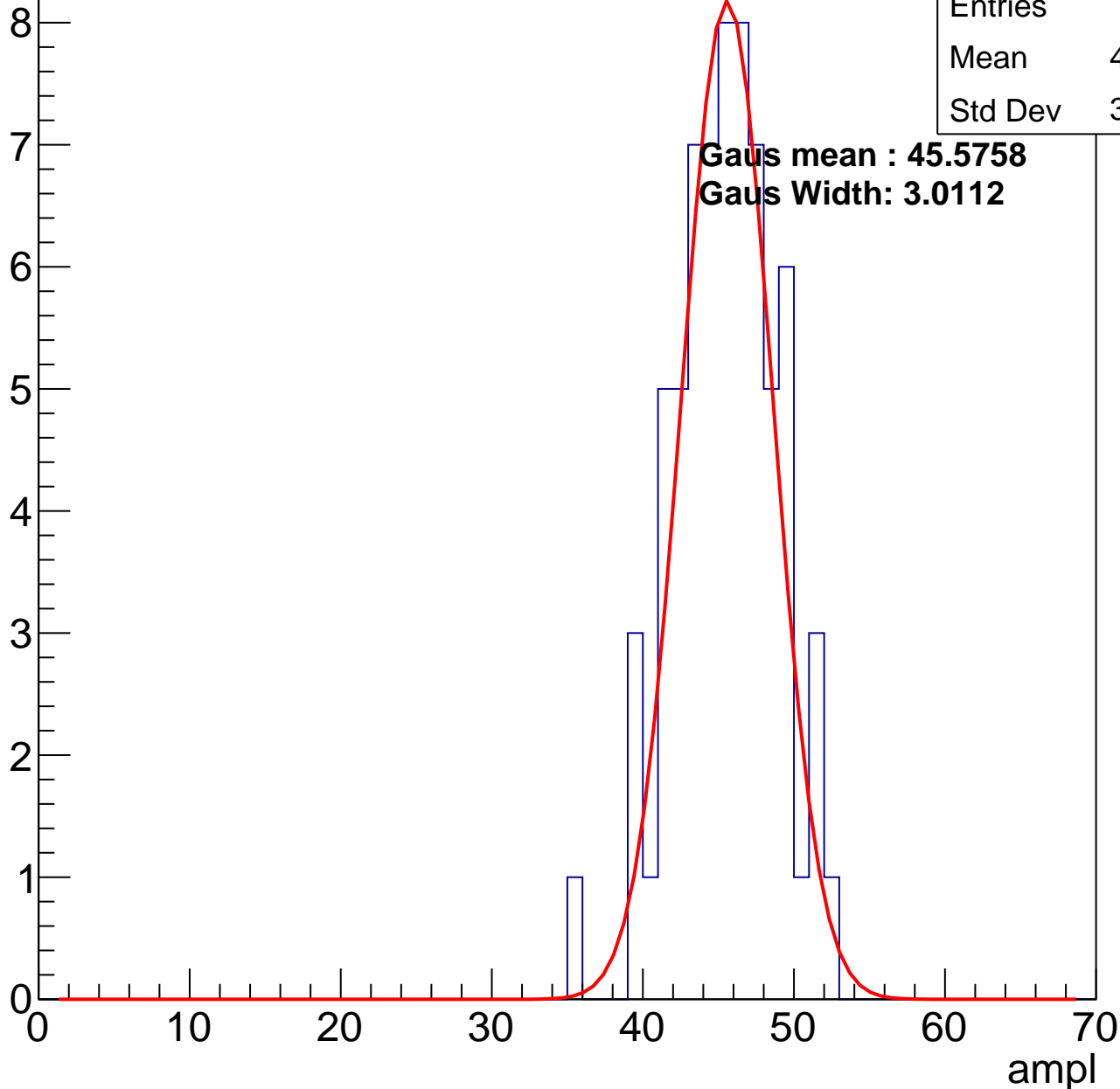
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	45.03
Std Dev	3.325

Gaus mean : 45.5758

Gaus Width: 3.0112

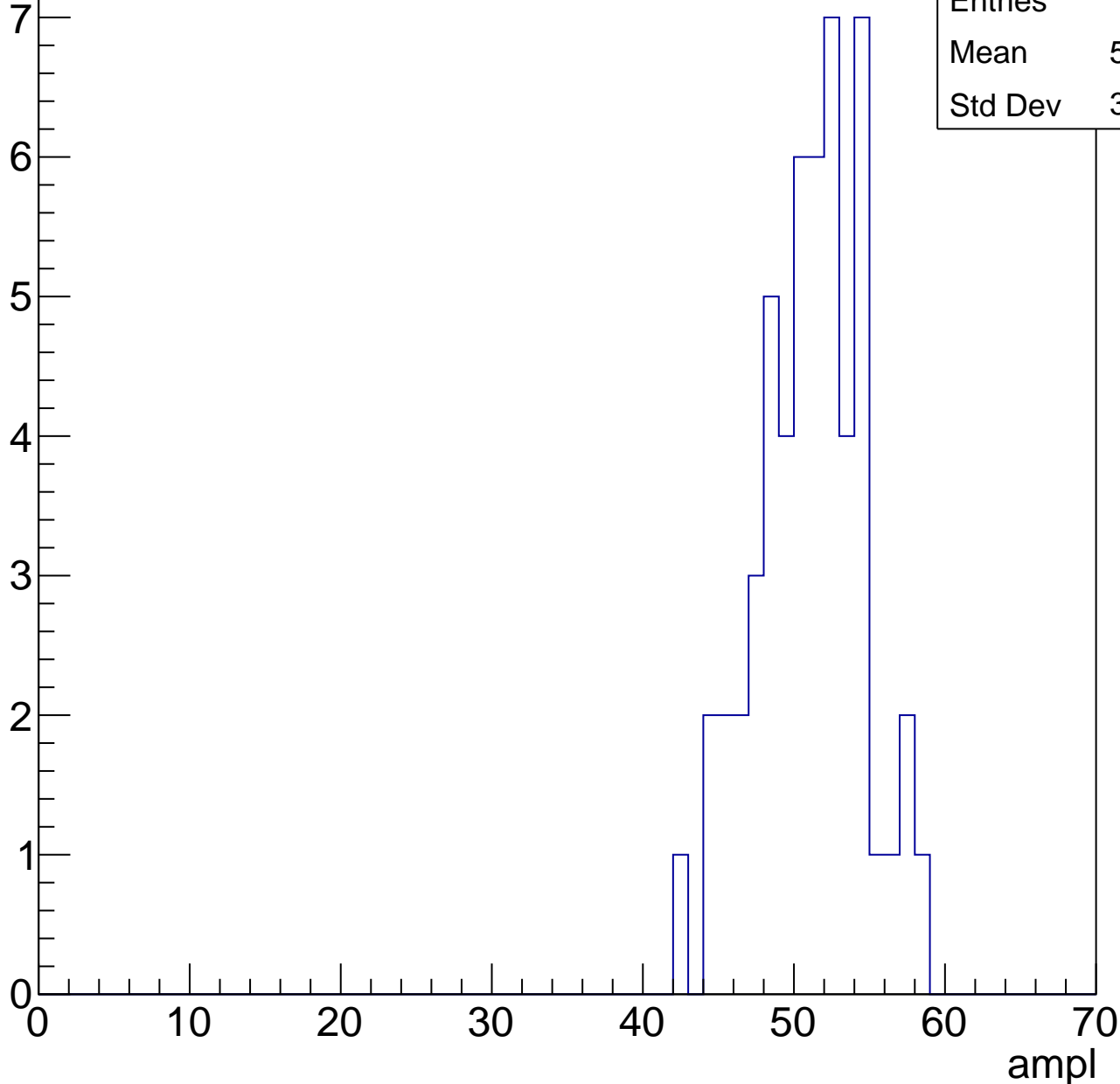


B1L103S, U10-ch63, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	50.59
Std Dev	3.483

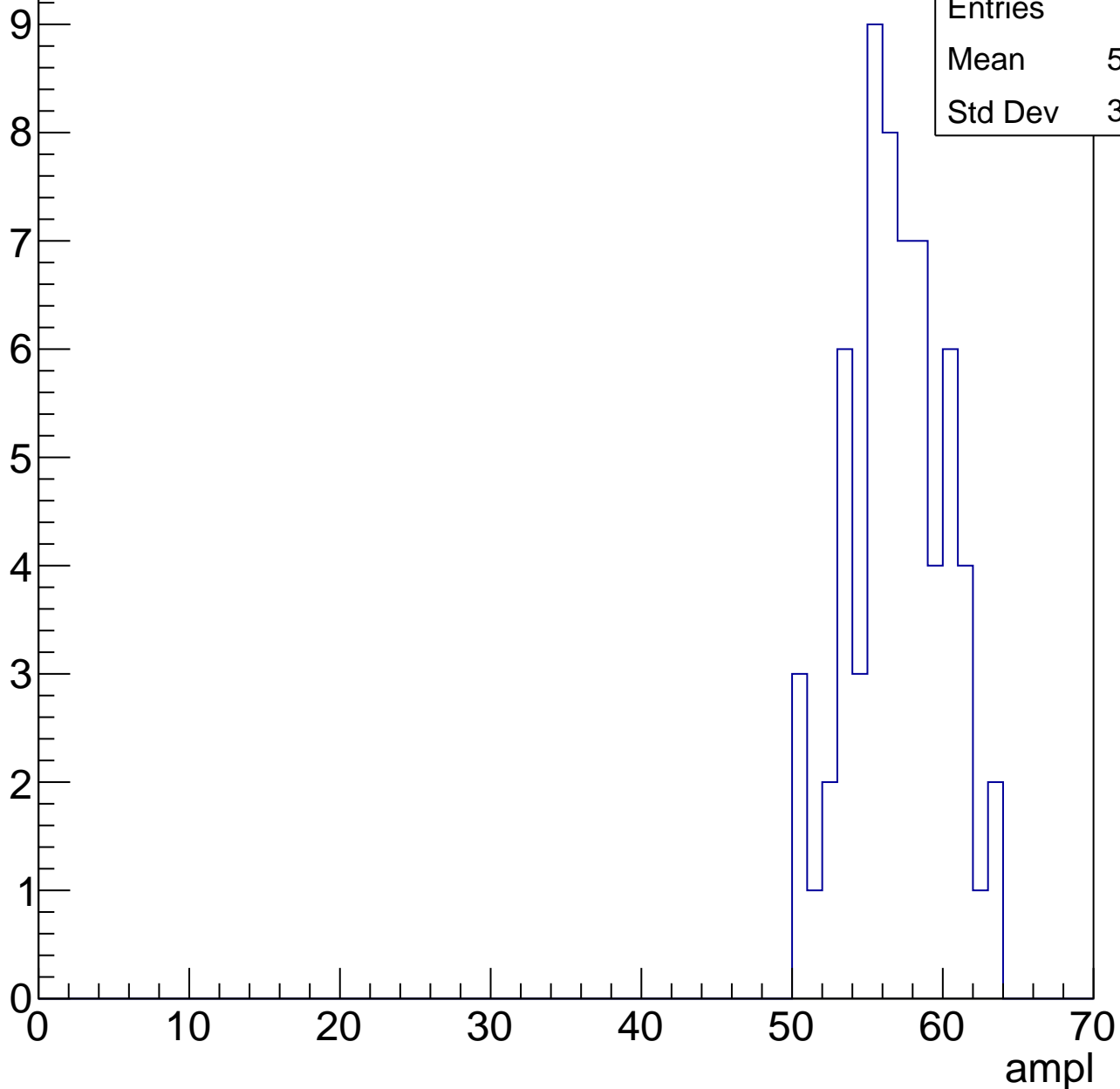


B1L103S, U10-ch63, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.52
Std Dev	3.146

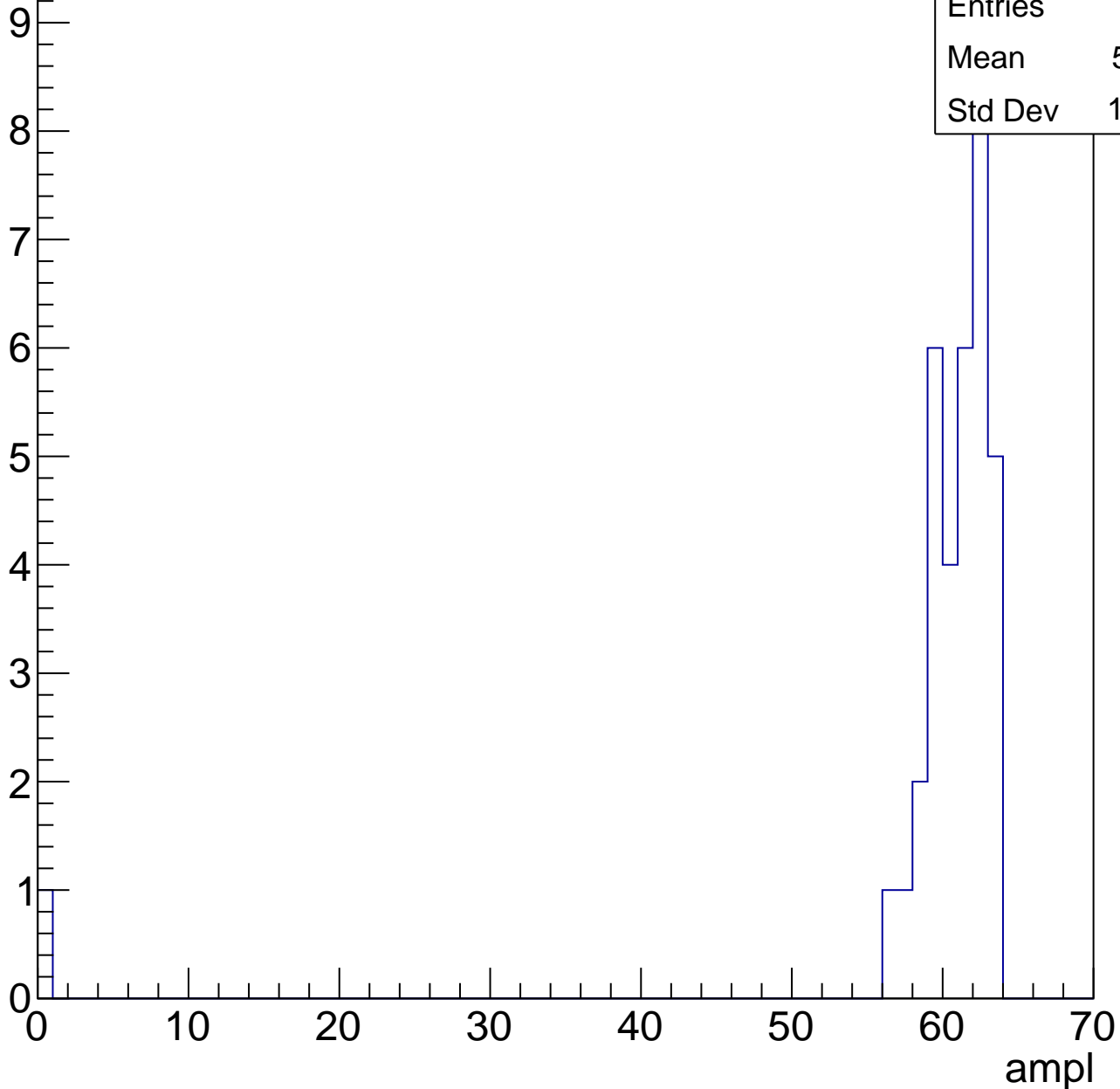


B1L103S, U10-ch63, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	58.91
Std Dev	10.26



B1L103S, U10-ch63, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch63, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	21
Std Dev	0

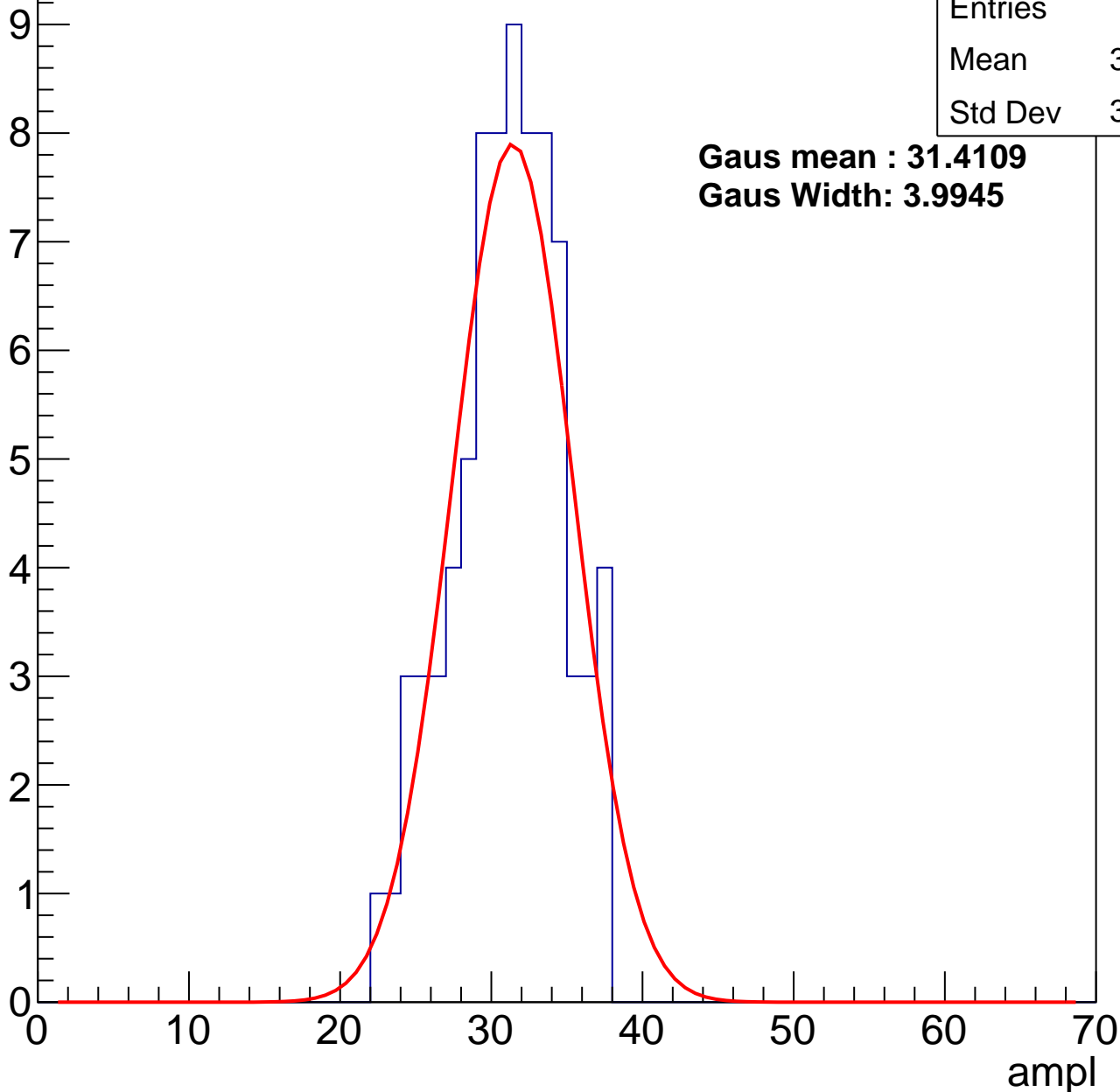
B1L103S, U10-ch64, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	30.62
Std Dev	3.549

Gaus mean : 31.4109
Gaus Width: 3.9945



B1L103S, U10-ch64, adc1

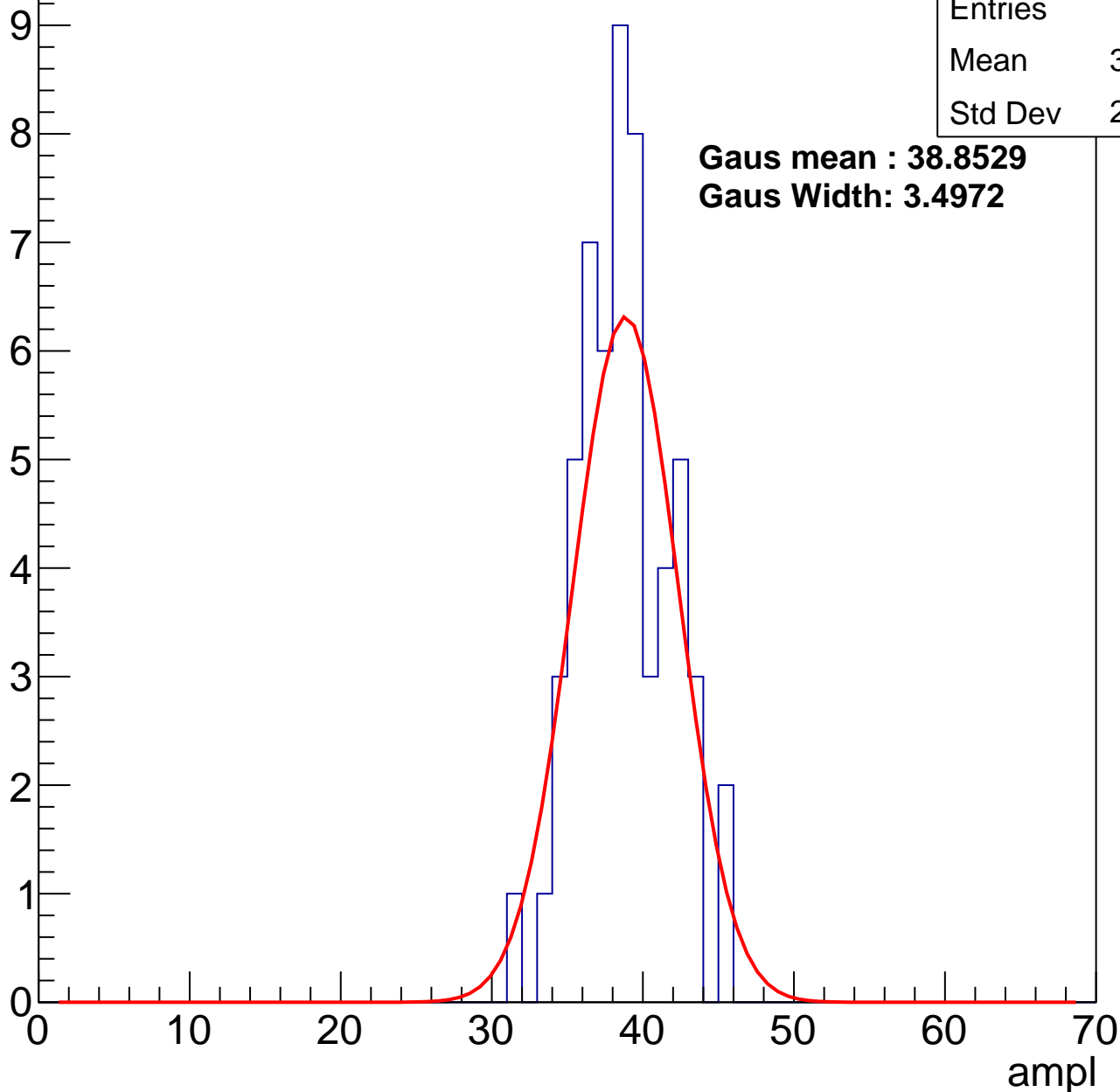
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	38.28
Std Dev	2.978

Gaus mean : 38.8529

Gaus Width: 3.4972



B1L103S, U10-ch64, adc2

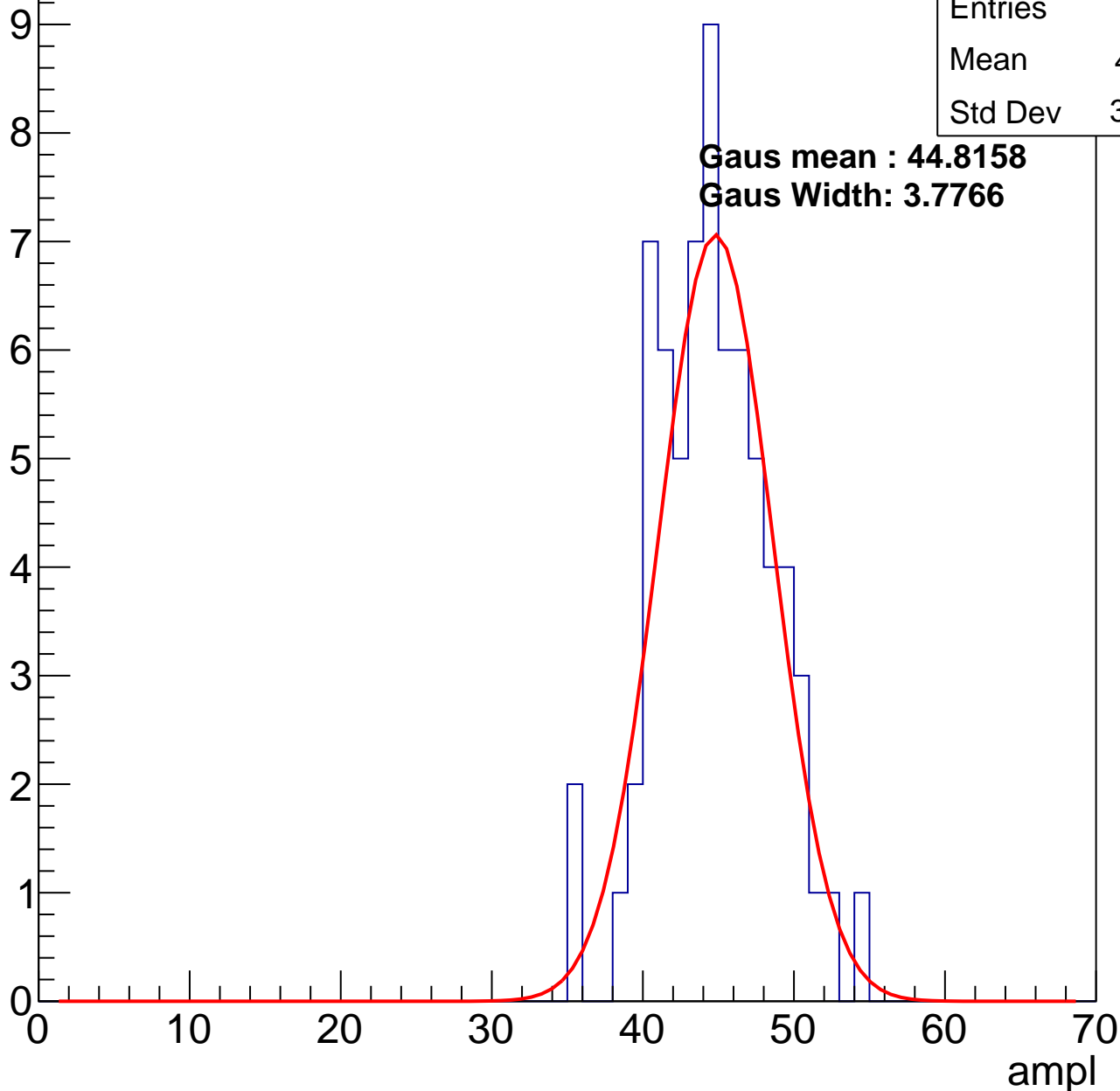
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	44.21
Std Dev	3.775

Gaus mean : 44.8158

Gaus Width: 3.7766

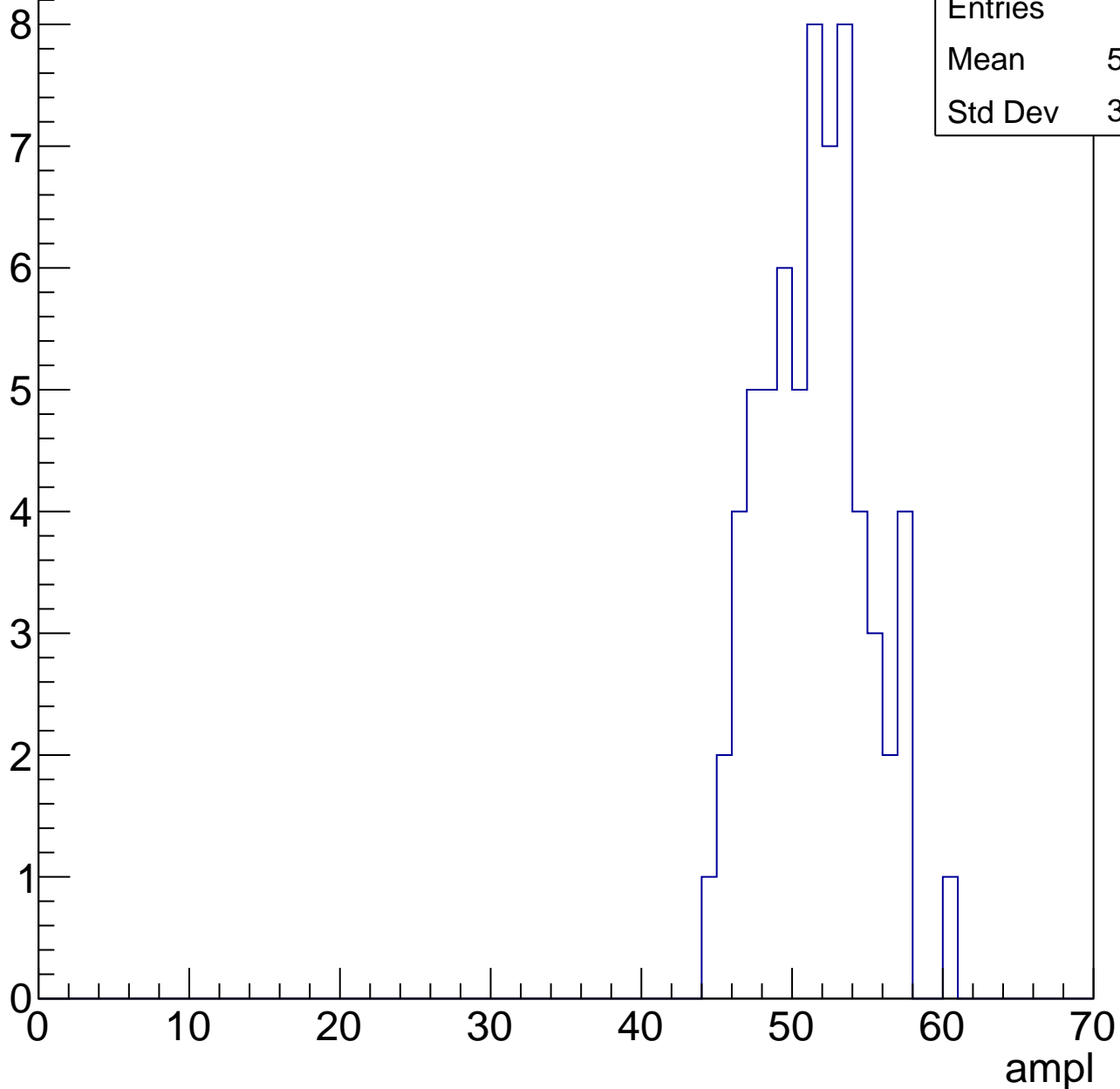


B1L103S, U10-ch64, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	50.98
Std Dev	3.448

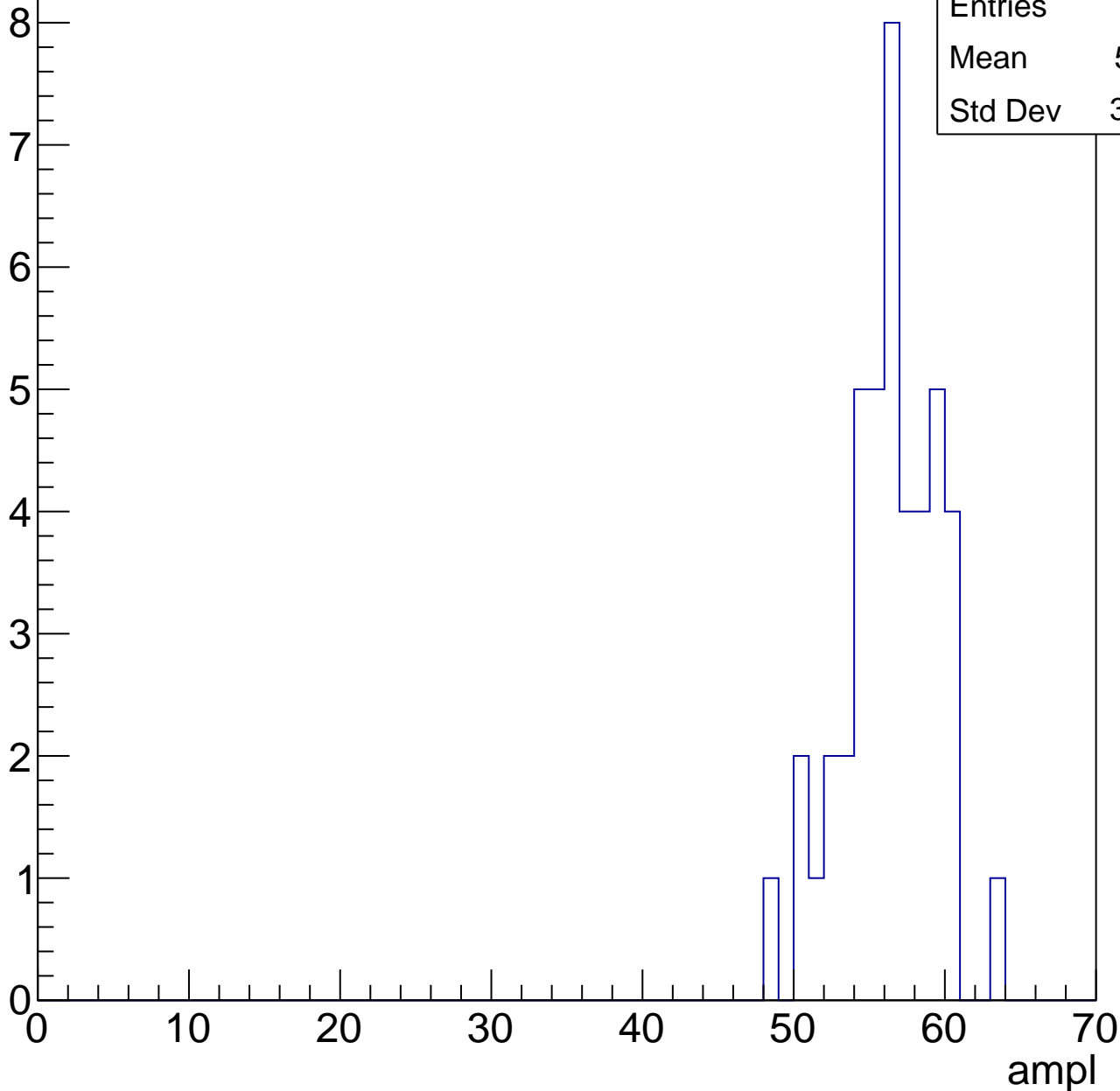


B1L103S, U10-ch64, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	55.91
Std Dev	3.066

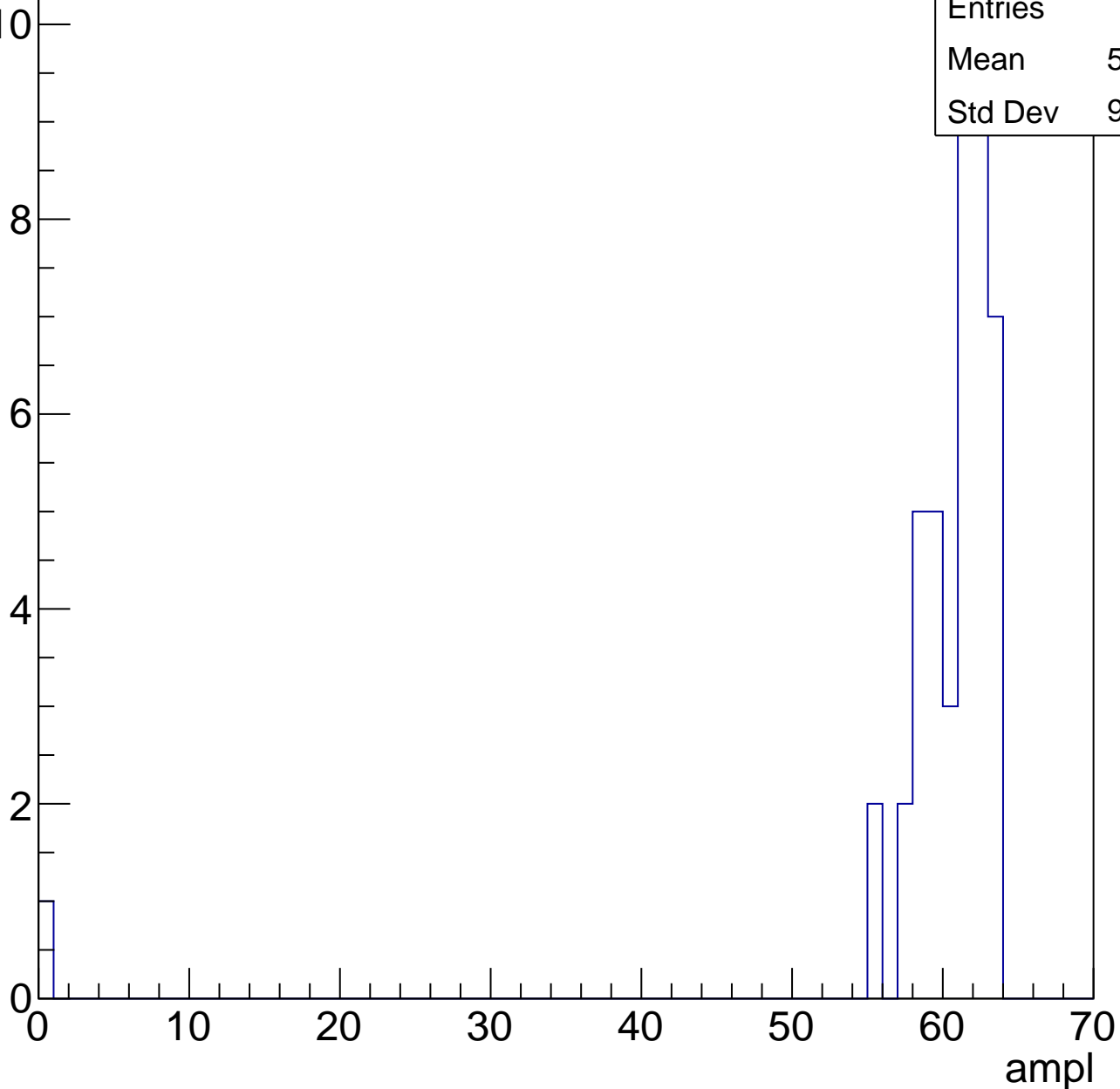


B1L103S, U10-ch64, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.05
Std Dev	9.247



B1L103S, U10-ch64, adc6

calib_packv5_042523_0143.root, FC#7, port C2

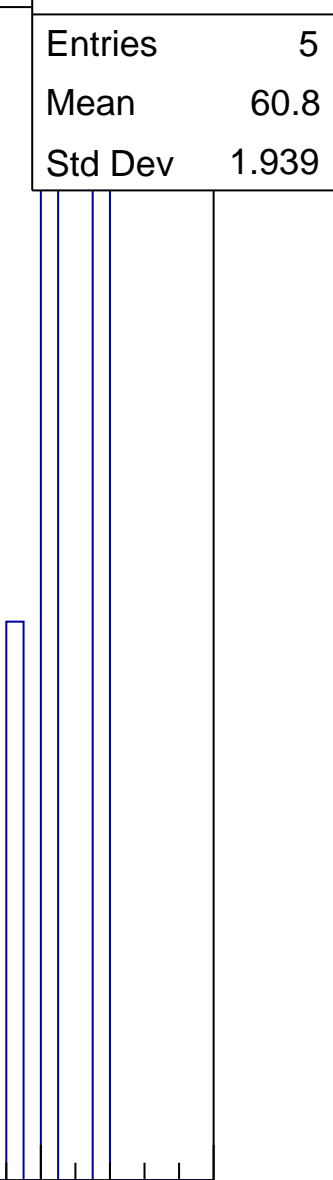
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.8
Std Dev	1.939

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch64, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch65, adc0

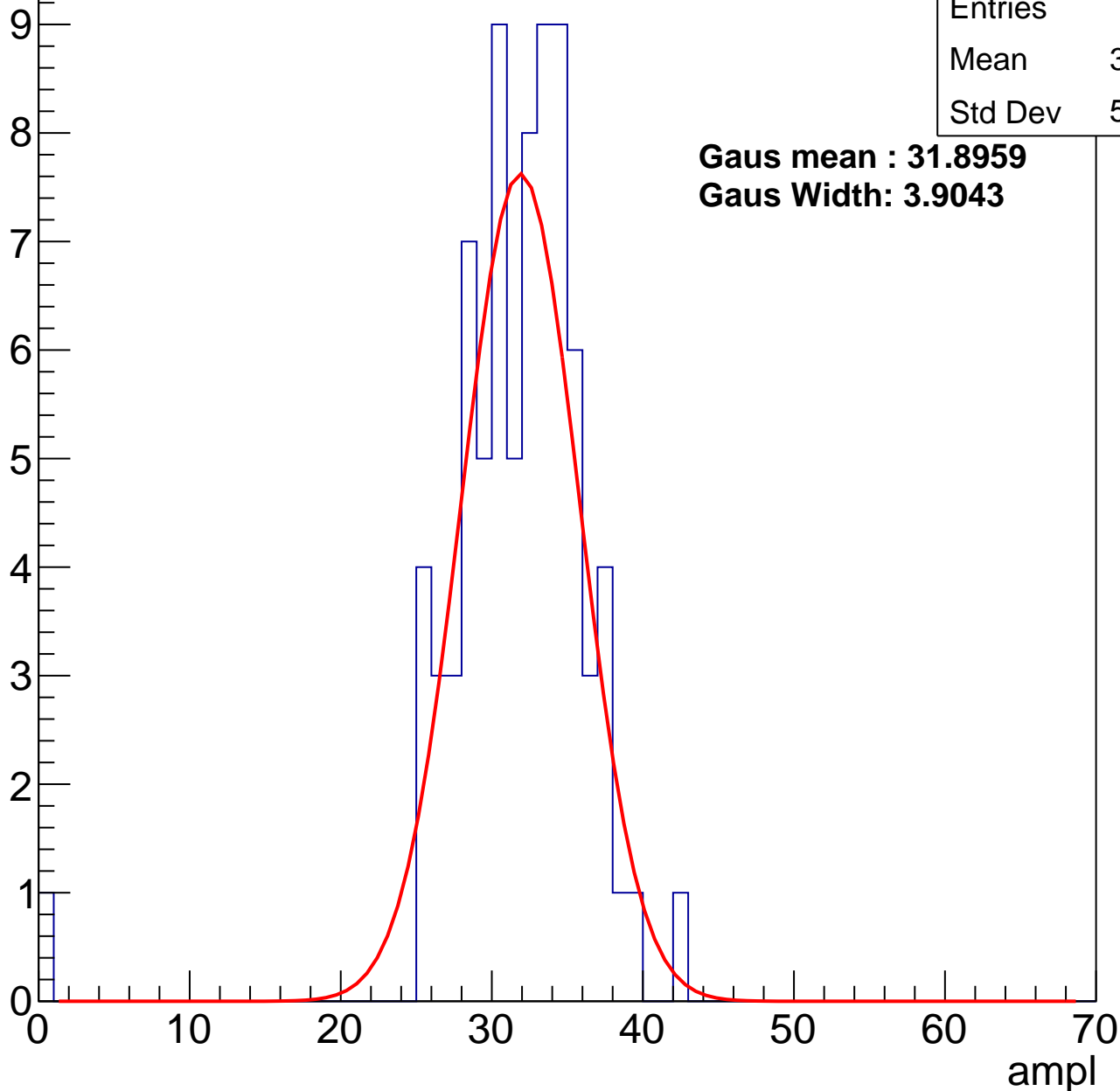
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	31.25
Std Dev	5.018

Gaus mean : 31.8959

Gaus Width: 3.9043



B1L103S, U10-ch65, adc1

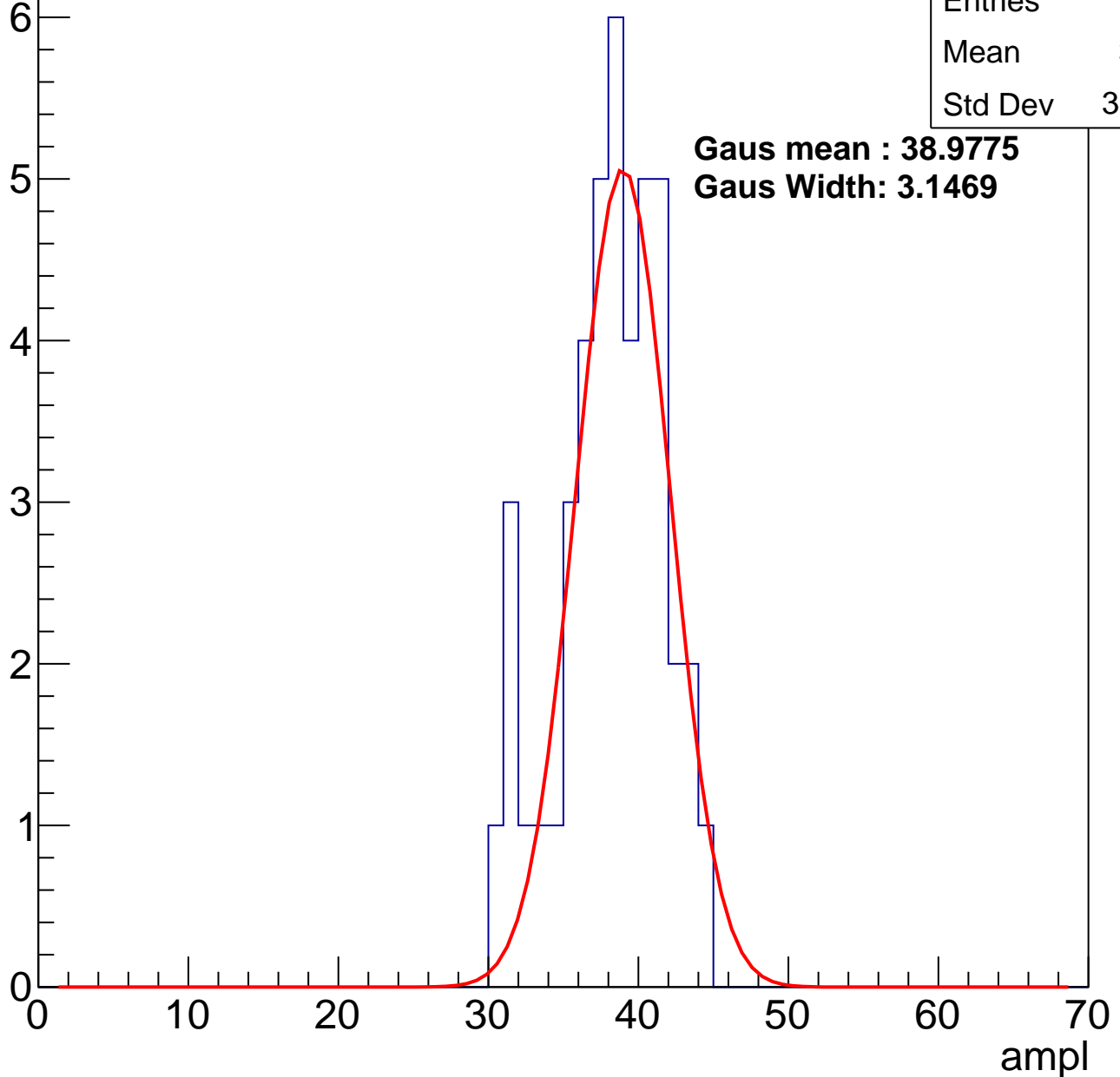
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	37.7
Std Dev	3.435

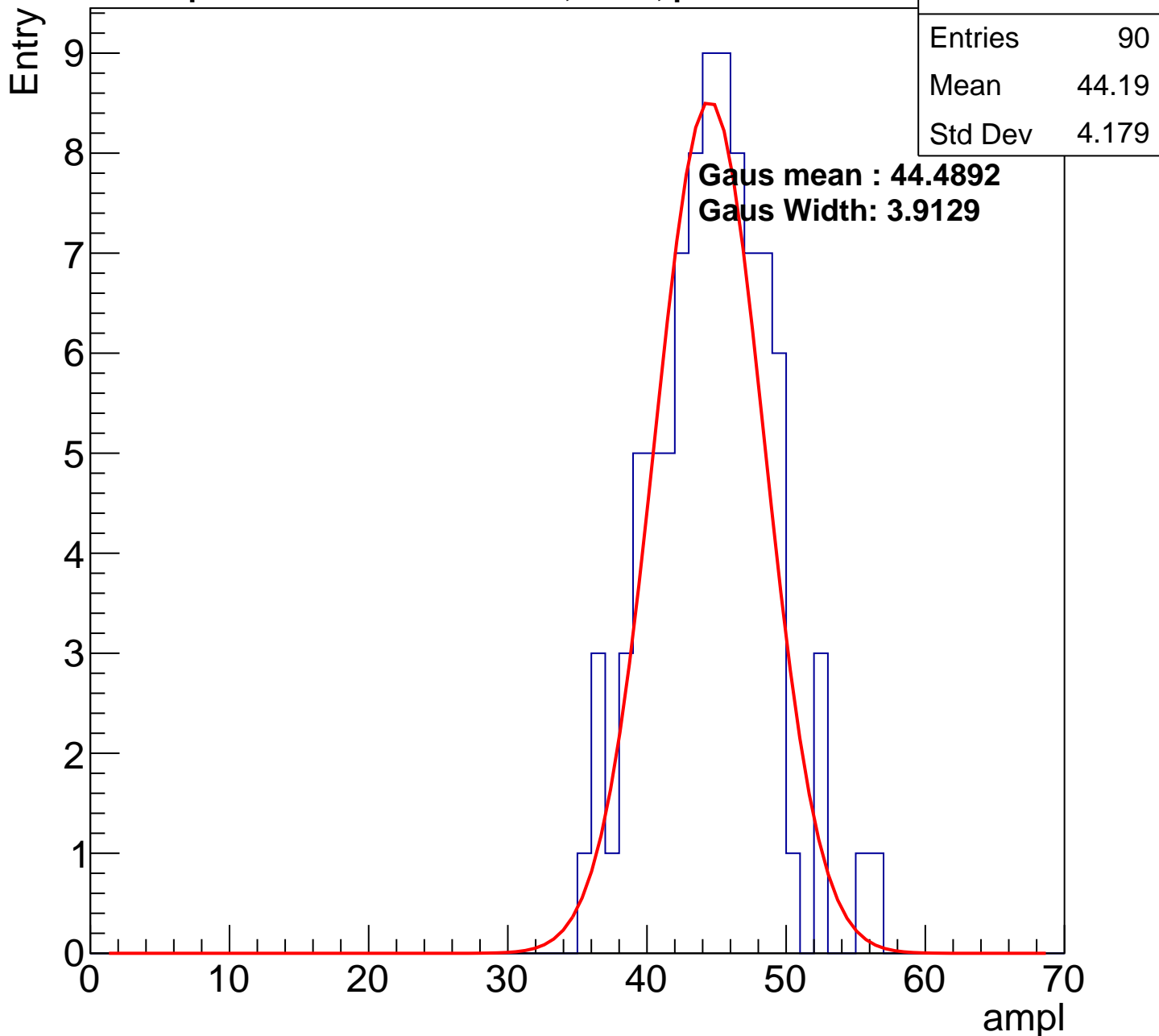
Gaus mean : 38.9775

Gaus Width: 3.1469



B1L103S, U10-ch65, adc2

calib_packv5_042523_0143.root, FC#7, port C2

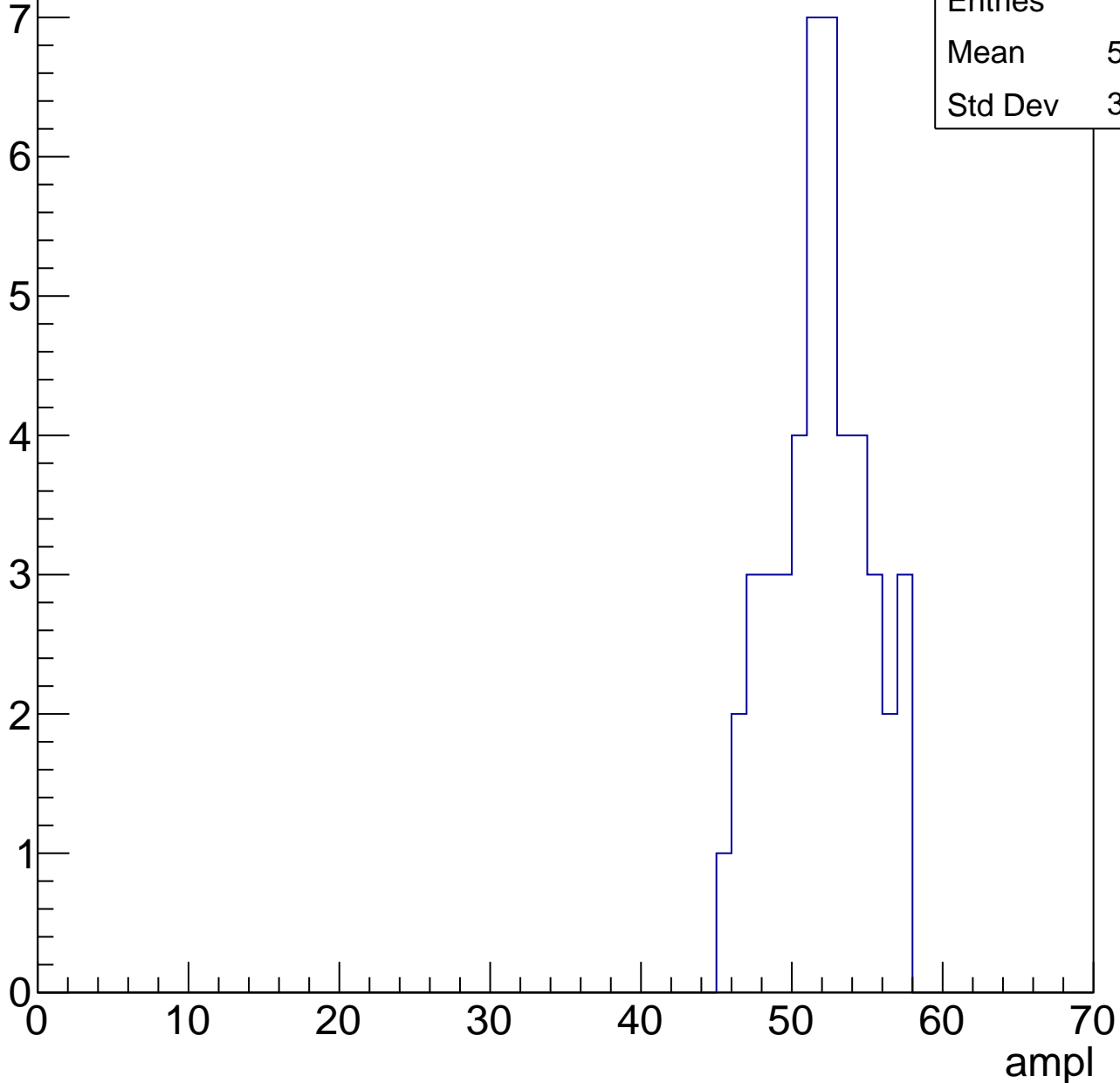


B1L103S, U10-ch65, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	51.43
Std Dev	3.069

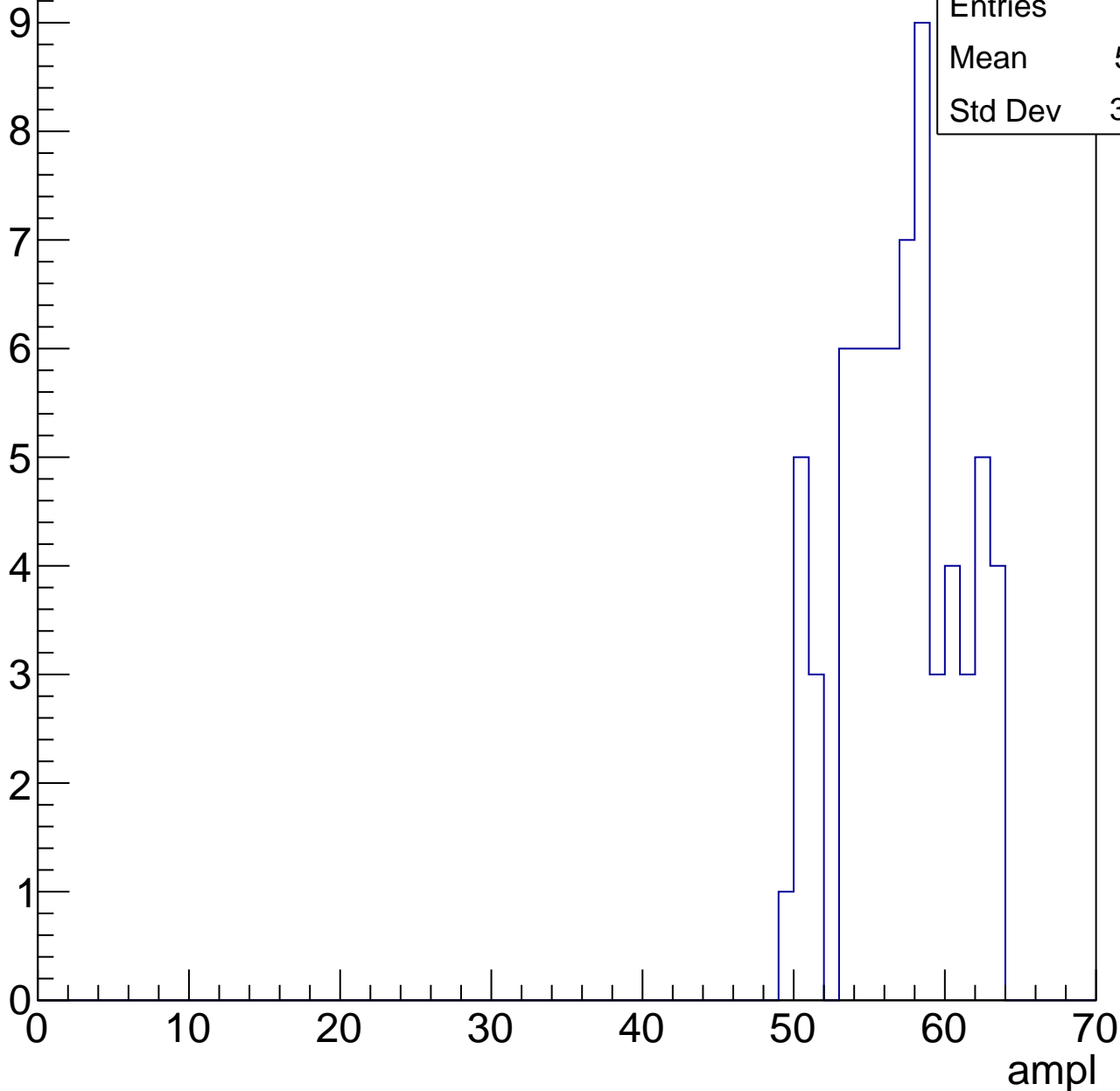


B1L103S, U10-ch65, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	56.51
Std Dev	3.748

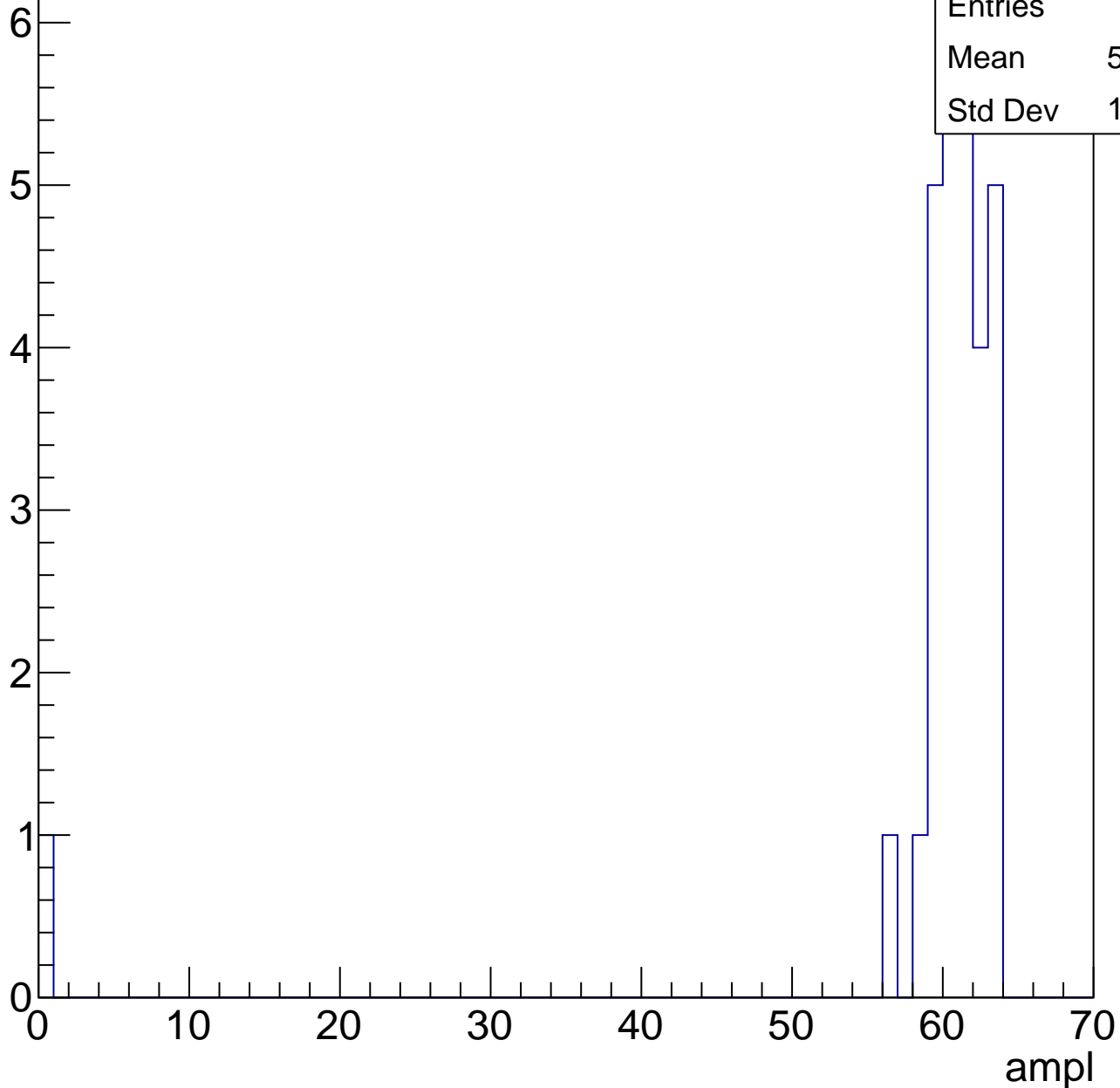


B1L103S, U10-ch65, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

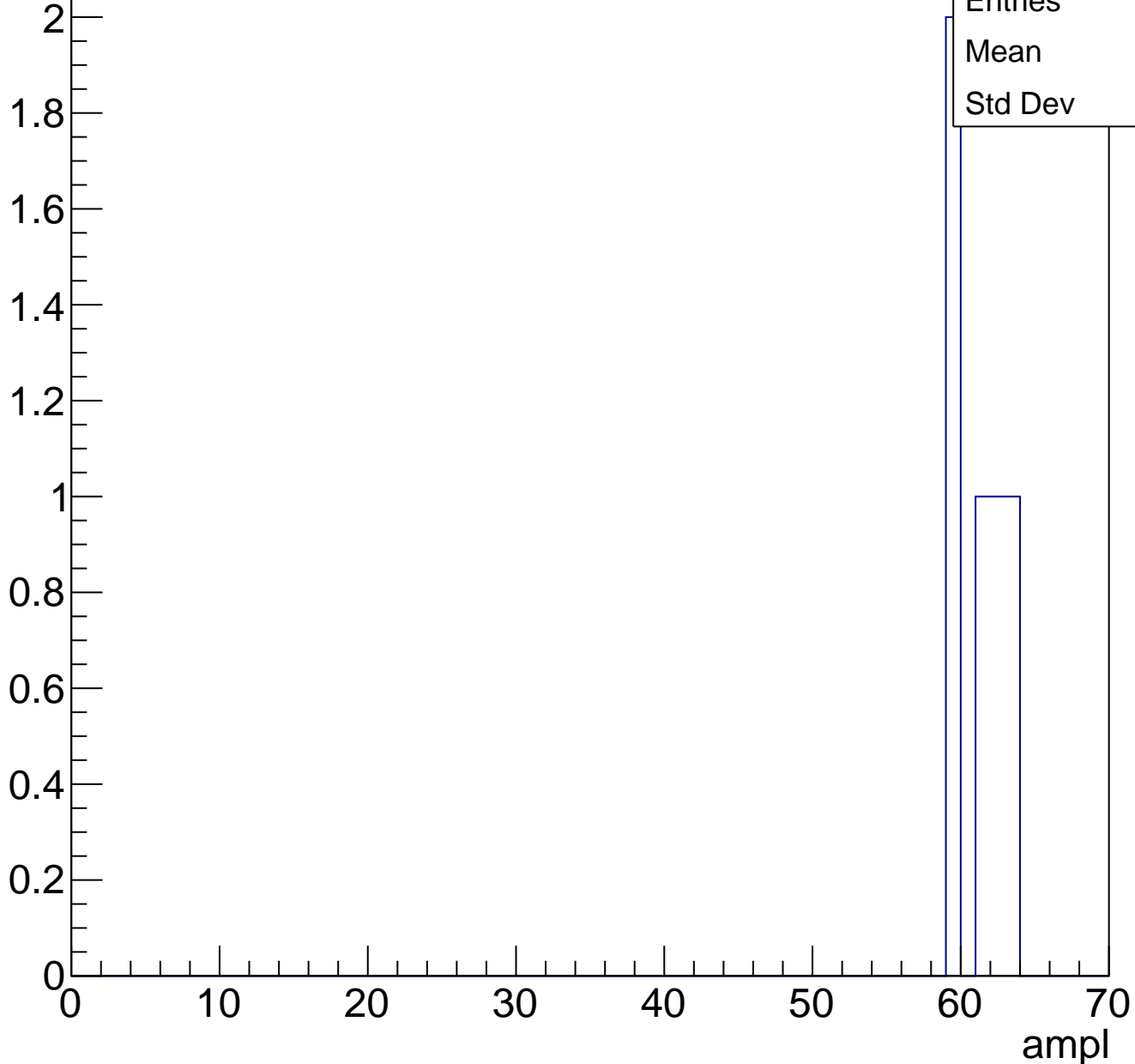
Entries	29
Mean	58.55
Std Dev	11.19



B1L103S, U10-ch65, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch65, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U10-ch66, adc0

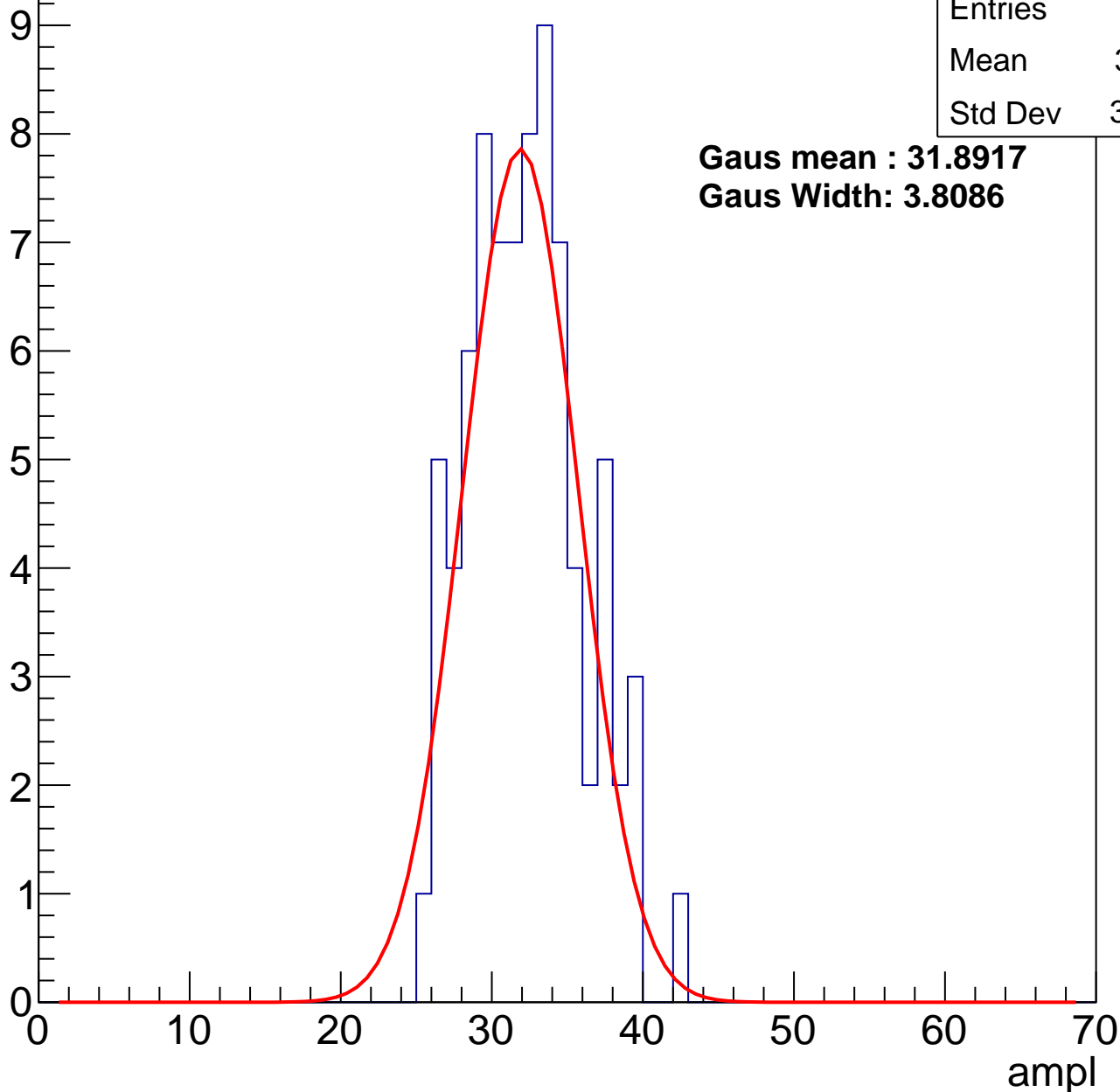
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	31.81
Std Dev	3.694

Gaus mean : 31.8917

Gaus Width: 3.8086



B1L103S, U10-ch66, adc1

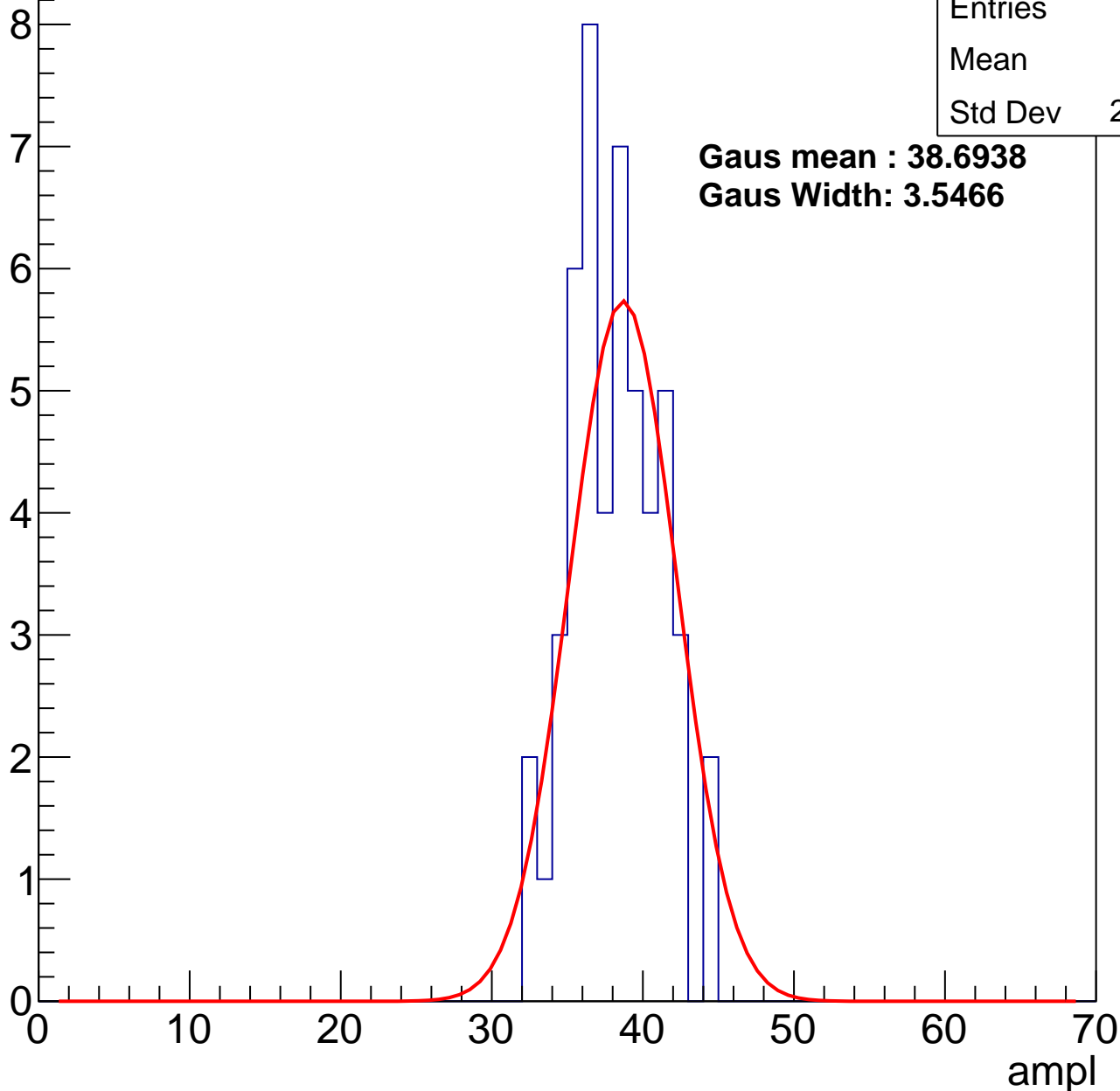
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	37.7
Std Dev	2.886

Gaus mean : 38.6938

Gaus Width: 3.5466



B1L103S, U10-ch66, adc2

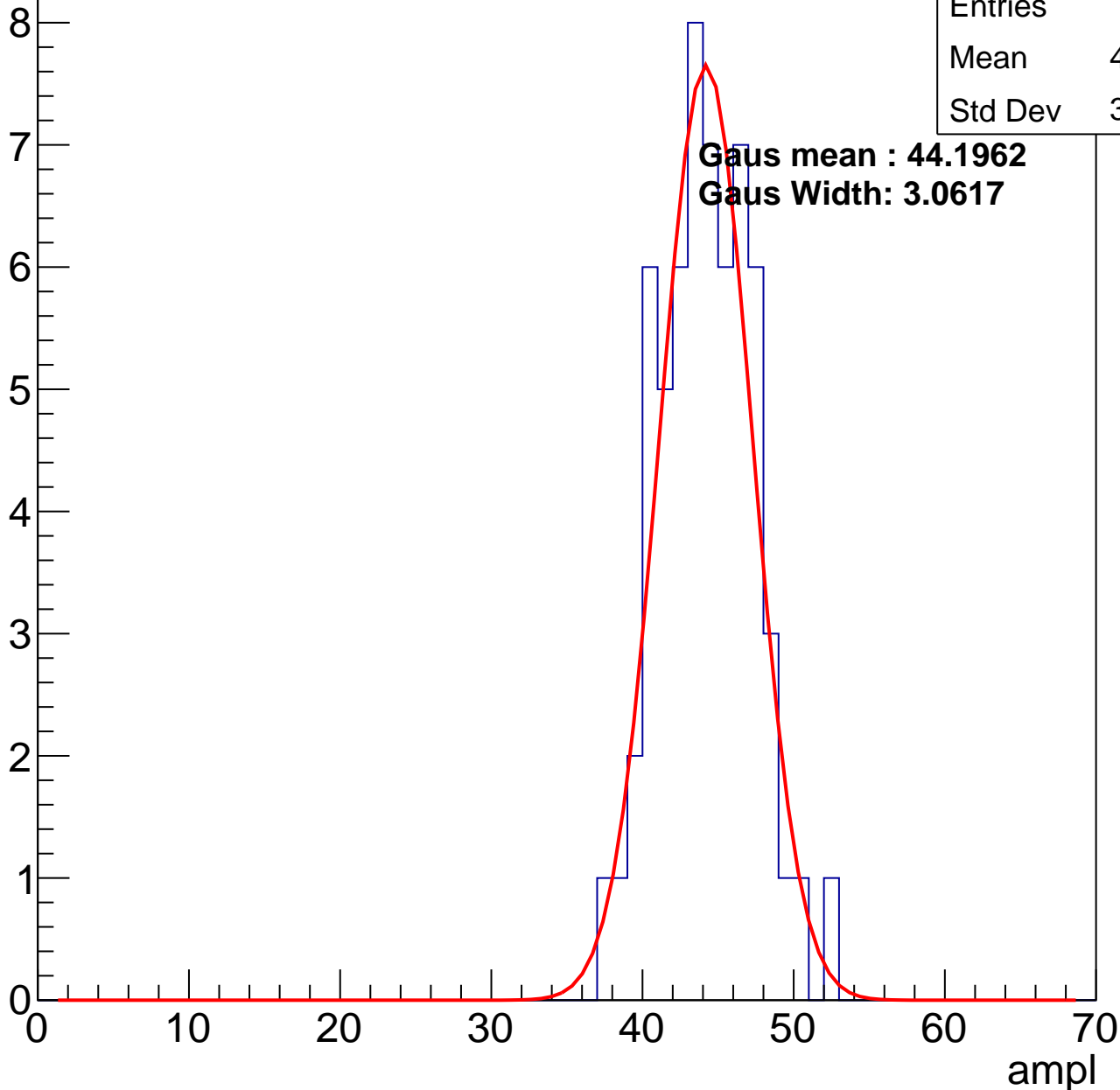
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	43.79
Std Dev	3.047

Gaus mean : 44.1962

Gaus Width: 3.0617

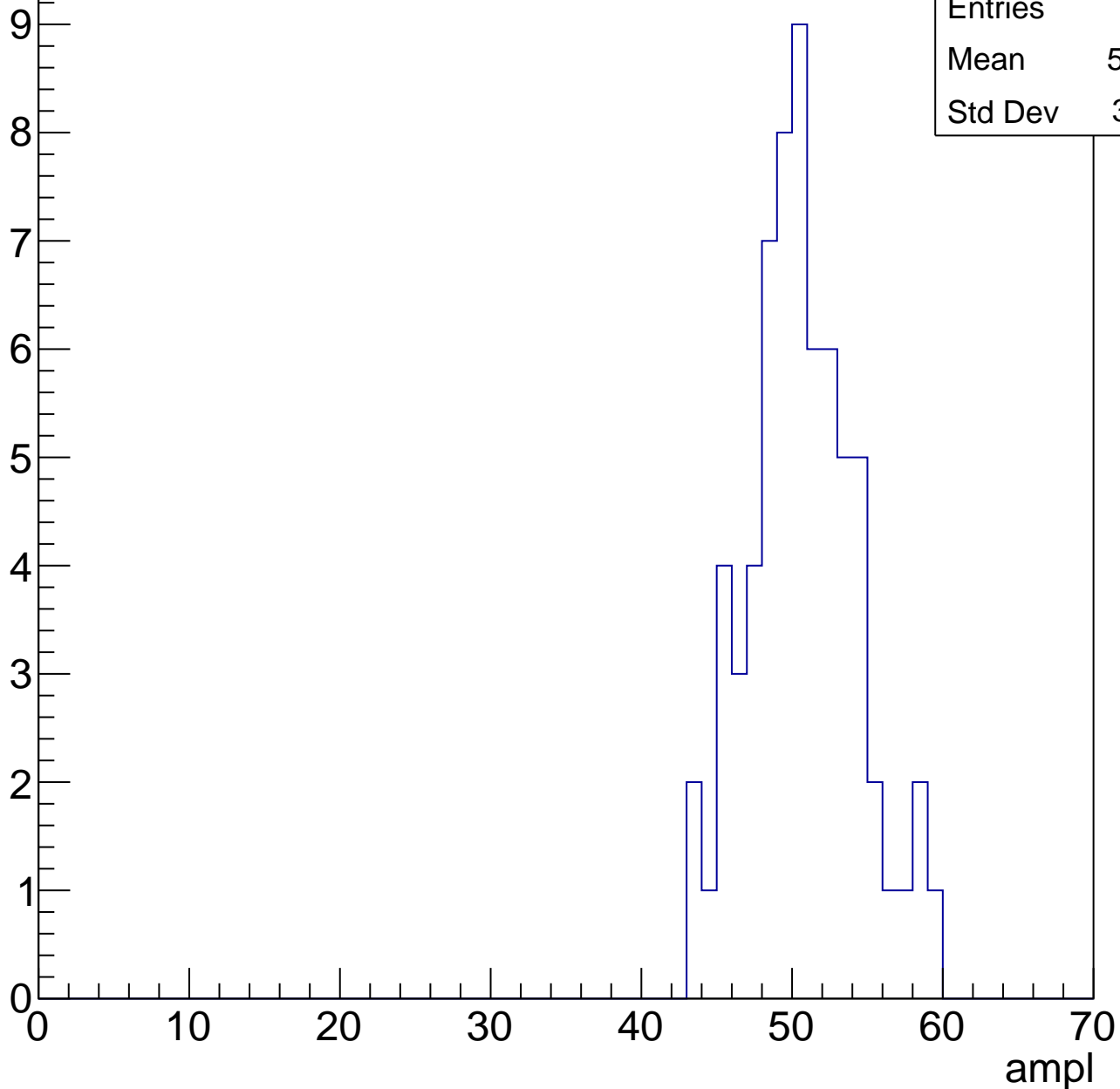


B1L103S, U10-ch66, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

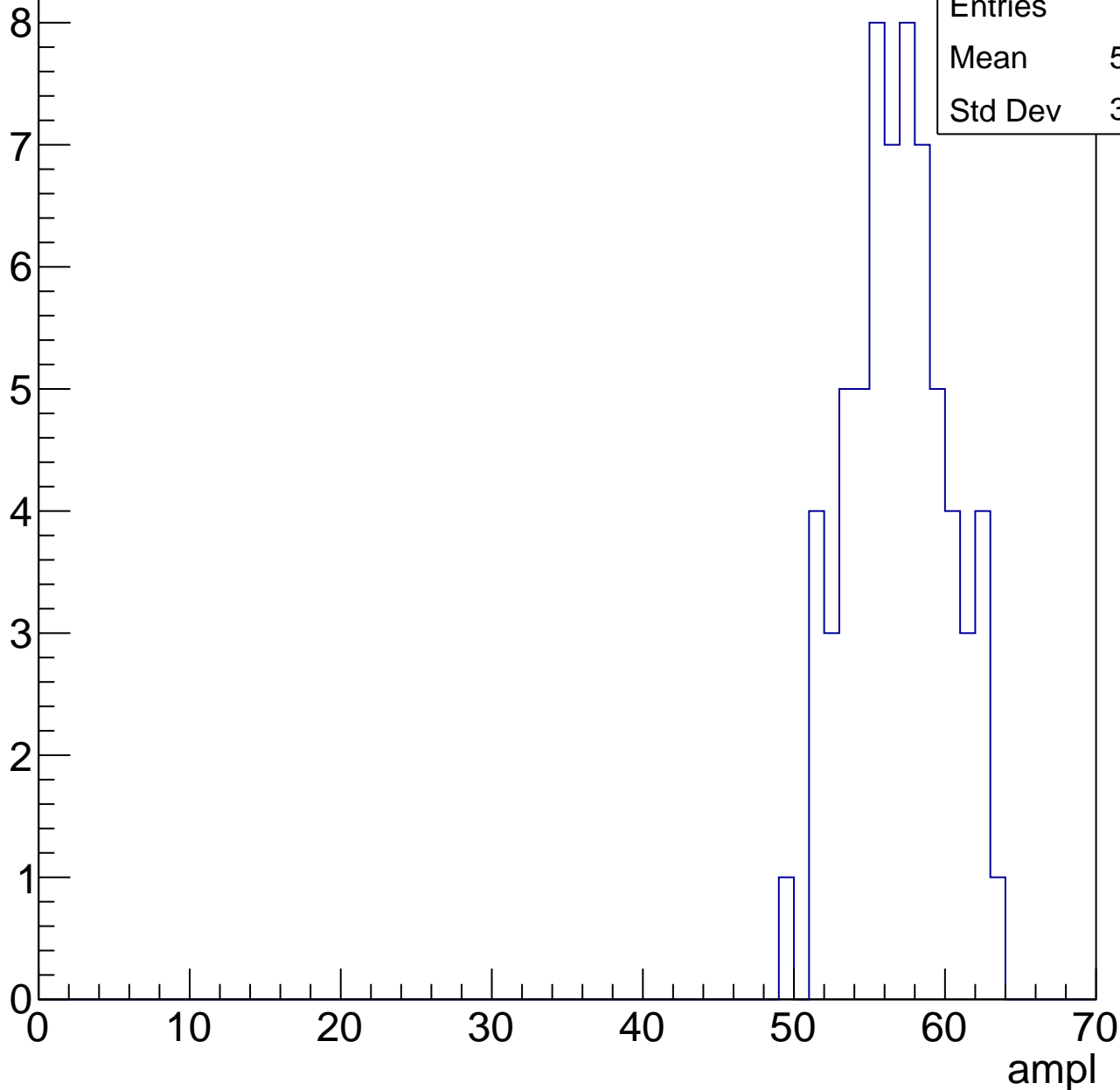
Entries	67
Mean	50.22
Std Dev	3.561



B1L103S, U10-ch66, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

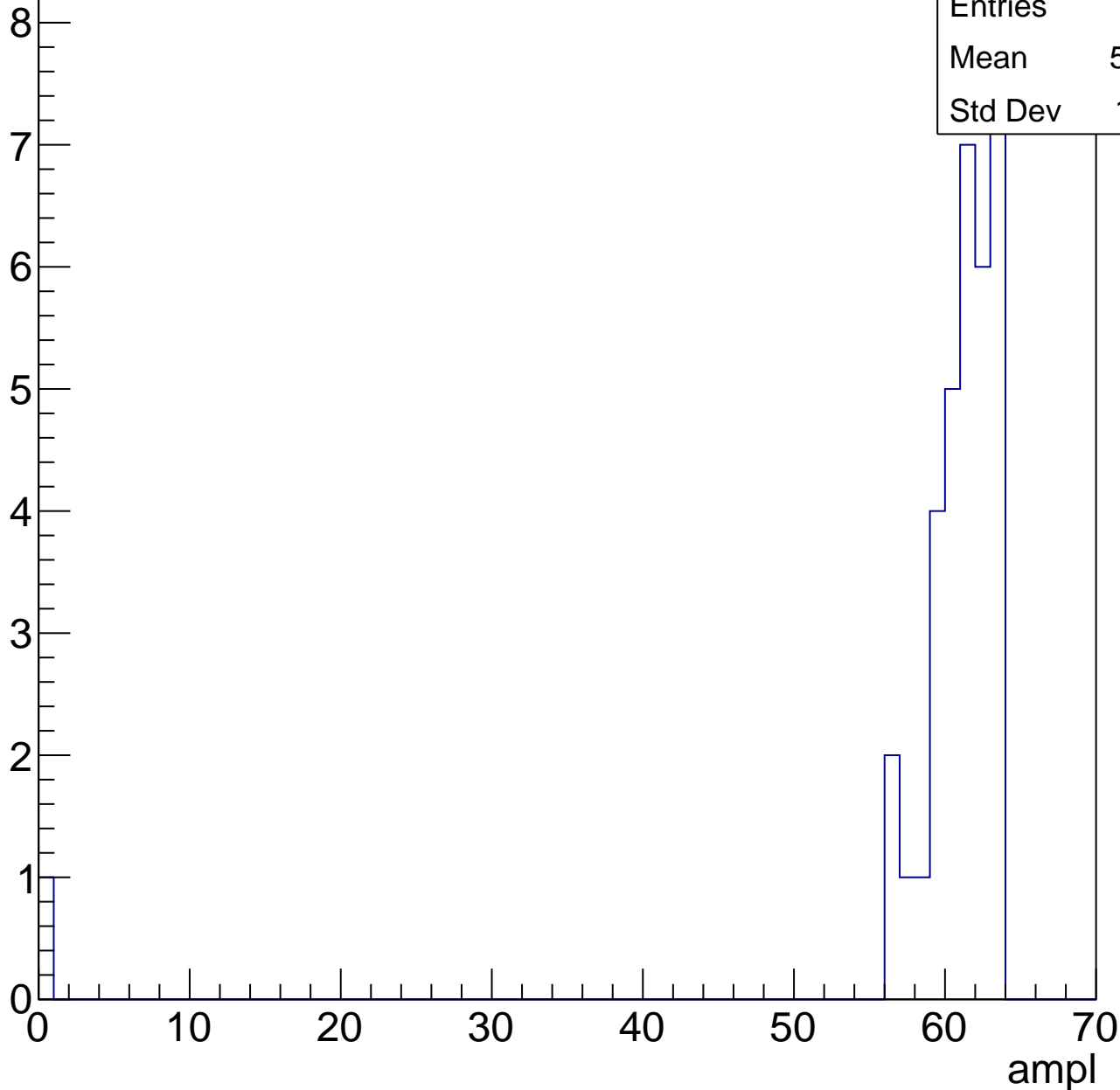


B1L103S, U10-ch66, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	35
Mean	59.03
Std Dev	10.31



B1L103S, U10-ch66, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch66, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch67, adc0

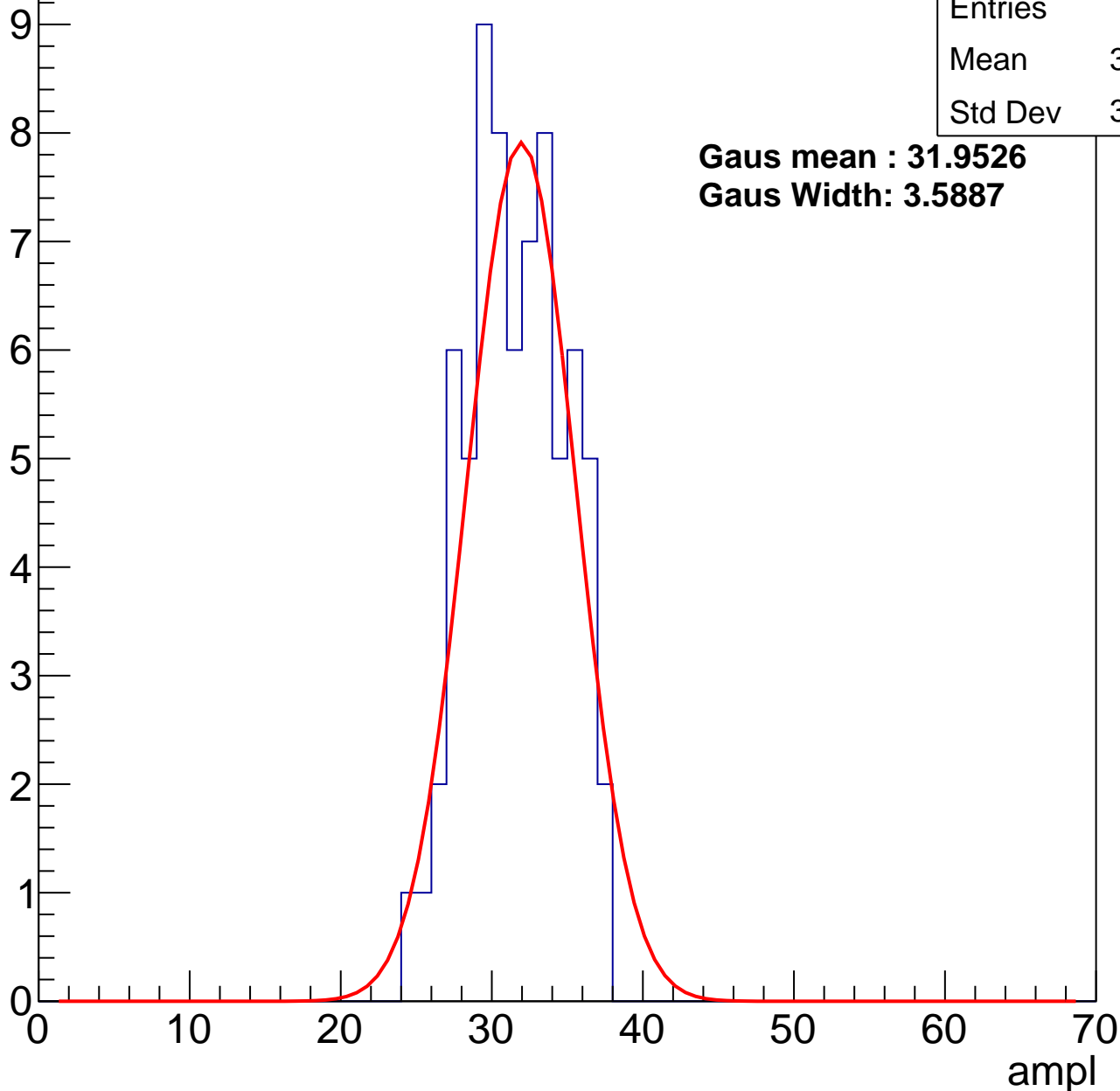
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	31.15
Std Dev	3.129

Gaus mean : 31.9526

Gaus Width: 3.5887



B1L103S, U10-ch67, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	38.3
Std Dev	3.356

Gaus mean : 38.9671

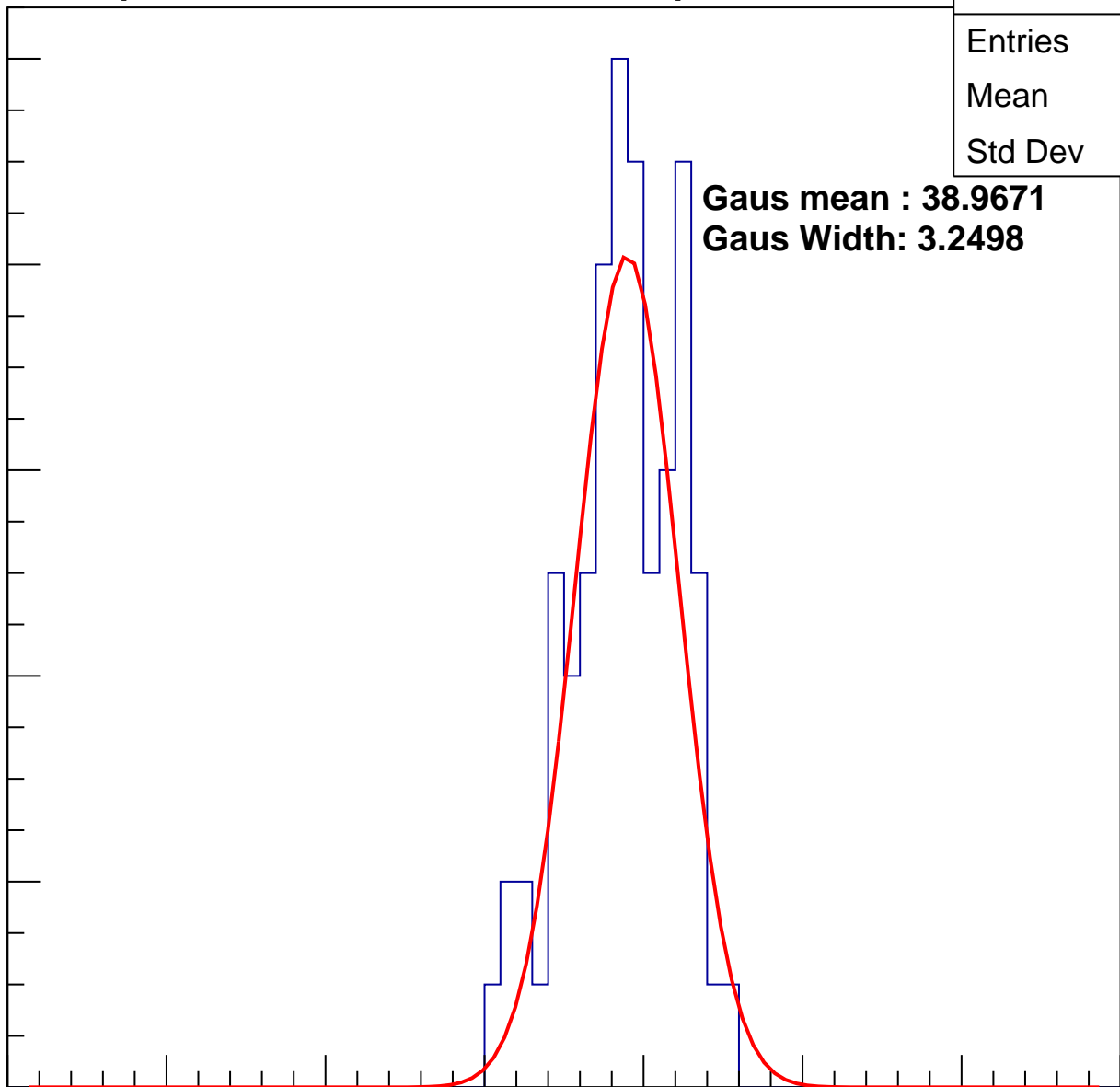
Gaus Width: 3.2498

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch67, adc2

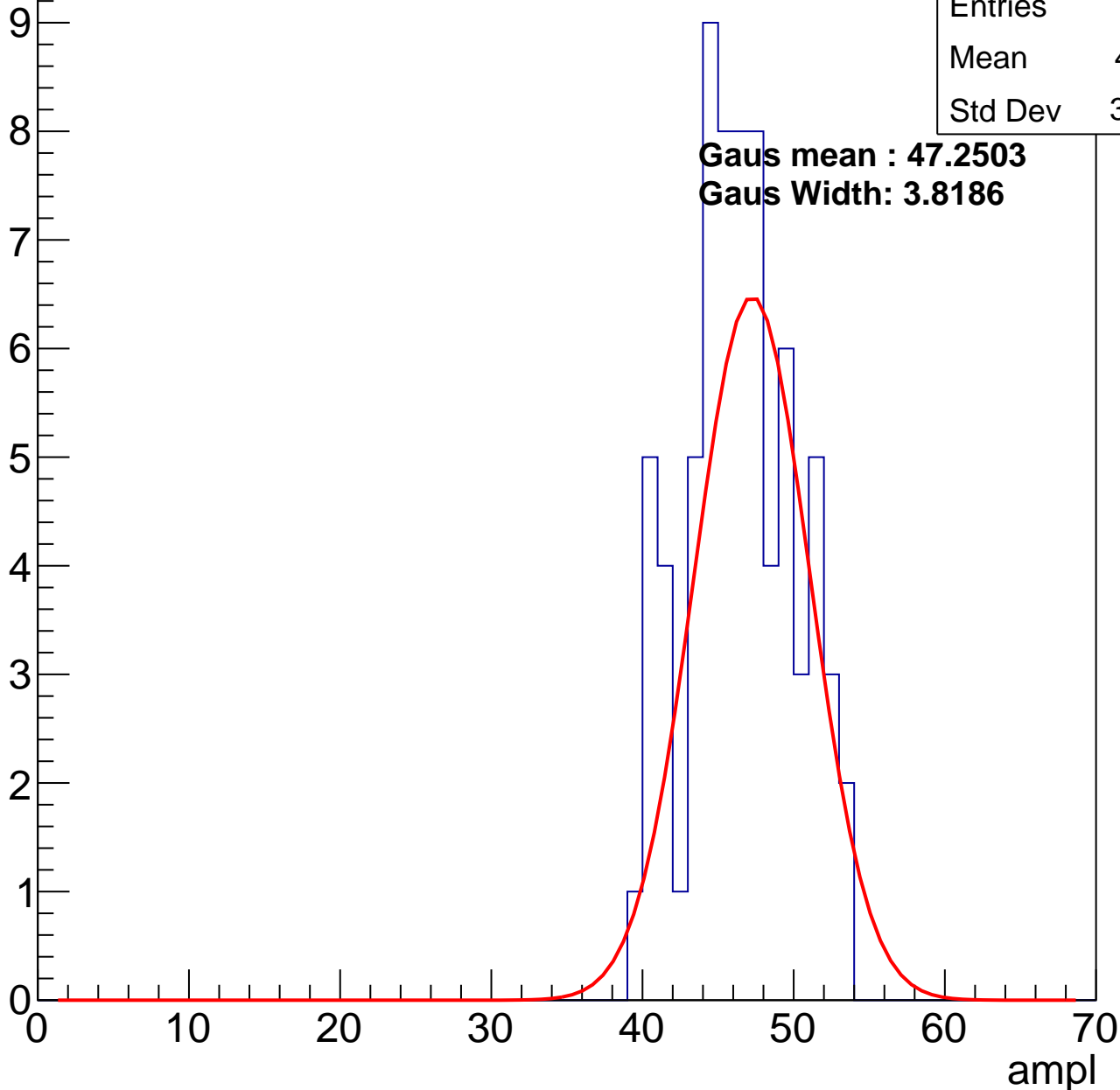
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	46.01
Std Dev	3.518

Gaus mean : 47.2503

Gaus Width: 3.8186

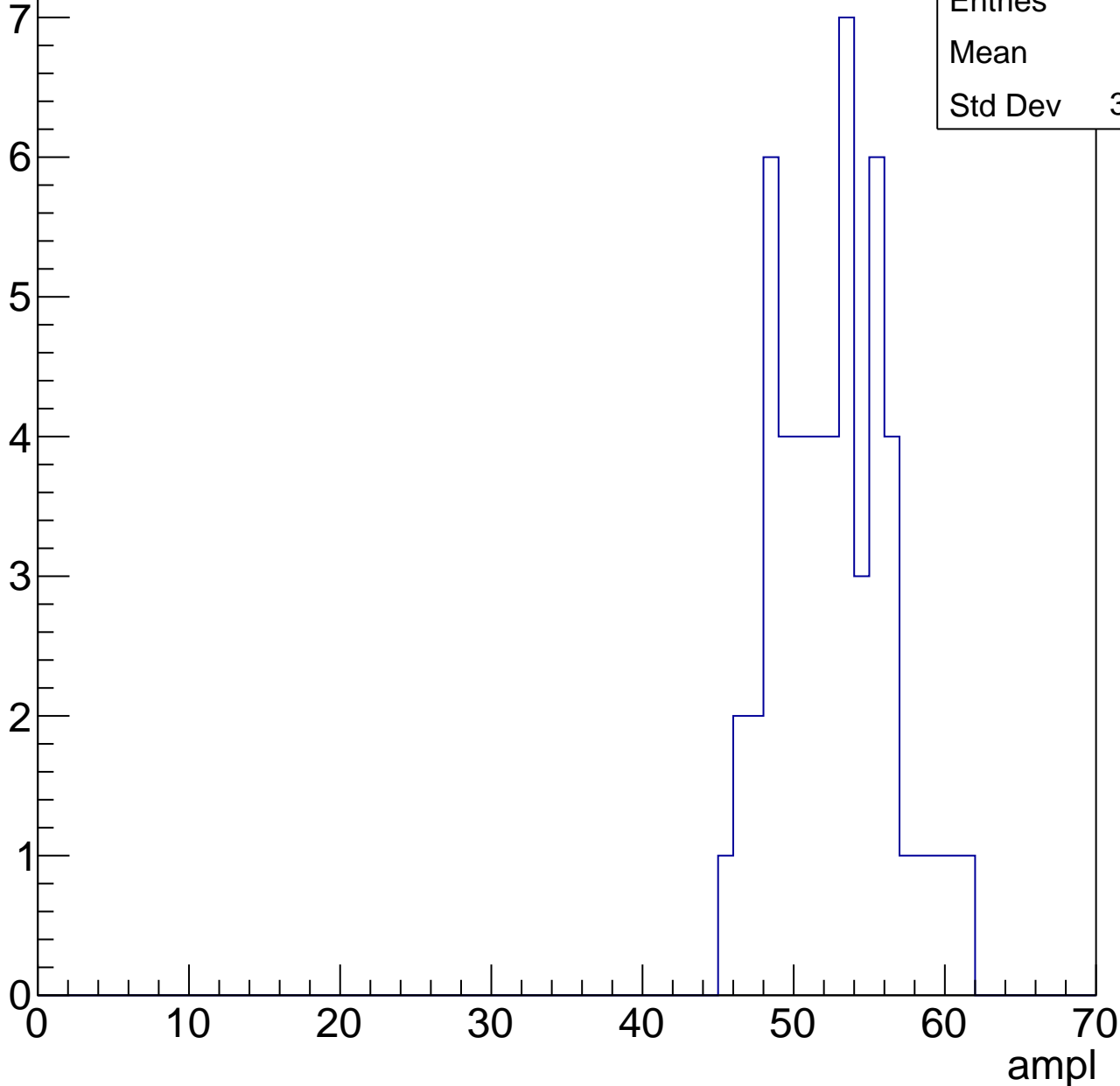


B1L103S, U10-ch67, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	52.1
Std Dev	3.707

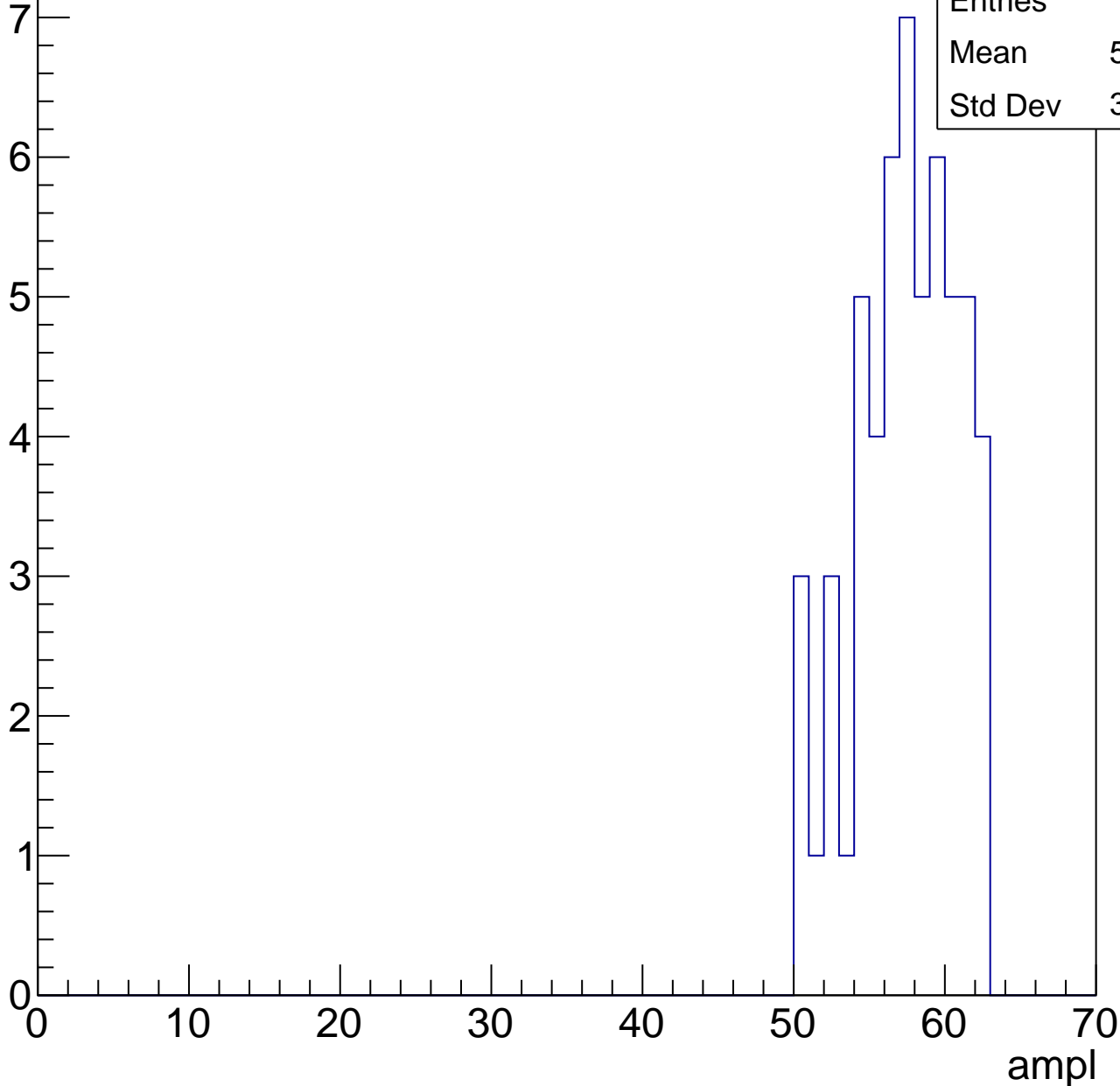


B1L103S, U10-ch67, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.95
Std Dev	3.289

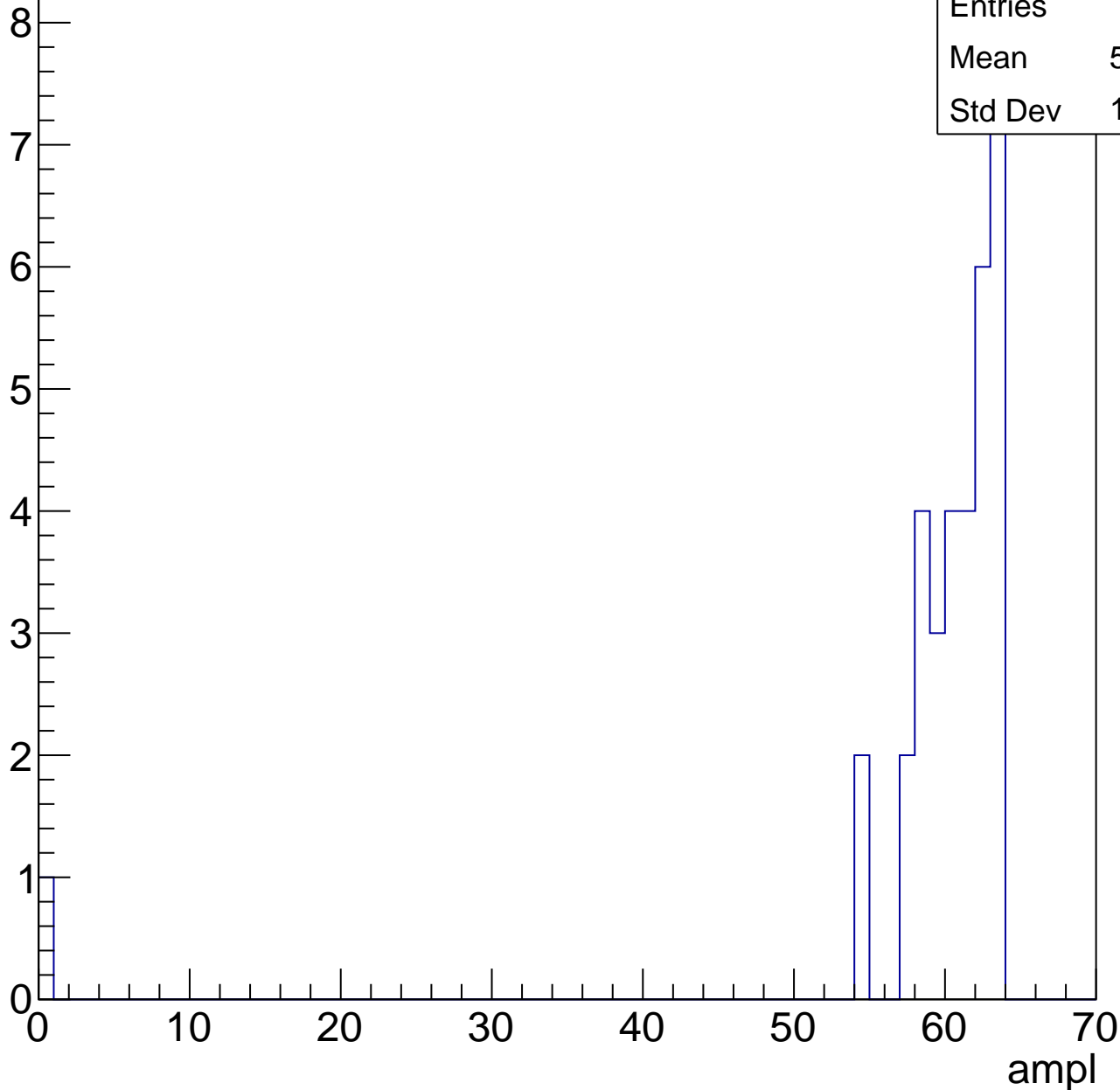


B1L103S, U10-ch67, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.56
Std Dev	10.49



B1L103S, U10-ch67, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	63
Std Dev	0

0 10 20 30 40 50 60 70

ampl

B1L103S, U10-ch67, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch68, adc0

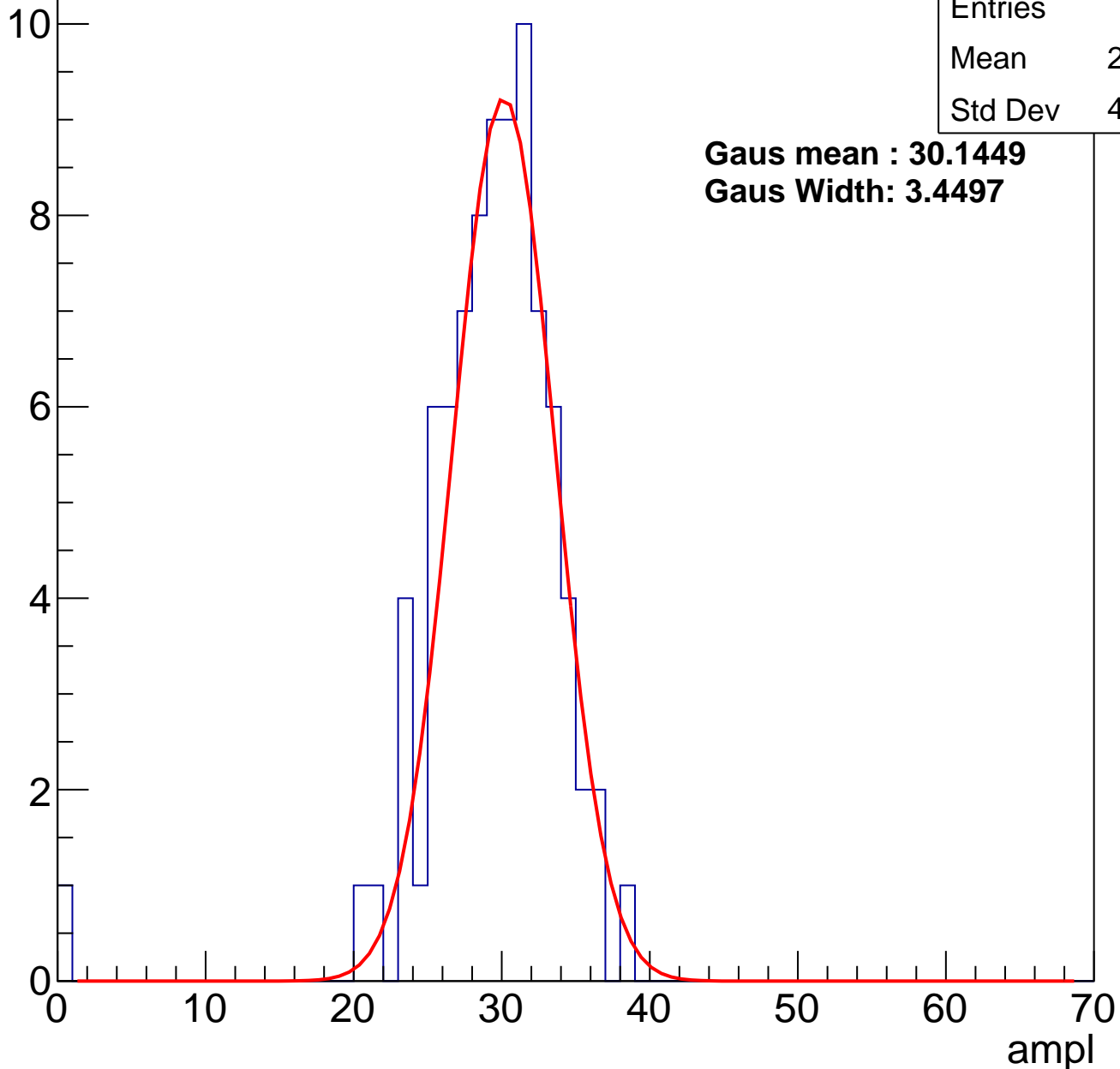
calib_packv5_042523_0143.root, FC#7, port C2

Entries	85
Mean	28.88
Std Dev	4.726

Gaus mean : 30.1449

Gaus Width: 3.4497

Entry



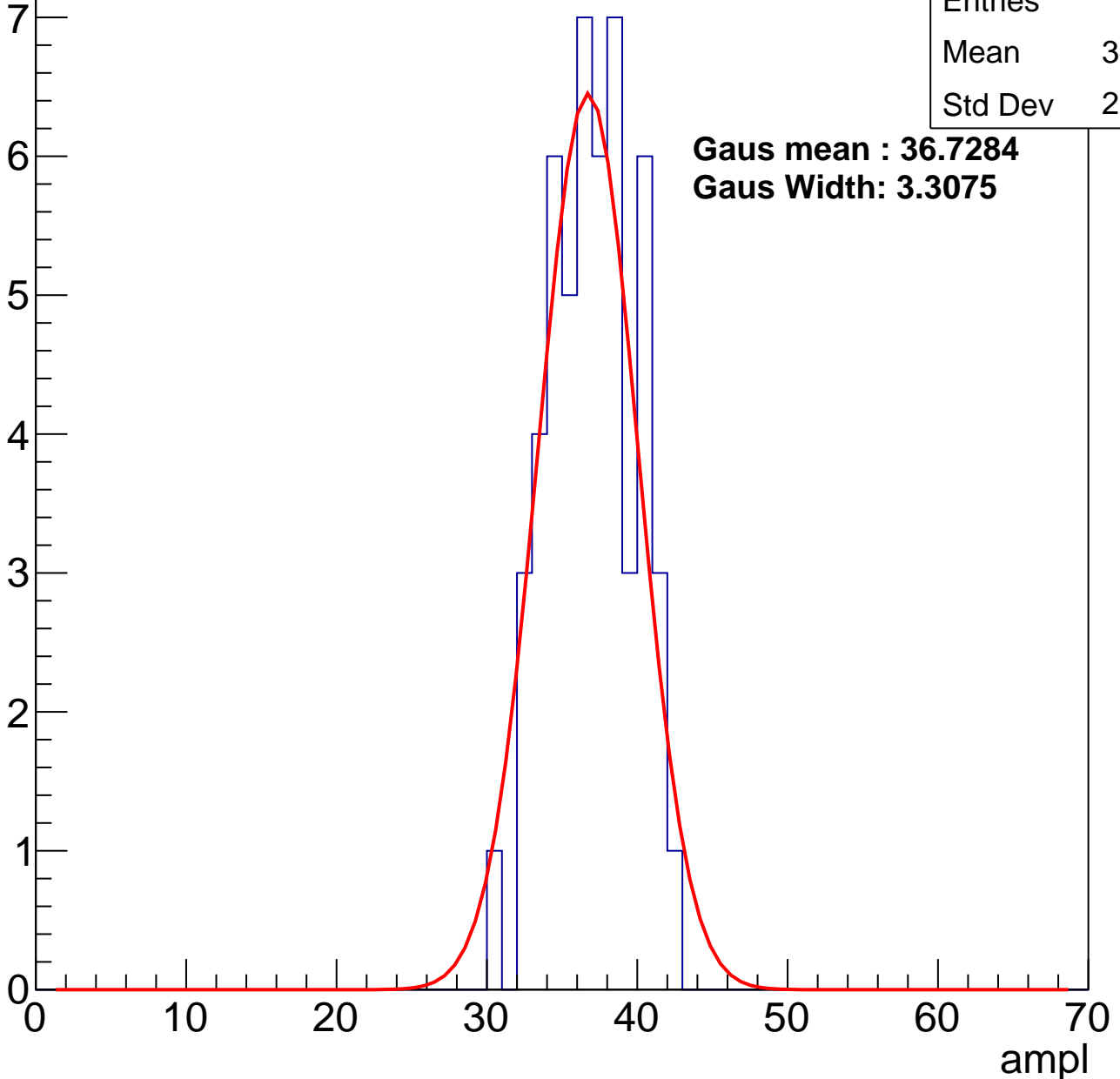
B1L103S, U10-ch68, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	36.52
Std Dev	2.784

Gaus mean : 36.7284
Gaus Width: 3.3075



B1L103S, U10-ch68, adc2

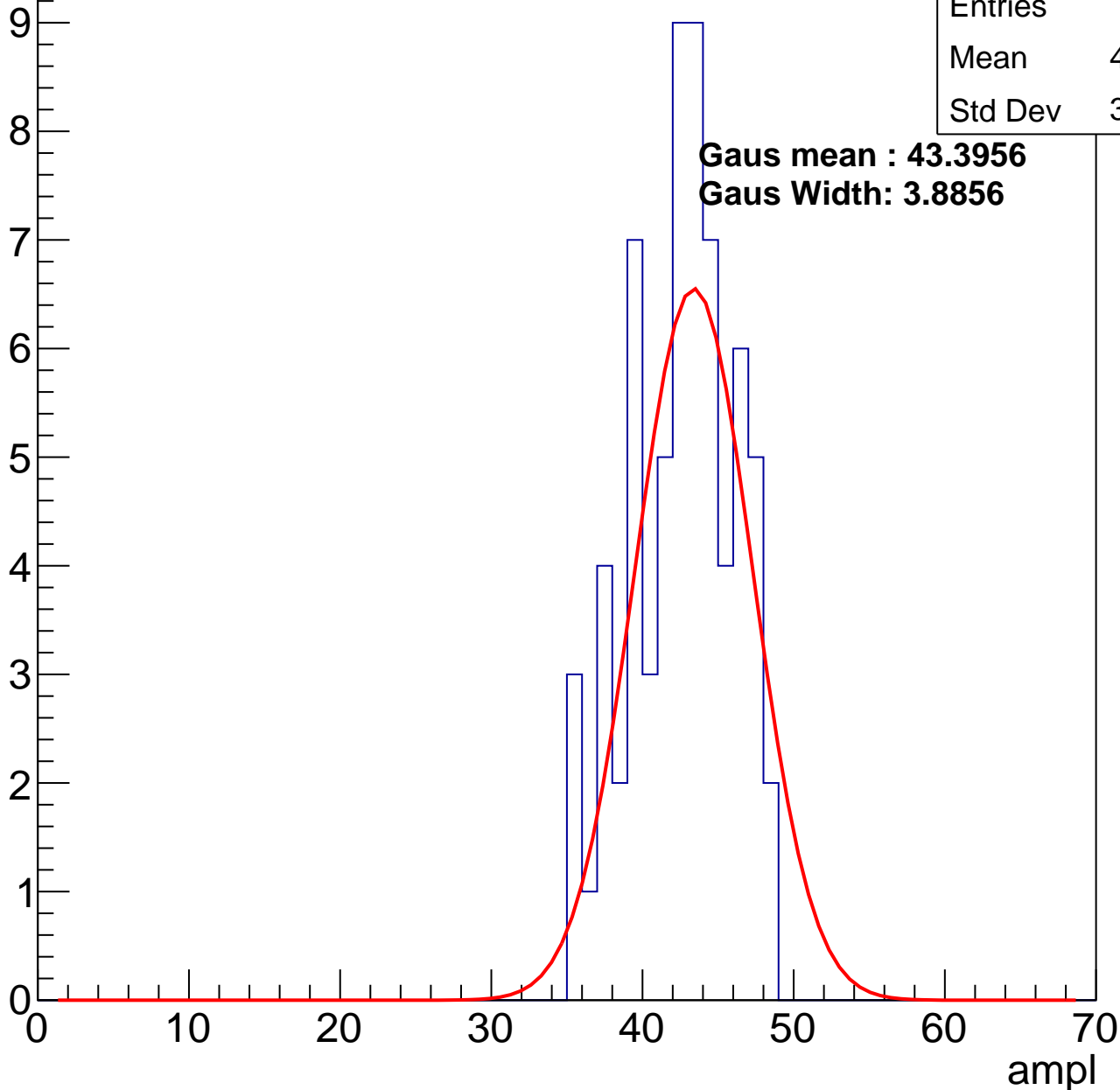
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.13
Std Dev	3.368

Gaus mean : 43.3956

Gaus Width: 3.8856

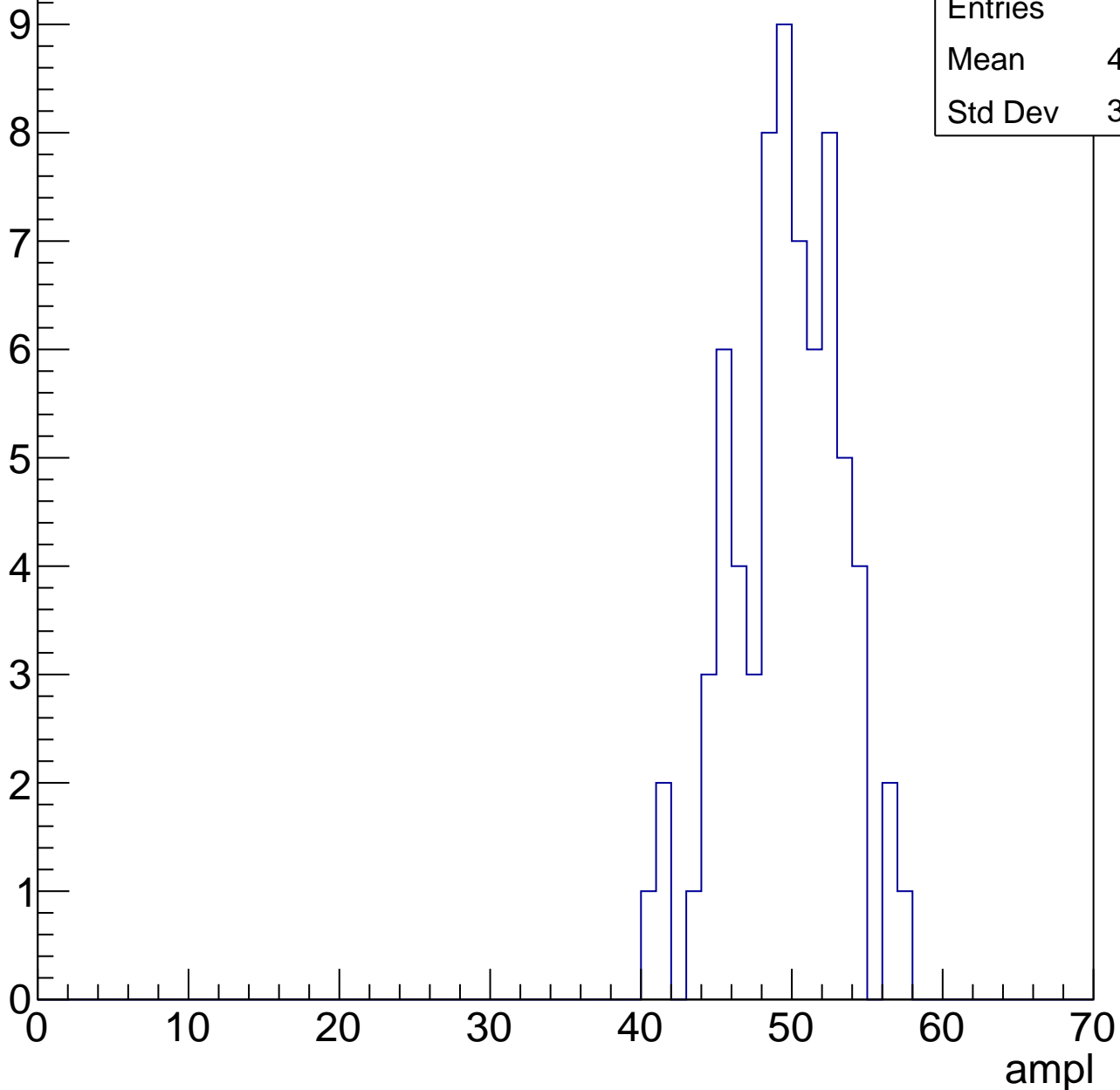


B1L103S, U10-ch68, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	49.13
Std Dev	3.617

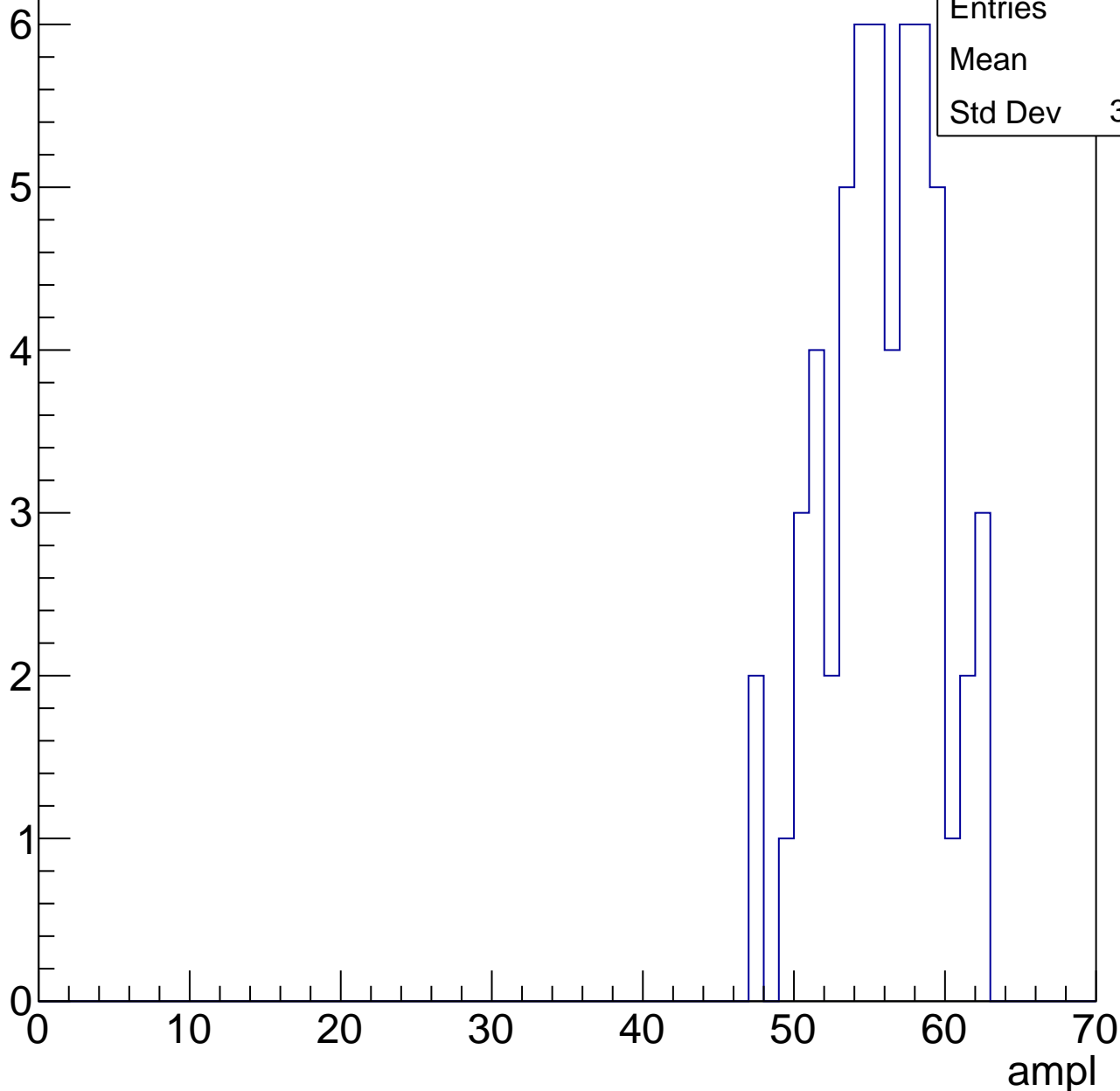


B1L103S, U10-ch68, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

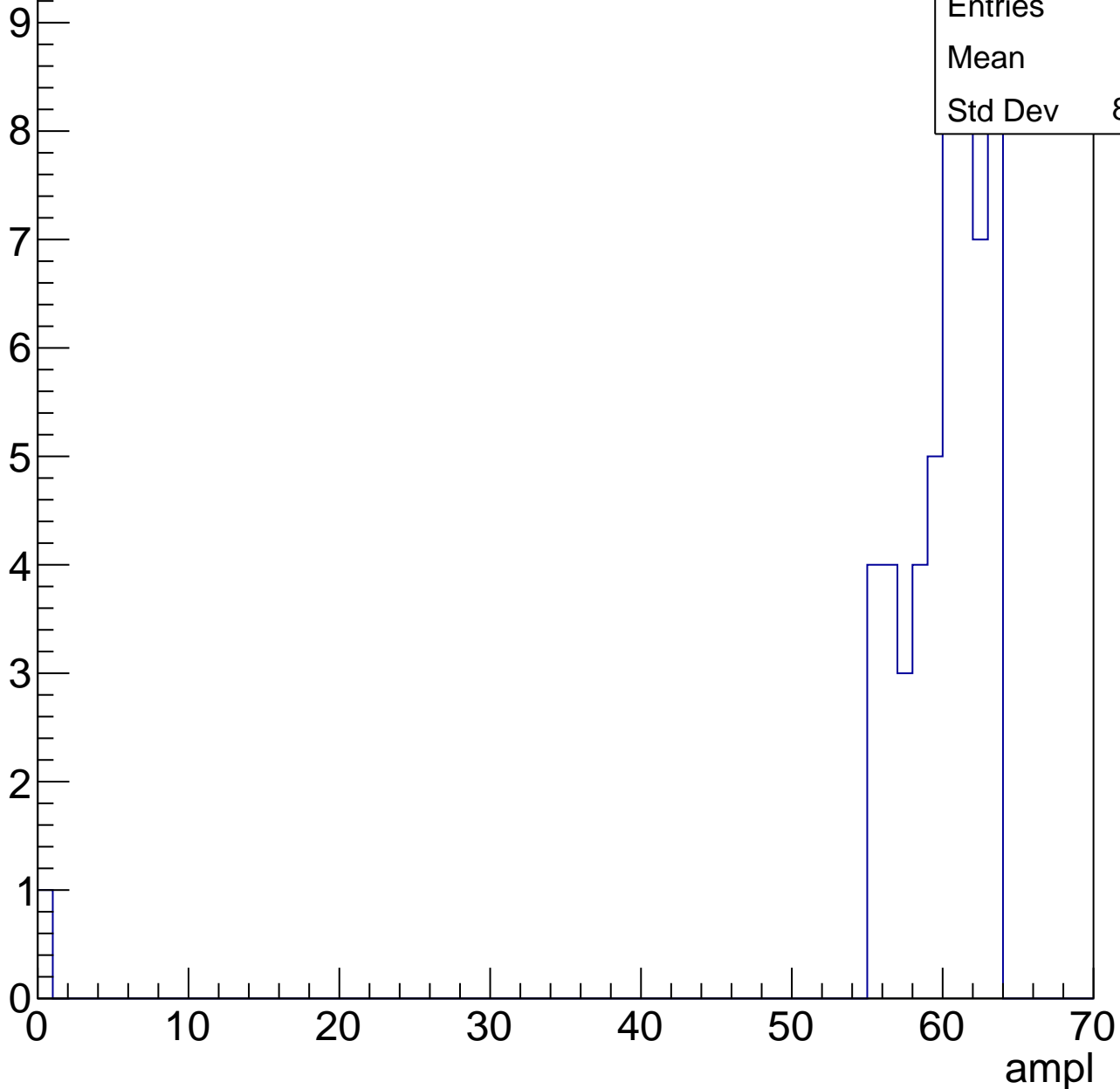
Entries	56
Mean	55.3
Std Dev	3.654



B1L103S, U10-ch68, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch68, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch68, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch69, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	72
Mean	29.53
Std Dev	5.011

Gaus mean : 30.5579

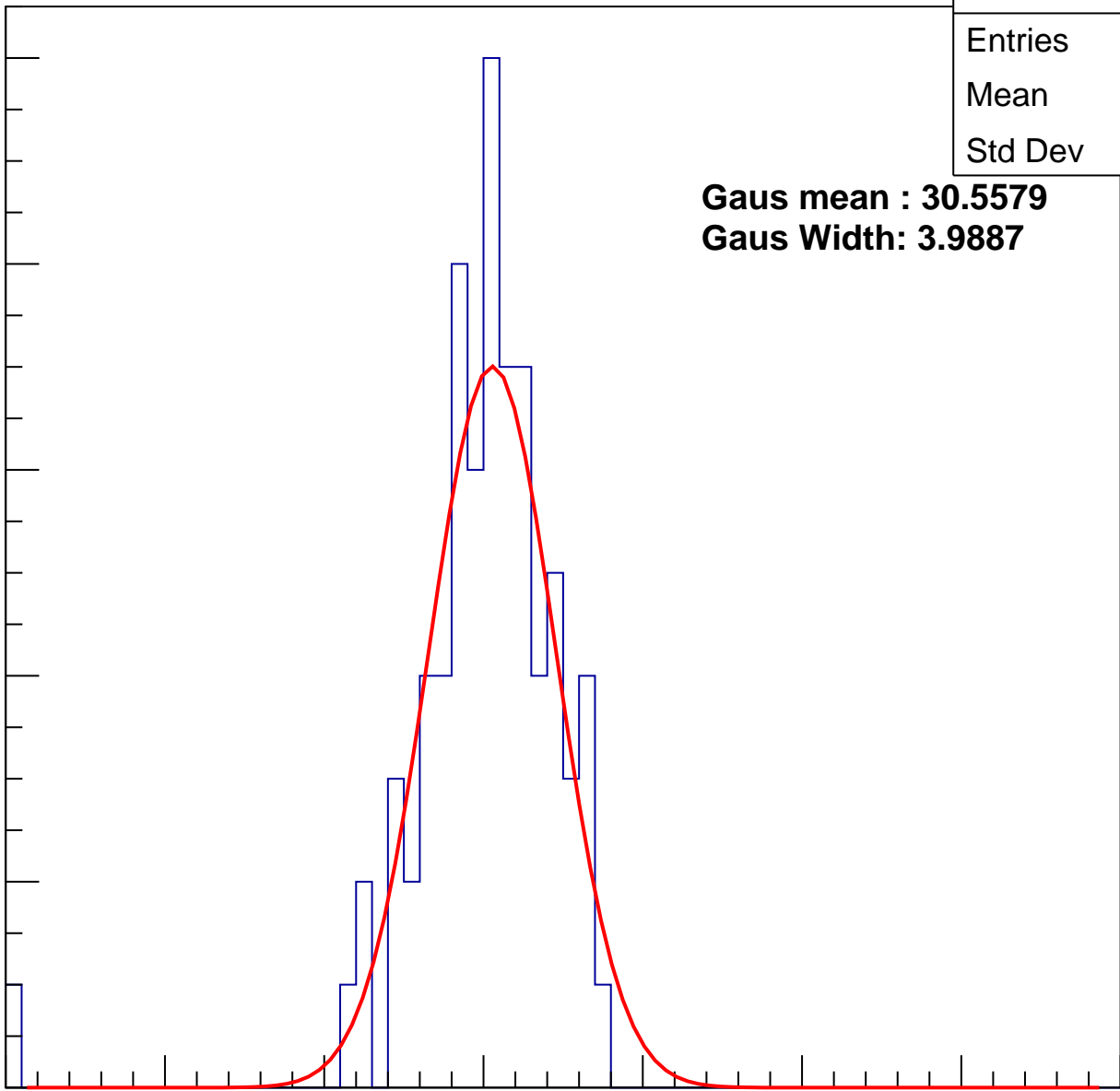
Gaus Width: 3.9887

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



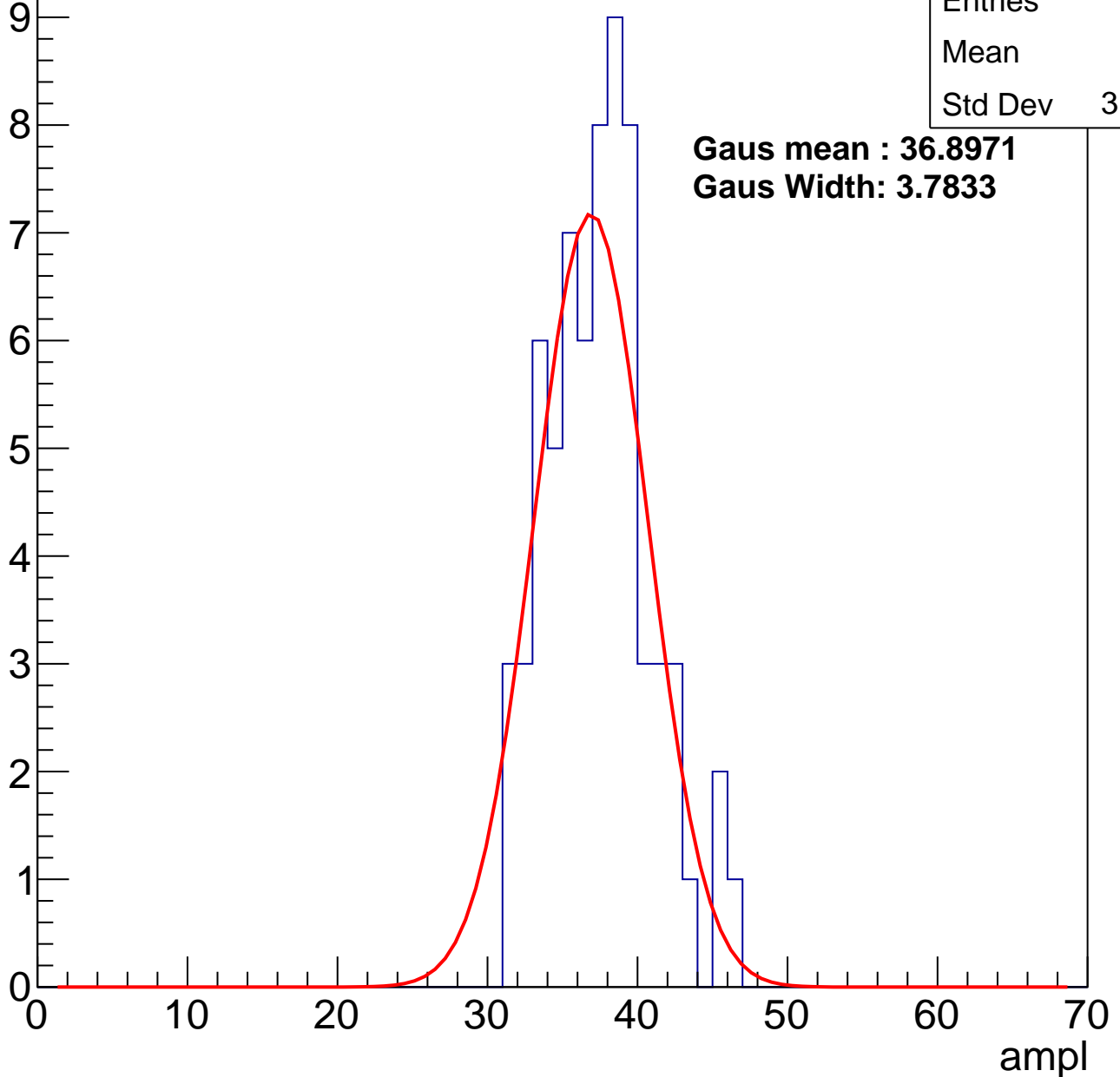
B1L103S, U10-ch69, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	37
Std Dev	3.417

Gaus mean : 36.8971
Gaus Width: 3.7833



B1L103S, U10-ch69, adc2

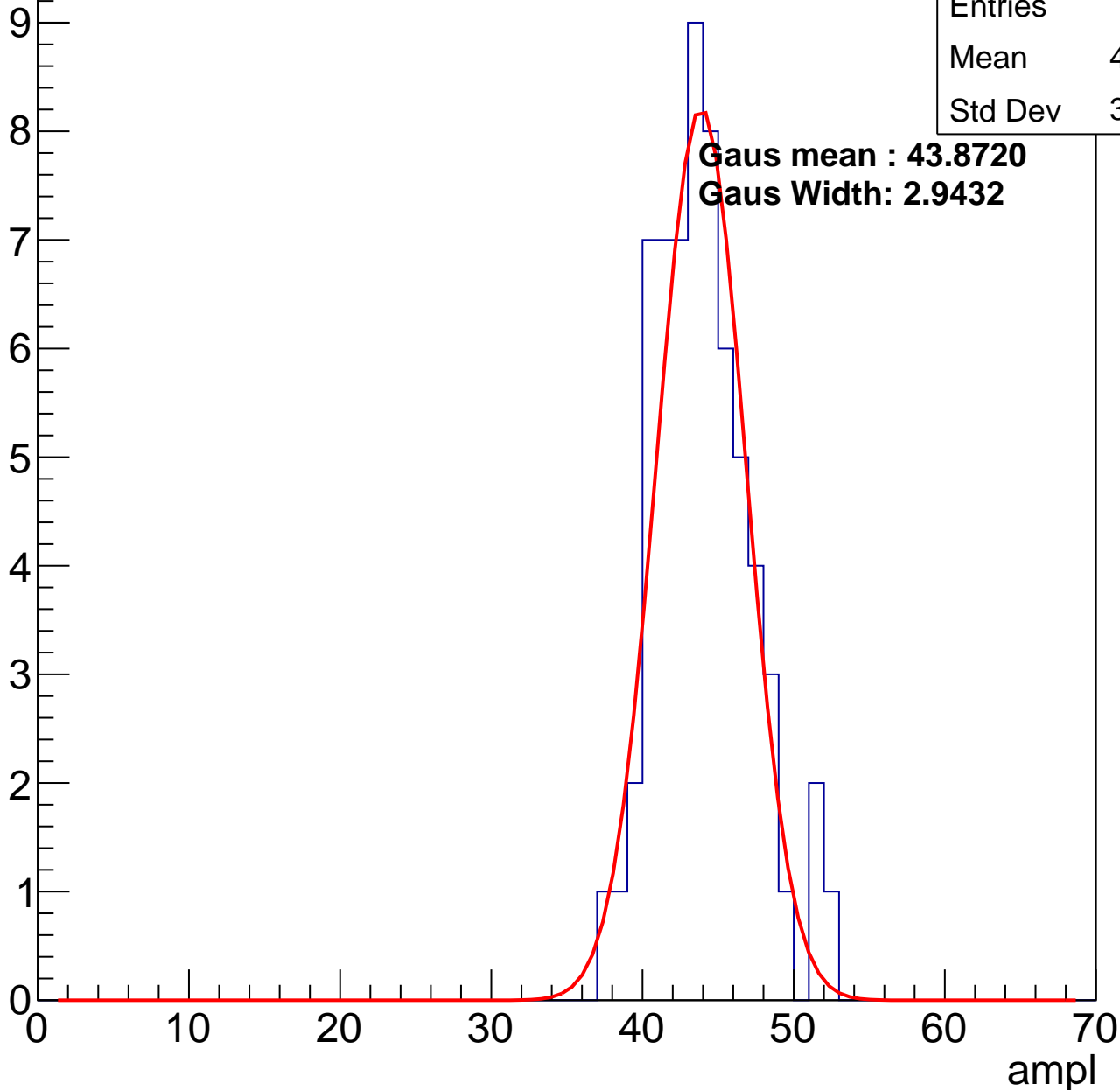
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	43.56
Std Dev	3.142

Gaus mean : 43.8720

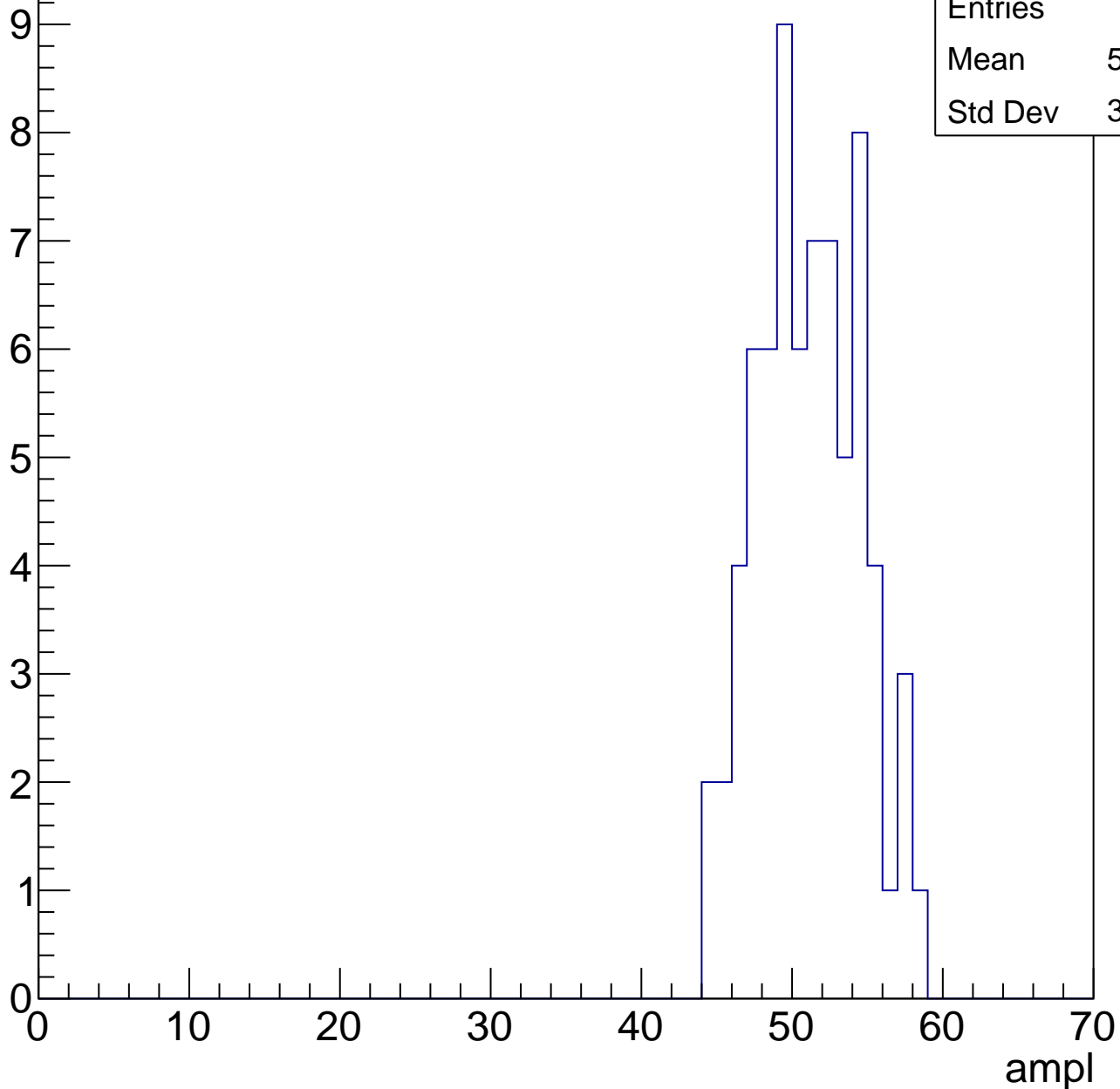
Gaus Width: 2.9432



B1L103S, U10-ch69, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



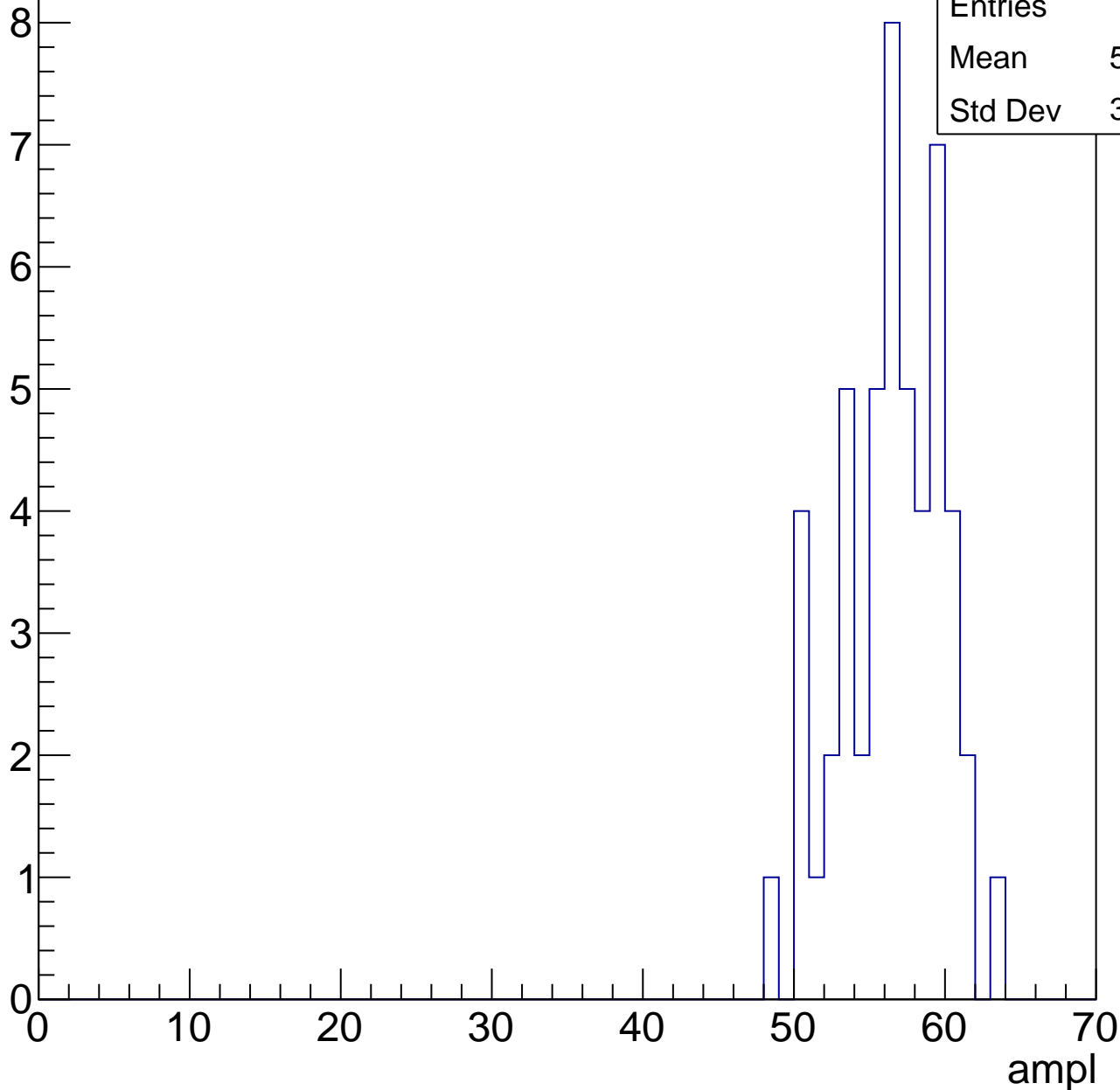
Entries	71
Mean	50.65
Std Dev	3.353

B1L103S, U10-ch69, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	55.96
Std Dev	3.343

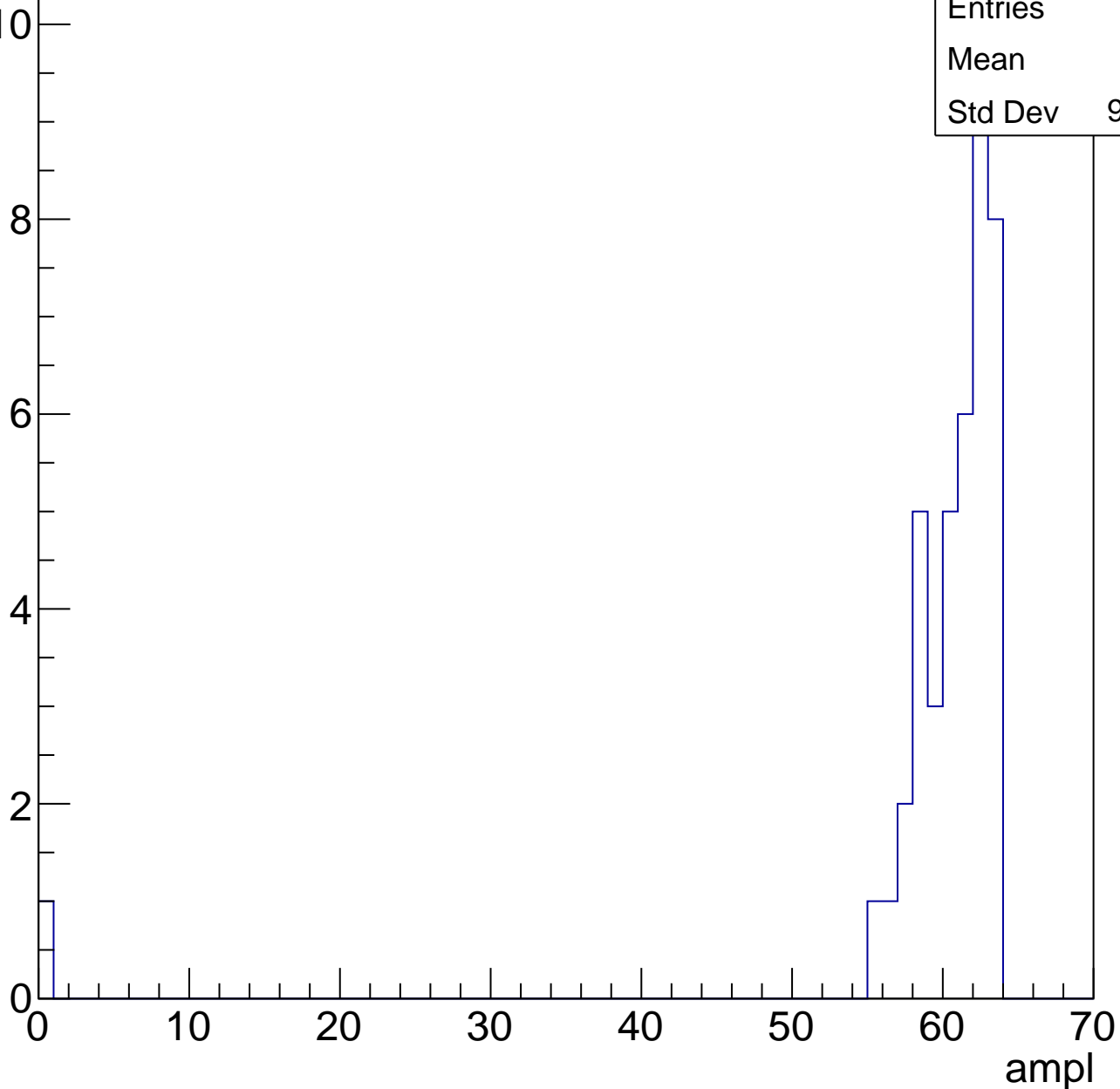


B1L103S, U10-ch69, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

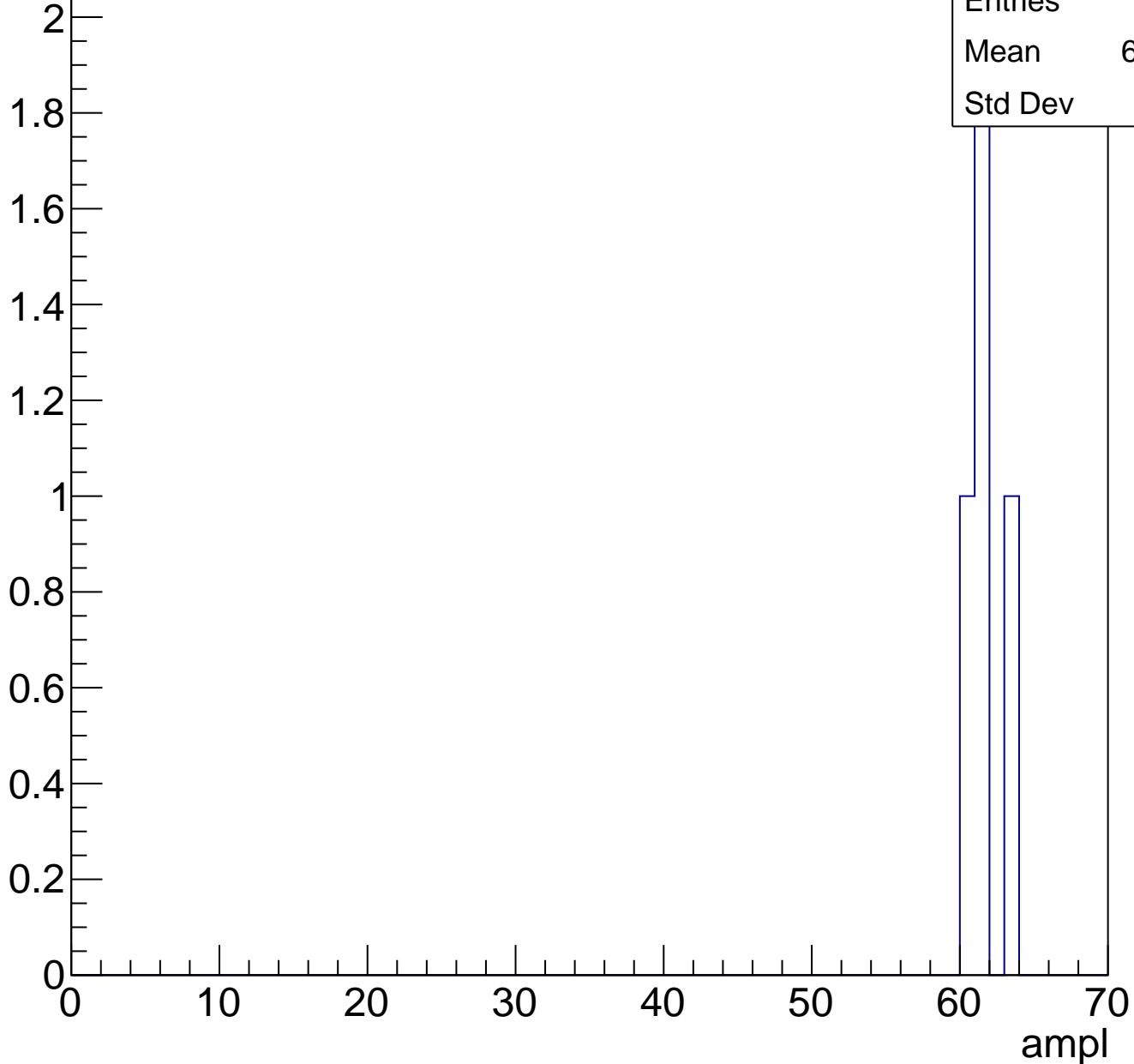
Entries	42
Mean	59.1
Std Dev	9.469



B1L103S, U10-ch69, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch69, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch70, adc0

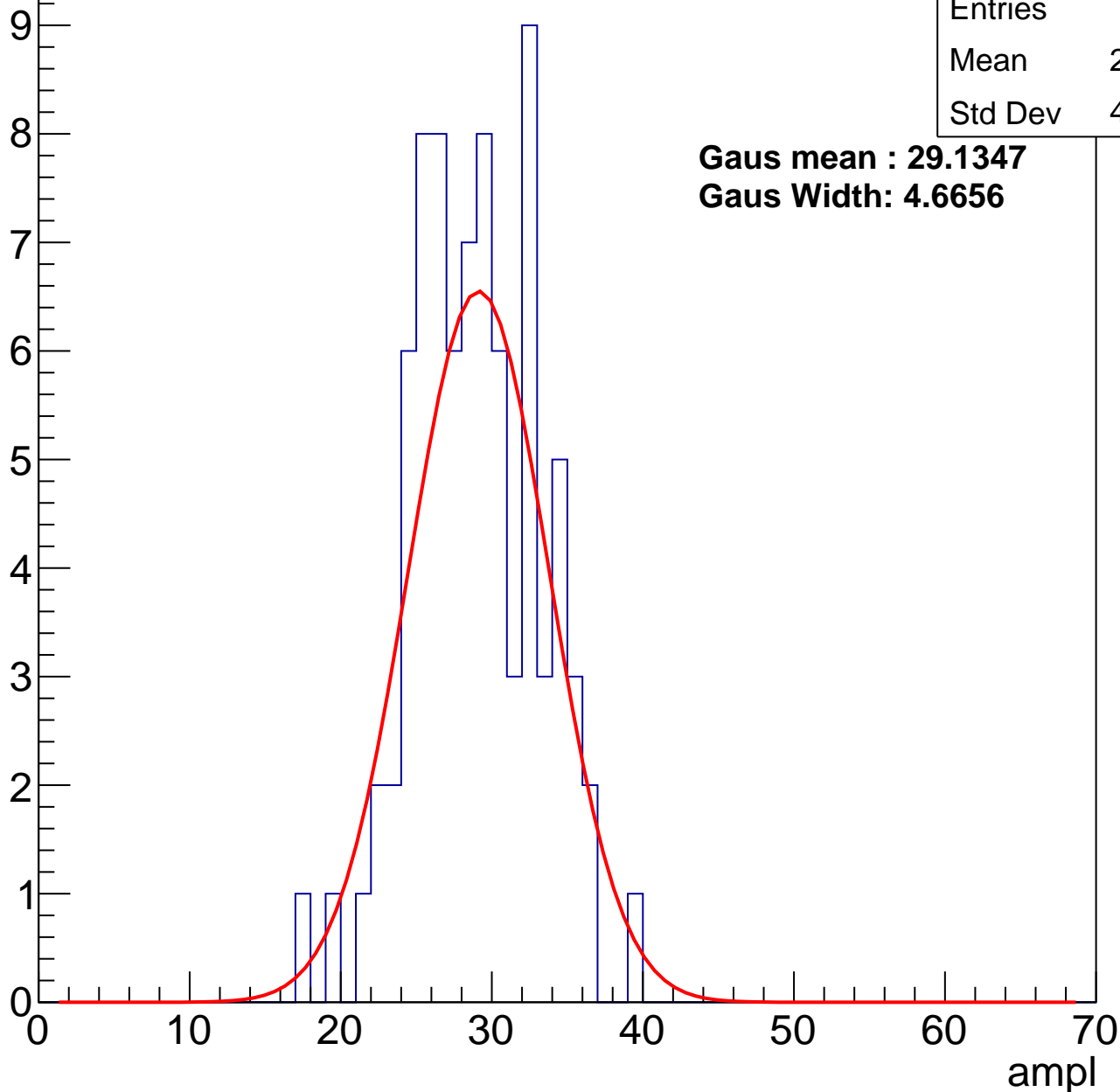
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	28.48
Std Dev	4.153

Gaus mean : 29.1347

Gaus Width: 4.6656



B1L103S, U10-ch70, adc1

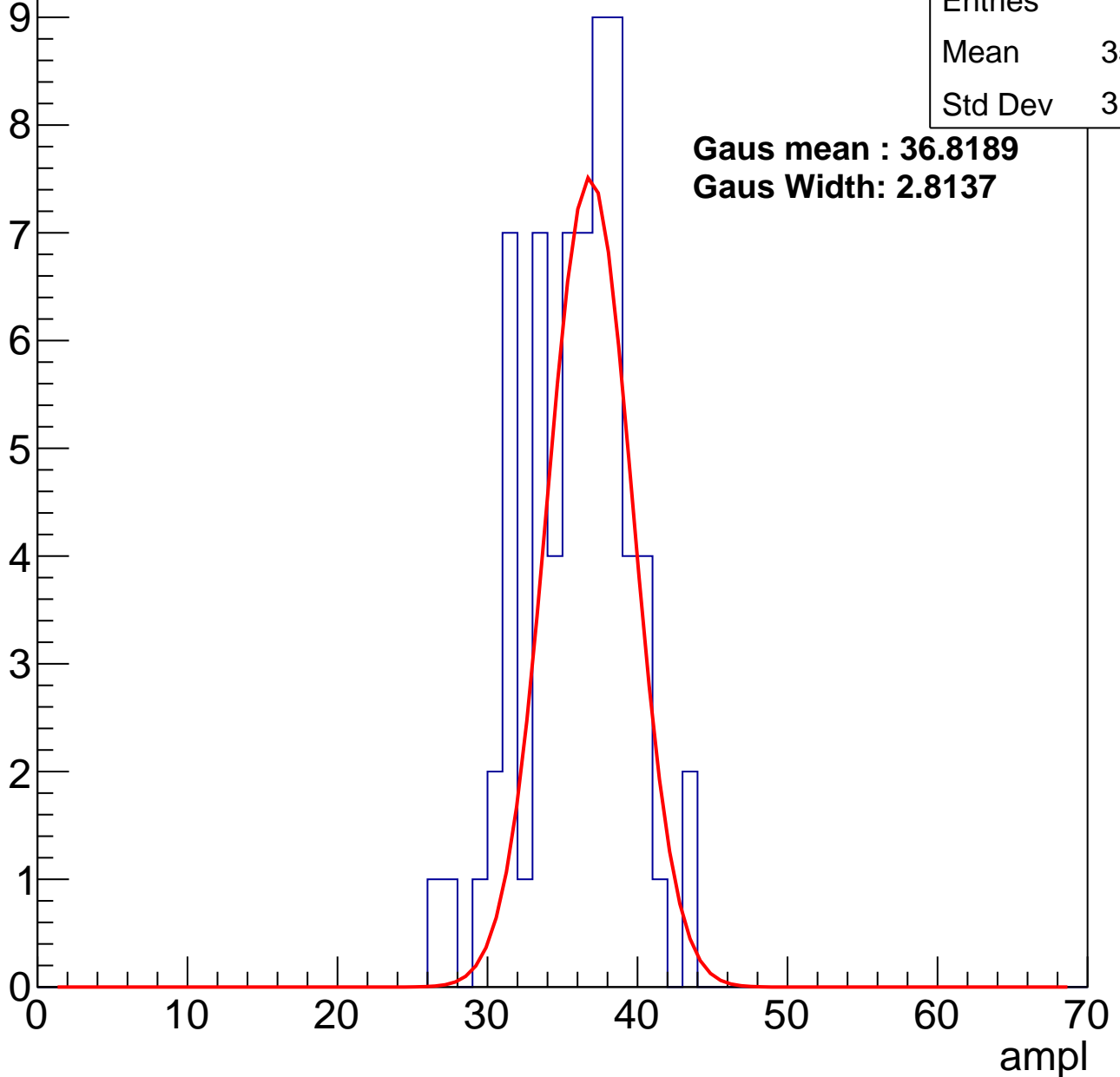
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	35.42
Std Dev	3.508

Gaus mean : 36.8189

Gaus Width: 2.8137



B1L103S, U10-ch70, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	42.99
Std Dev	3.394

Gaus mean : 43.6135

Gaus Width: 3.2017

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

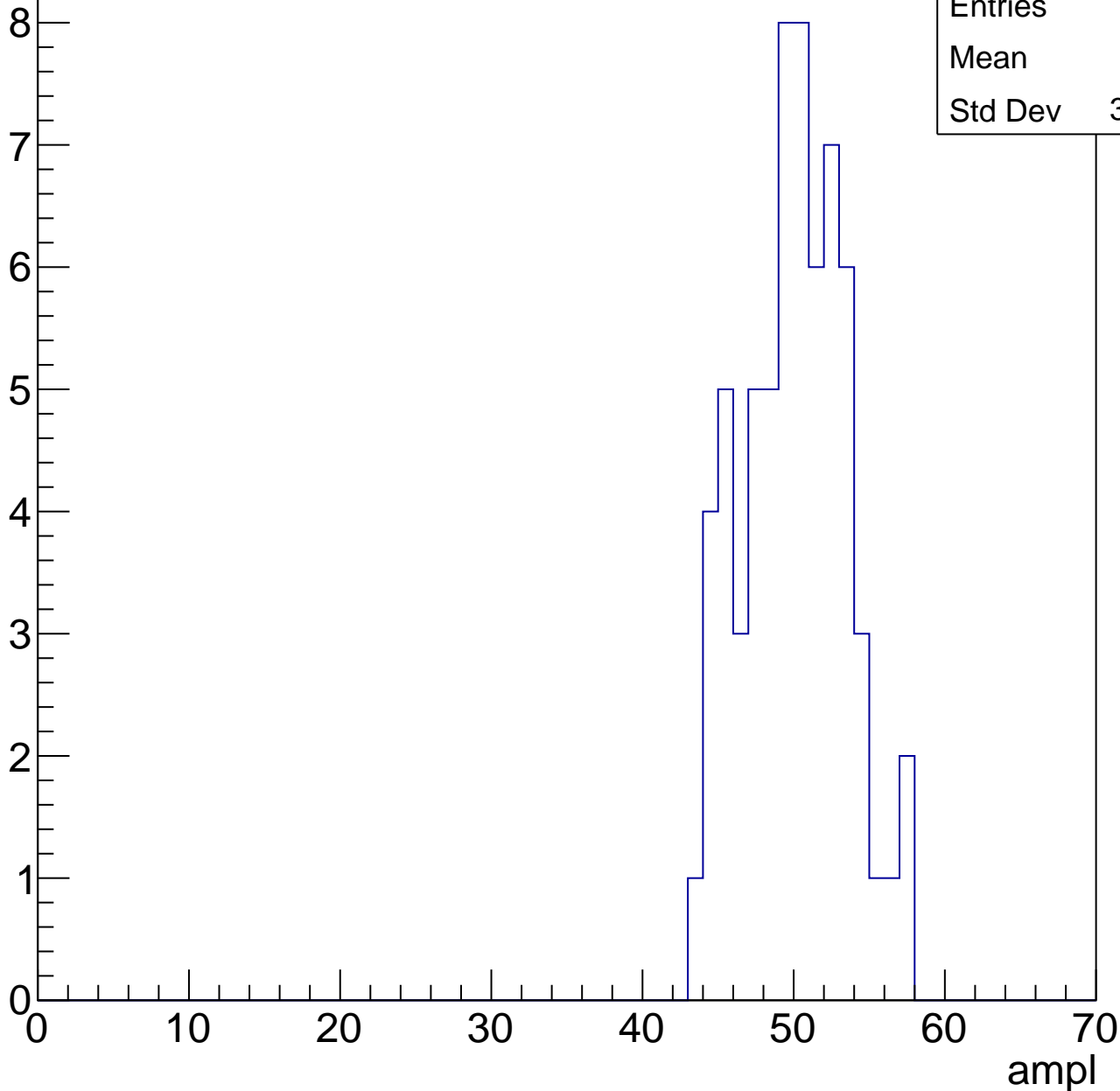
70

B1L103S, U10-ch70, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	49.6
Std Dev	3.337

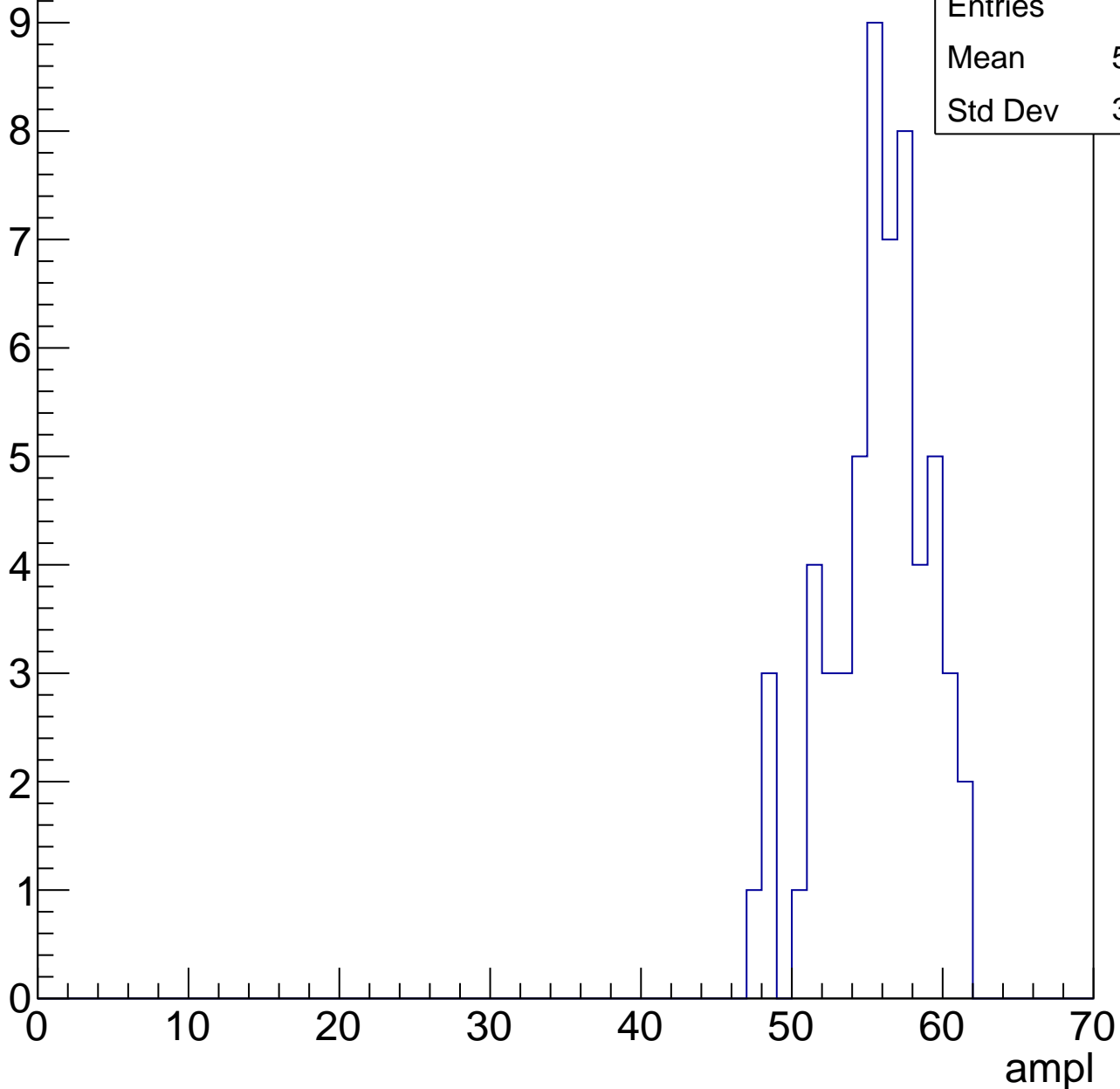


B1L103S, U10-ch70, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	55.21
Std Dev	3.331

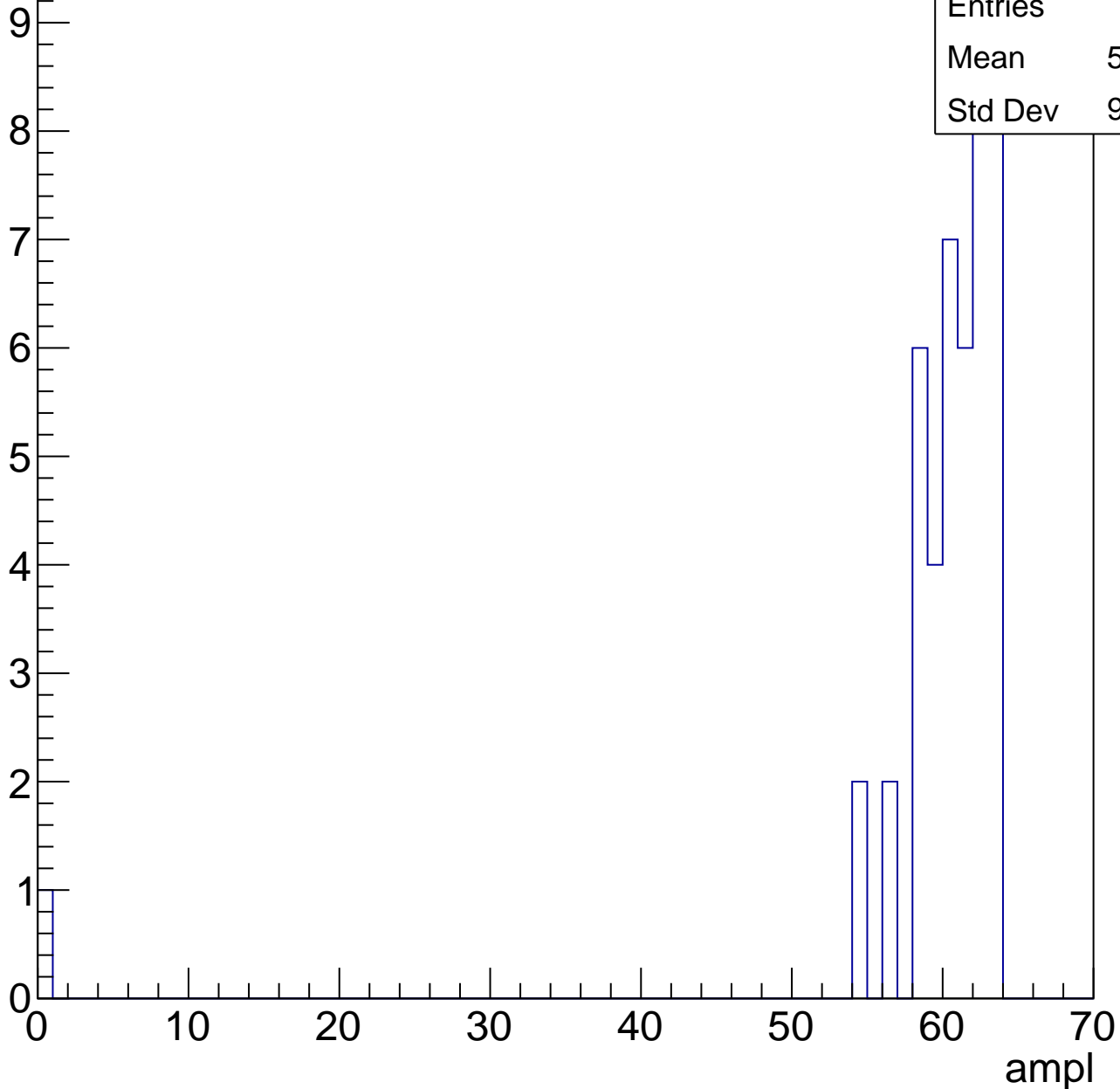


B1L103S, U10-ch70, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

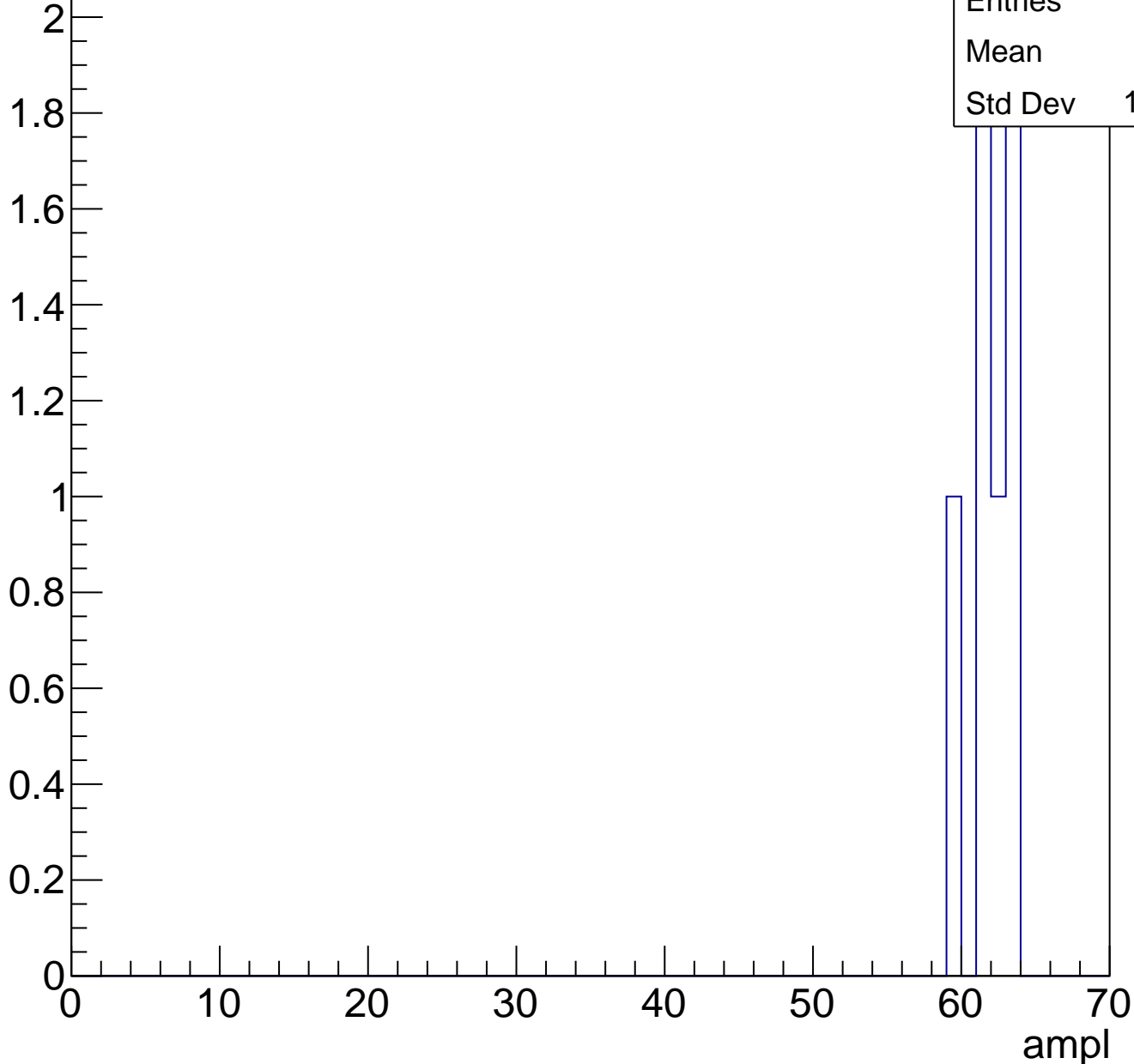
Entries	45
Mean	58.93
Std Dev	9.183



B1L103S, U10-ch70, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch70, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch71, adc0

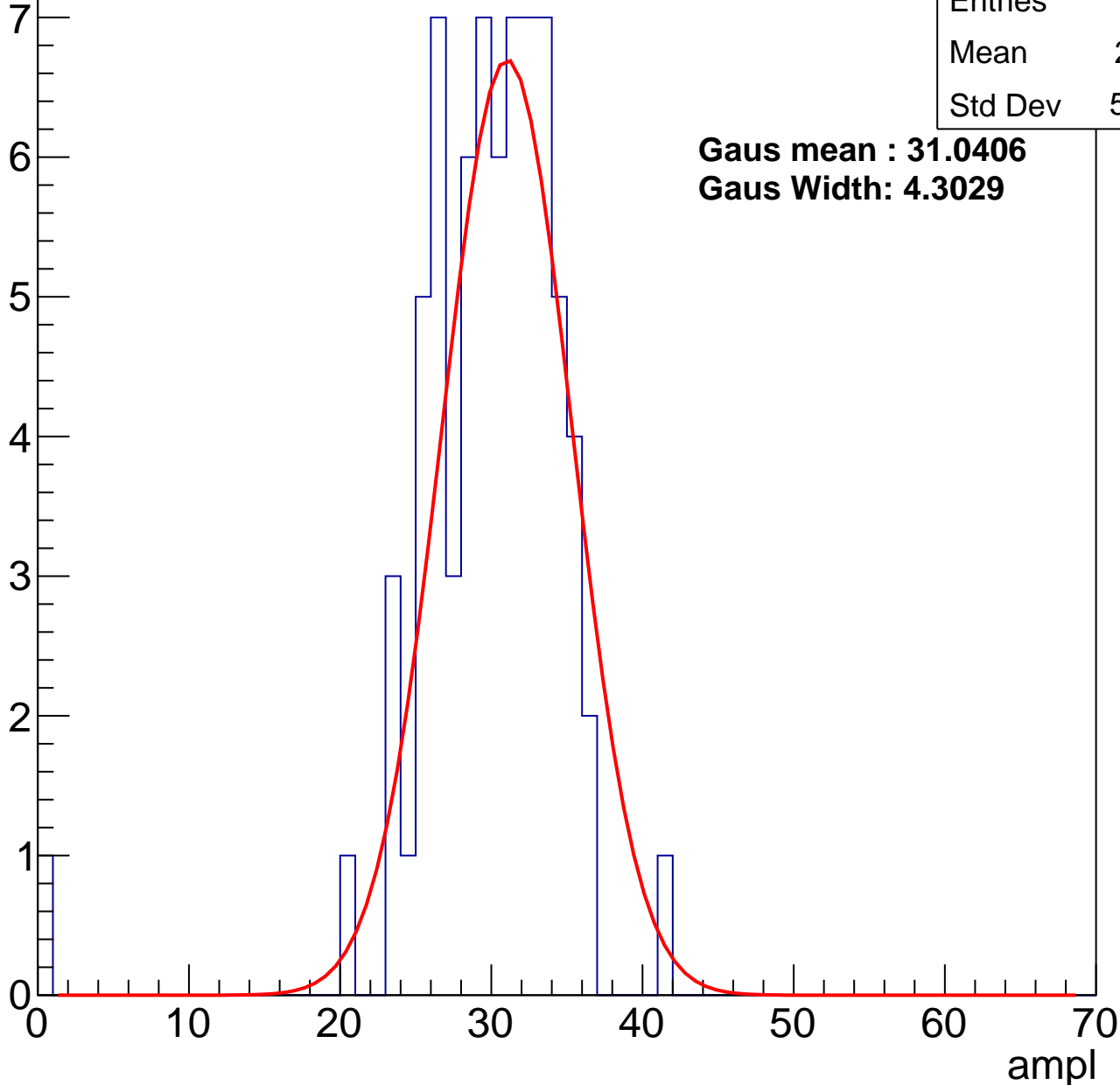
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	29.41
Std Dev	5.133

Gaus mean : 31.0406

Gaus Width: 4.3029



B1L103S, U10-ch71, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	37.21
Std Dev	3.599

Gaus mean : 37.3695

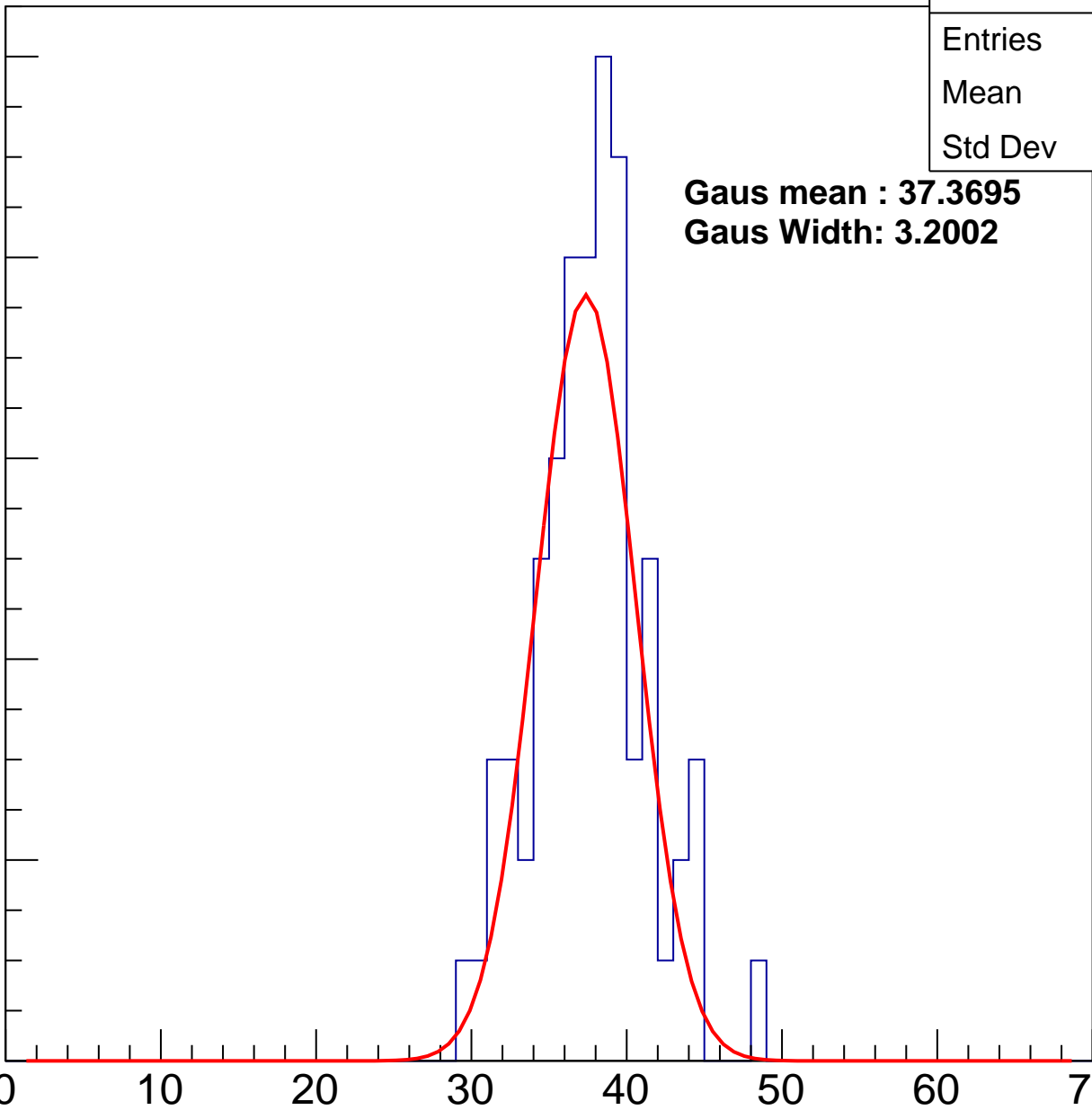
Gaus Width: 3.2002

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch71, adc2

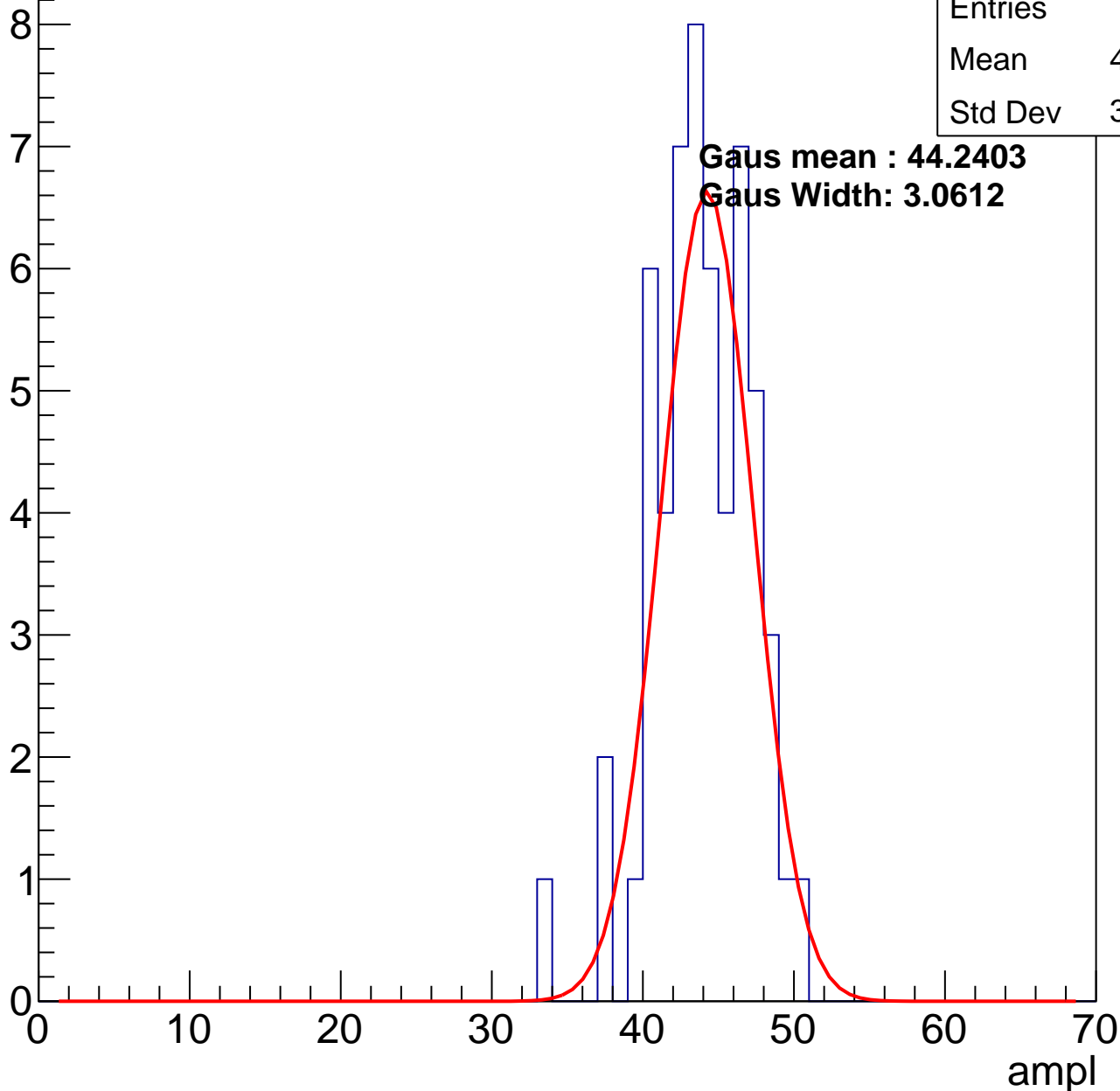
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.43
Std Dev	3.217

Gaus mean : 44.2403

Gaus Width: 3.0612

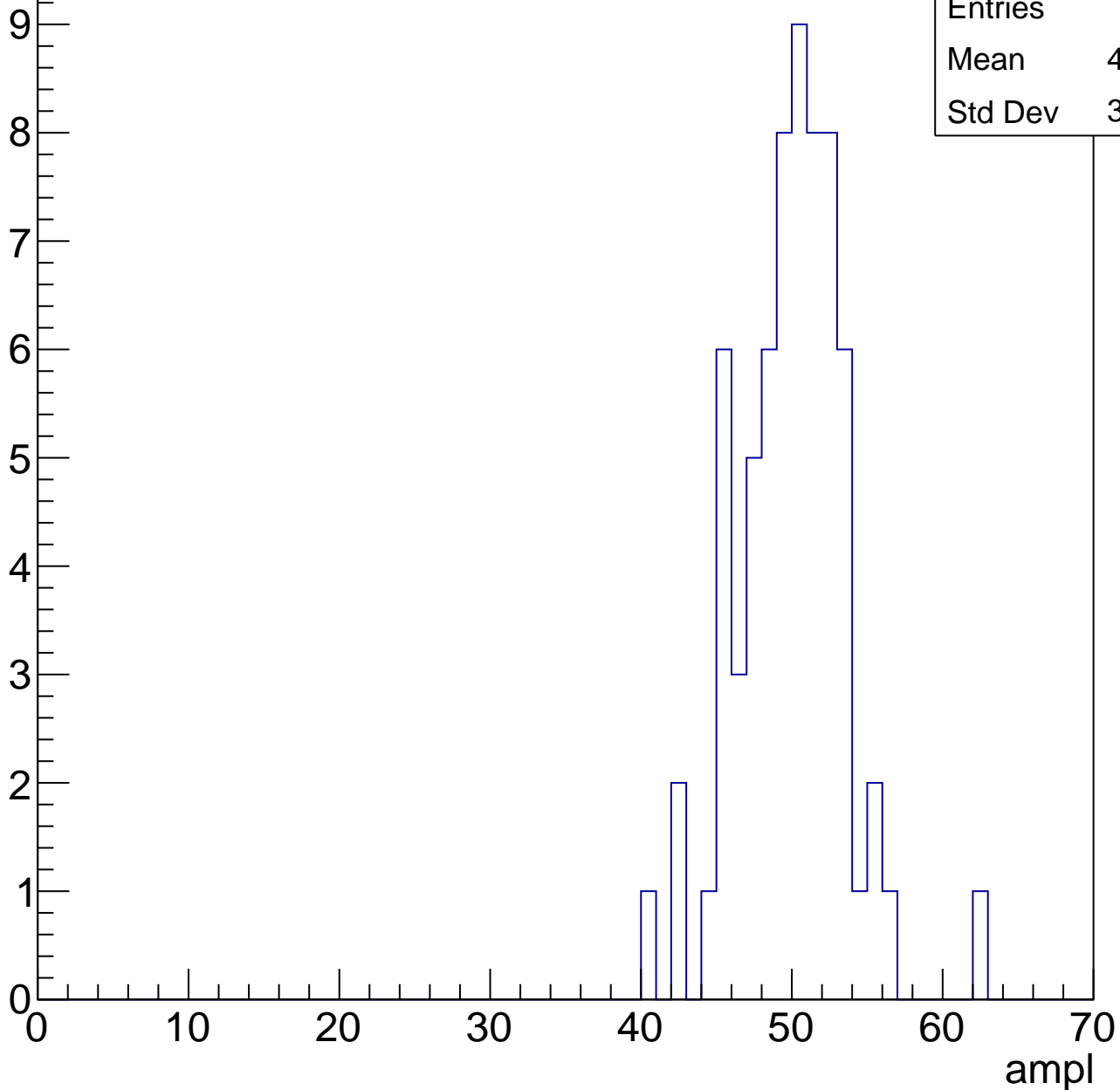


B1L103S, U10-ch71, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	49.49
Std Dev	3.558

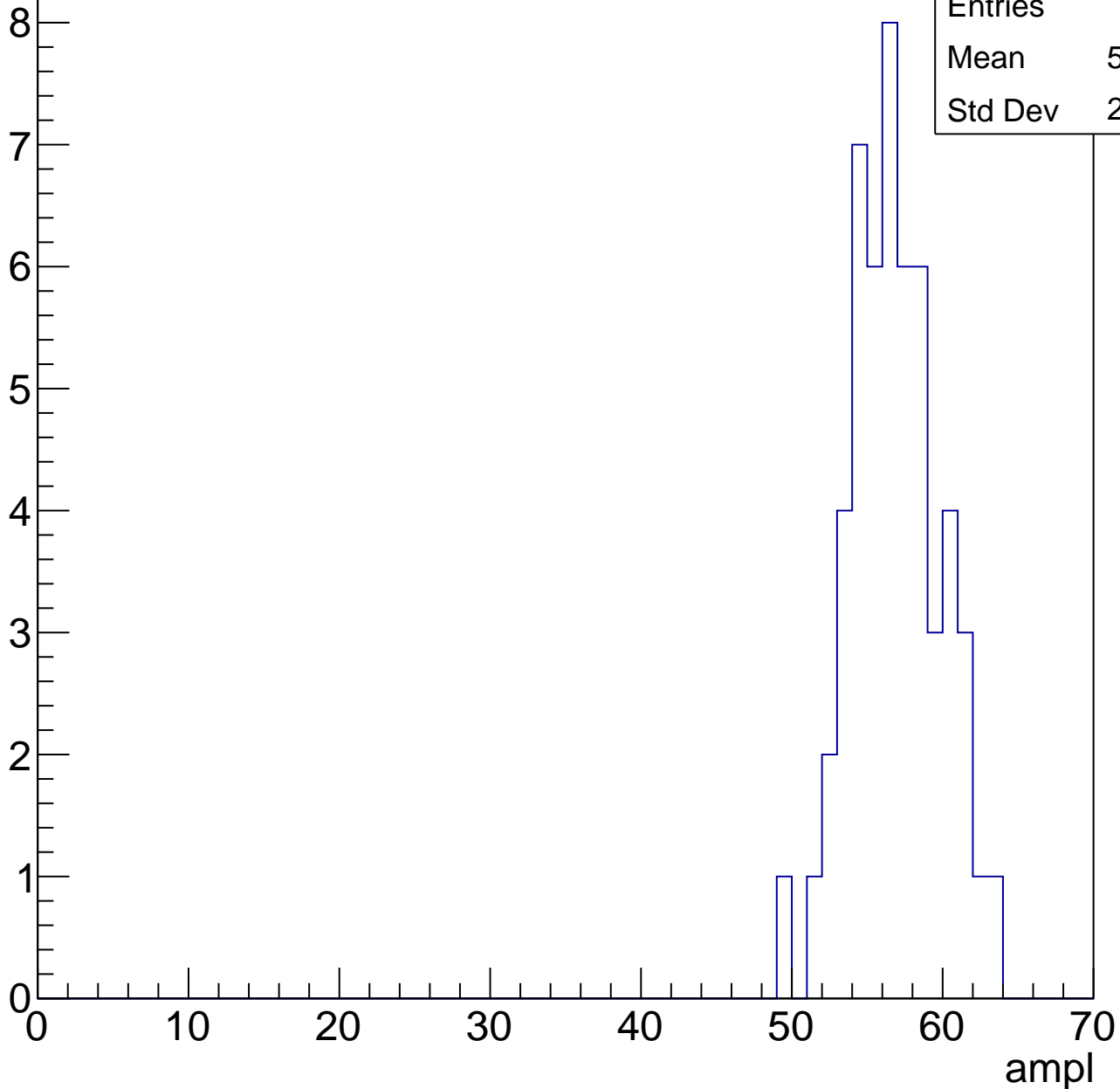


B1L103S, U10-ch71, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	56.36
Std Dev	2.914

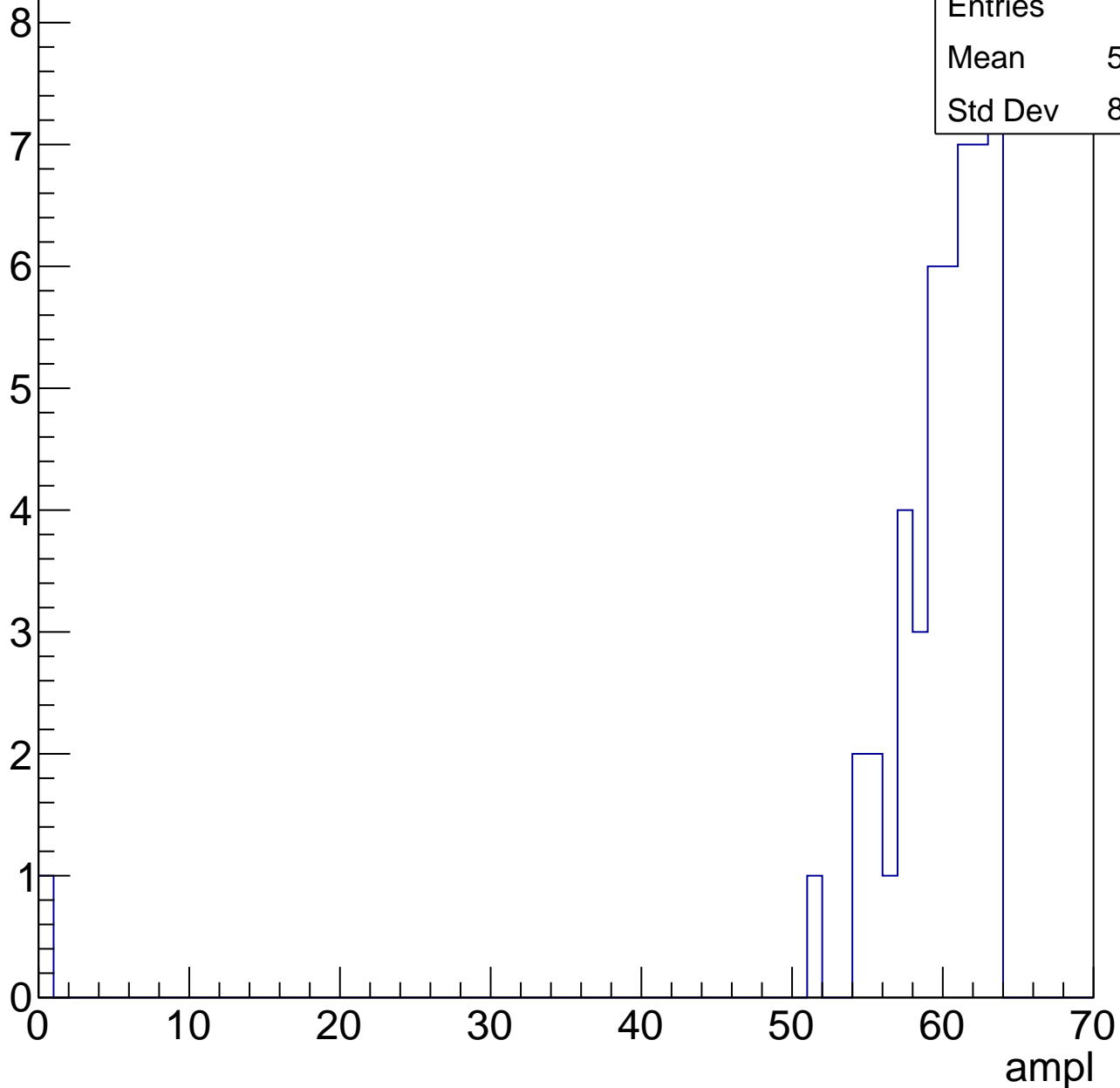


B1L103S, U10-ch71, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

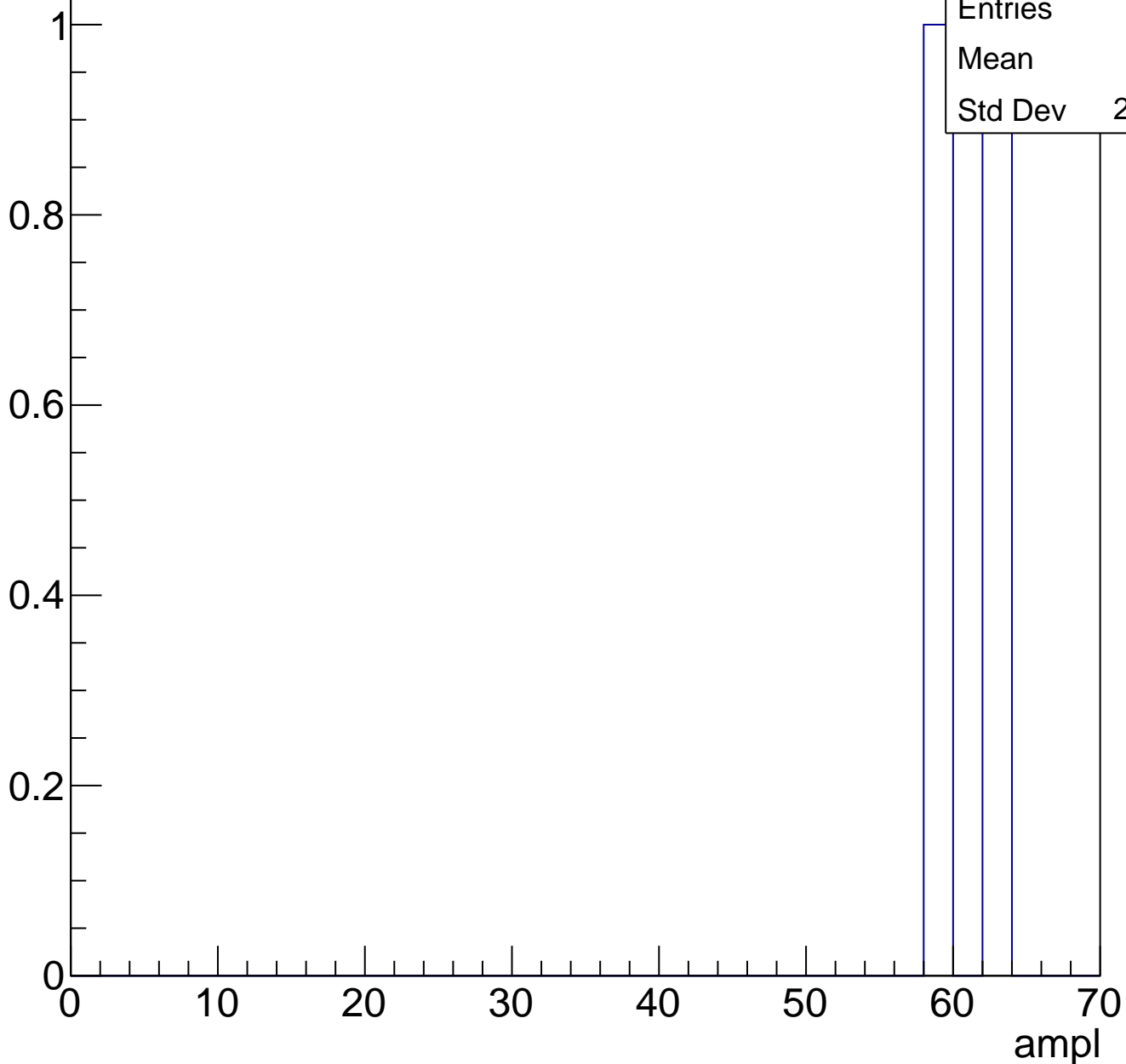
Entries	48
Mean	58.46
Std Dev	8.974



B1L103S, U10-ch71, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch71, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch72, adc0

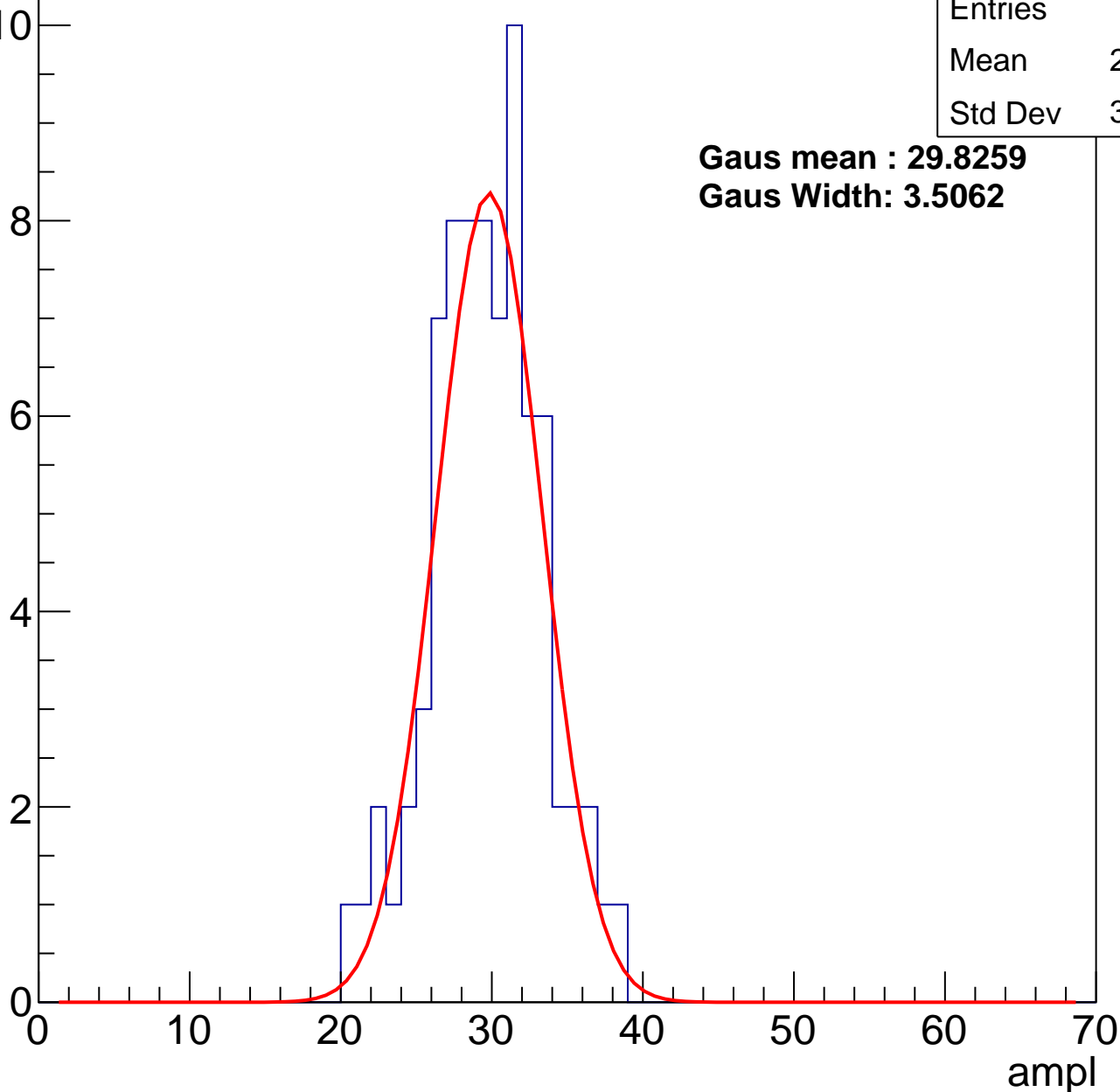
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	29.23
Std Dev	3.648

Gaus mean : 29.8259

Gaus Width: 3.5062



B1L103S, U10-ch72, adc1

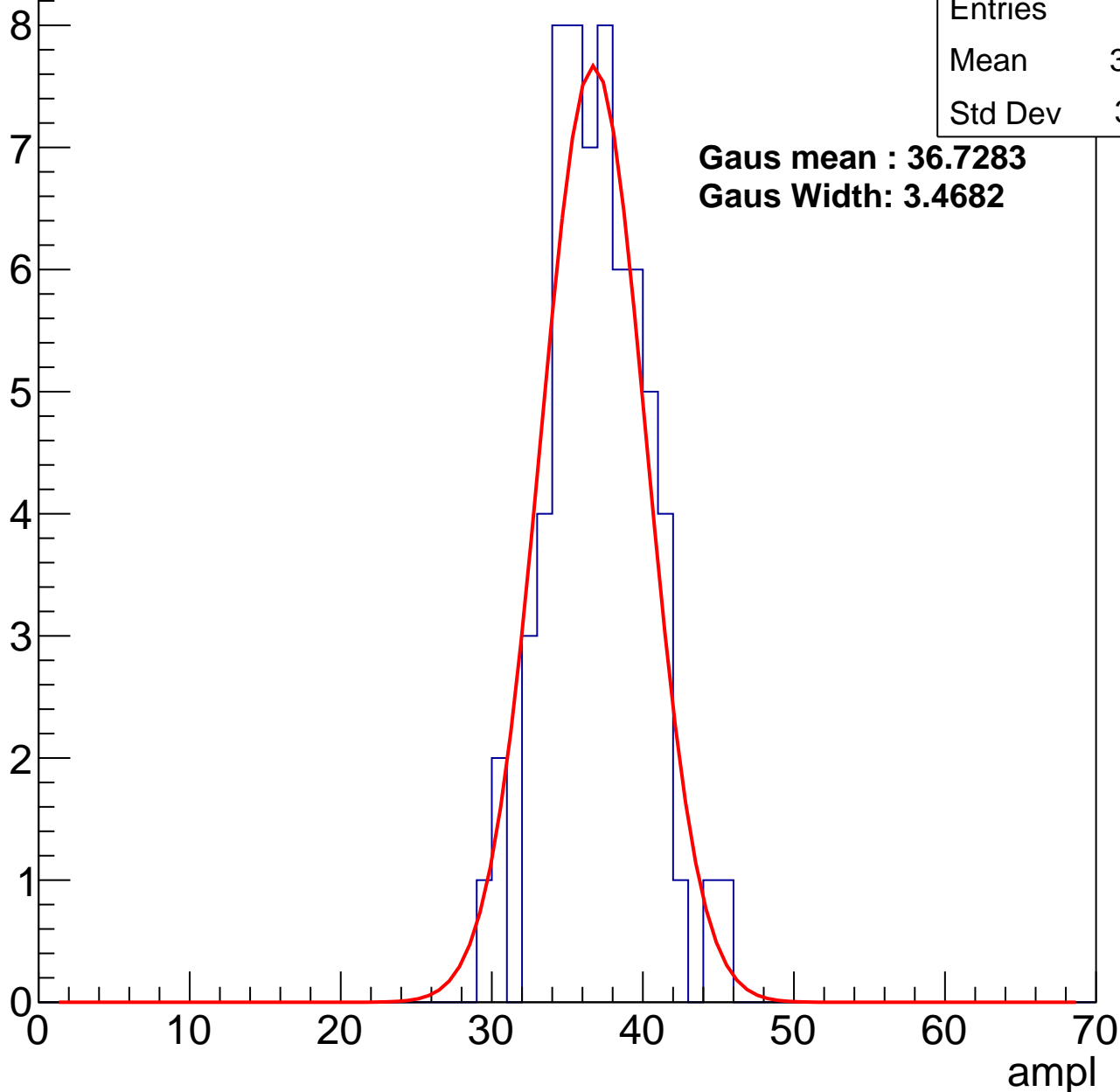
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.52
Std Dev	3.221

Gaus mean : 36.7283

Gaus Width: 3.4682



B1L103S, U10-ch72, adc2

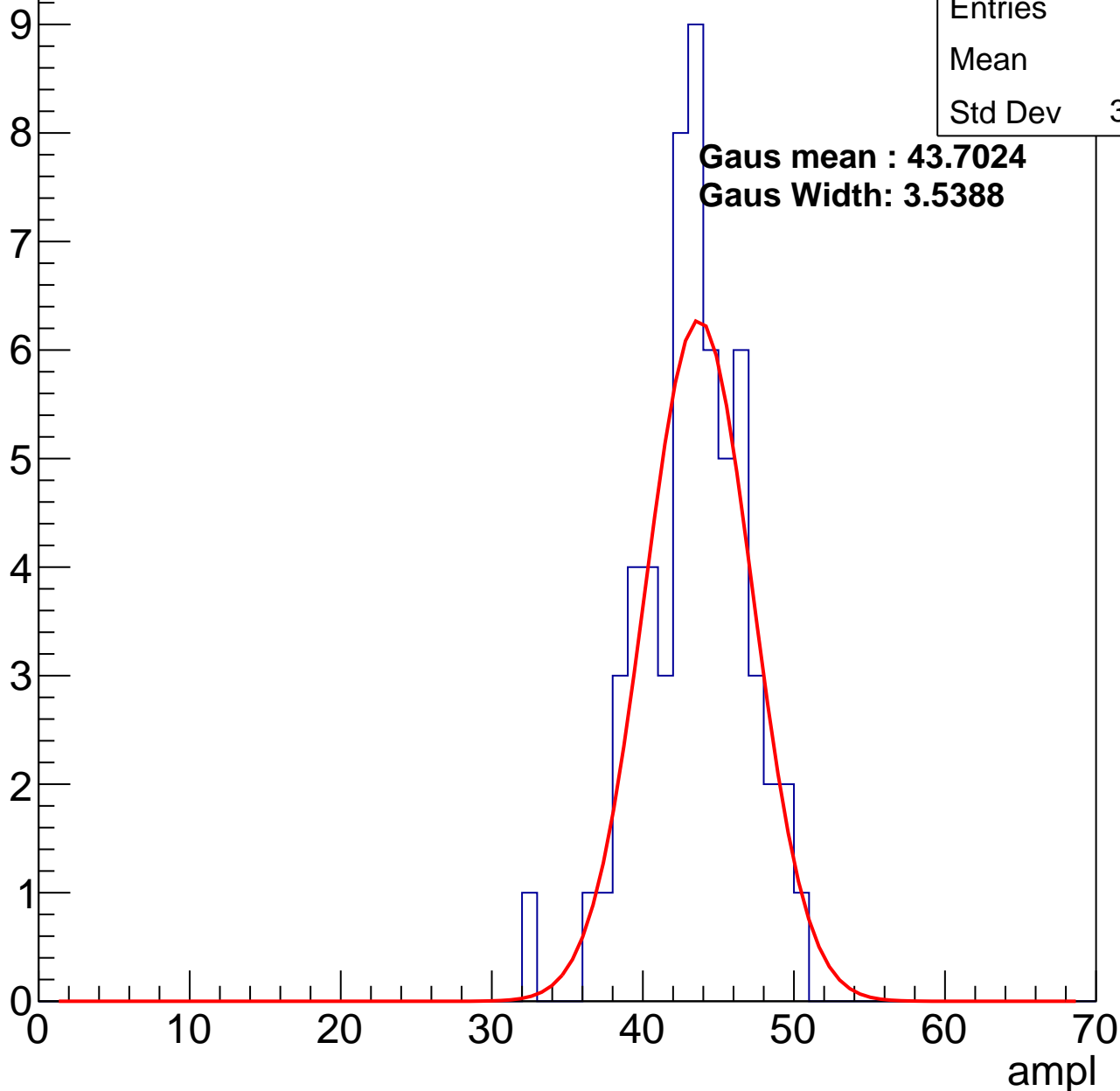
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.9
Std Dev	3.443

Gaus mean : 43.7024

Gaus Width: 3.5388

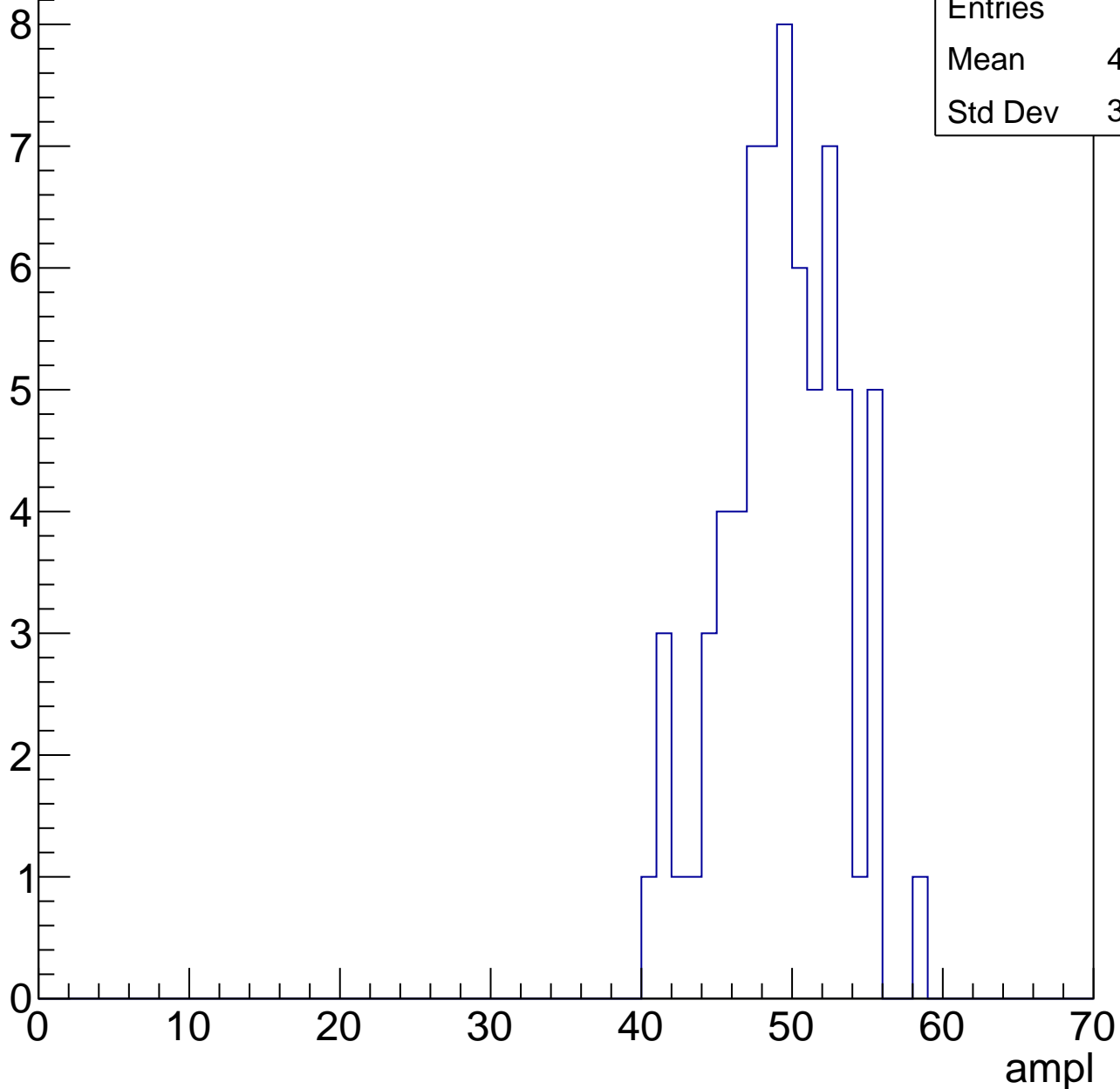


B1L103S, U10-ch72, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	48.87
Std Dev	3.848

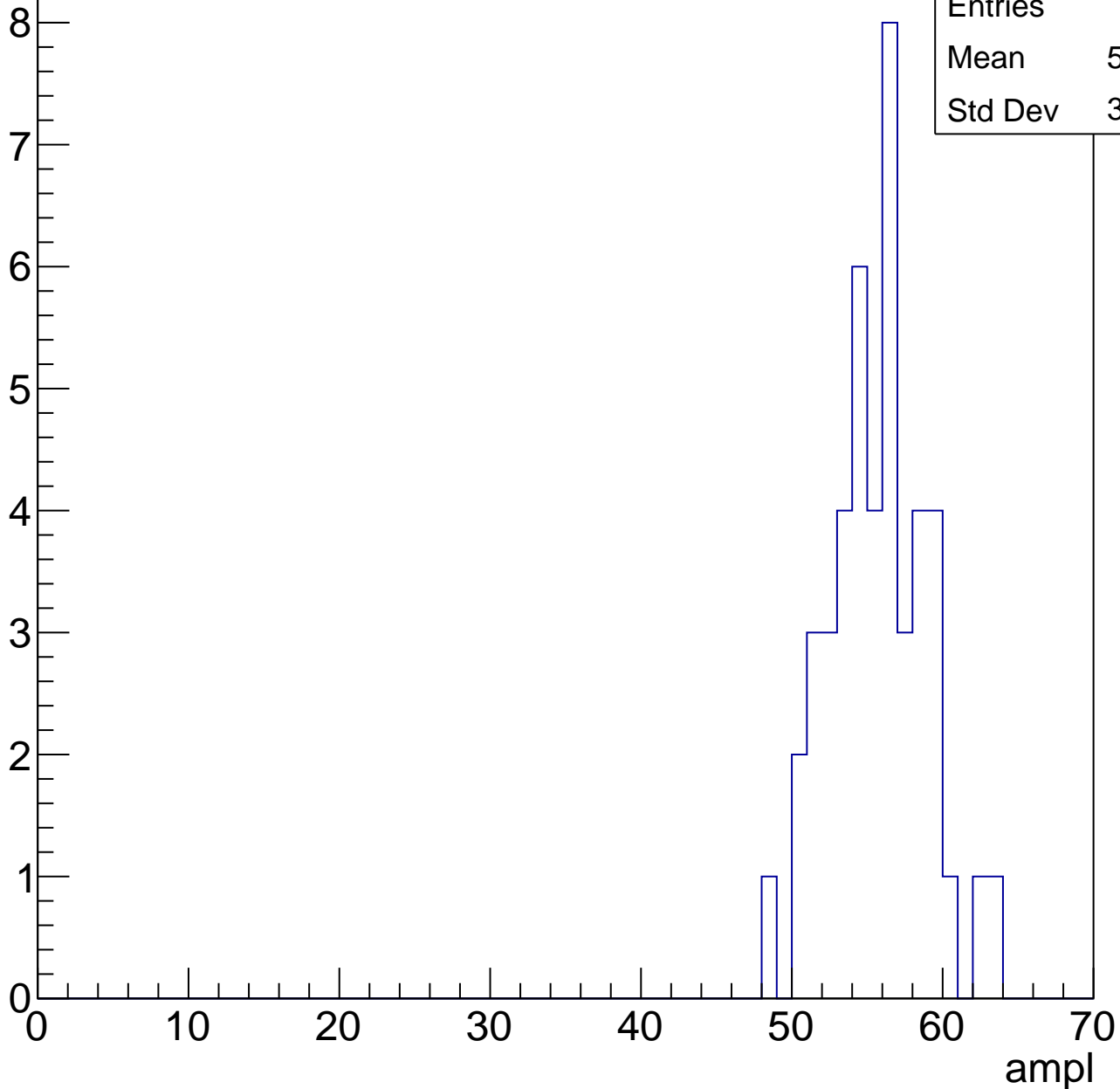


B1L103S, U10-ch72, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	55.22
Std Dev	3.169

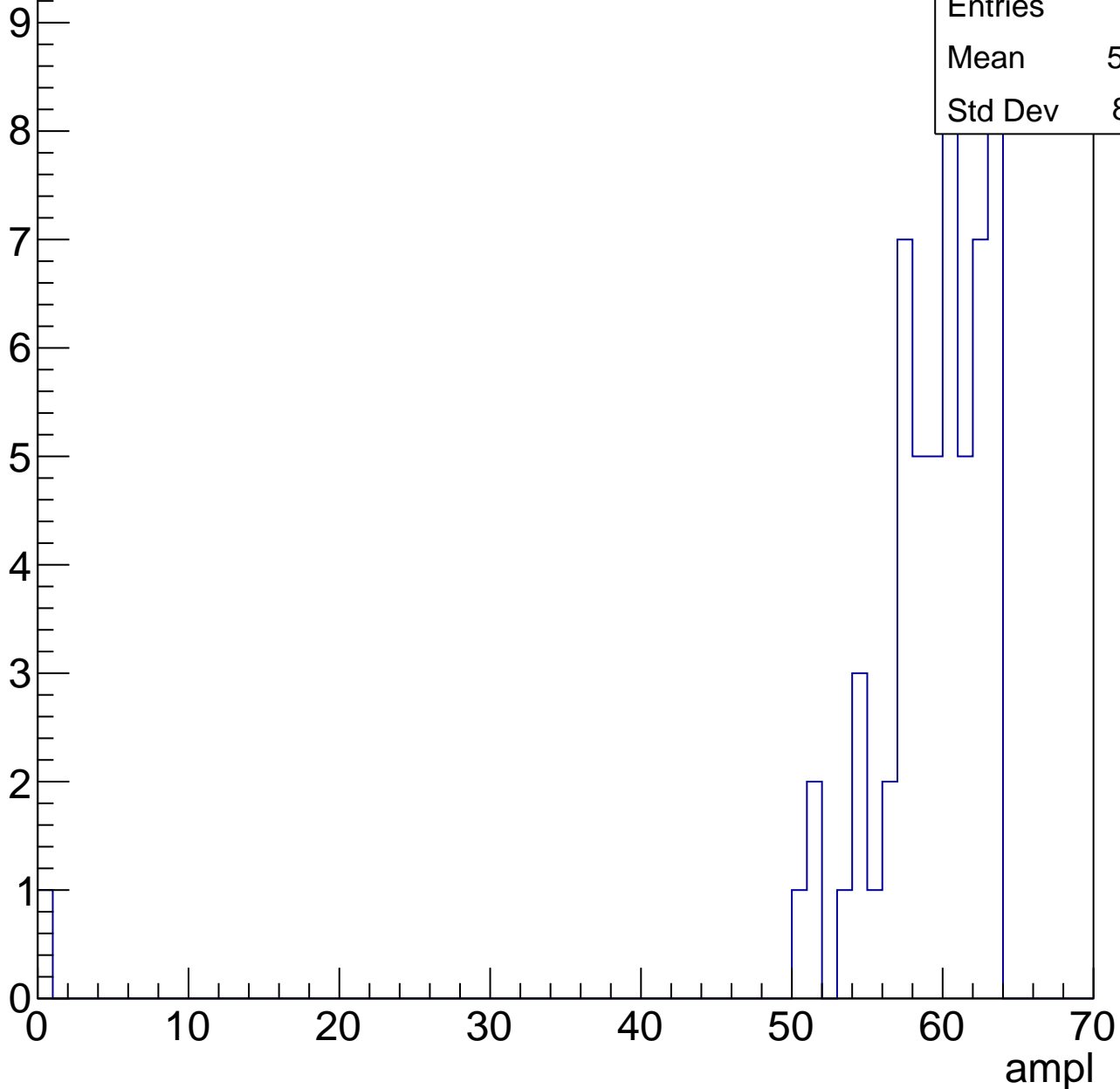


B1L103S, U10-ch72, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	57.96
Std Dev	8.421

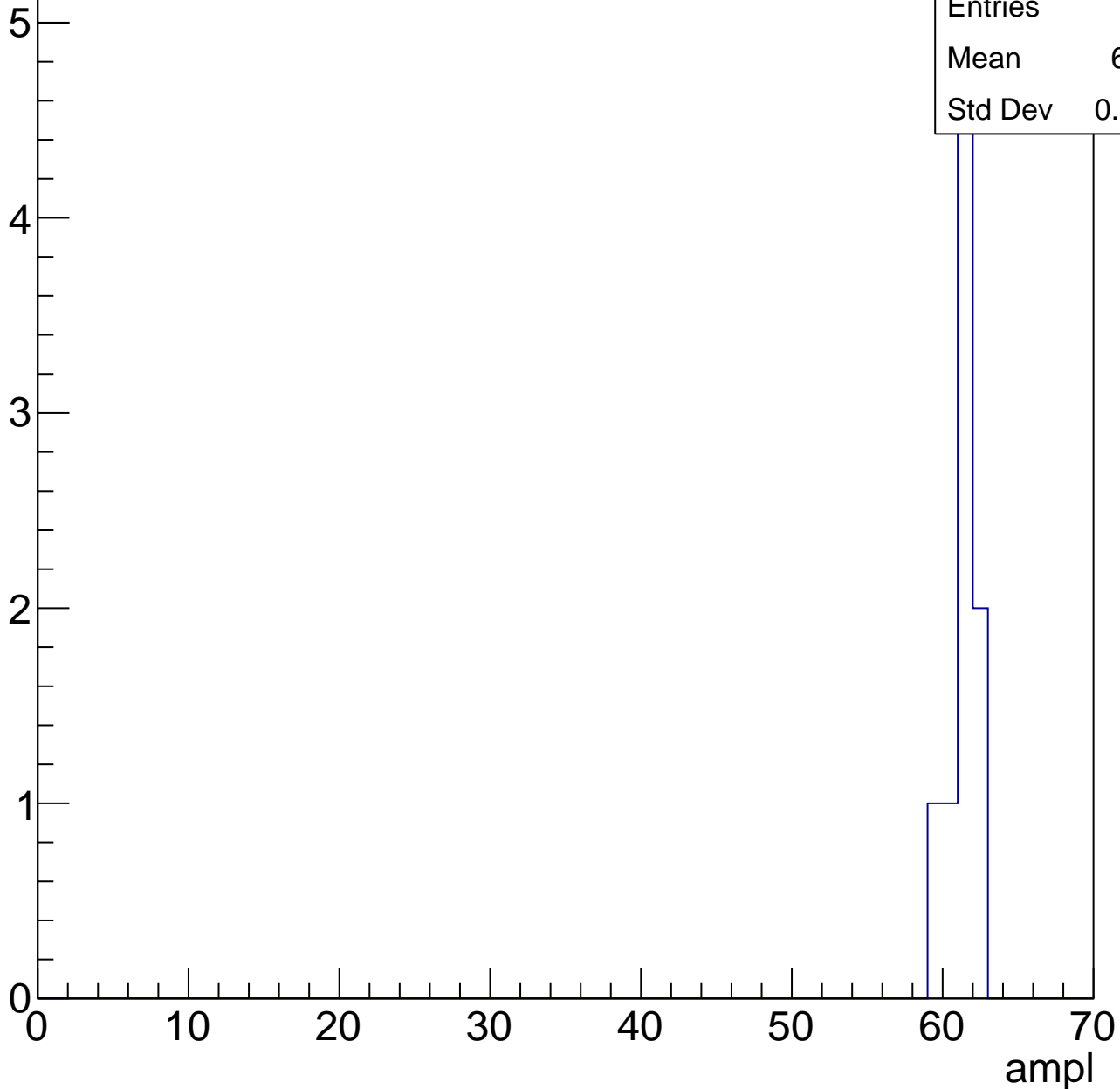


B1L103S, U10-ch72, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	60.89
Std Dev	0.8749



B1L103S, U10-ch72, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch73, adc0

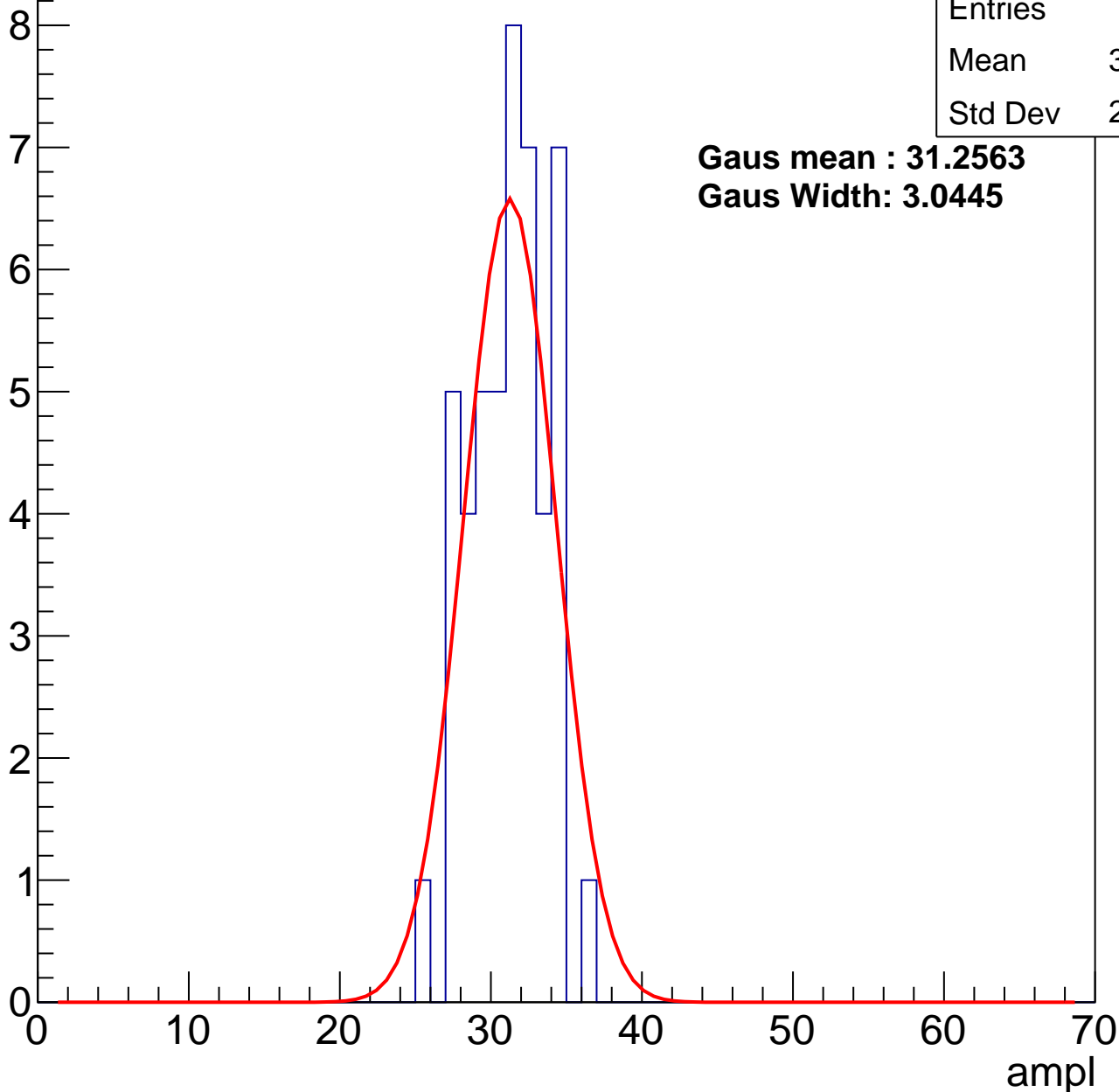
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	30.74
Std Dev	2.462

Gaus mean : 31.2563

Gaus Width: 3.0445



B1L103S, U10-ch73, adc1

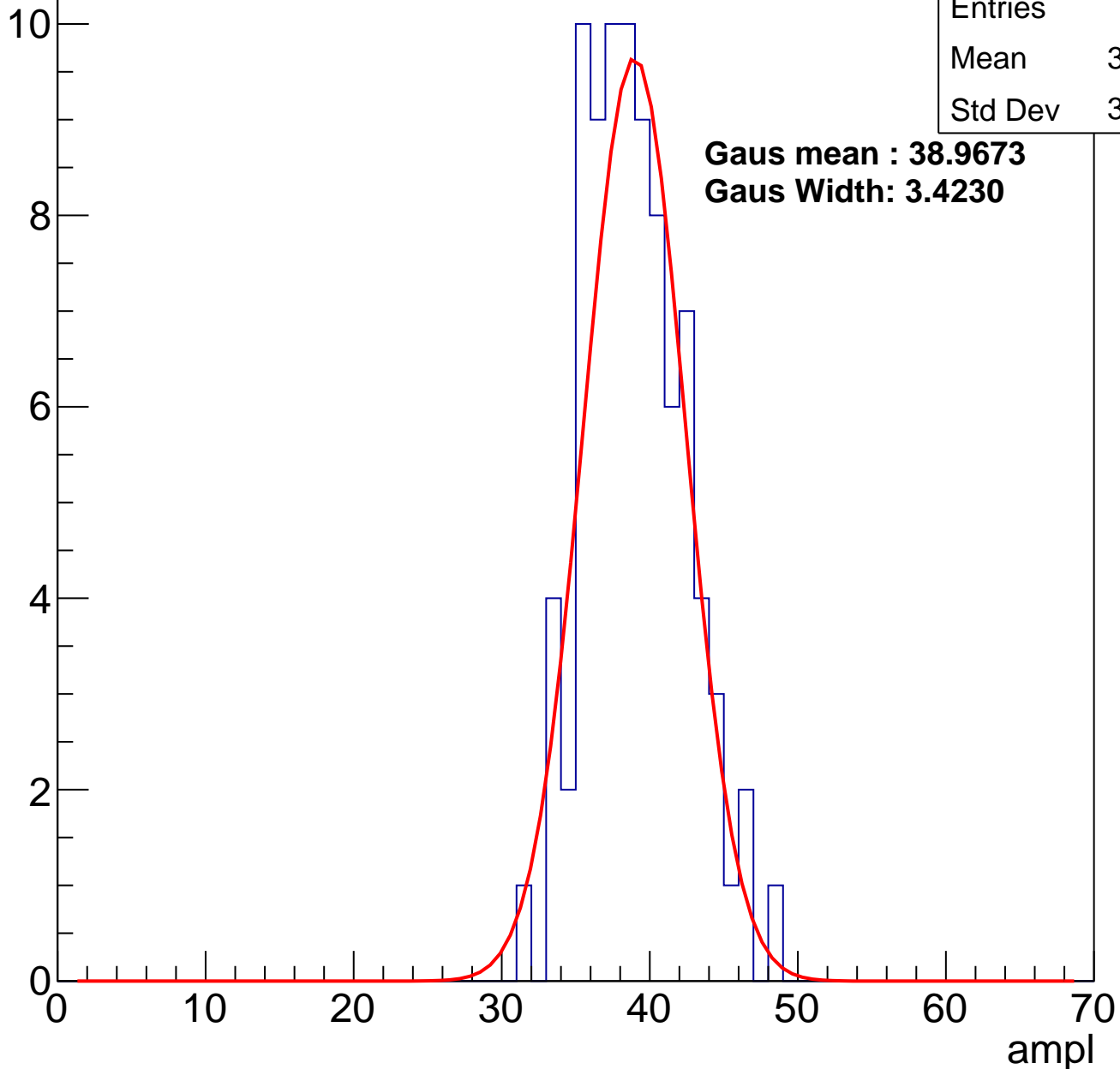
calib_packv5_042523_0143.root, FC#7, port C2

Entries	87
Mean	38.56
Std Dev	3.359

Gaus mean : 38.9673

Gaus Width: 3.4230

Entry



B1L103S, U10-ch73, adc2

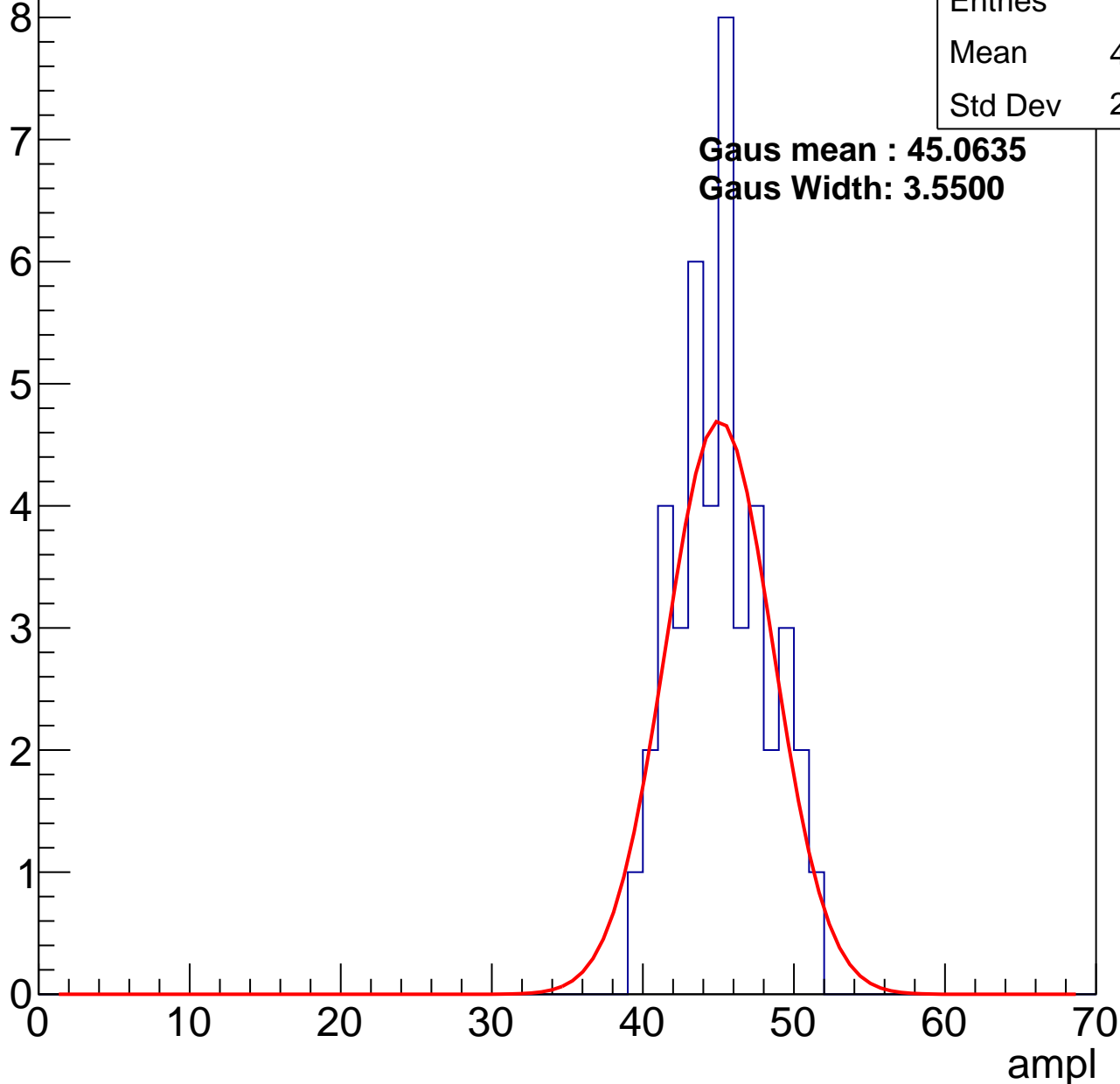
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	43
Mean	44.72
Std Dev	2.944

Gaus mean : 45.0635

Gaus Width: 3.5500



B1L103S, U10-ch73, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	50.56
Std Dev	3.23

Entry

10

8

6

4

2

0

0

10

20

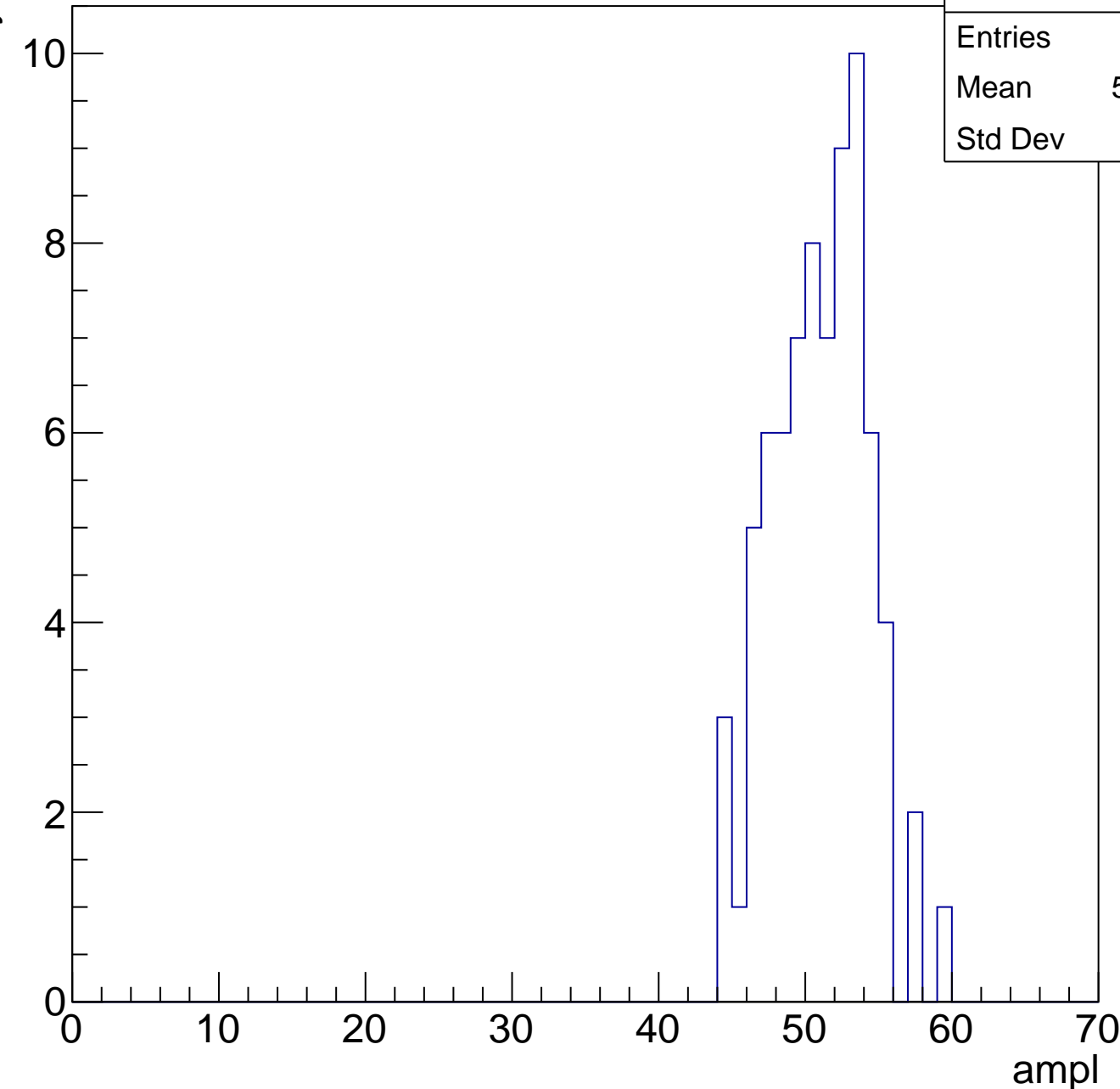
30

40

50

60

ampl



B1L103S, U10-ch73, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	52
Mean	56.88
Std Dev	2.792

Entry

10

8

6

4

2

0

0

10

20

30

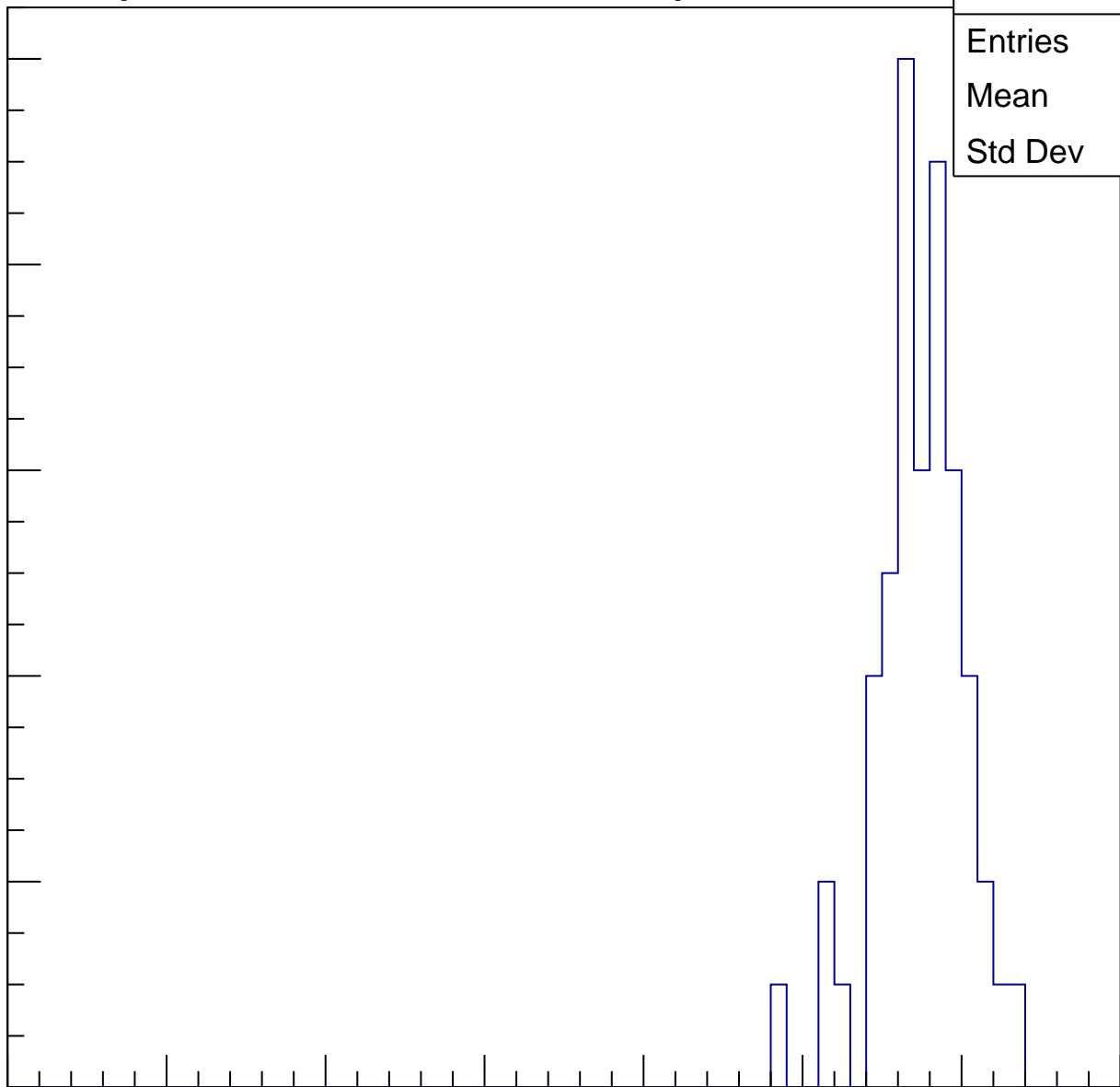
40

50

60

70

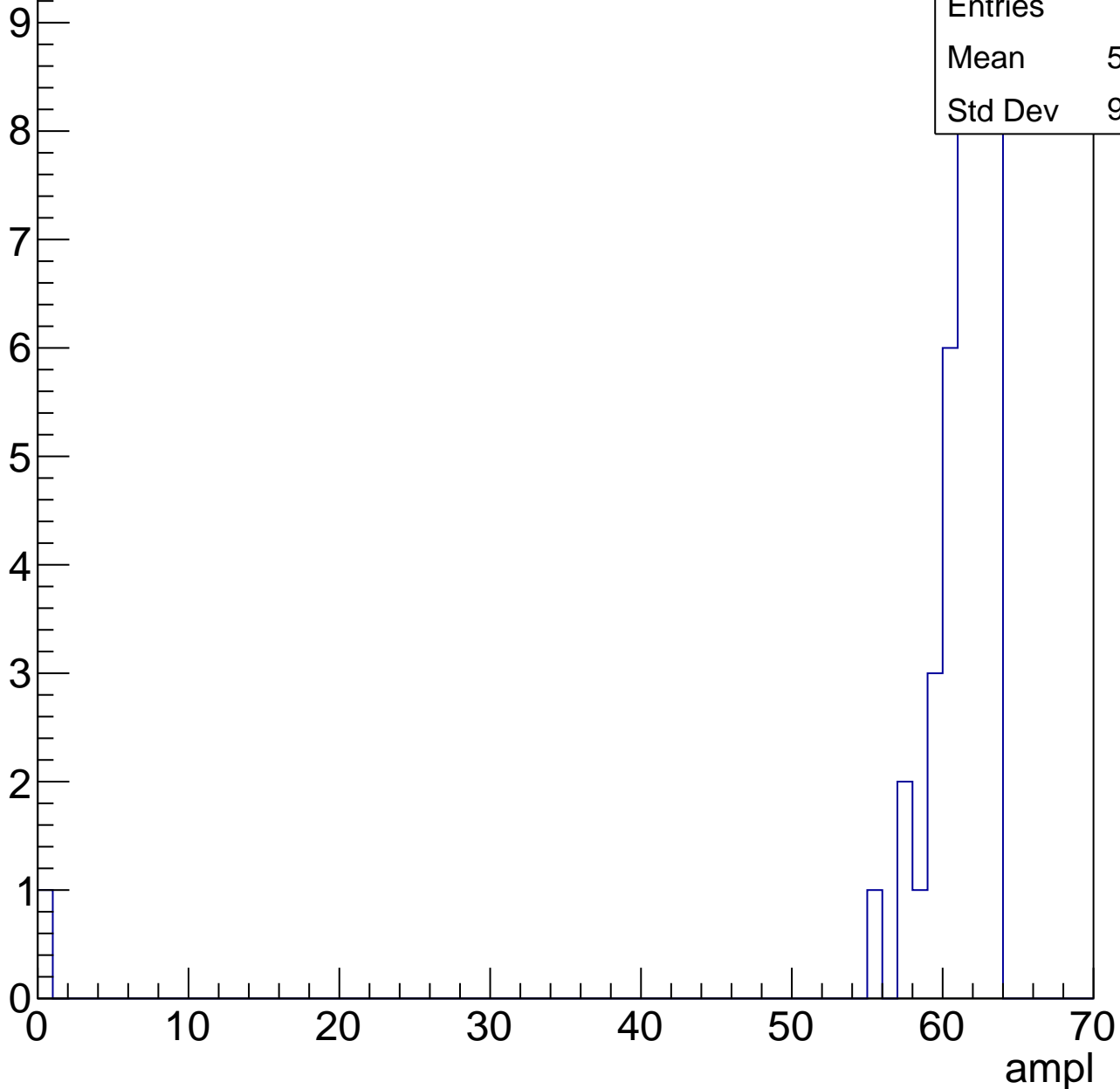
ampl



B1L103S, U10-ch73, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch73, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch73, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	8
Std Dev	11.31

B1L103S, U10-ch74, adc0

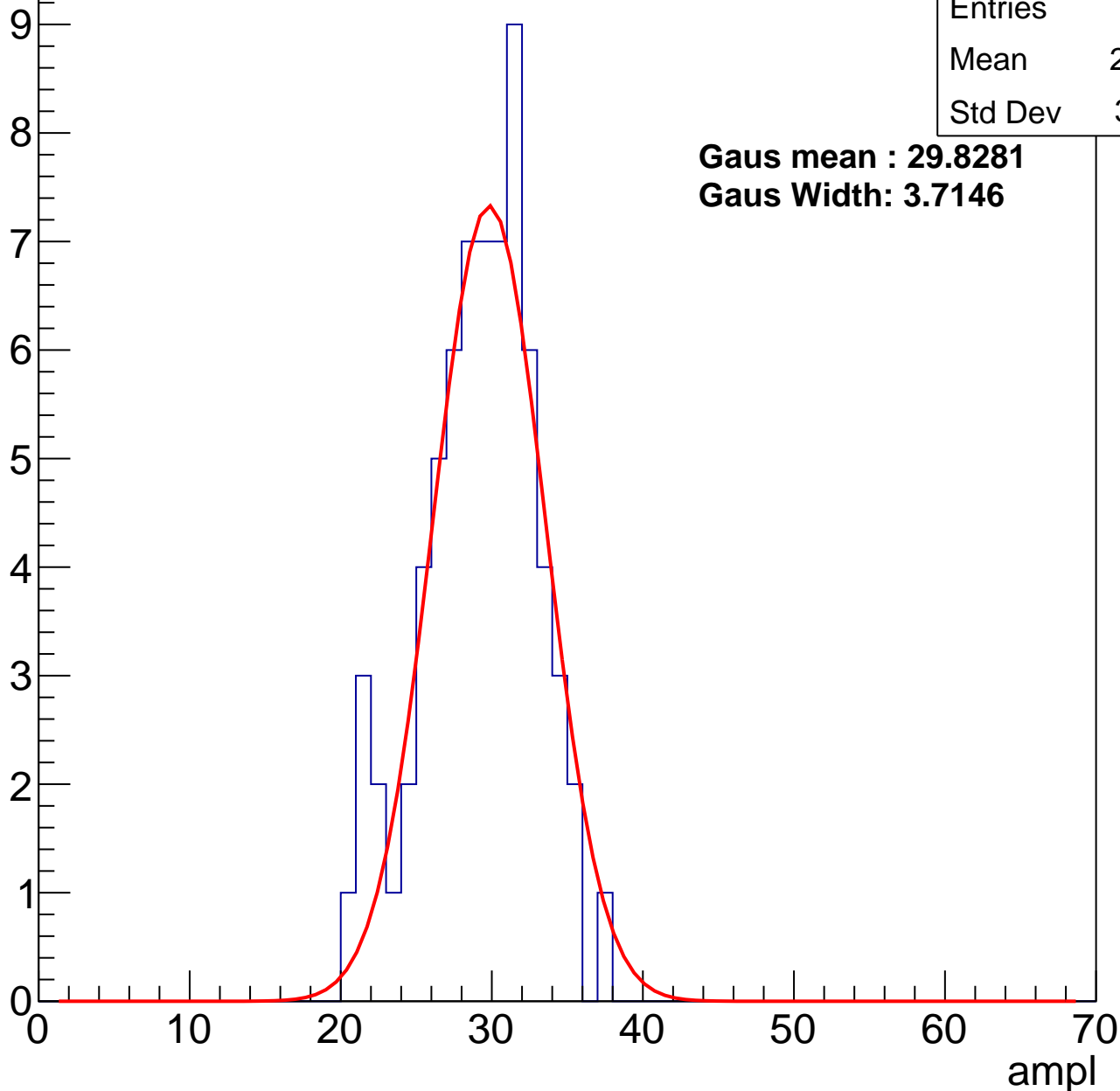
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.73
Std Dev	3.711

Gaus mean : 29.8281

Gaus Width: 3.7146



B1L103S, U10-ch74, adc1

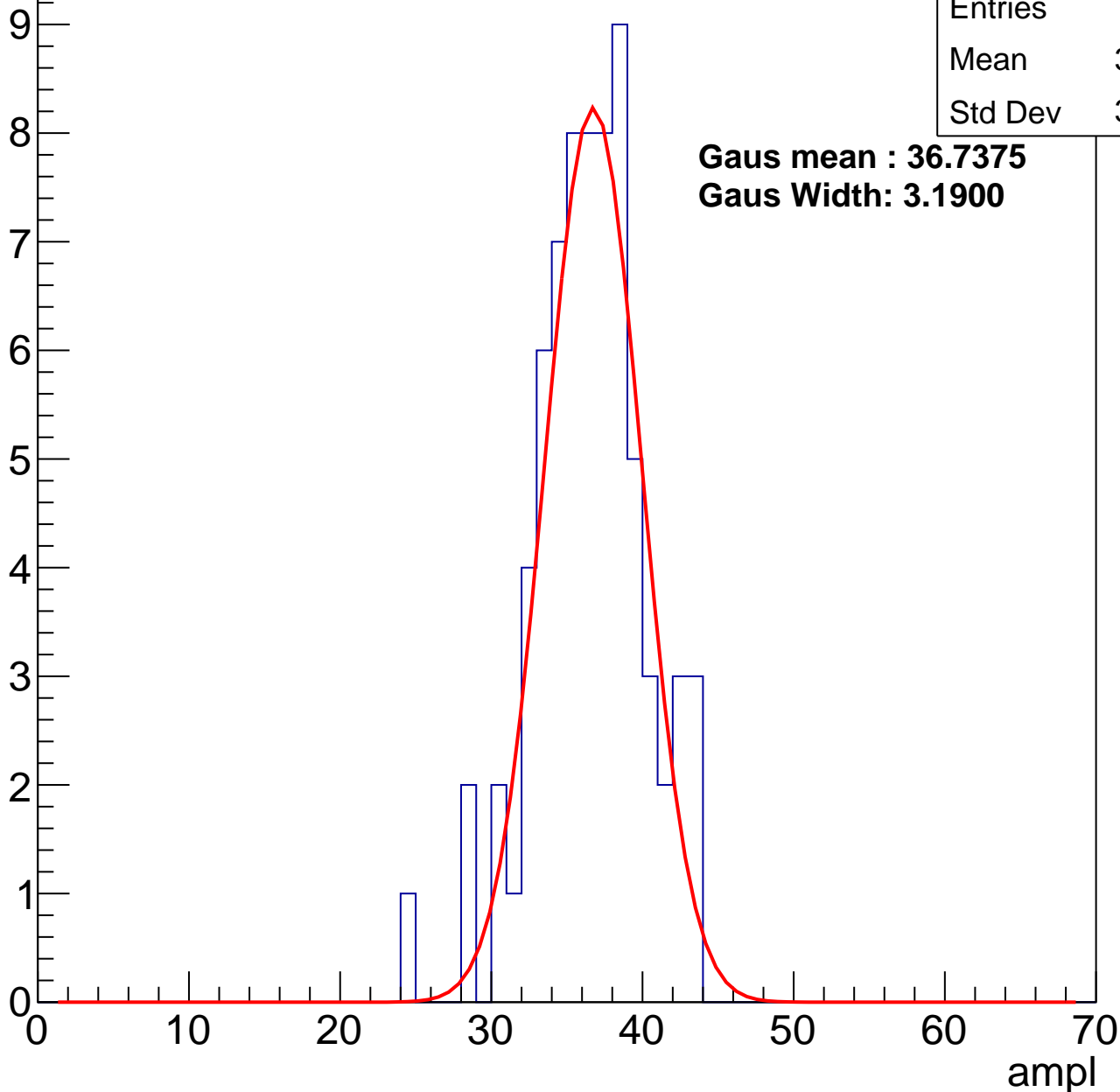
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	36.01
Std Dev	3.661

Gaus mean : 36.7375

Gaus Width: 3.1900



B1L103S, U10-ch74, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	43.63
Std Dev	3.512

Gaus mean : 44.5462

Gaus Width: 3.7259

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

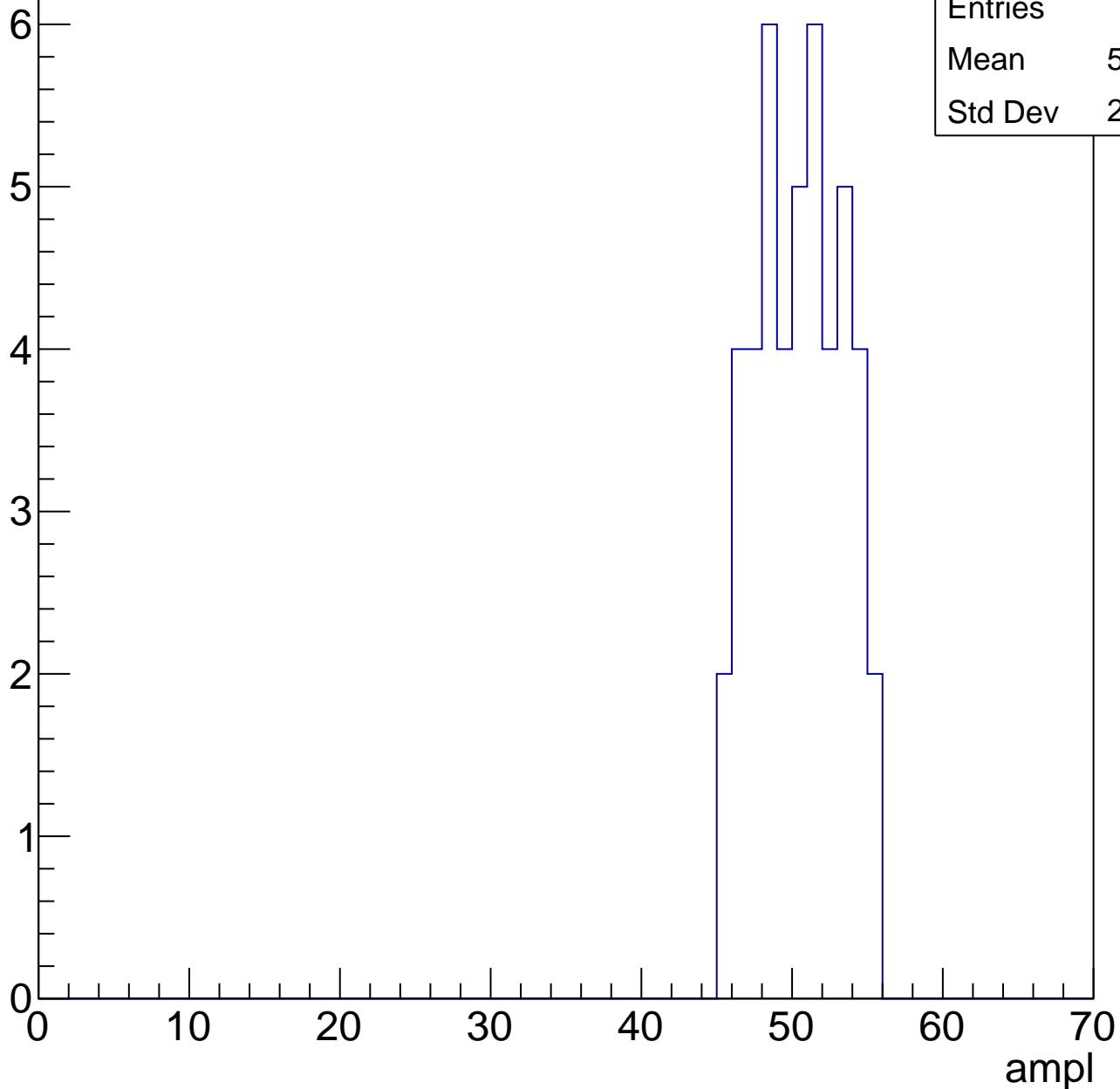
70

B1L103S, U10-ch74, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	50.02
Std Dev	2.794

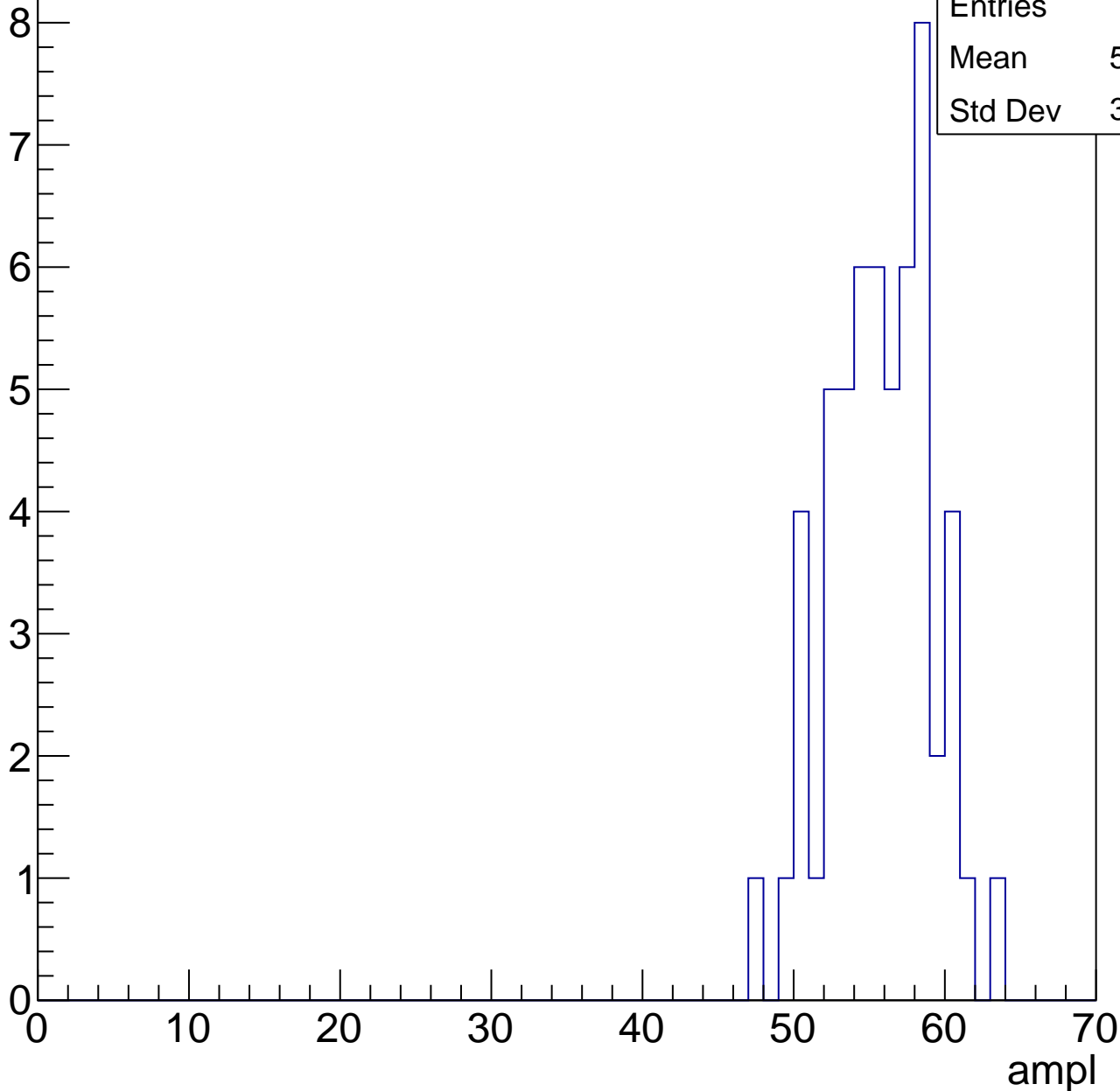


B1L103S, U10-ch74, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	55.25
Std Dev	3.318

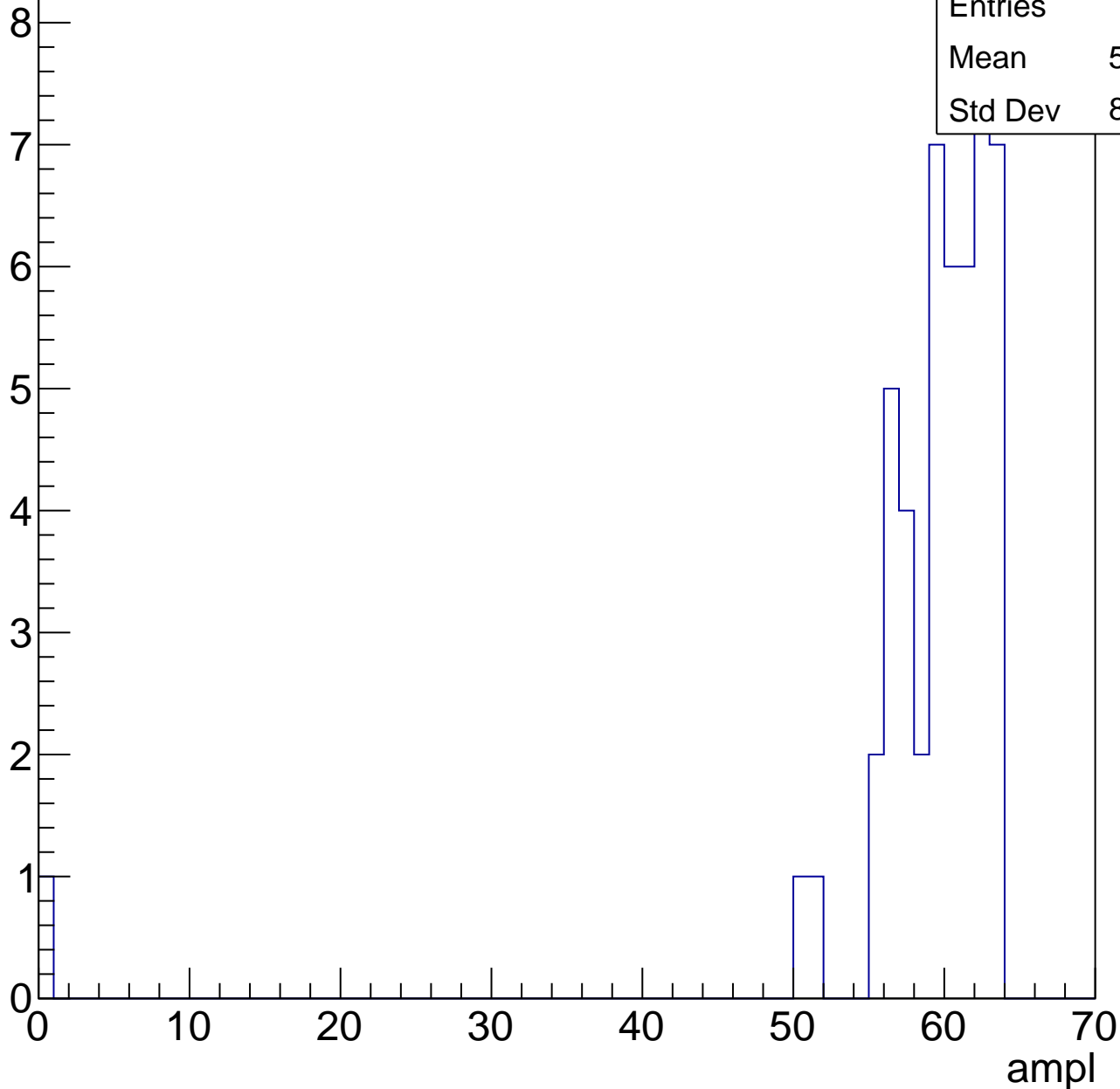


B1L103S, U10-ch74, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.22
Std Dev	8.837



B1L103S, U10-ch74, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

1

0.5

0

ampl

Entries	8
Mean	61.5
Std Dev	1.225

0 10 20 30 40 50 60 70

B1L103S, U10-ch74, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	20
Std Dev	0

B1L103S, U10-ch75, adc0

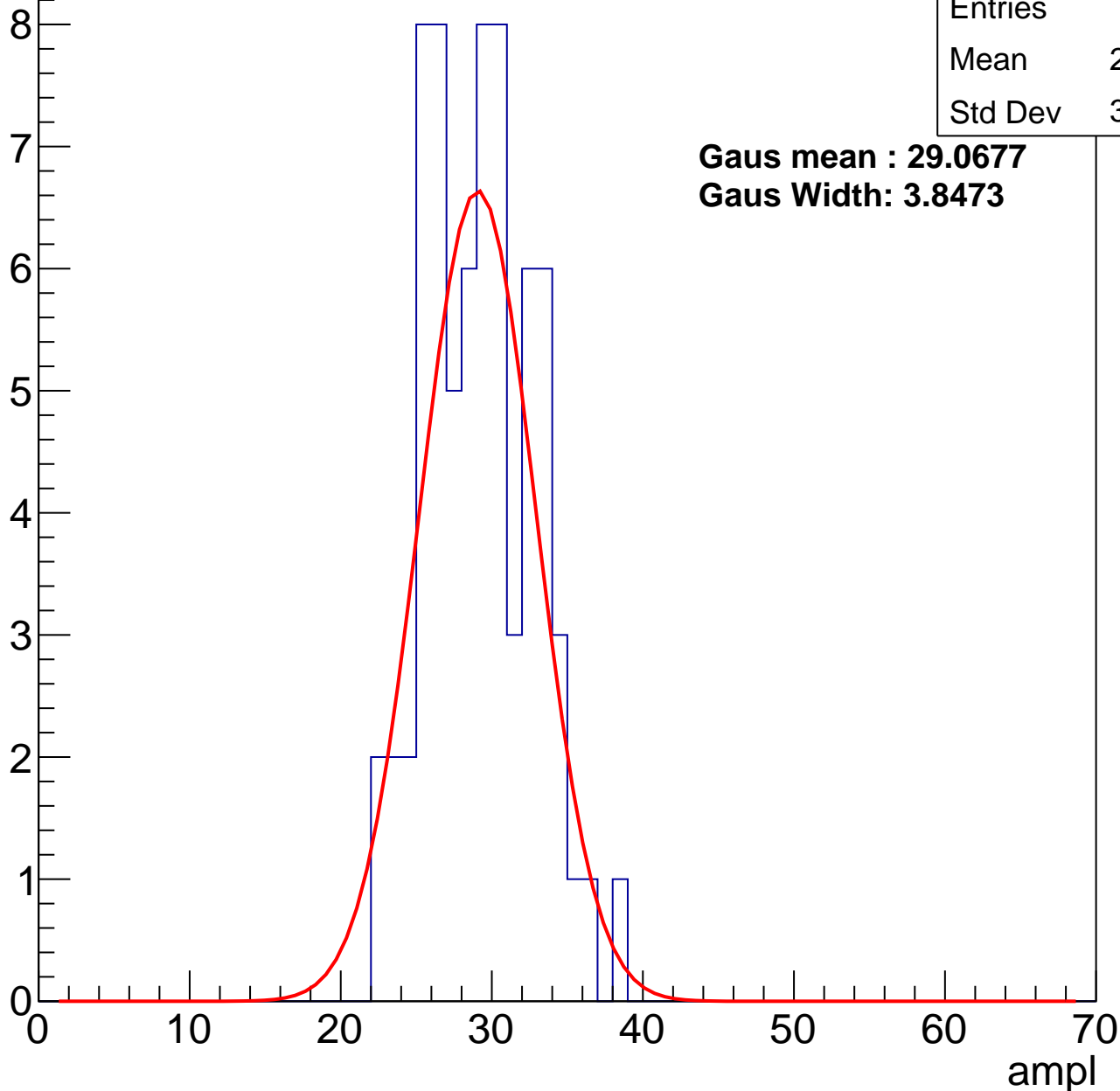
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	28.79
Std Dev	3.492

Gaus mean : 29.0677

Gaus Width: 3.8473



B1L103S, U10-ch75, adc1

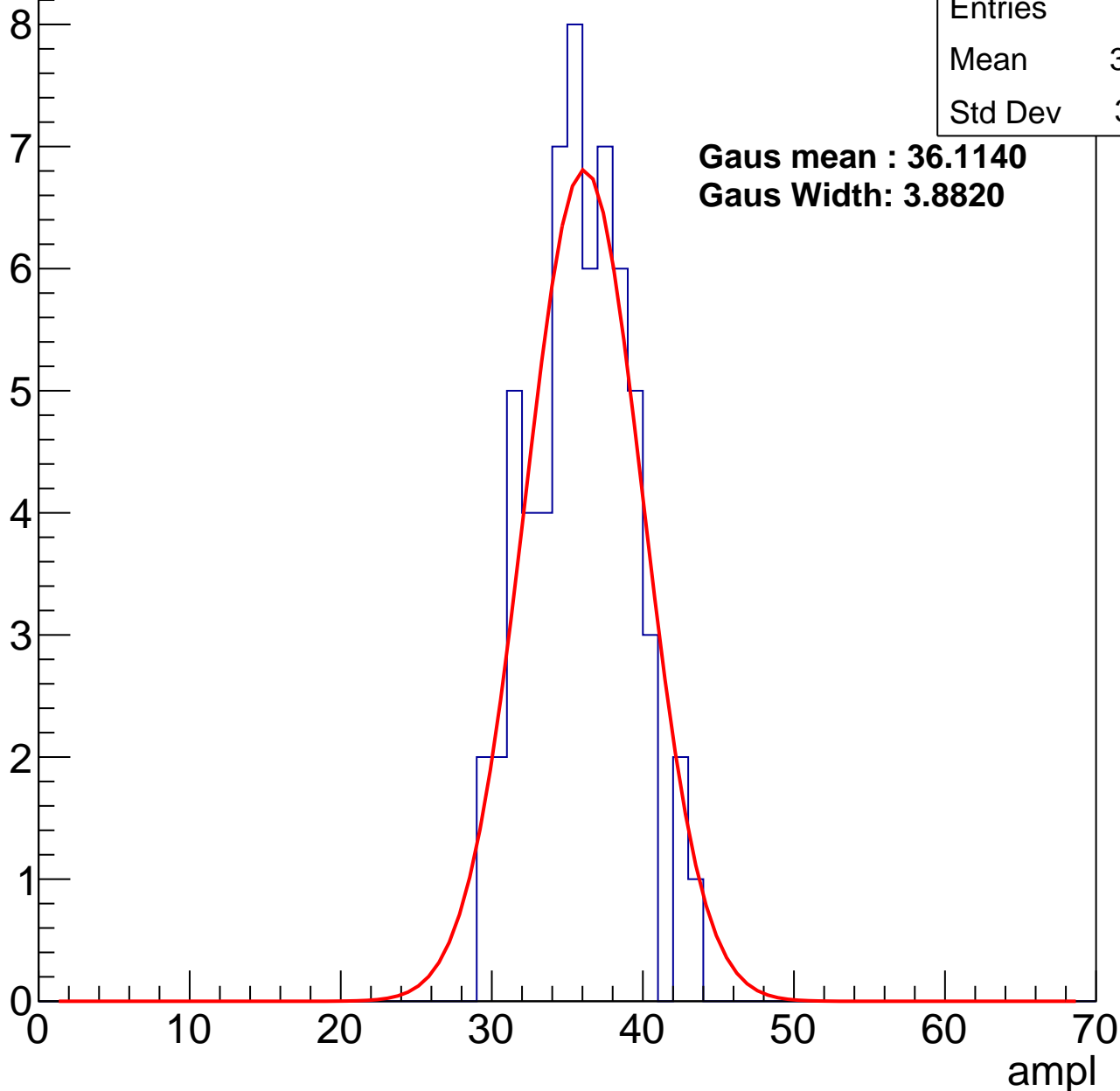
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	35.42
Std Dev	3.251

Gaus mean : 36.1140

Gaus Width: 3.8820



B1L103S, U10-ch75, adc2

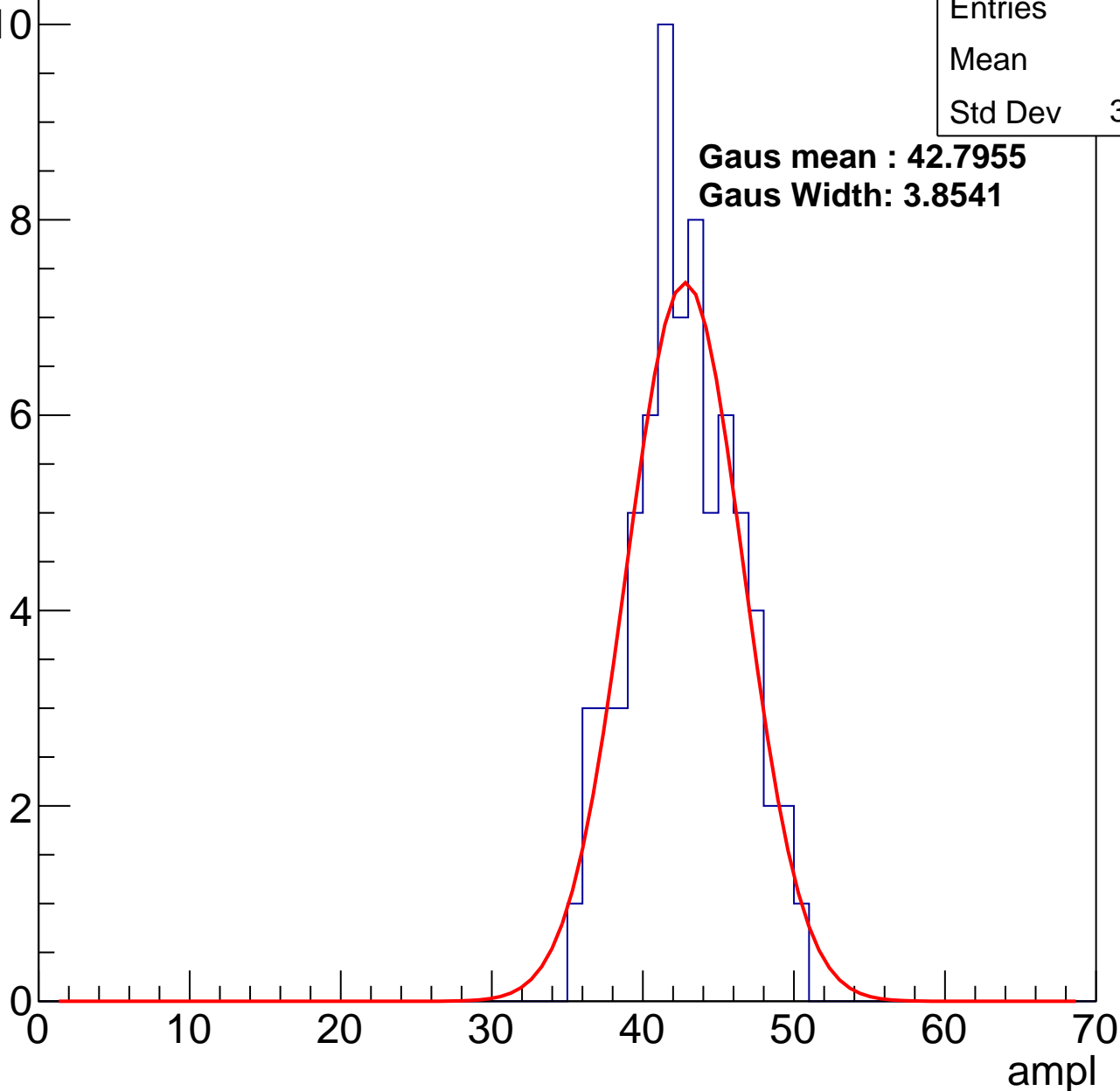
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	42.3
Std Dev	3.458

Gaus mean : 42.7955

Gaus Width: 3.8541

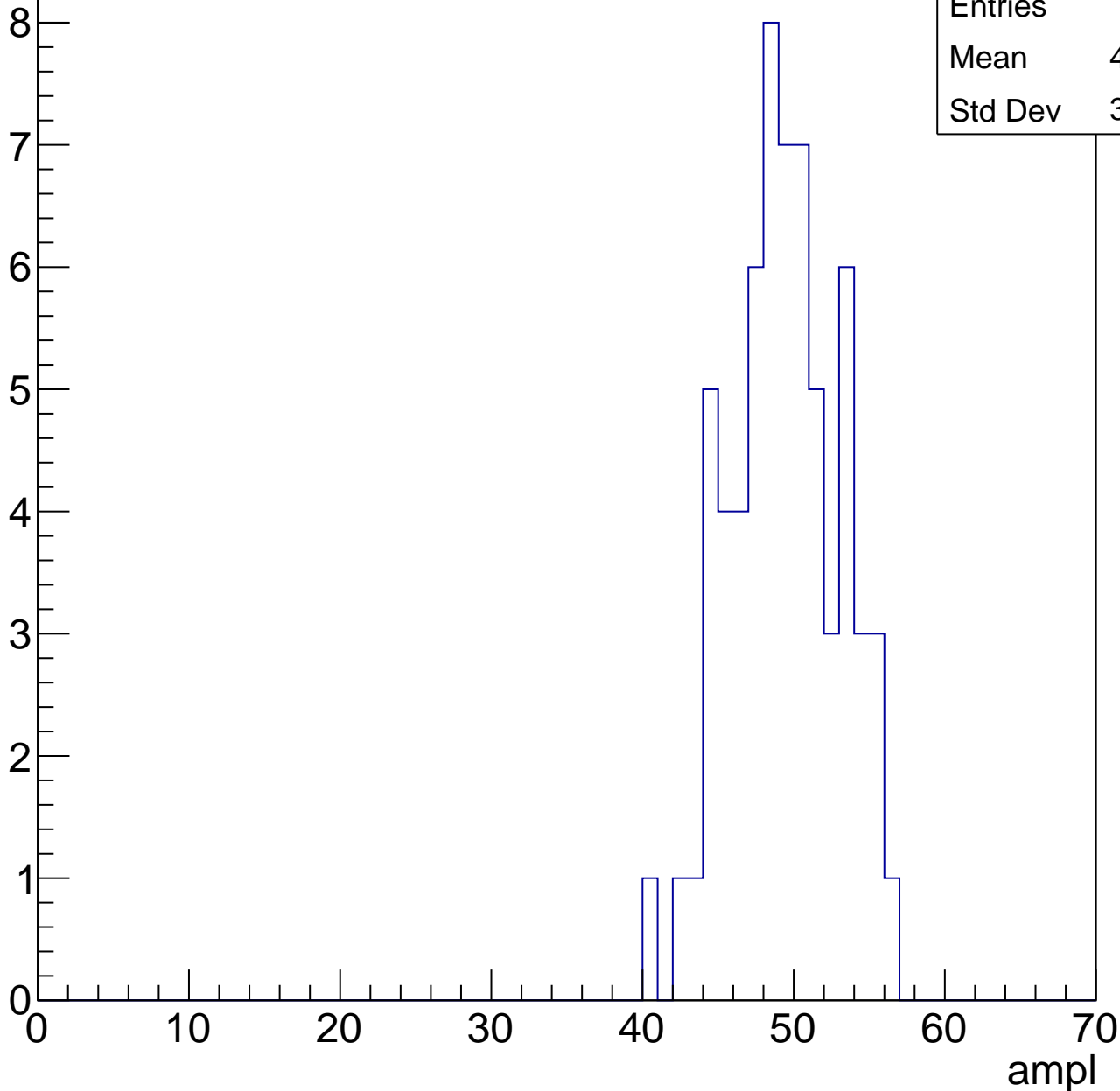


B1L103S, U10-ch75, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	48.92
Std Dev	3.518

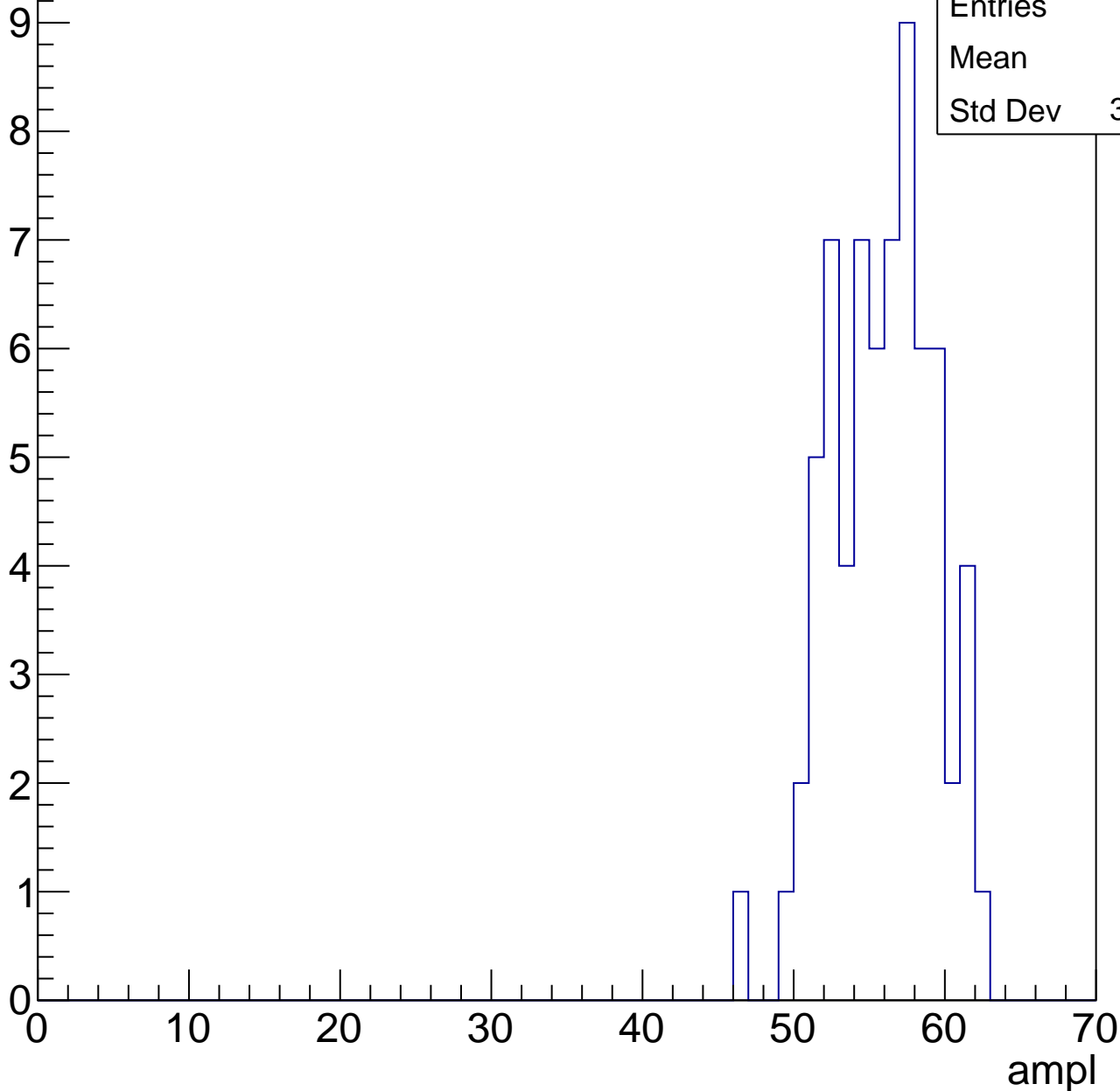


B1L103S, U10-ch75, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.4
Std Dev	3.339



B1L103S, U10-ch75, adc5

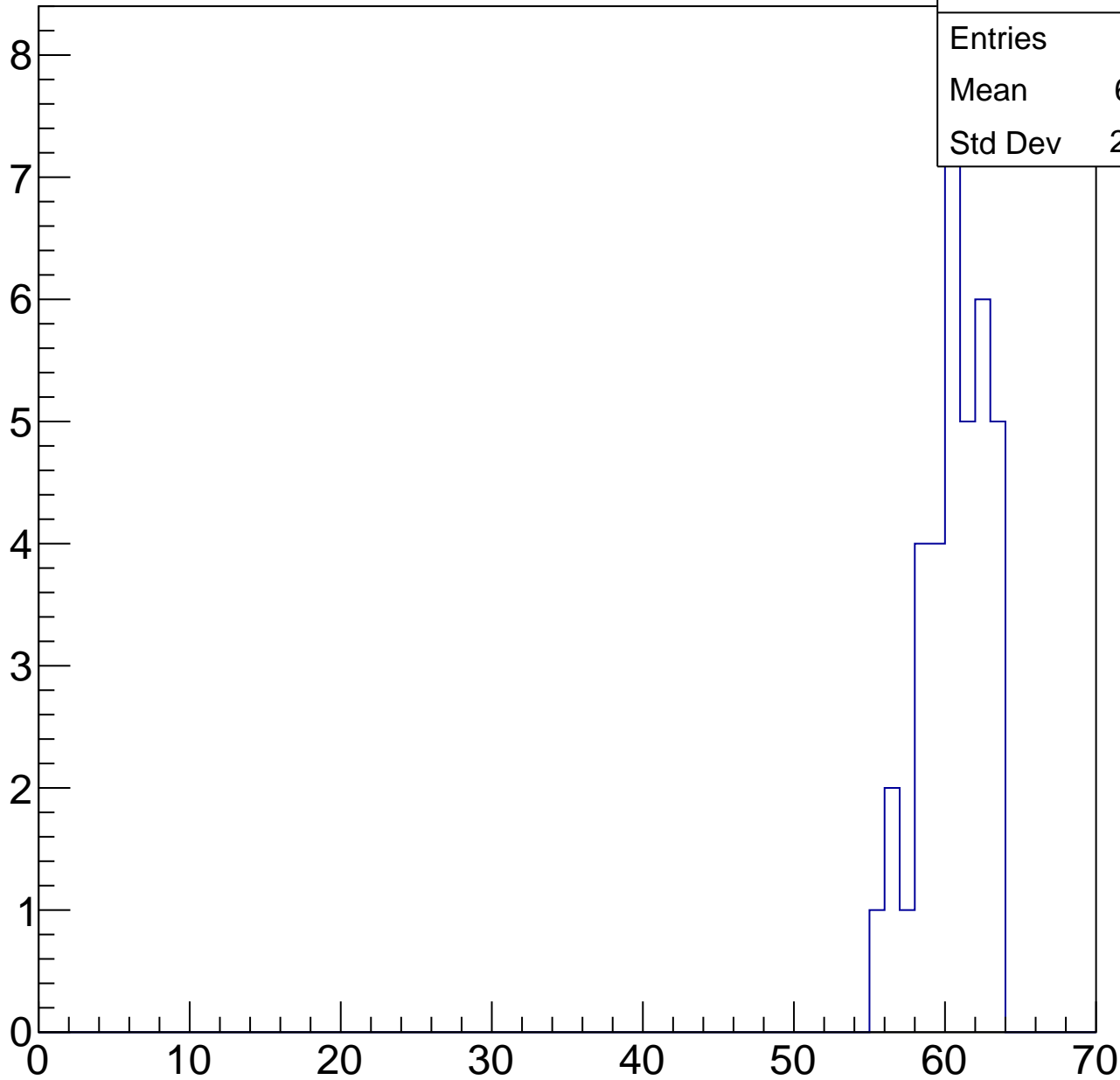
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	36
Mean	60.11
Std Dev	2.105

ampl

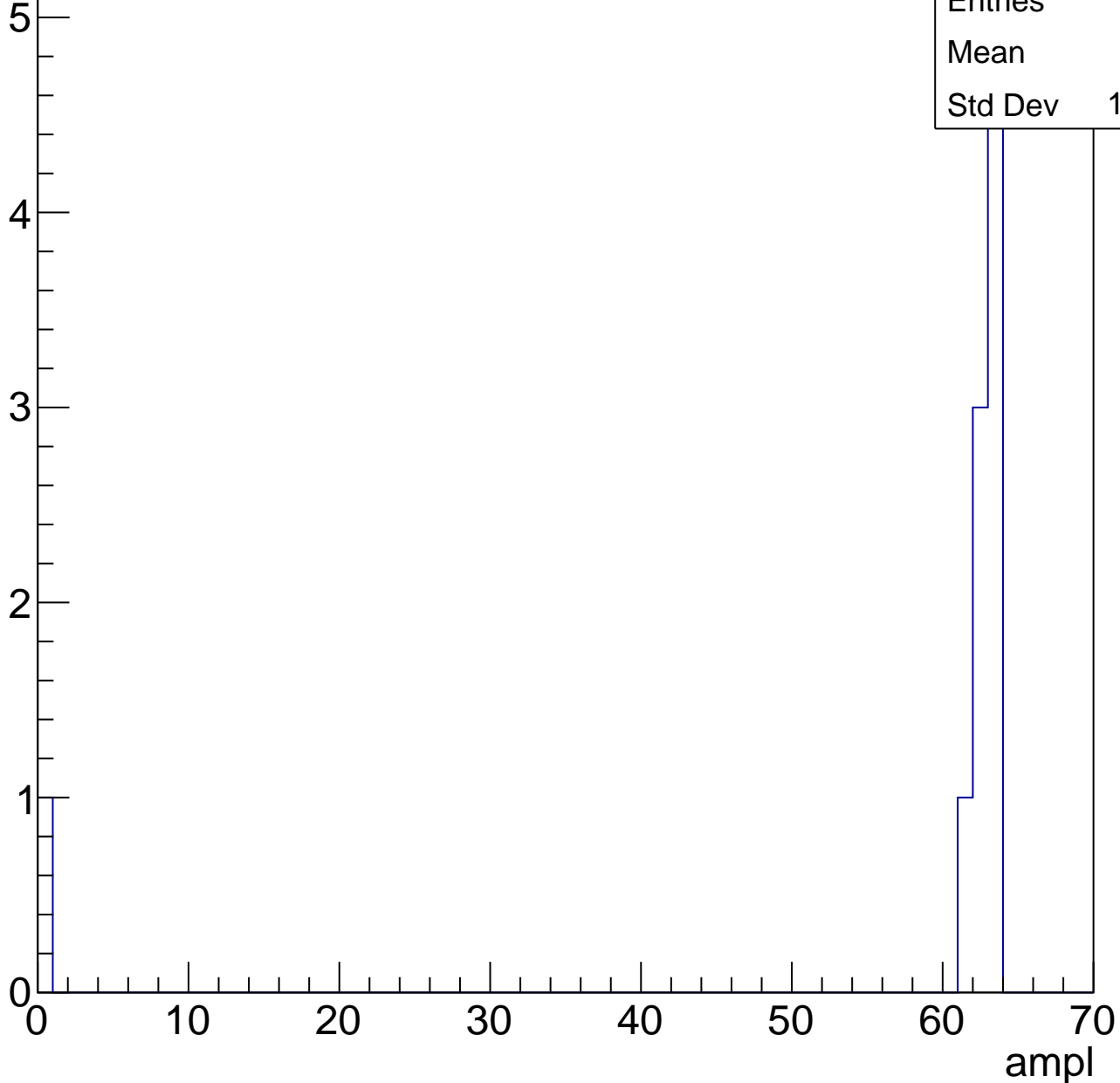


B1L103S, U10-ch75, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	10
Mean	56.2
Std Dev	18.74



B1L103S, U10-ch75, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U10-ch76, adc0

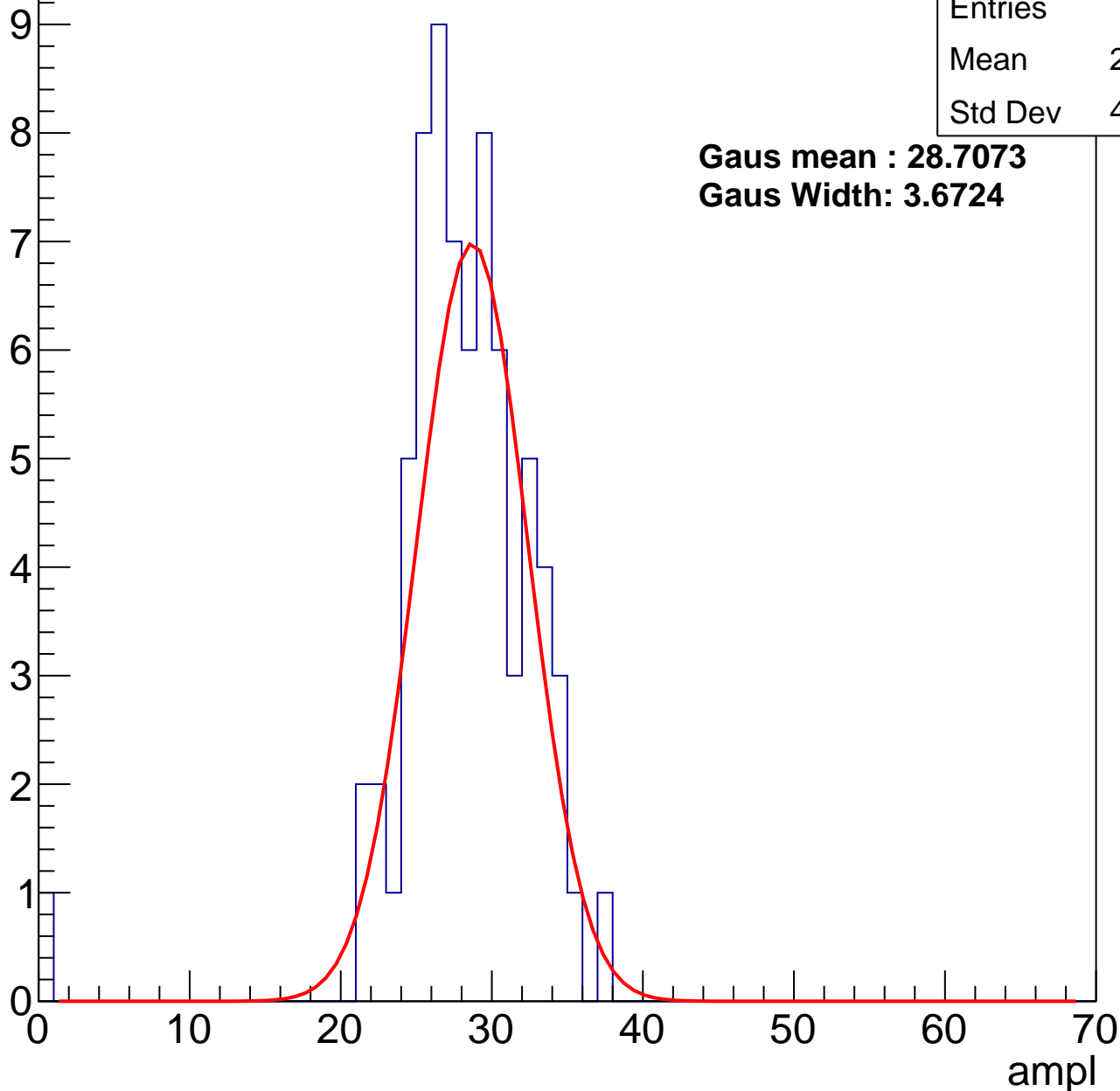
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	27.65
Std Dev	4.776

Gaus mean : 28.7073

Gaus Width: 3.6724



B1L103S, U10-ch76, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	73
Mean	35.14
Std Dev	3.473

Gaus mean : 35.6355

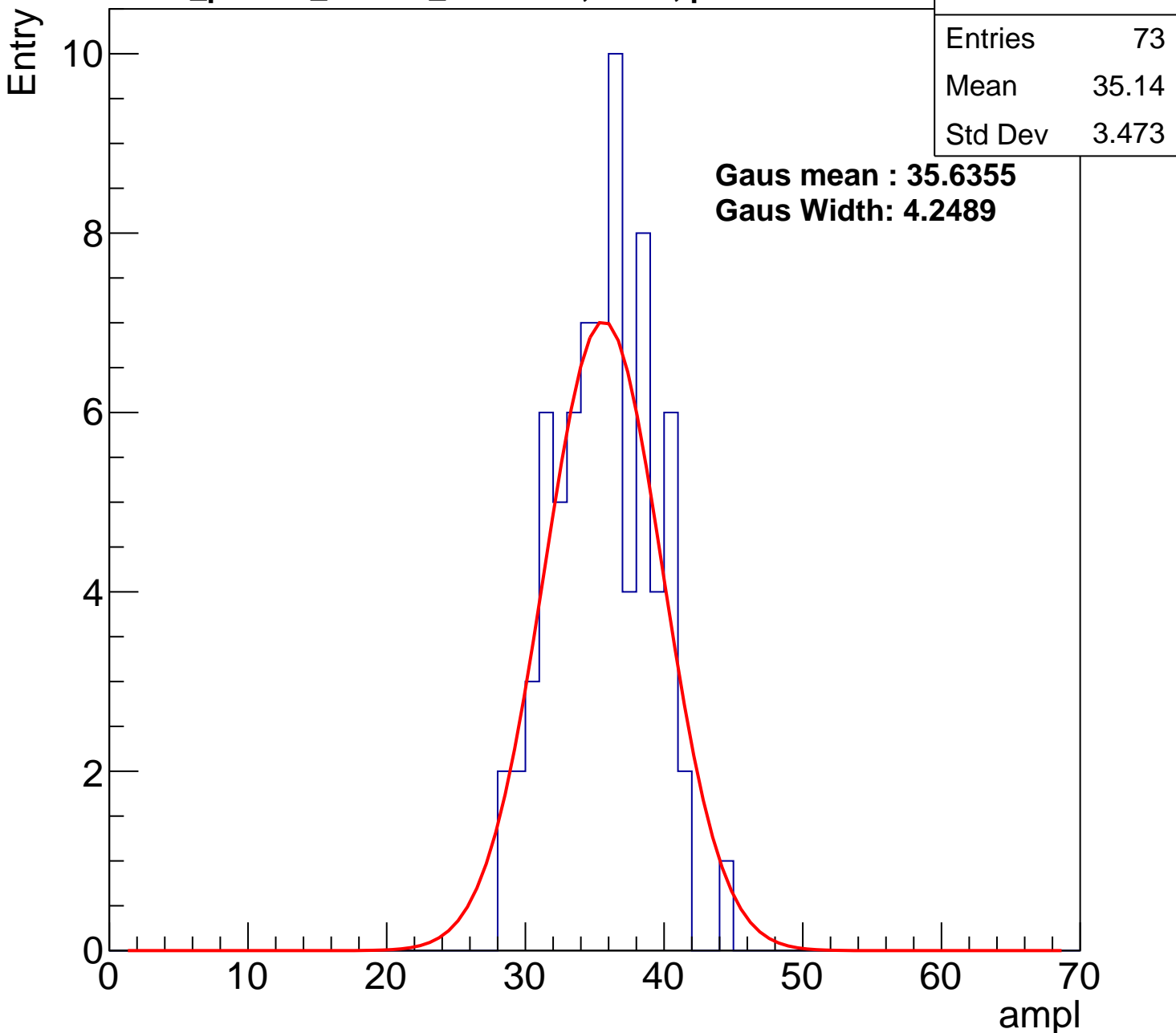
Gaus Width: 4.2489

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch76, adc2

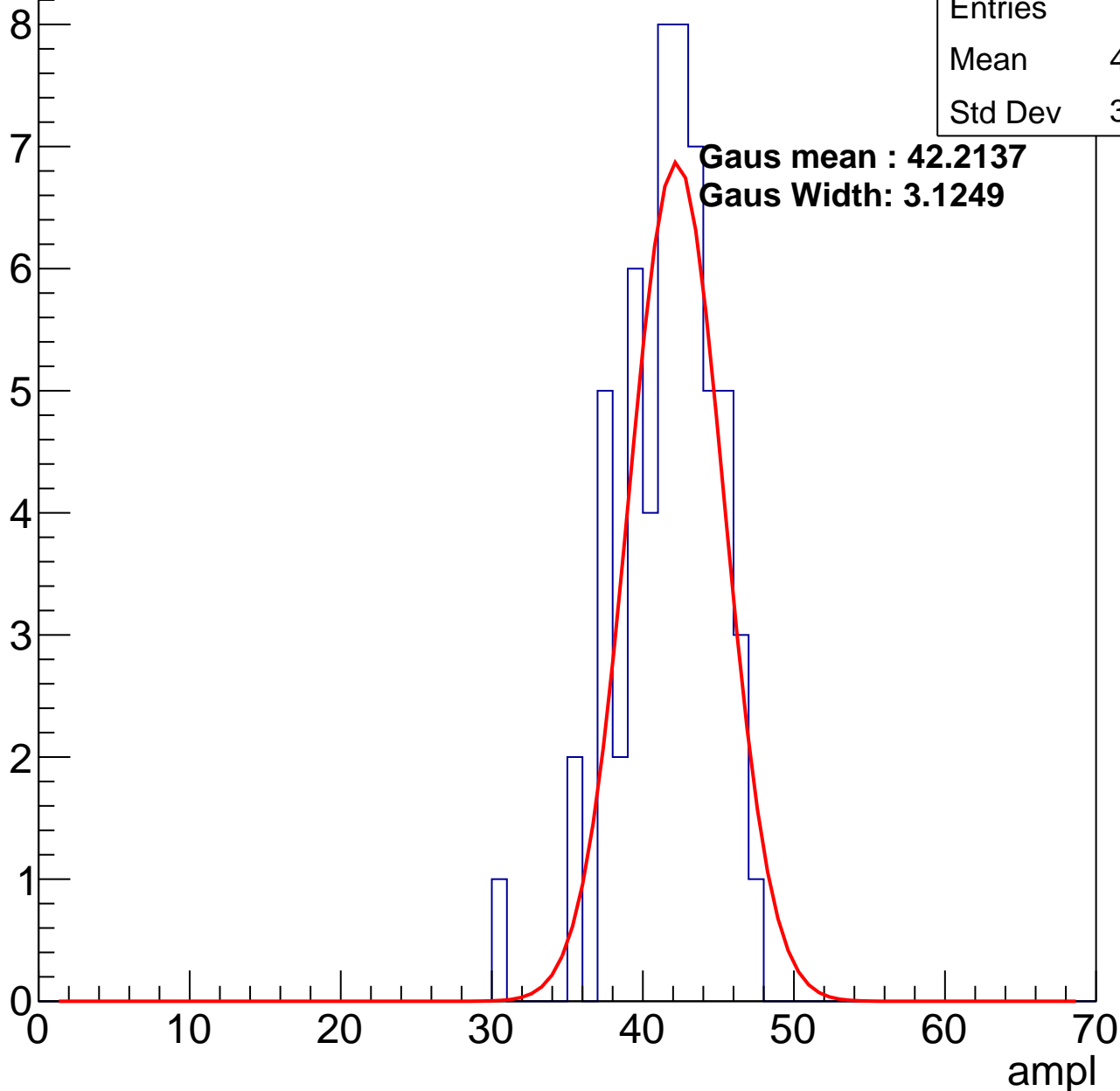
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	41.23
Std Dev	3.212

Gaus mean : 42.2137

Gaus Width: 3.1249



B1L103S, U10-ch76, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	60
Mean	47.85
Std Dev	3.145

Entry

10

8

6

4

2

0

0

10

20

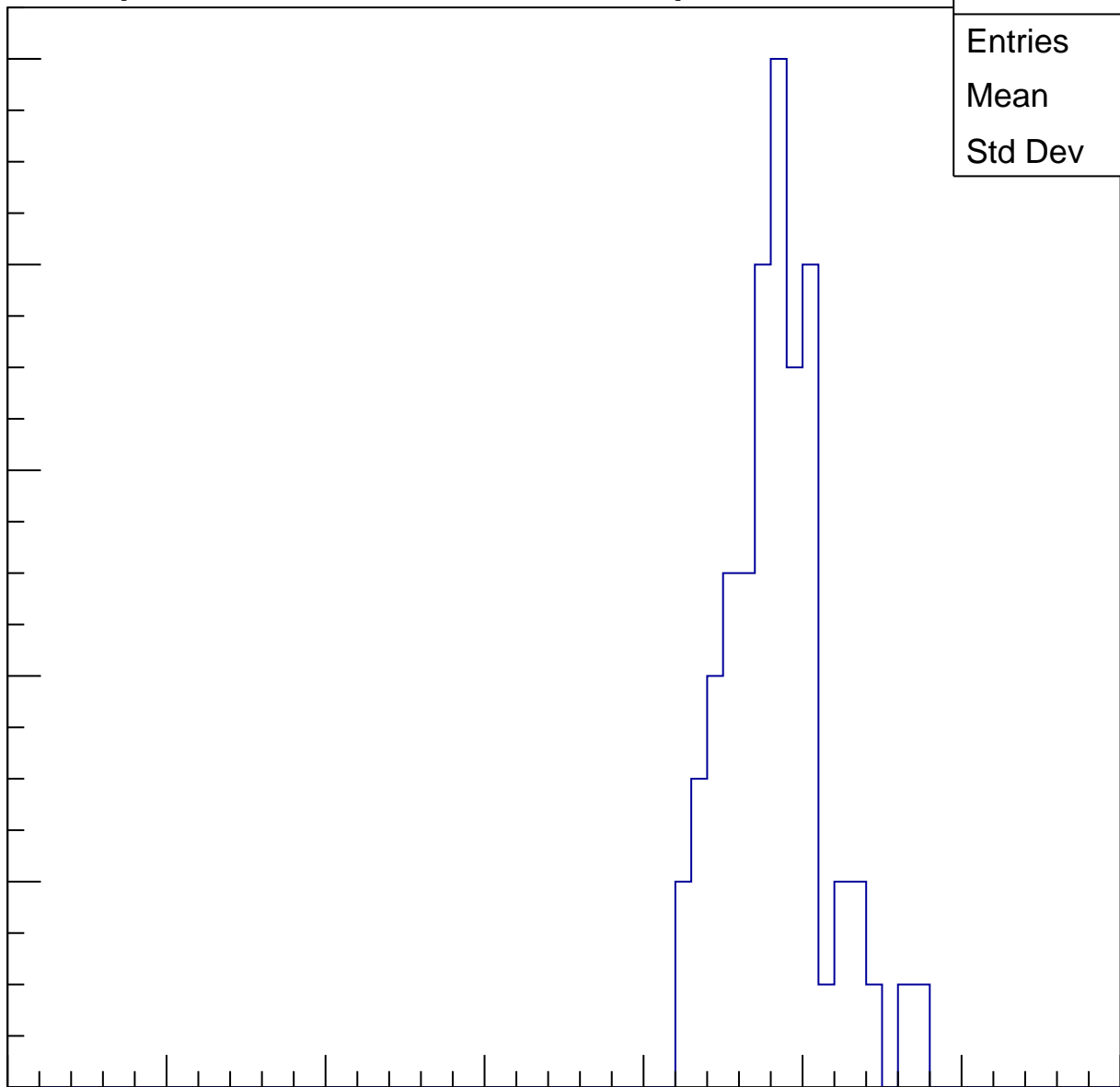
30

40

50

60

ampl

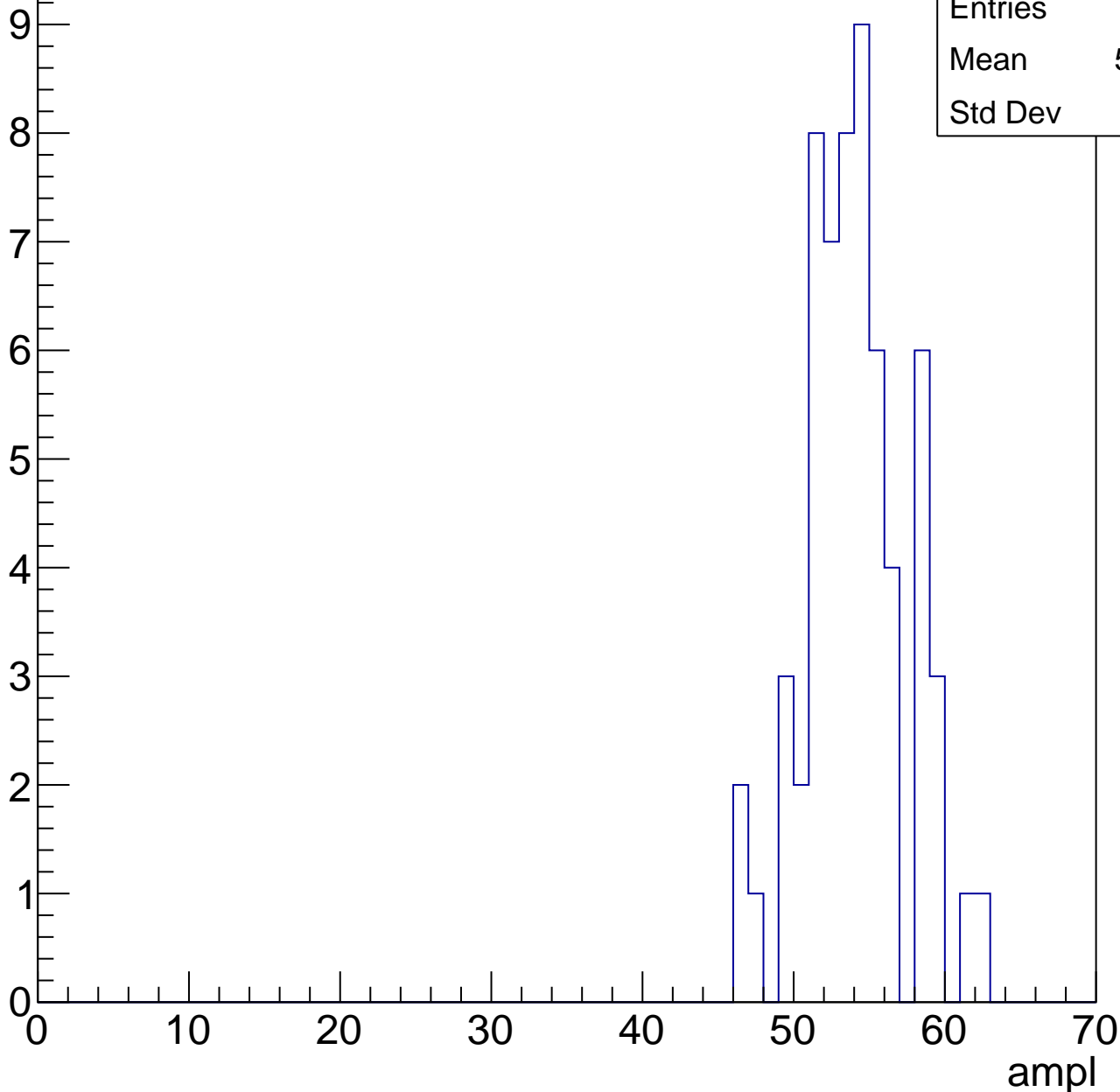


B1L103S, U10-ch76, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

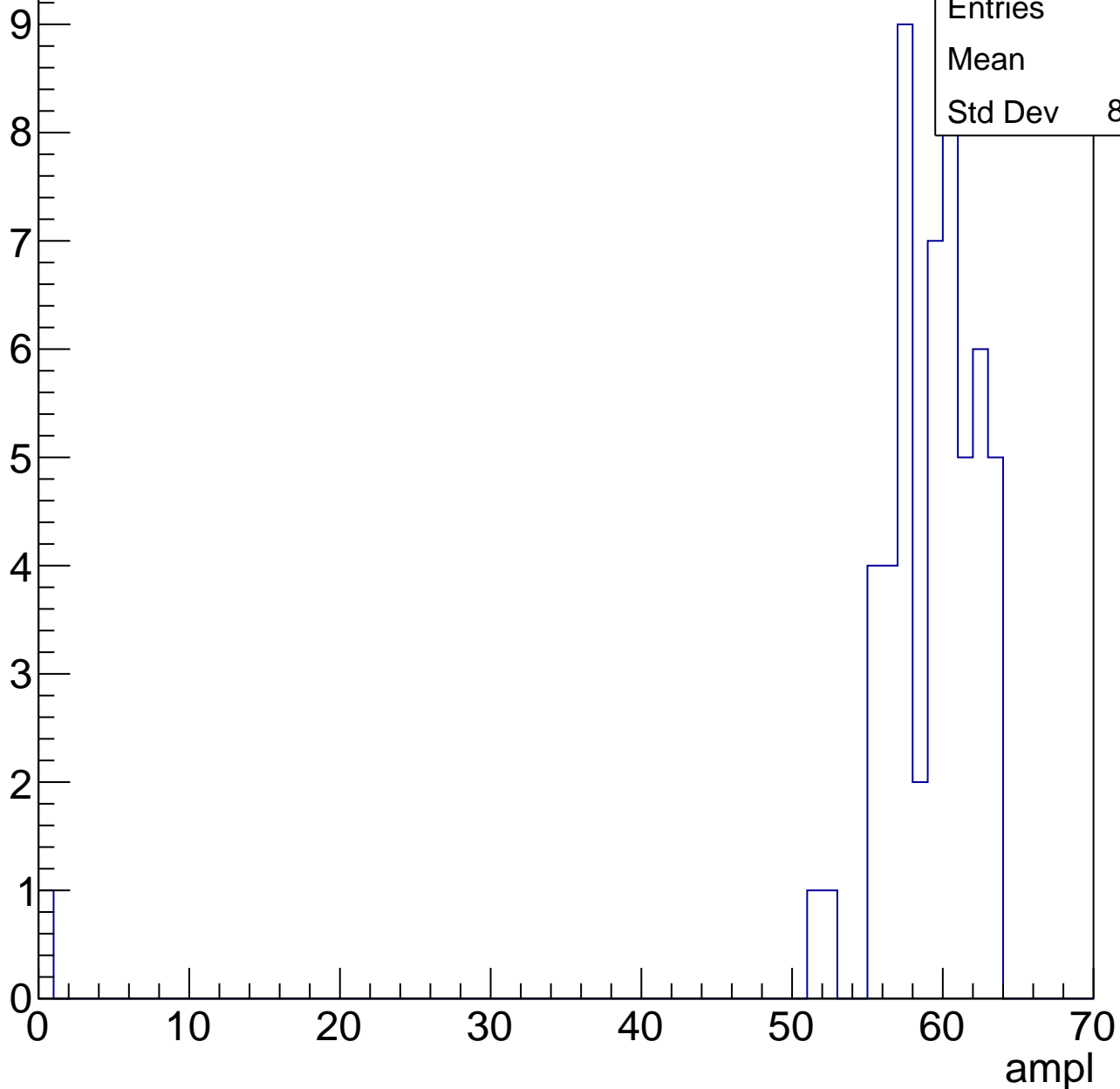
Entries	61
Mean	53.61
Std Dev	3.36



B1L103S, U10-ch76, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

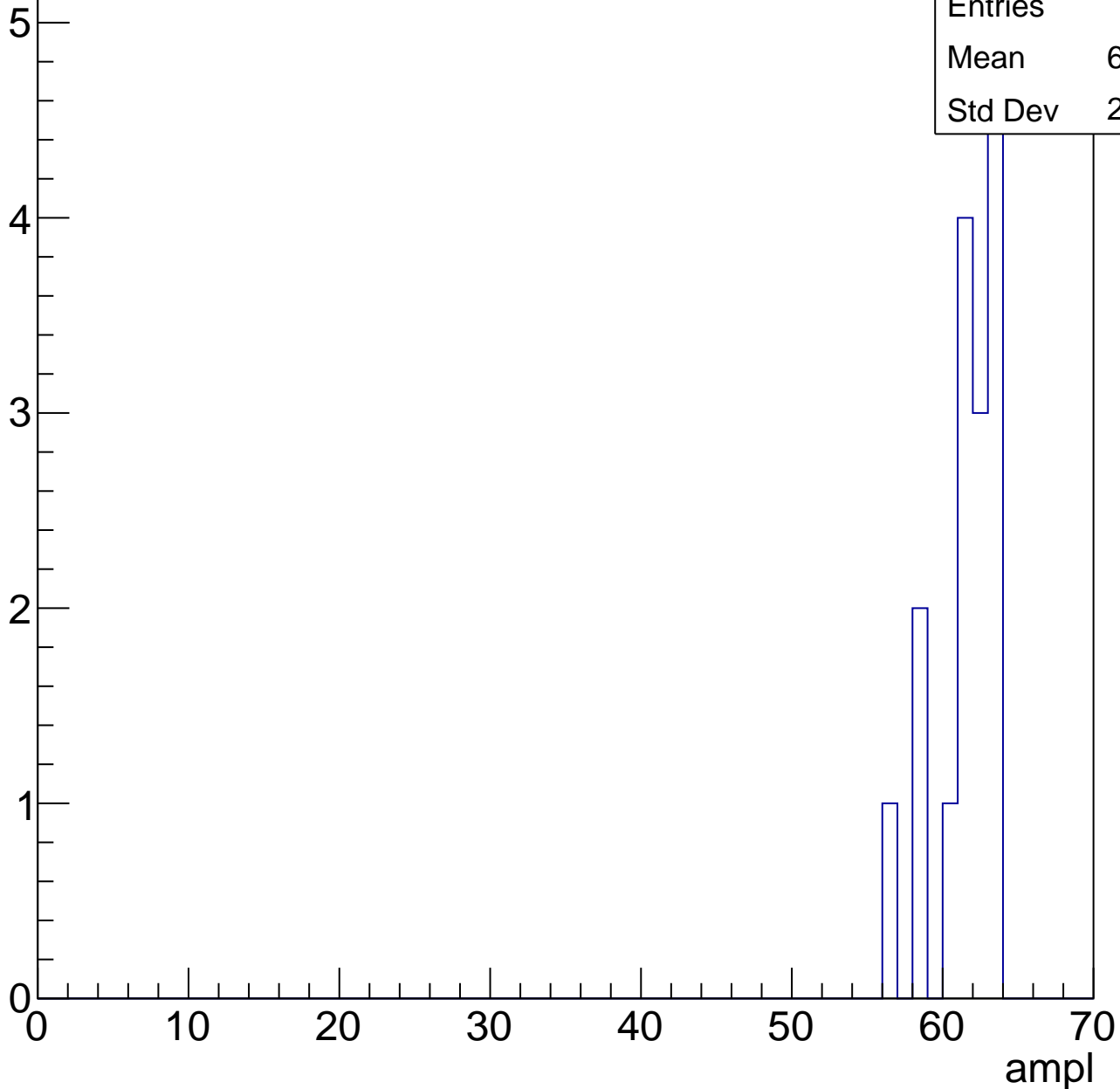


B1L103S, U10-ch76, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	16
Mean	61.06
Std Dev	2.045



B1L103S, U10-ch76, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch77, adc0

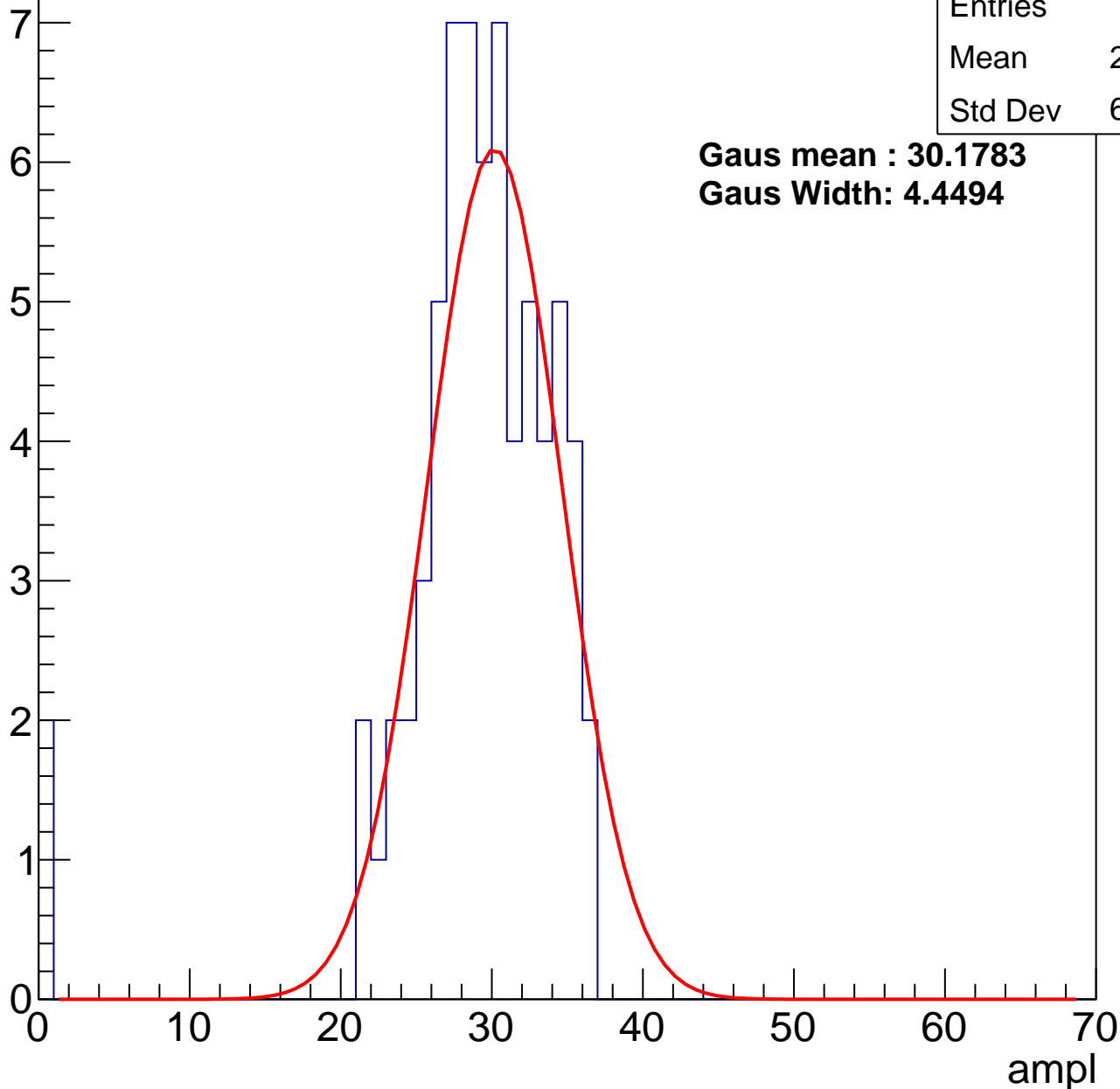
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	28.38
Std Dev	6.162

Gaus mean : 30.1783

Gaus Width: 4.4494



B1L103S, U10-ch77, adc1

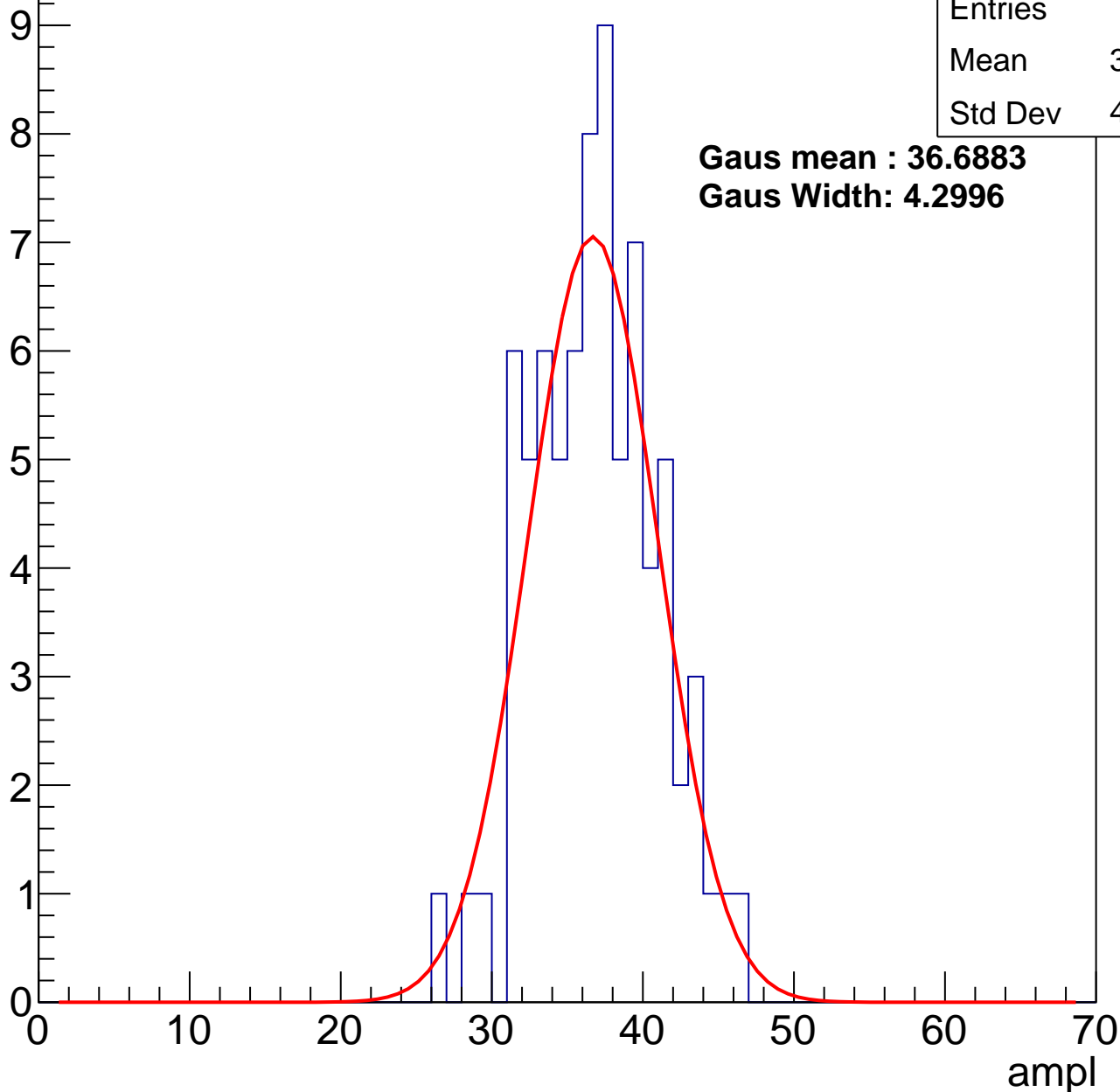
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.42
Std Dev	4.037

Gaus mean : 36.6883

Gaus Width: 4.2996



B1L103S, U10-ch77, adc2

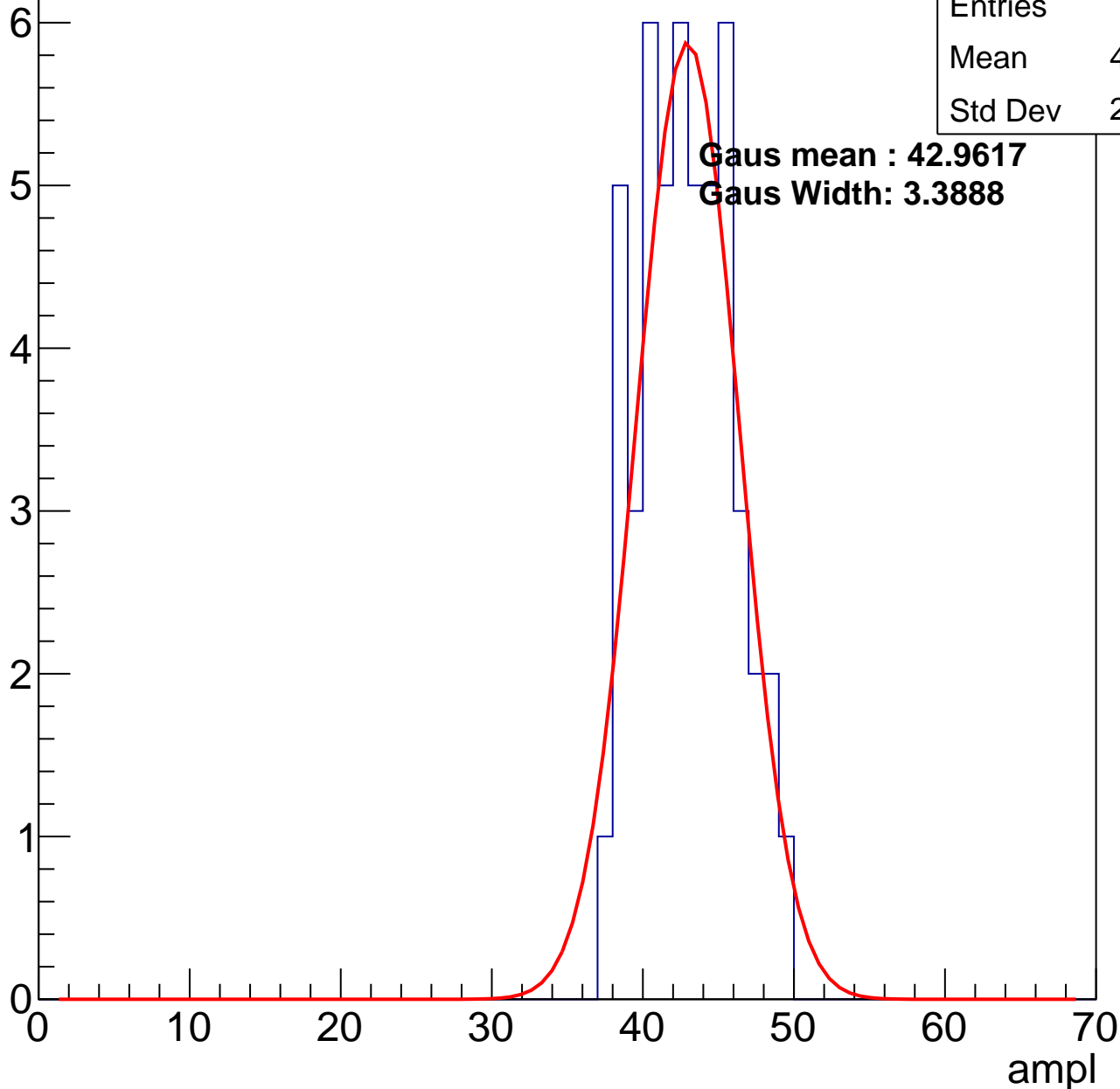
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	42.46
Std Dev	2.995

Gaus mean : 42.9617

Gaus Width: 3.3888

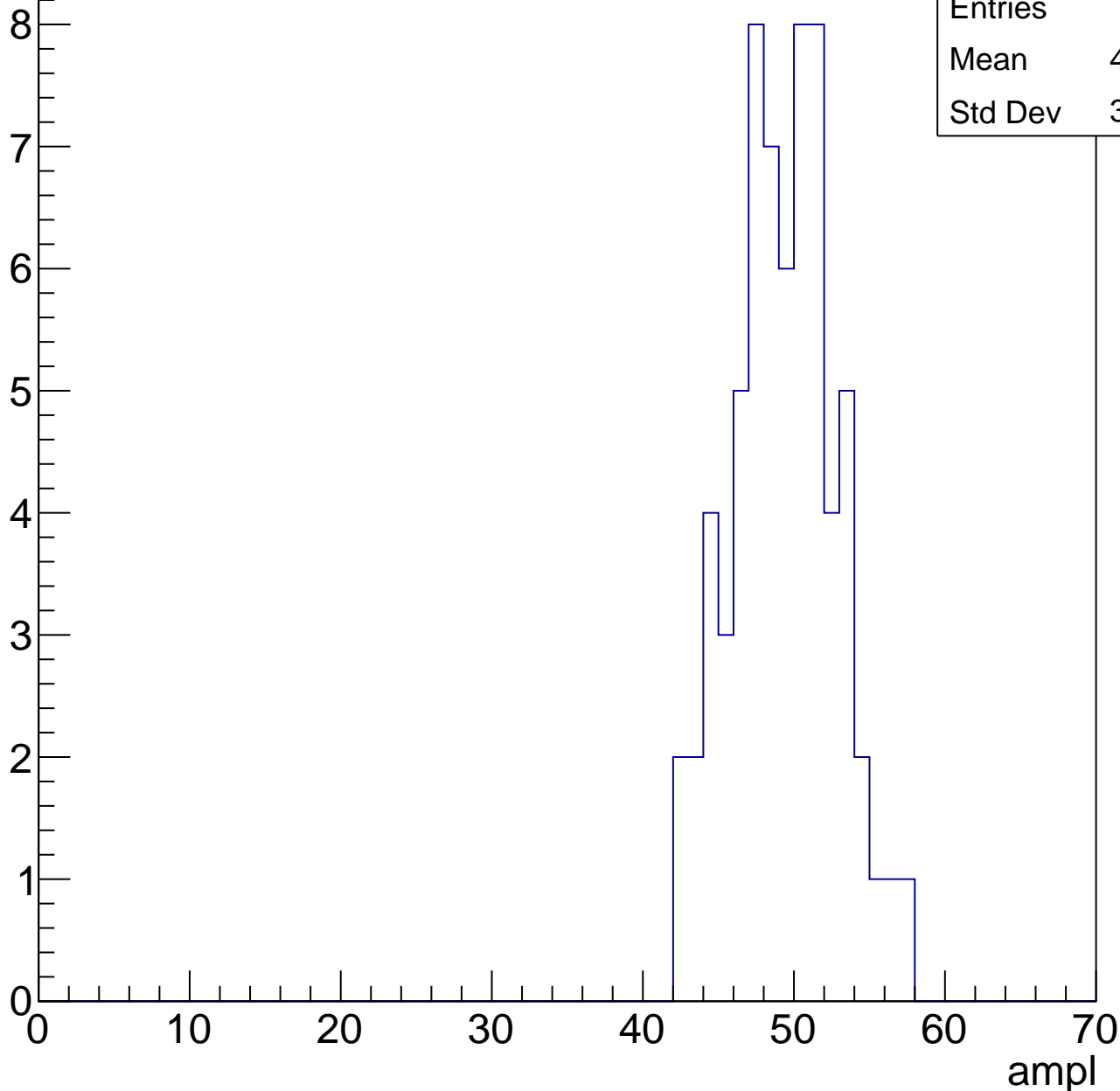


B1L103S, U10-ch77, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	48.87
Std Dev	3.359

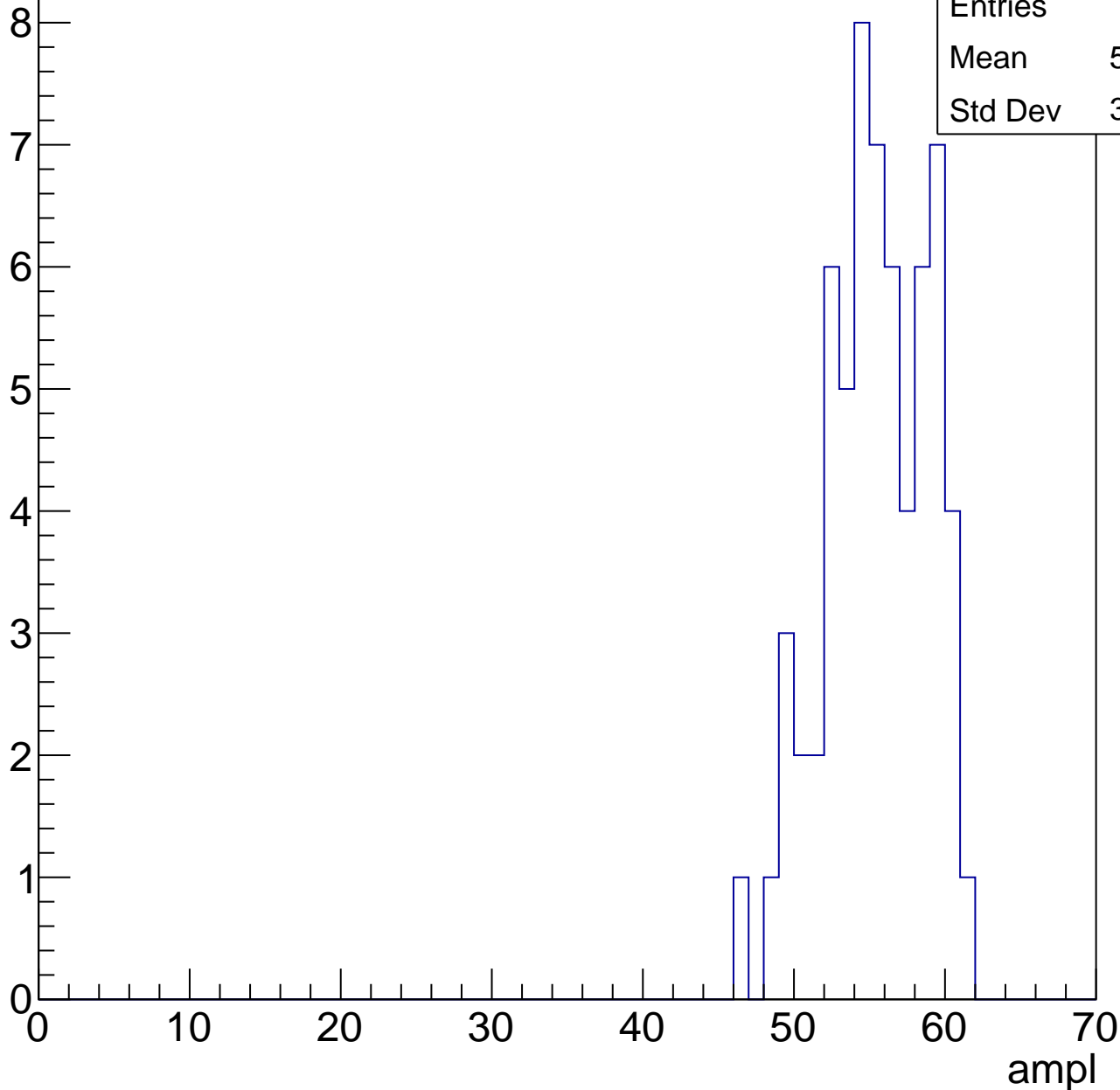


B1L103S, U10-ch77, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	54.97
Std Dev	3.395

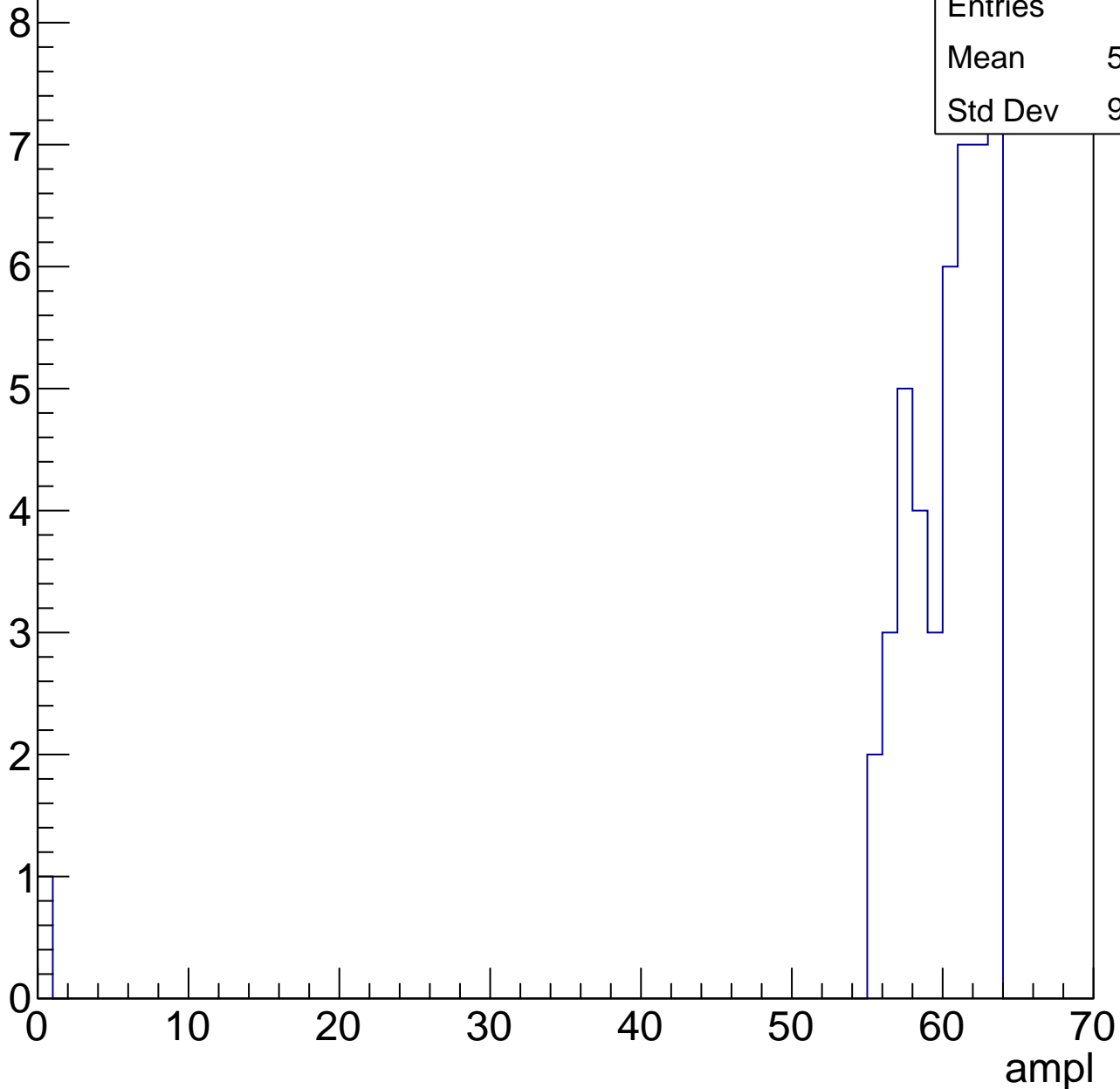


B1L103S, U10-ch77, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

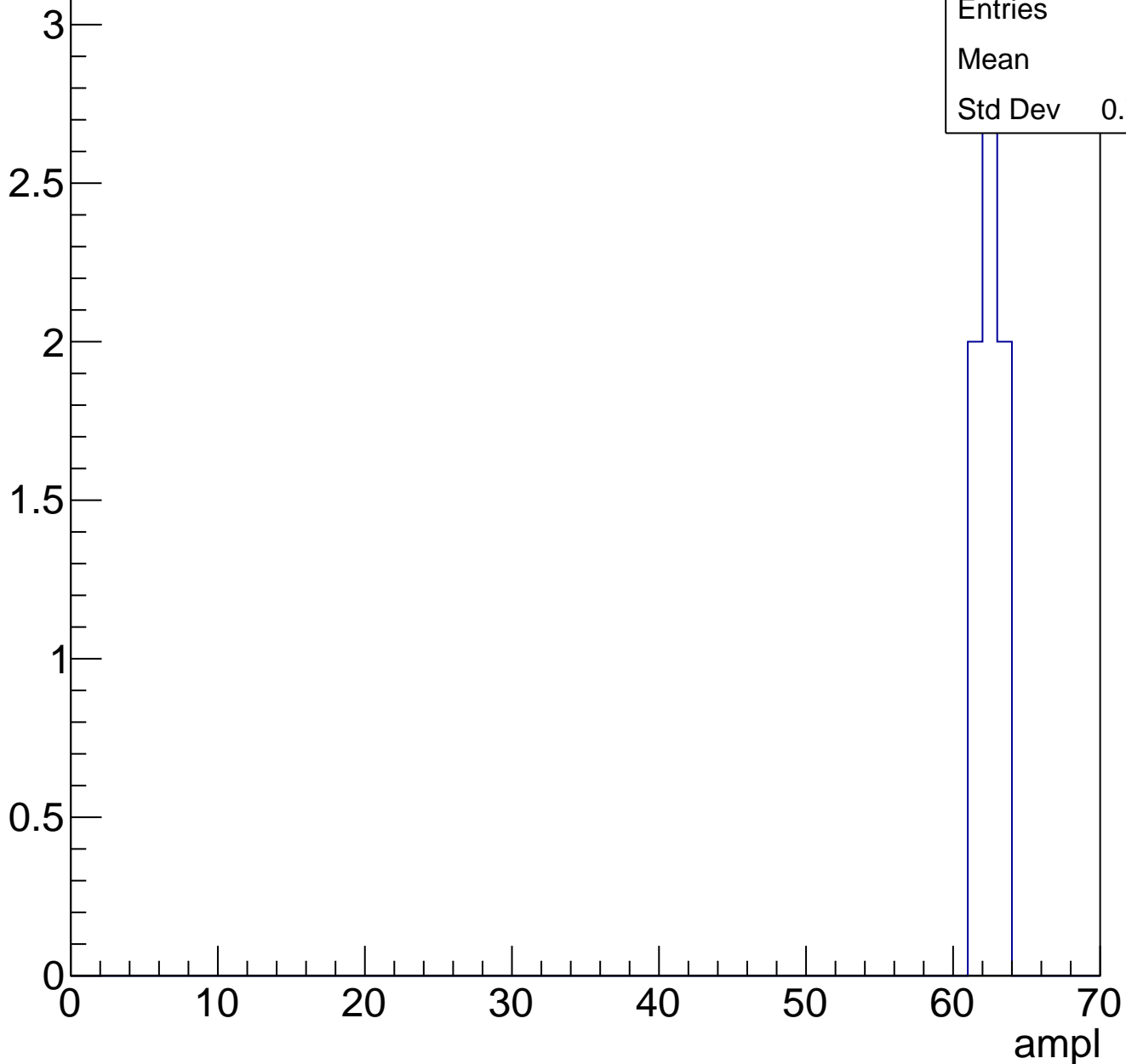
Entries	46
Mean	58.63
Std Dev	9.068



B1L103S, U10-ch77, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch77, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch78, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	29.32
Std Dev	6.437

Gaus mean : 30.9226

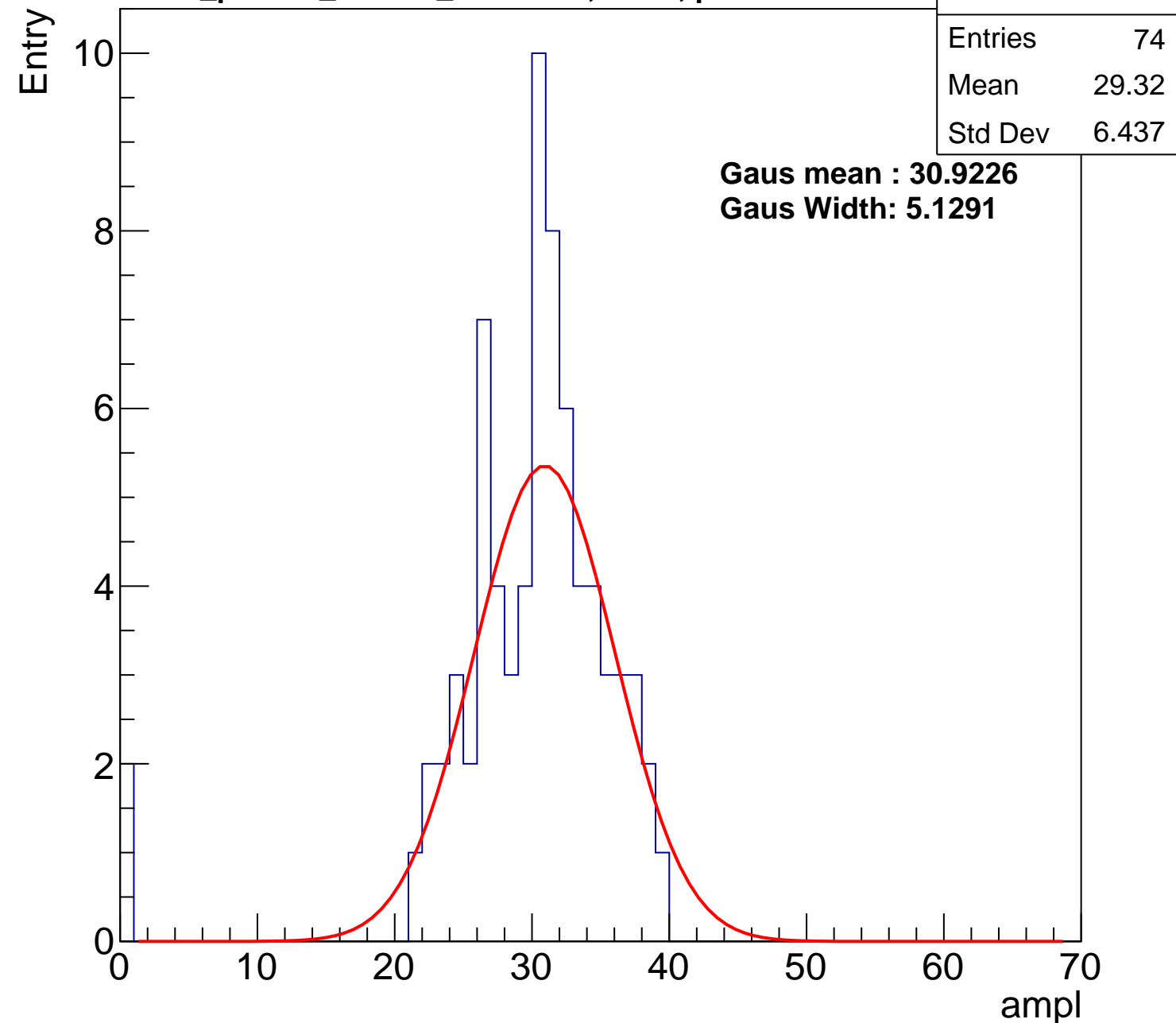
Gaus Width: 5.1291

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch78, adc1

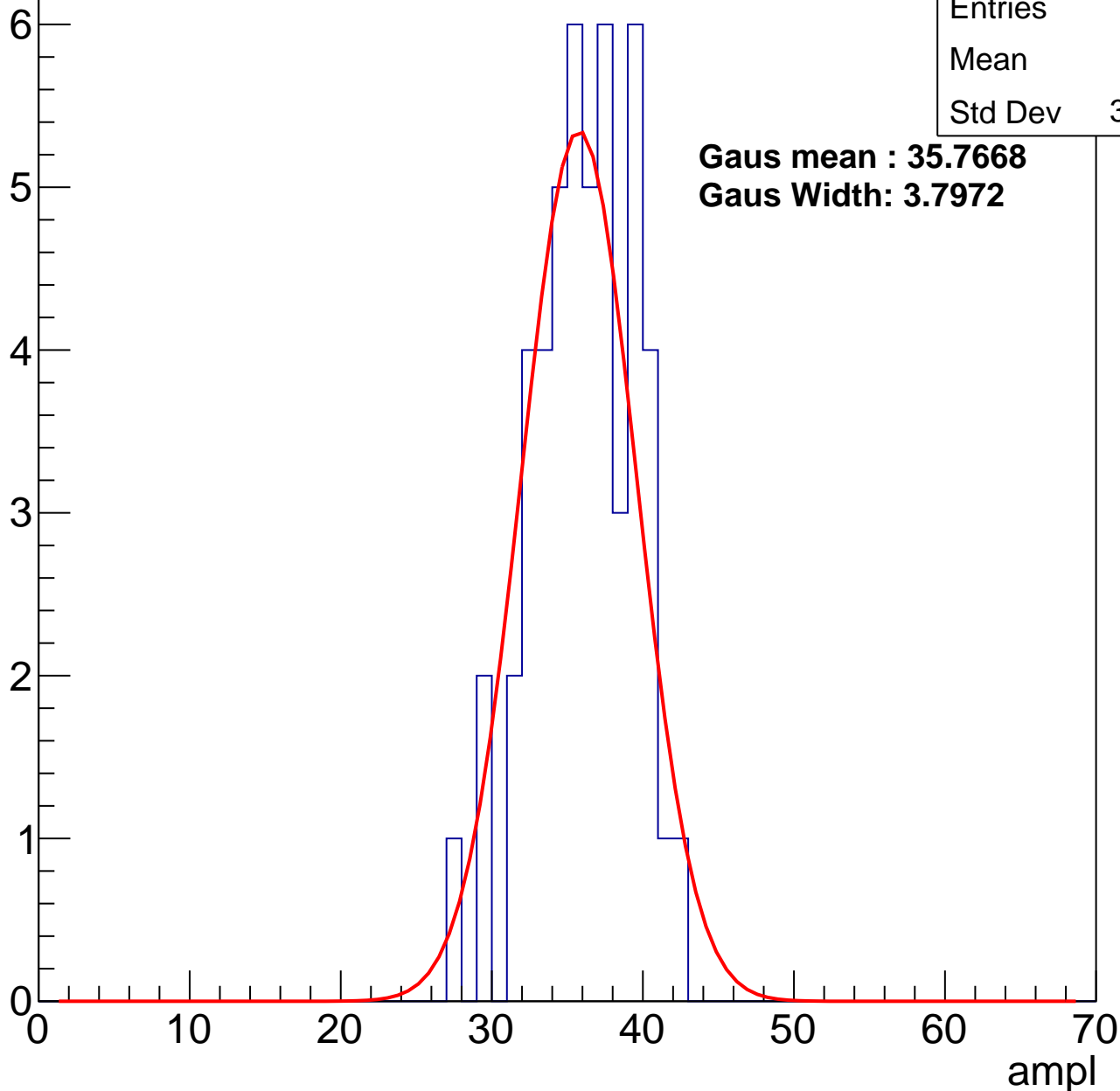
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	35.6
Std Dev	3.298

Gaus mean : 35.7668

Gaus Width: 3.7972



B1L103S, U10-ch78, adc2

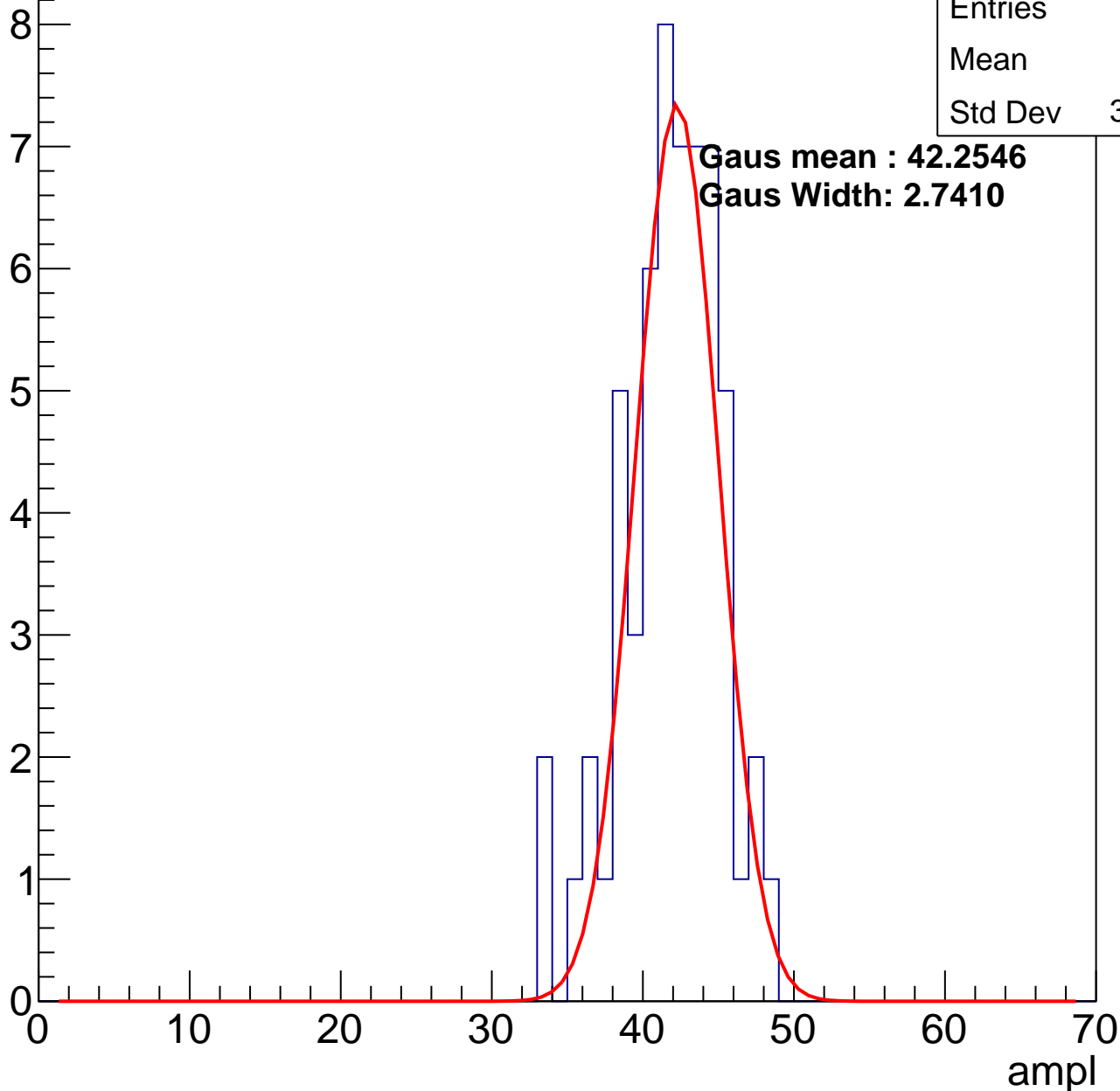
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	41.4
Std Dev	3.237

Gaus mean : 42.2546

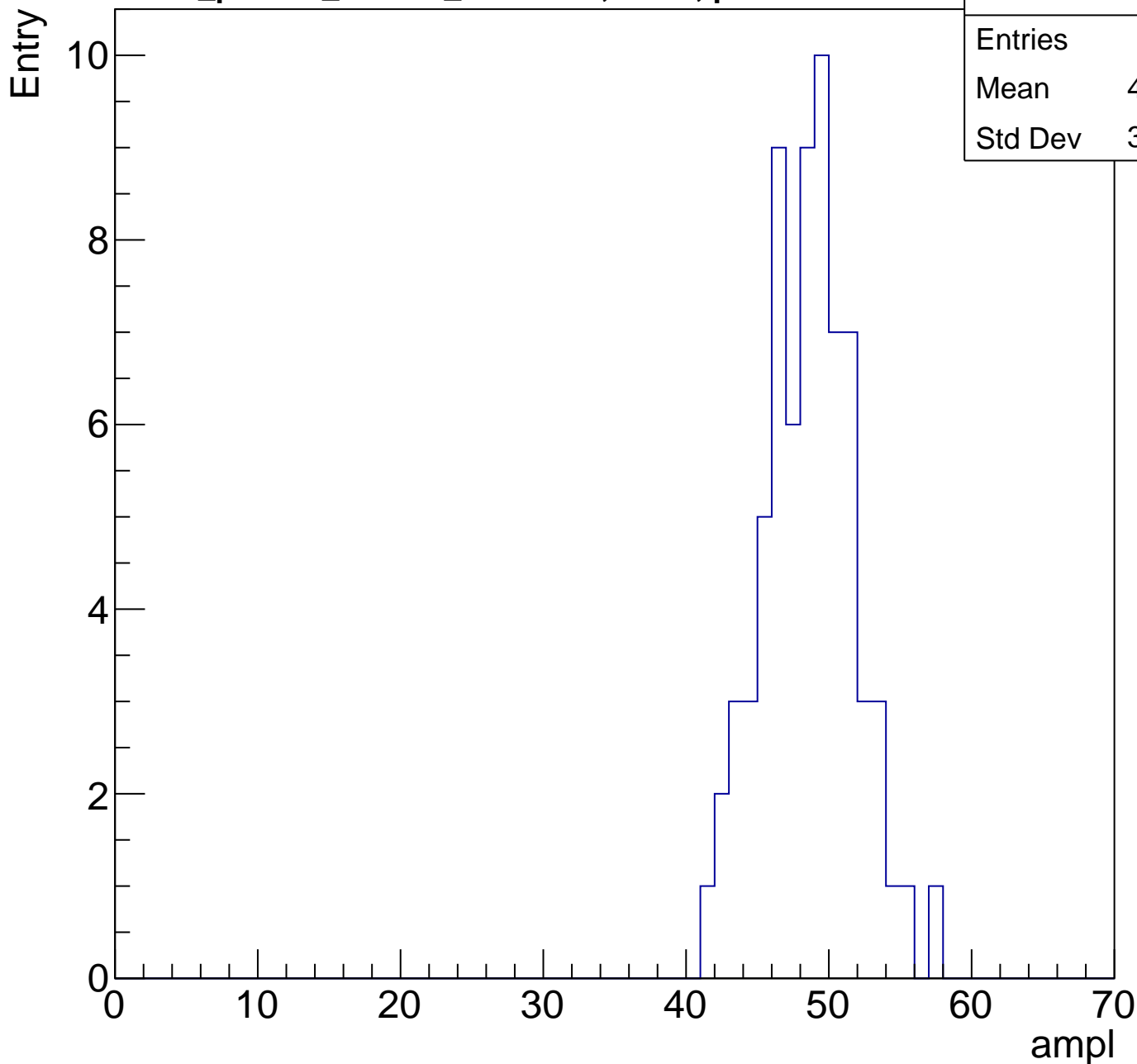
Gaus Width: 2.7410



B1L103S, U10-ch78, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	71
Mean	48.13
Std Dev	3.184

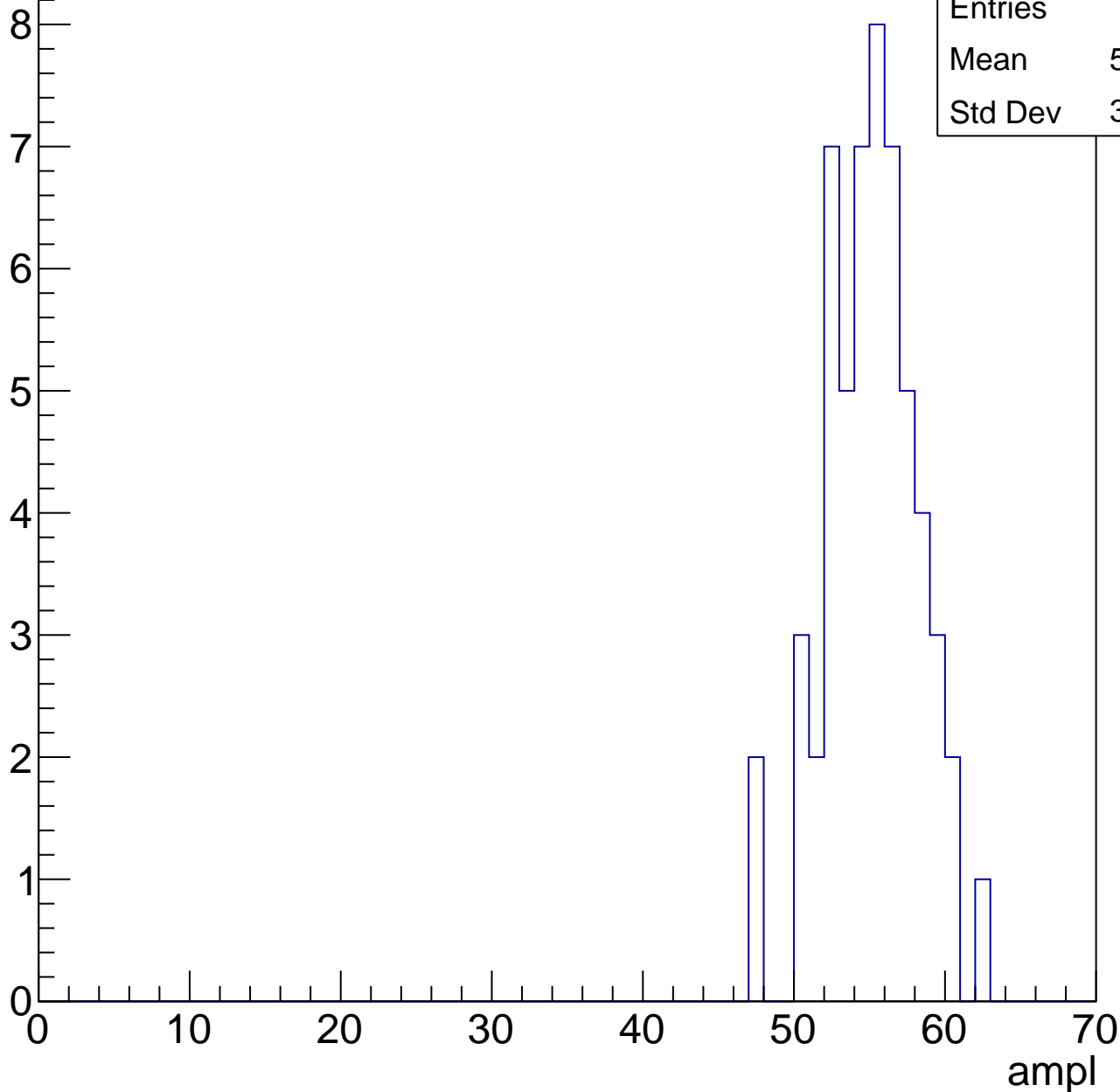


B1L103S, U10-ch78, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	54.66
Std Dev	3.072



B1L103S, U10-ch78, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

7

6

5

4

3

2

1

0

Entries 49

Mean 57.8

Std Dev 8.825

ampl

0

10

20

30

40

50

60

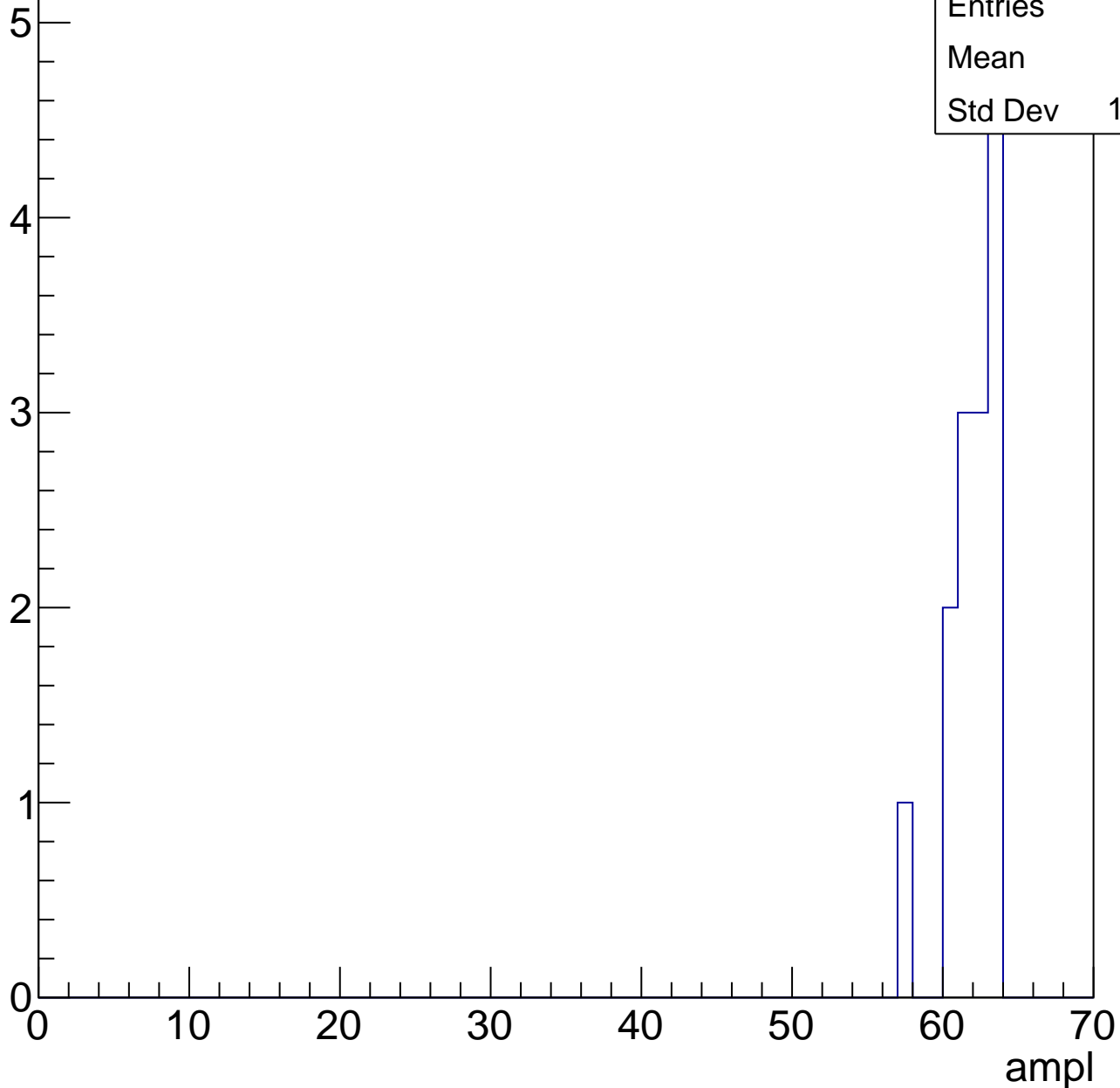
70

B1L103S, U10-ch78, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	61.5
Std Dev	1.637



B1L103S, U10-ch78, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch79, adc0

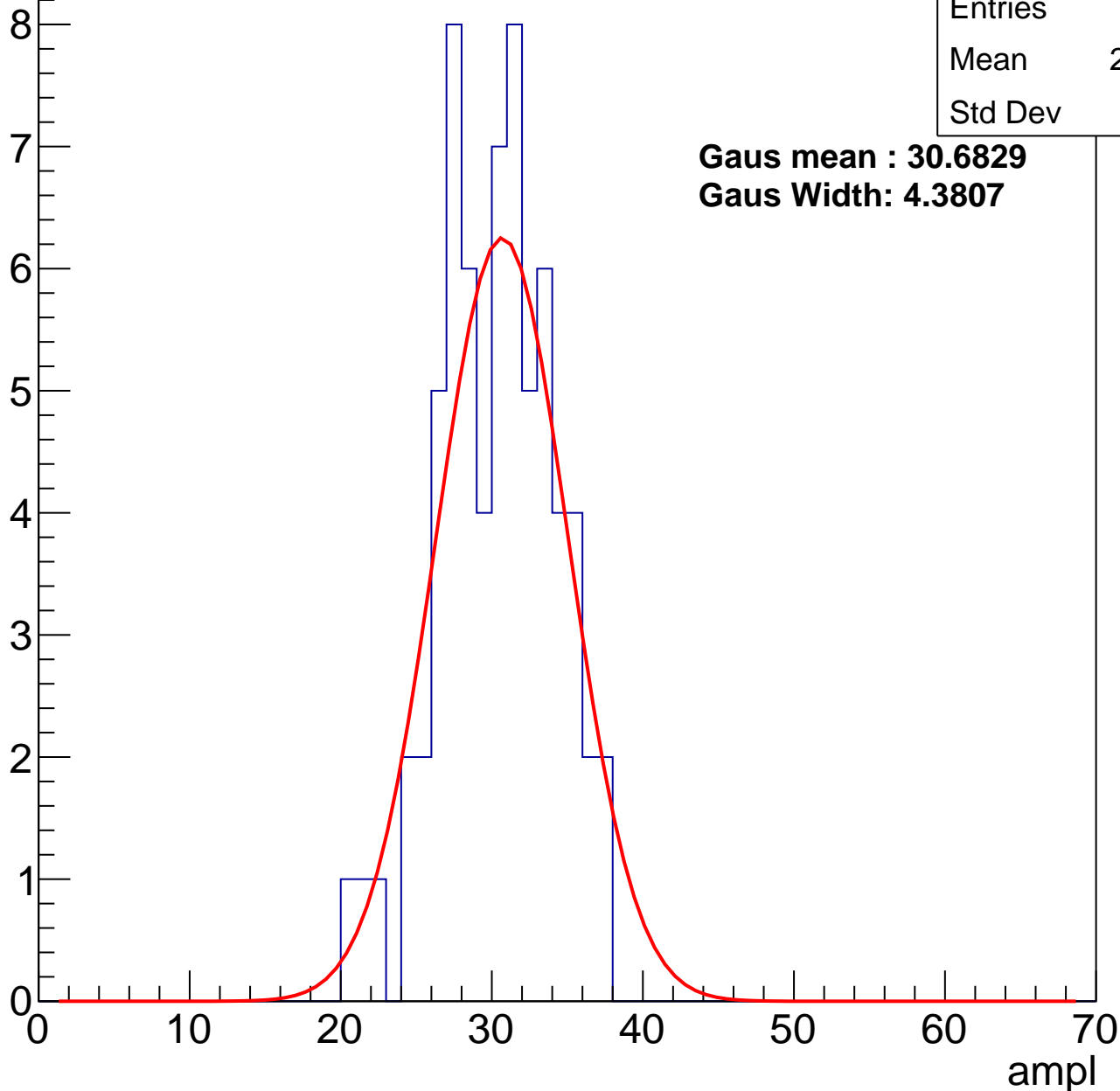
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	29.84
Std Dev	3.76

Gaus mean : 30.6829

Gaus Width: 4.3807



B1L103S, U10-ch79, adc1

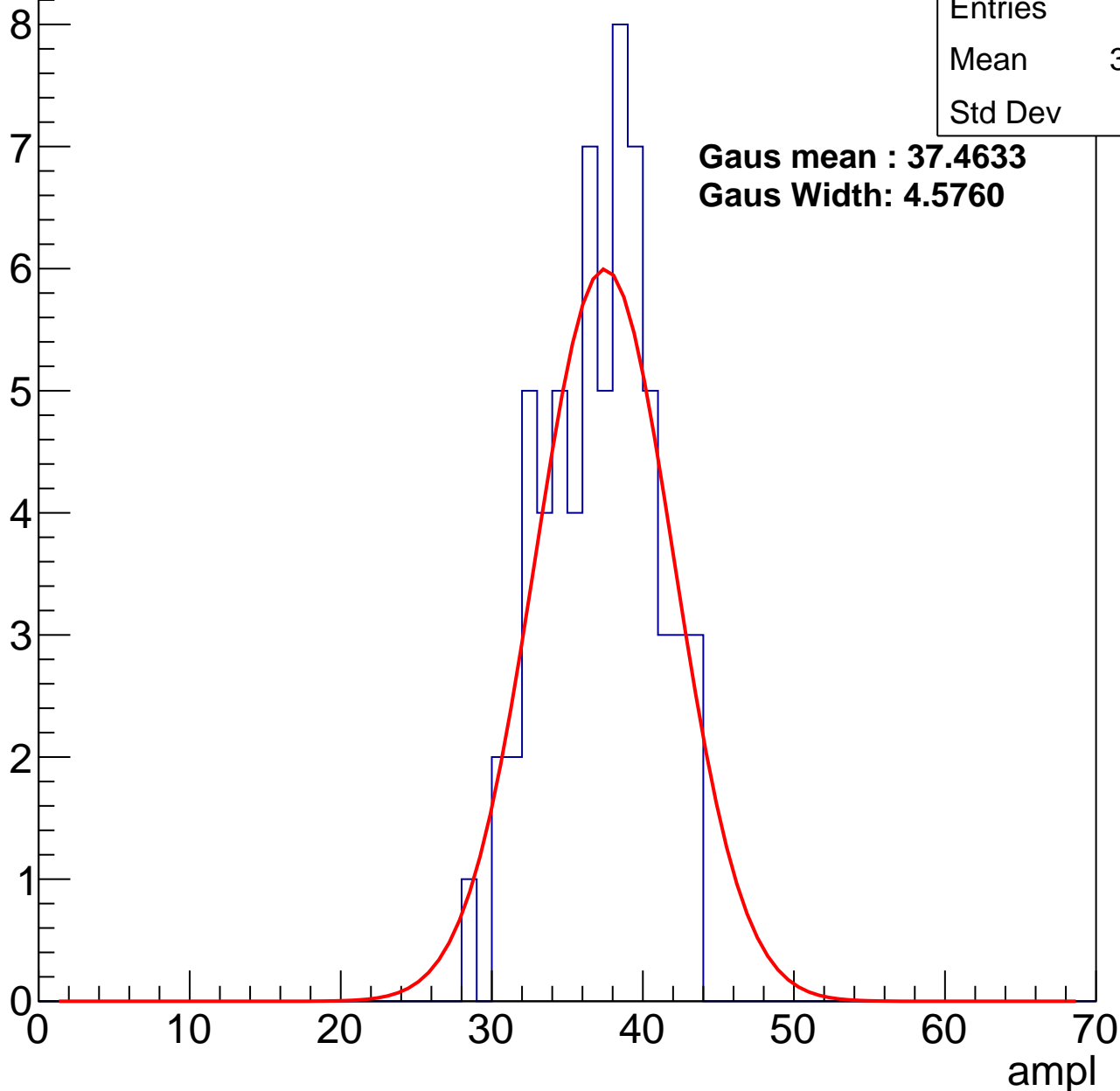
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	36.62
Std Dev	3.56

Gaus mean : 37.4633

Gaus Width: 4.5760



B1L103S, U10-ch79, adc2

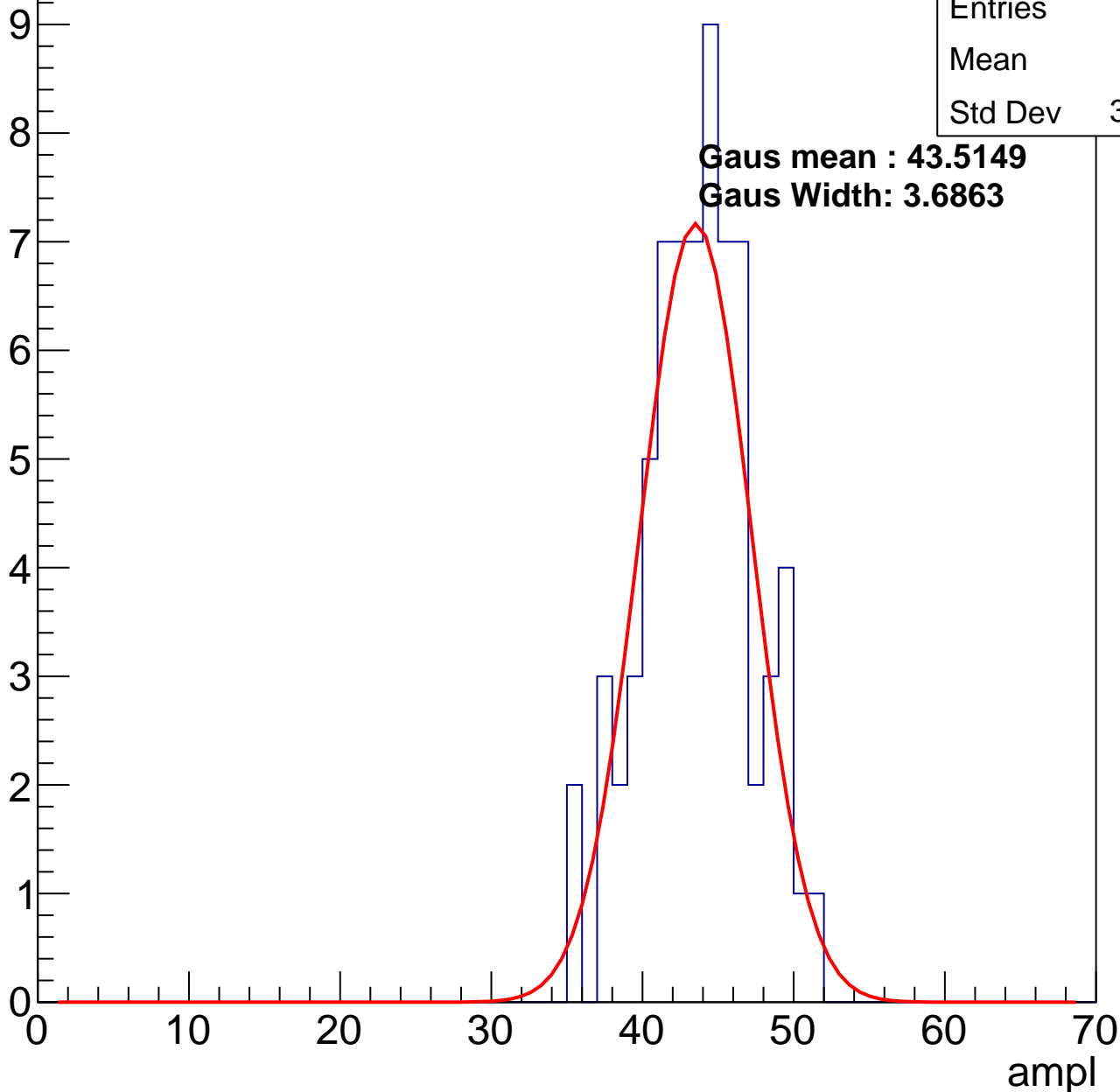
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.2
Std Dev	3.536

Gaus mean : 43.5149

Gaus Width: 3.6863

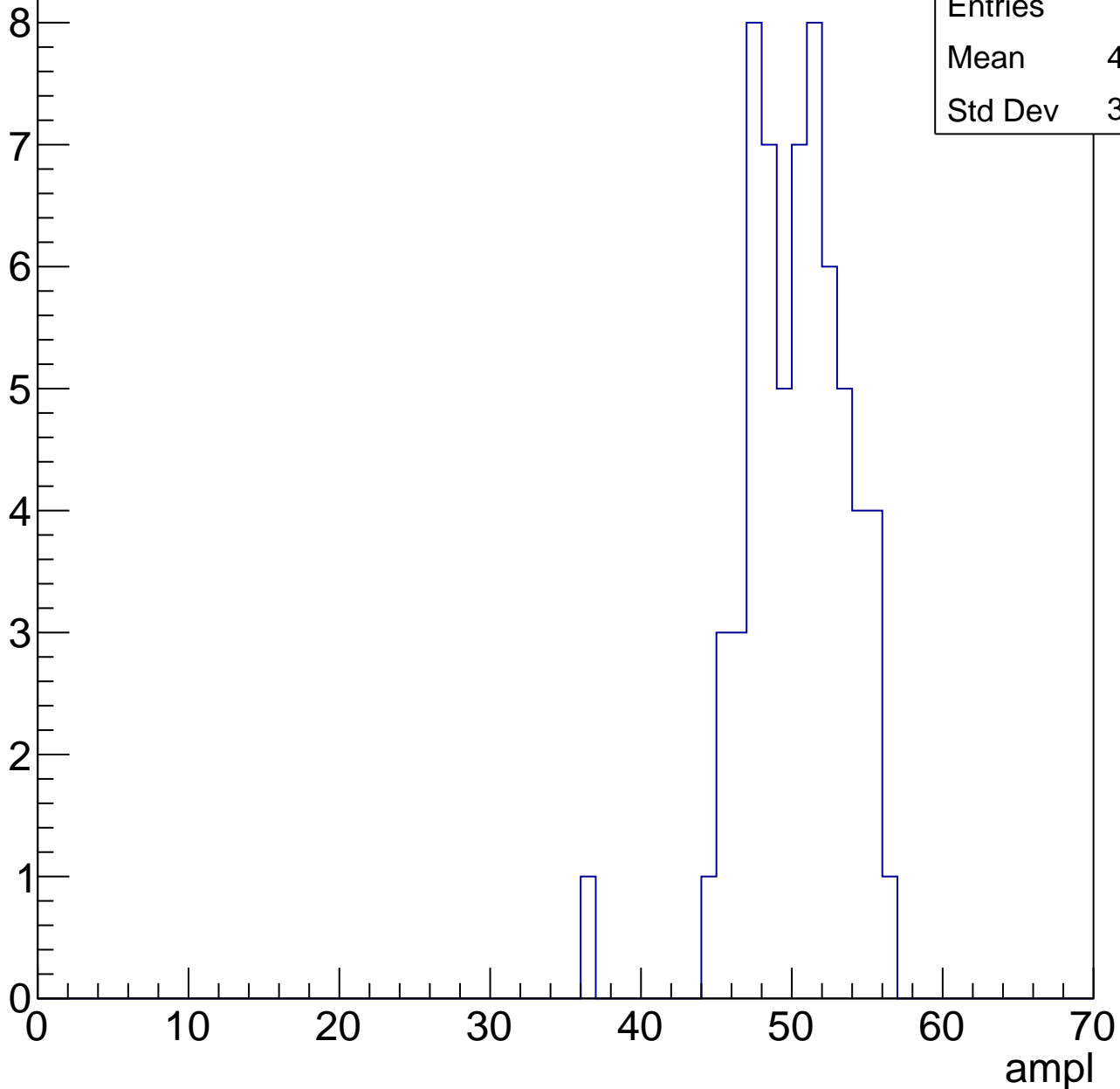


B1L103S, U10-ch79, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	49.79
Std Dev	3.414

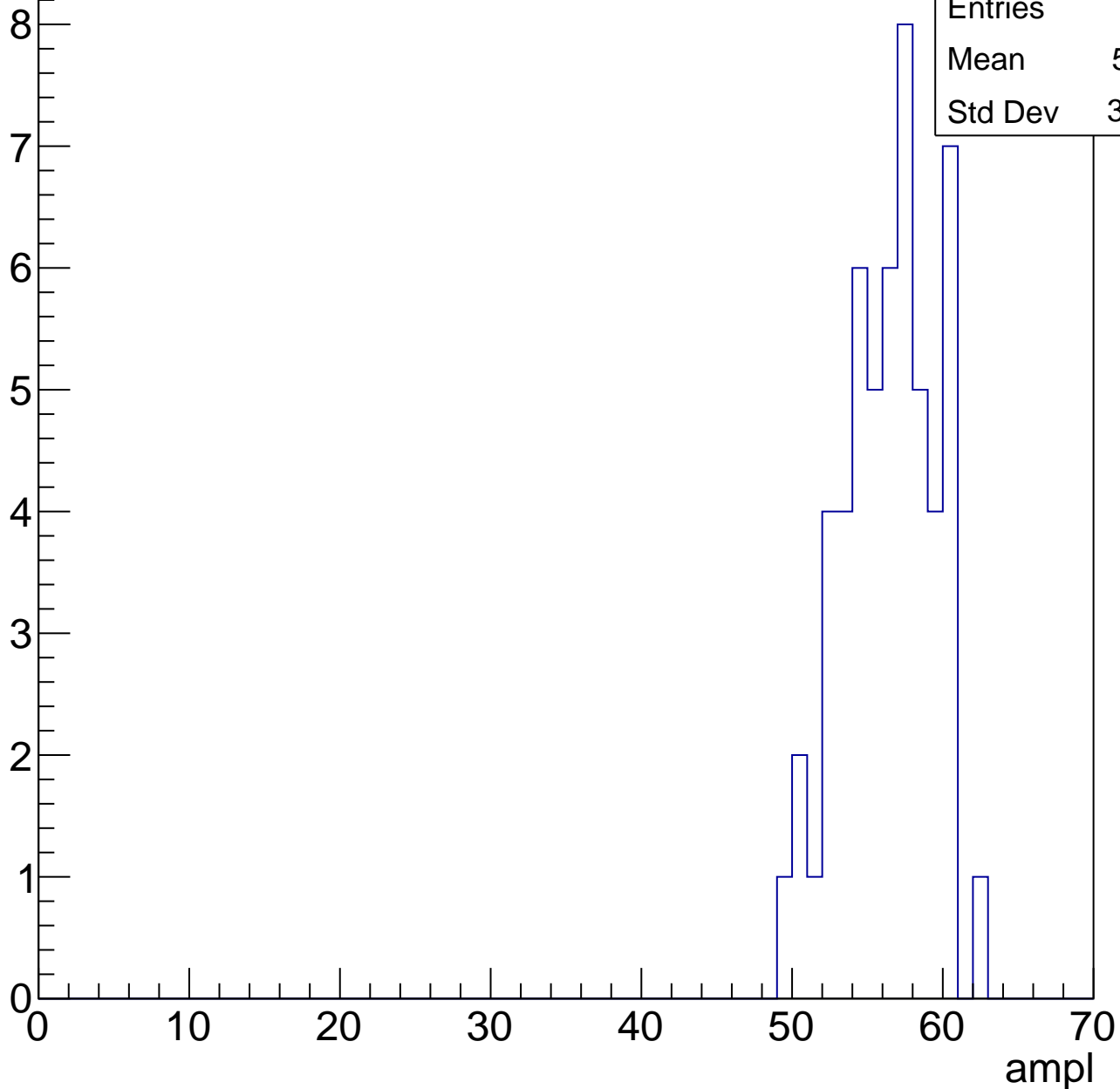


B1L103S, U10-ch79, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.91
Std Dev	3.002

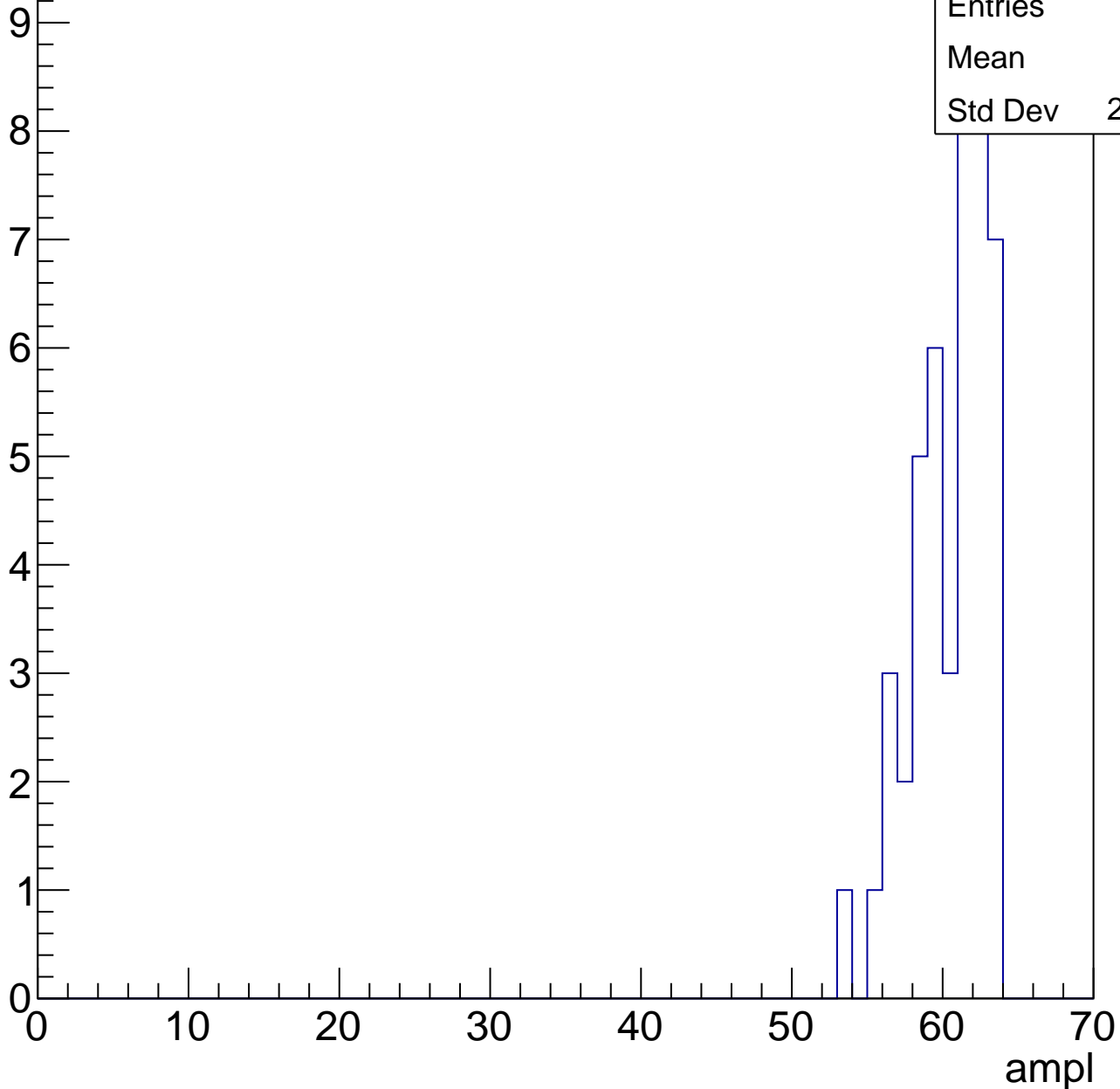


B1L103S, U10-ch79, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	60
Std Dev	2.449



B1L103S, U10-ch79, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	52
Std Dev	23.27

B1L103S, U10-ch79, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch80, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	55
Mean	30.16
Std Dev	2.846

Gaus mean : 30.6354

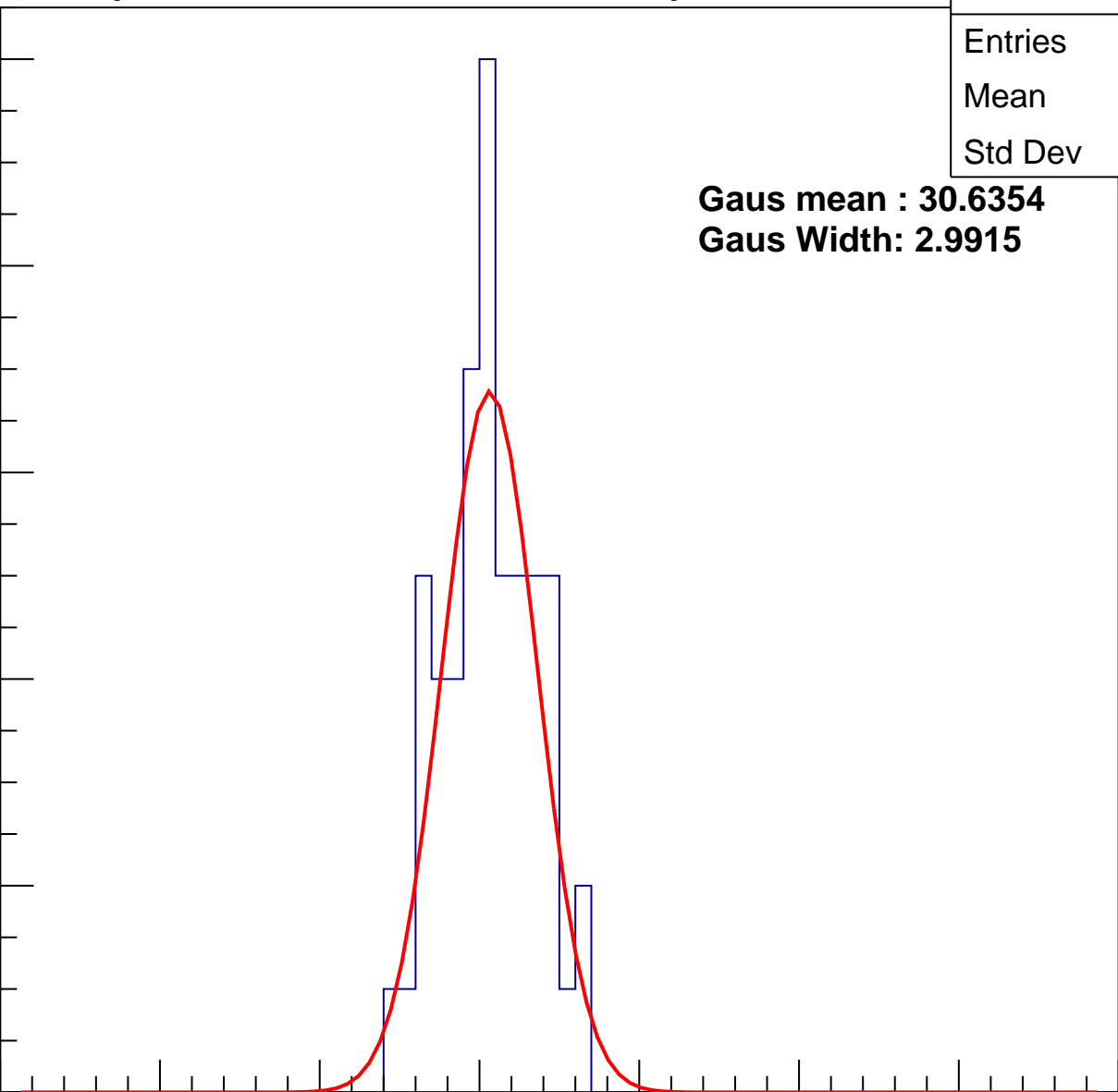
Gaus Width: 2.9915

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch80, adc1

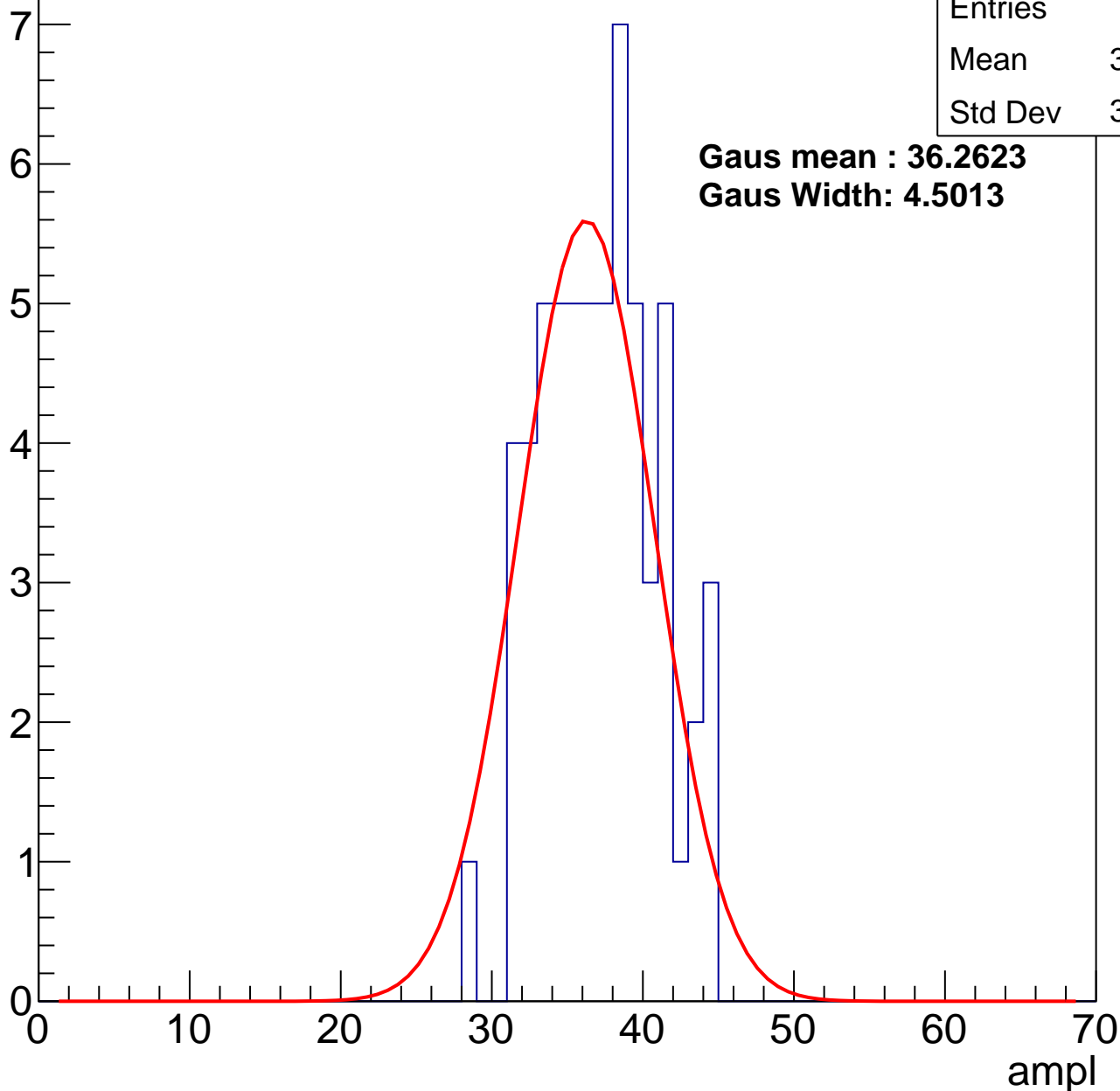
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	36.68
Std Dev	3.757

Gaus mean : 36.2623

Gaus Width: 4.5013



B1L103S, U10-ch80, adc2

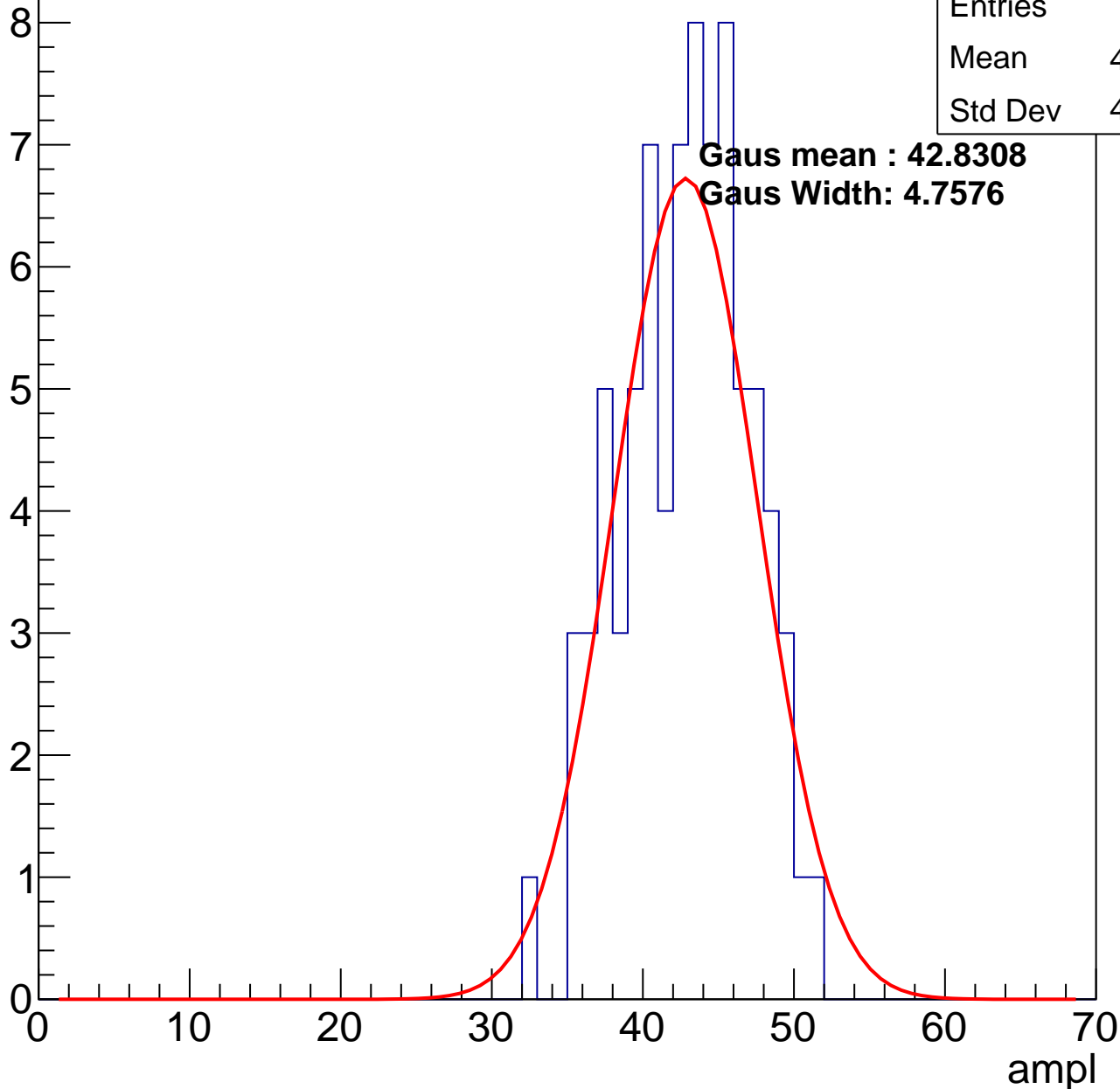
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	42.42
Std Dev	4.092

Gaus mean : 42.8308

Gaus Width: 4.7576

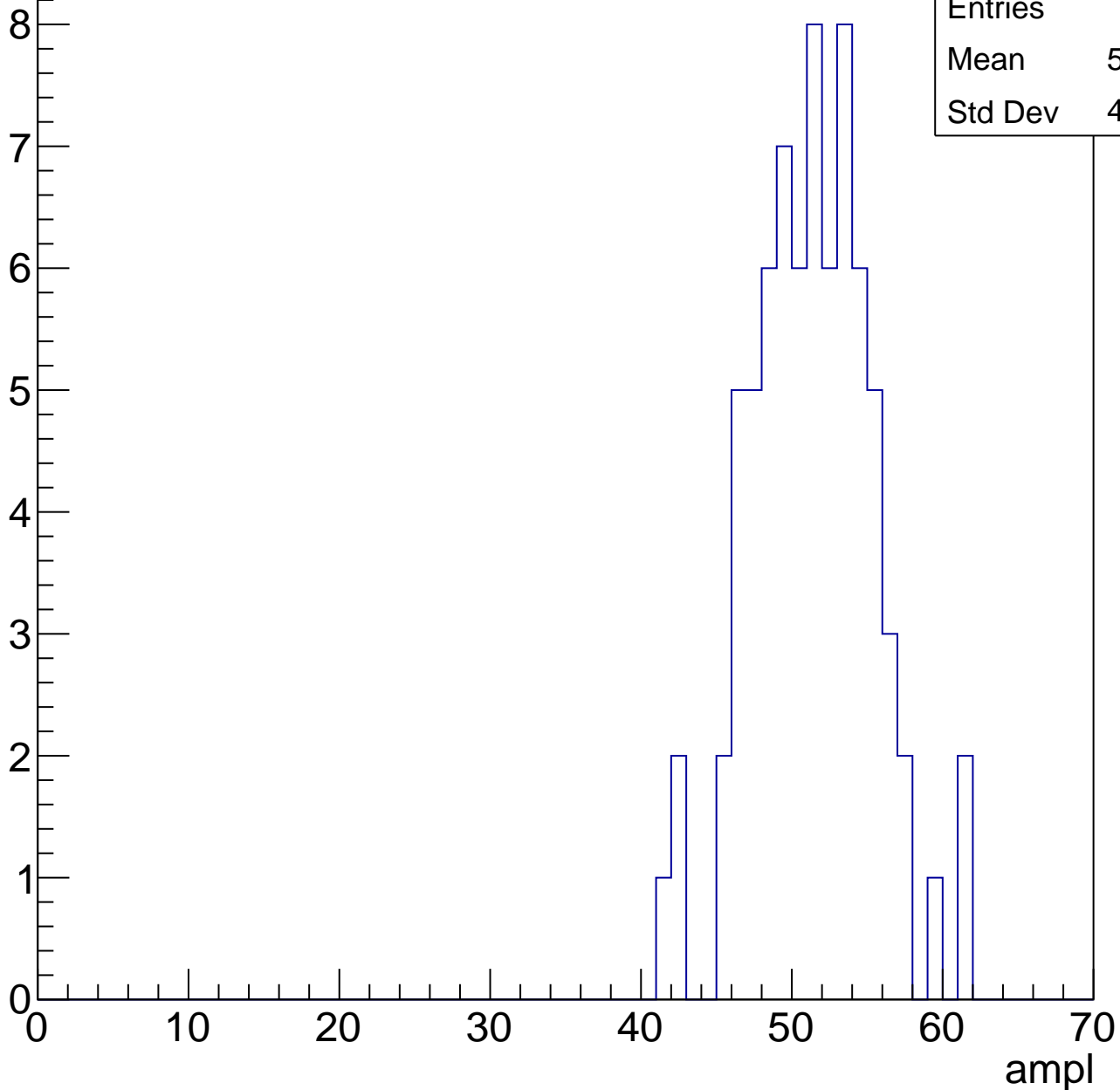


B1L103S, U10-ch80, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	50.89
Std Dev	4.015



B1L103S, U10-ch80, adc4

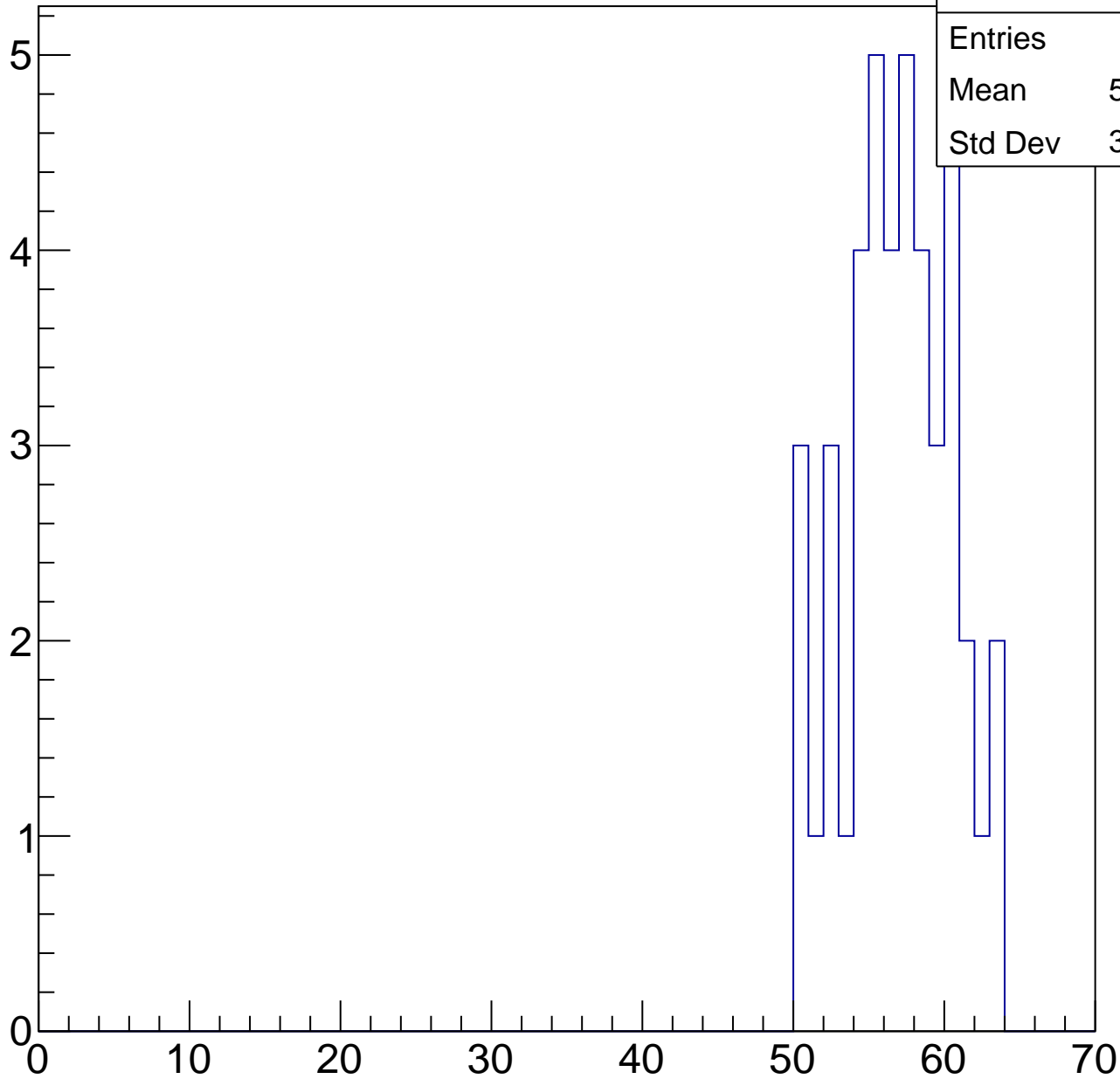
calib_packv5_042523_0143.root, FC#7, port C2

Entry

5
4
3
2
1
0

Entries	43
Mean	56.49
Std Dev	3.453

ampl

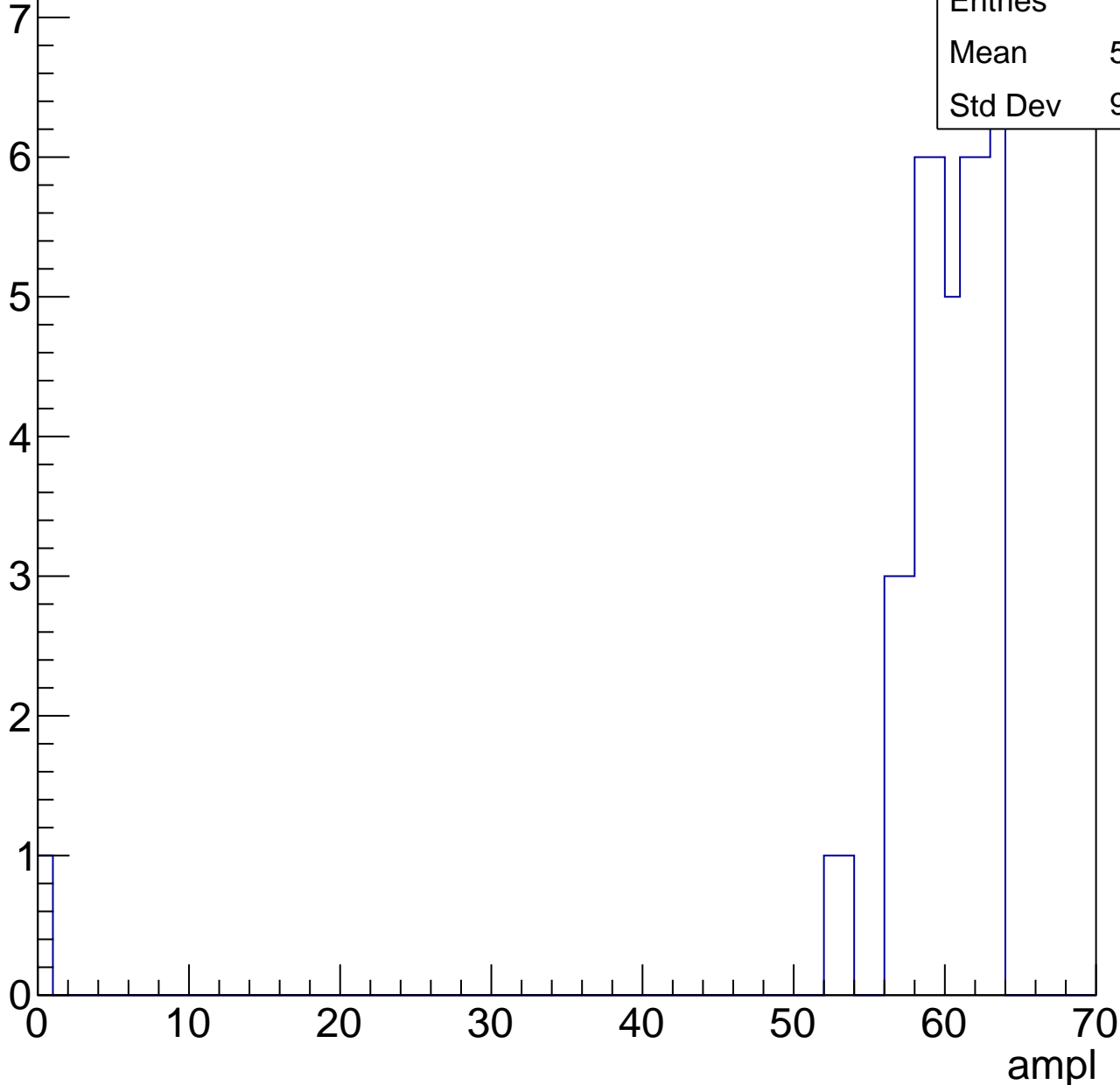


B1L103S, U10-ch80, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

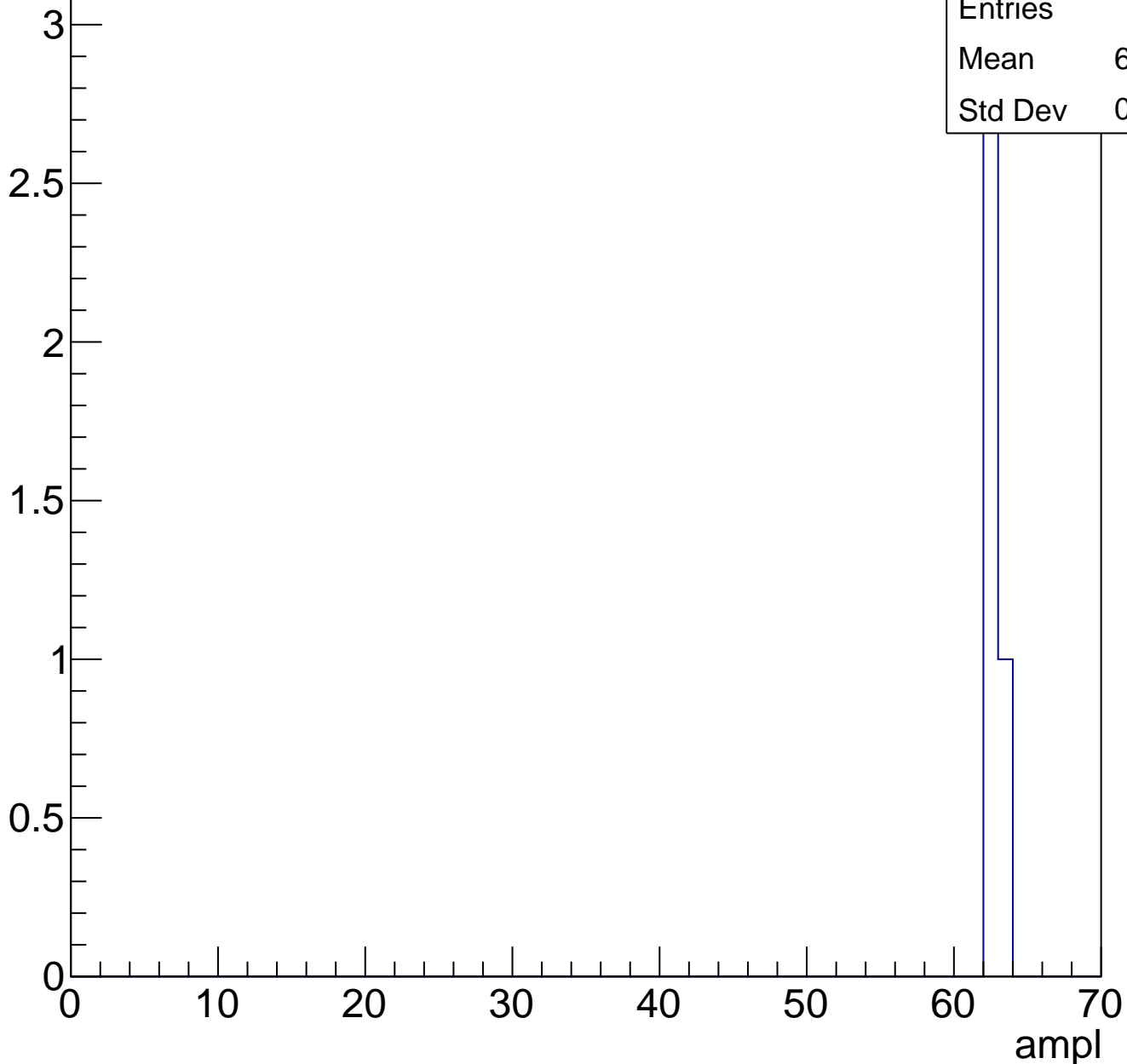
Entries	45
Mean	58.33
Std Dev	9.172



B1L103S, U10-ch80, adc6

calib_packv5_042523_0143.root, FC#7, port C2

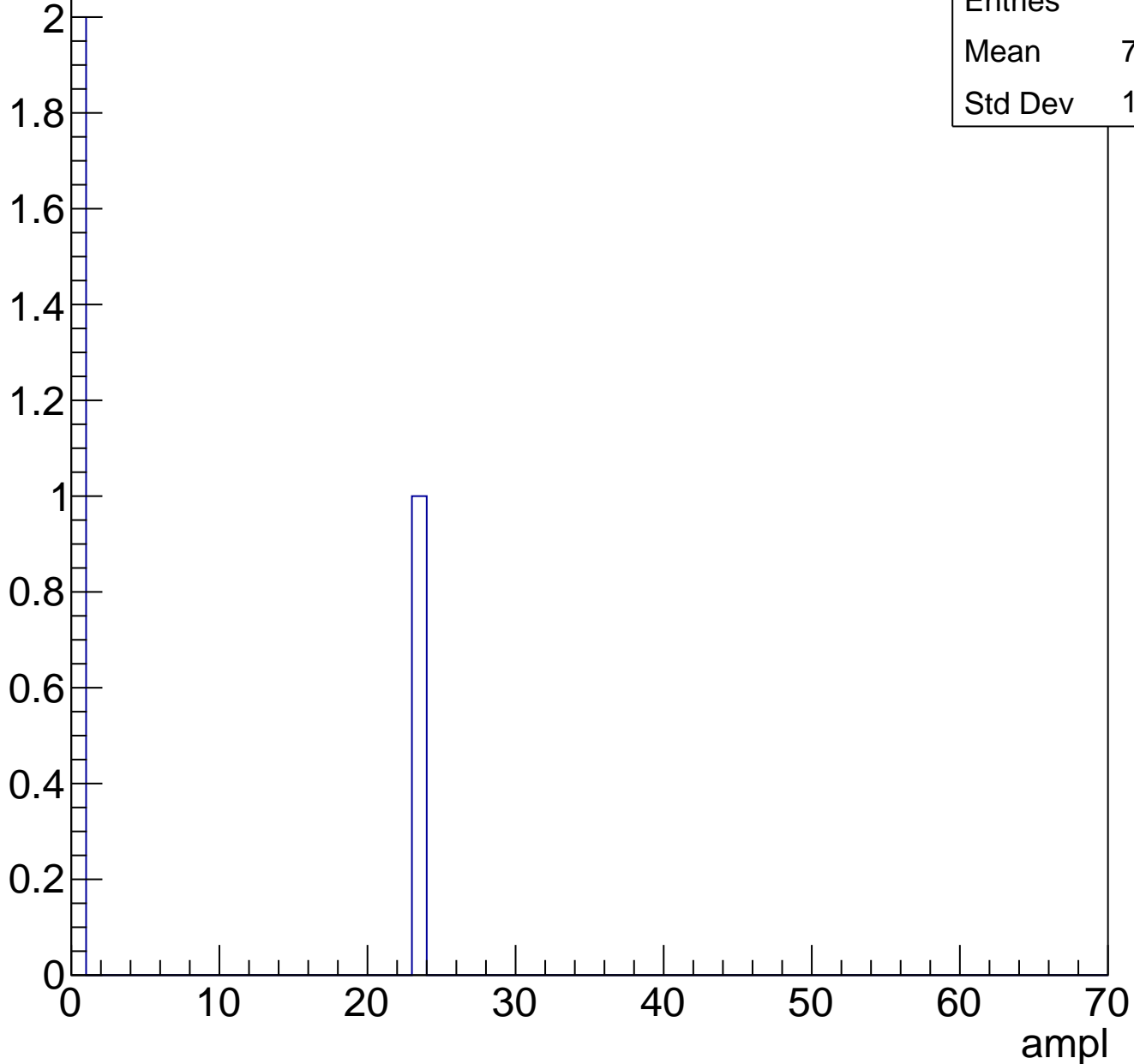
Entry



B1L103S, U10-ch80, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L103S, U10-ch81, adc0

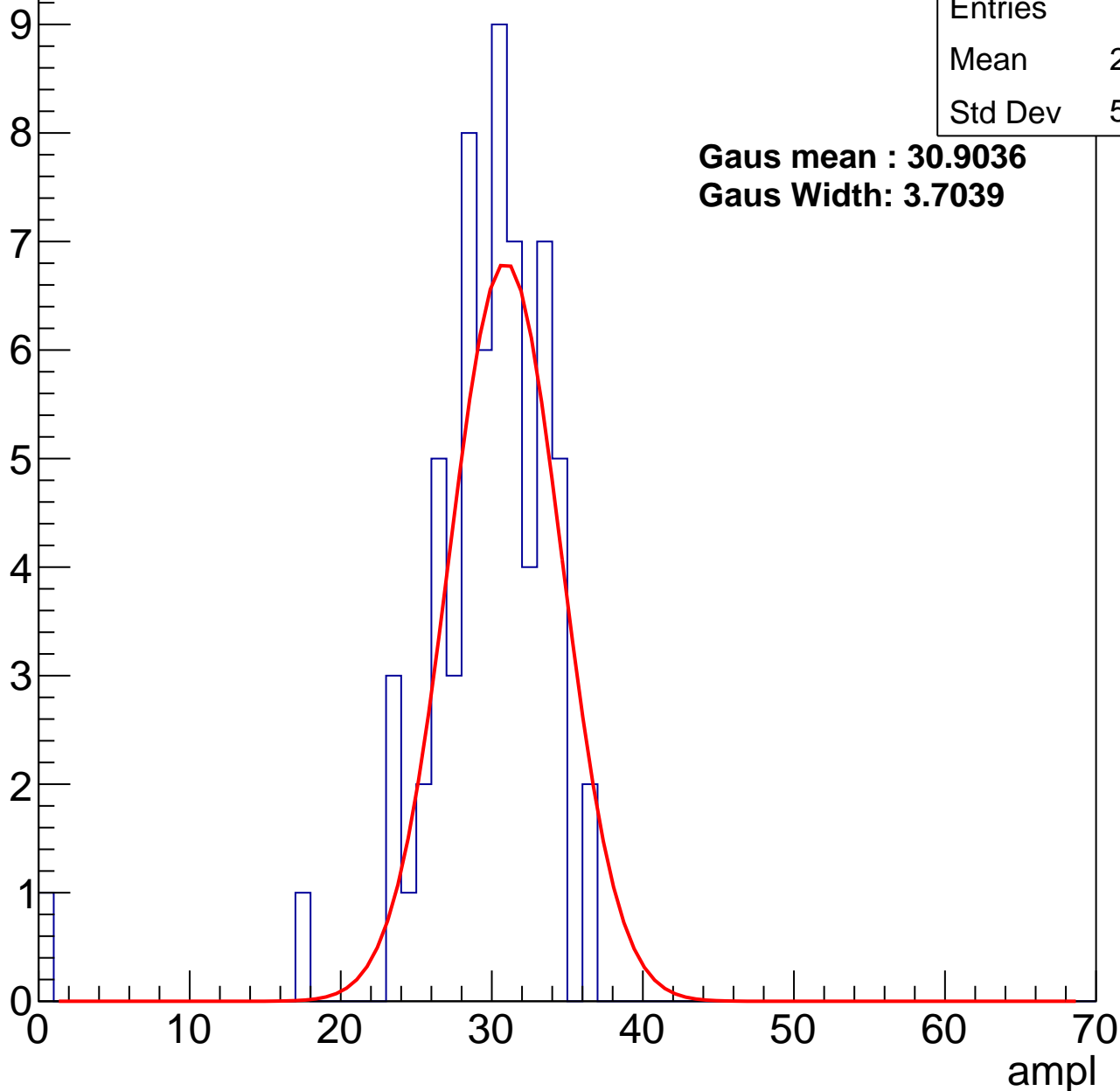
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	29.02
Std Dev	5.036

Gaus mean : 30.9036

Gaus Width: 3.7039



B1L103S, U10-ch81, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	36.65
Std Dev	3.391

Gaus mean : 37.0046

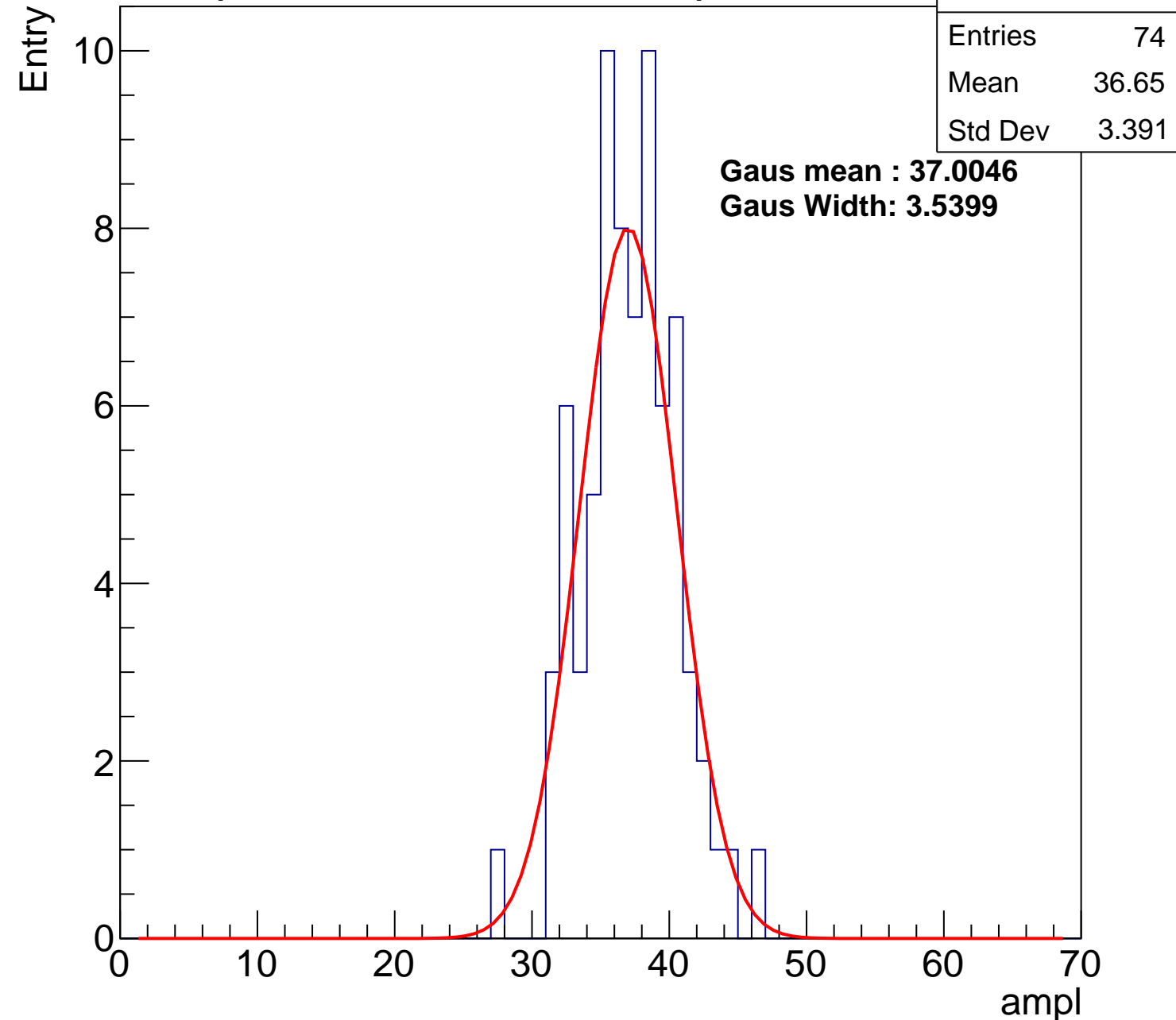
Gaus Width: 3.5399

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch81, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	44.73
Std Dev	3.834

Gaus mean : 44.3982

Gaus Width: 4.1837

10

8

6

4

2

0

0

10

20

30

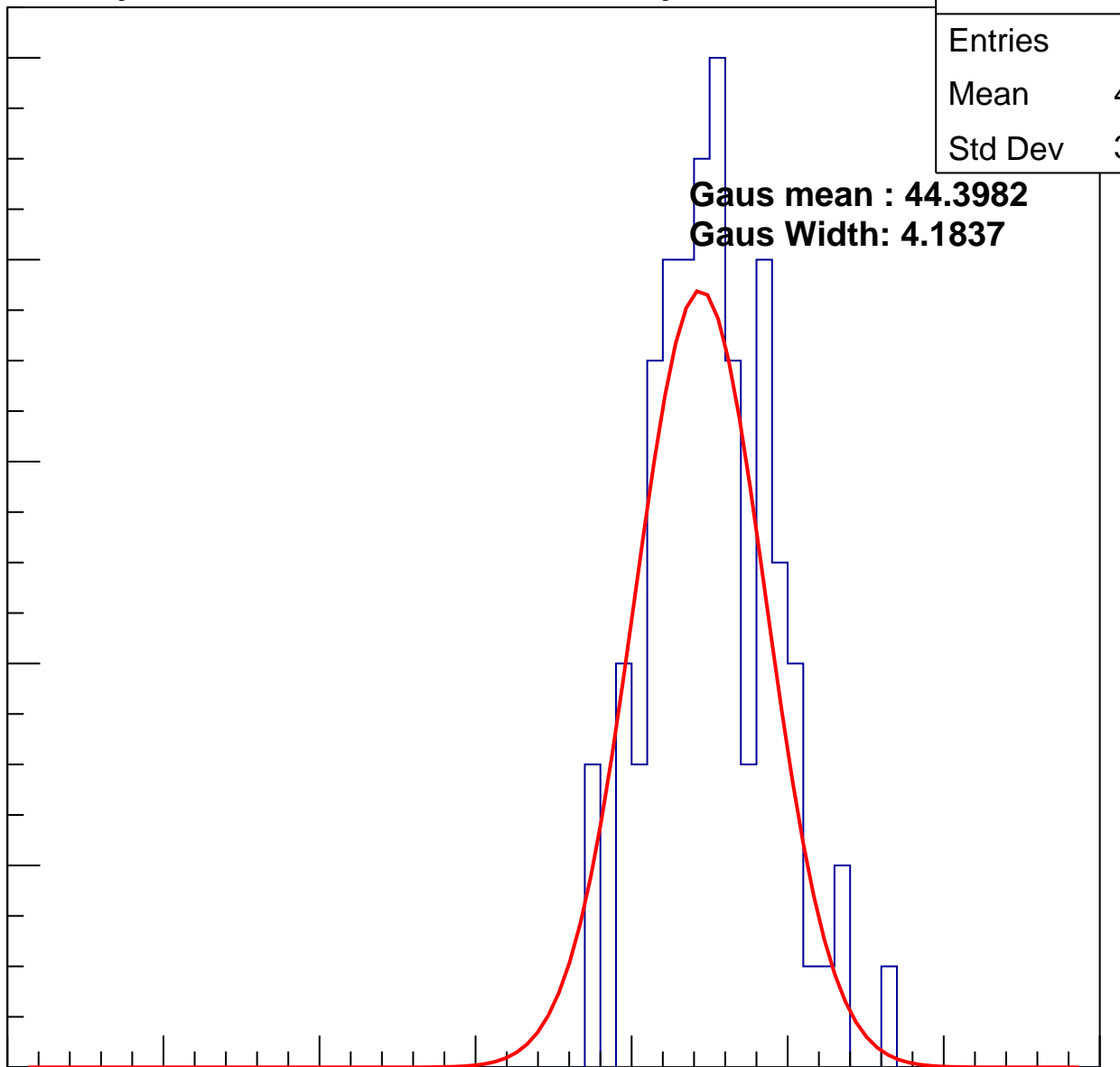
40

50

60

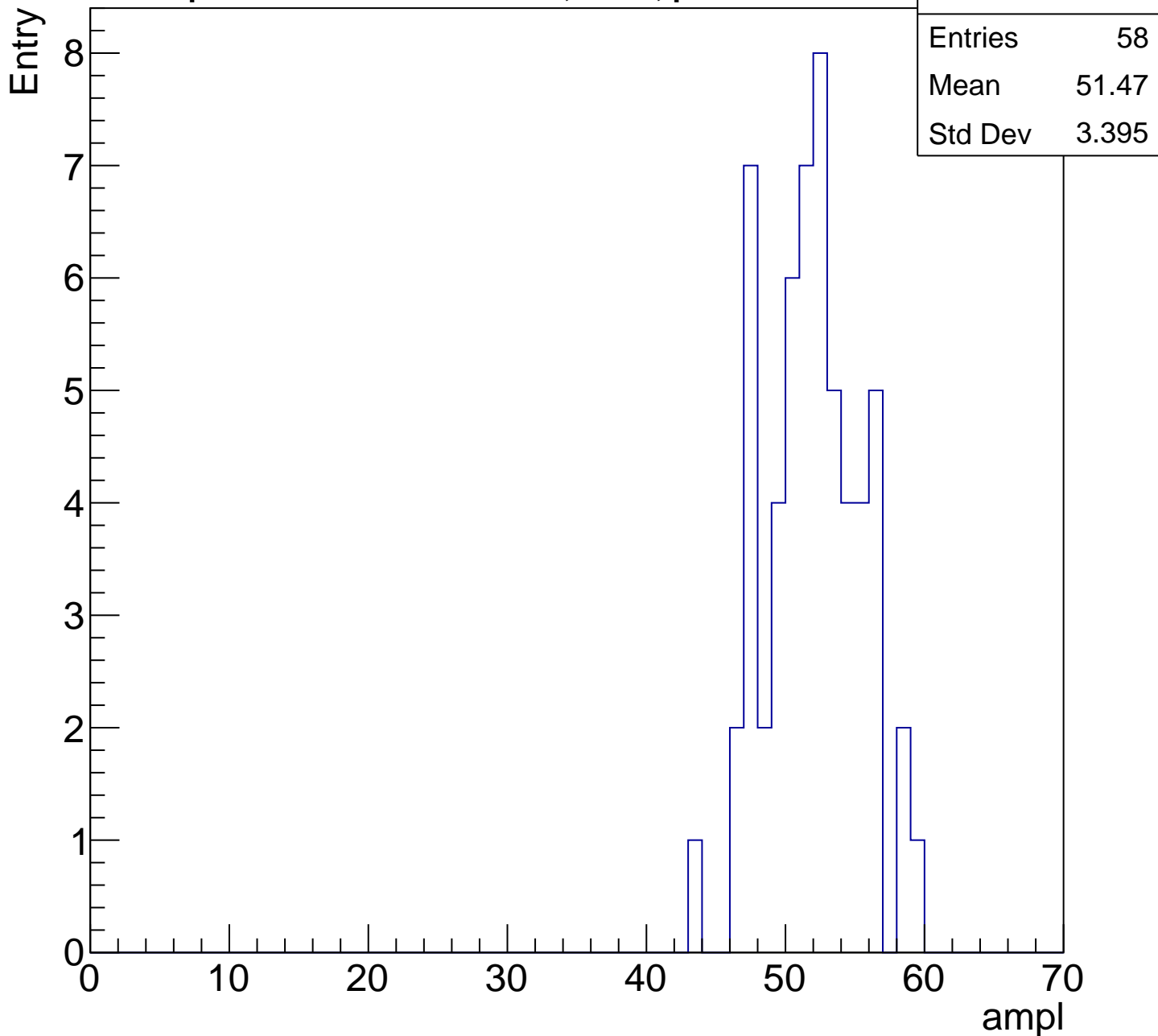
70

ampl



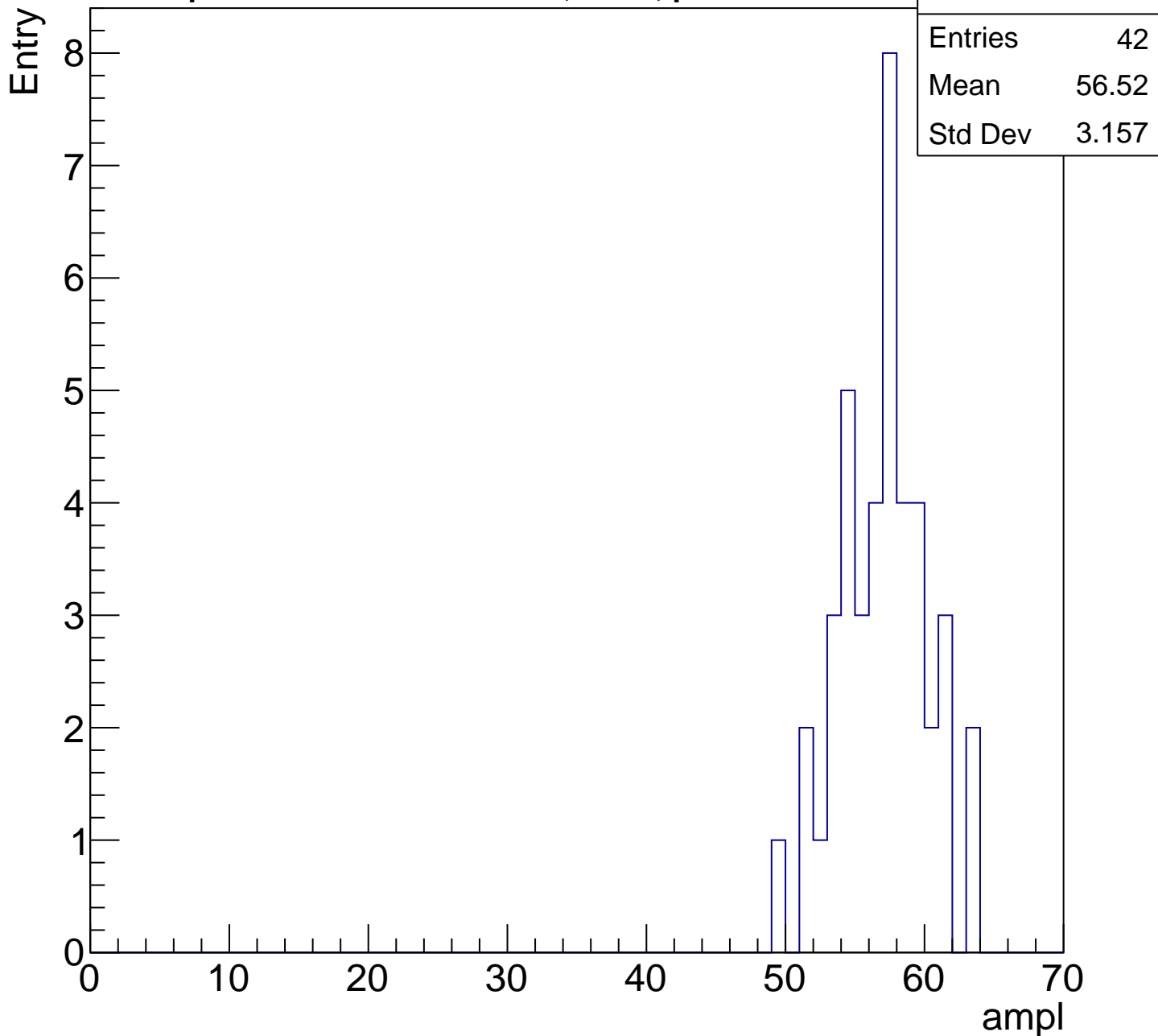
B1L103S, U10-ch81, adc3

calib_packv5_042523_0143.root, FC#7, port C2



B1L103S, U10-ch81, adc4

calib_packv5_042523_0143.root, FC#7, port C2

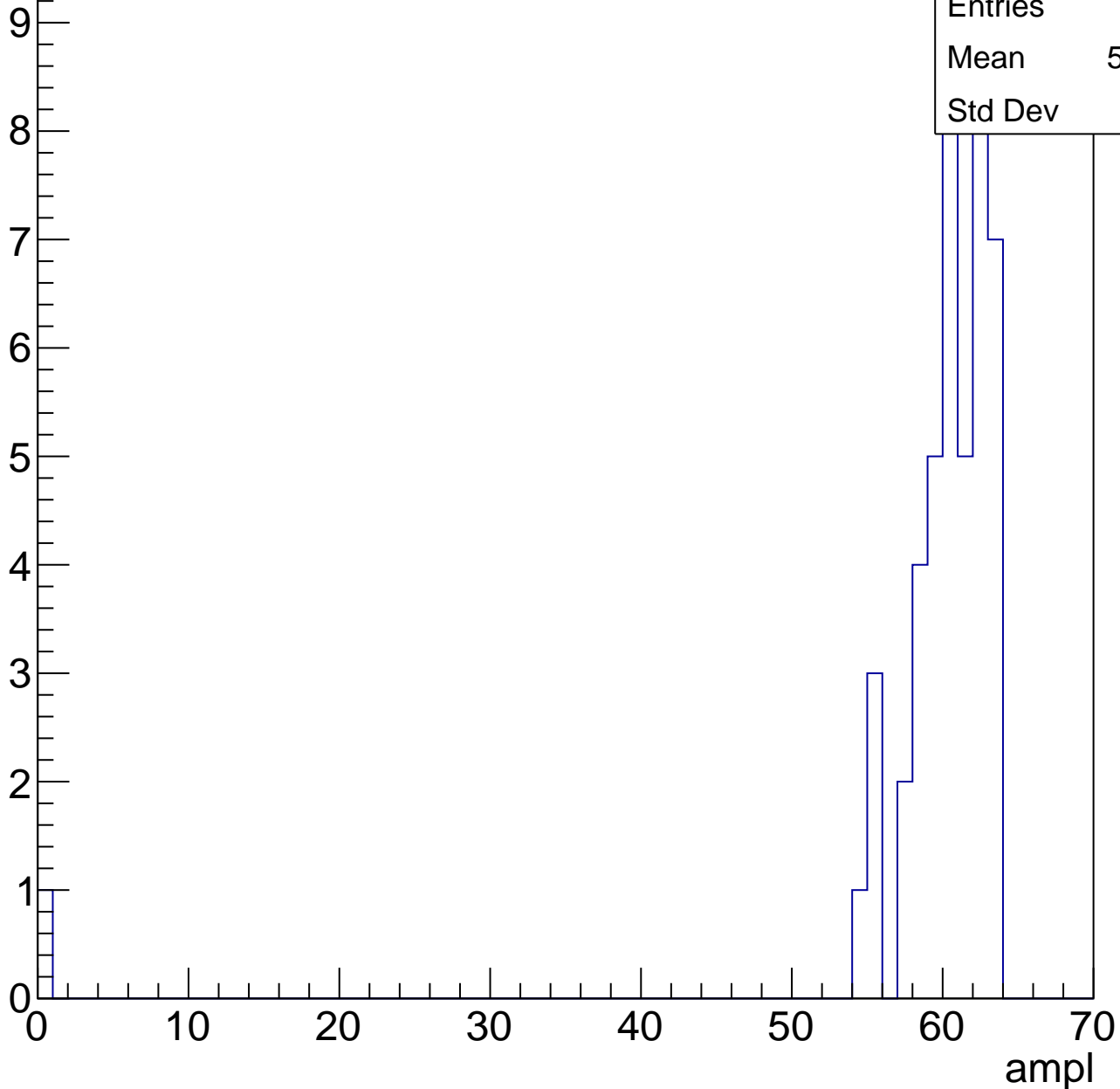


B1L103S, U10-ch81, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

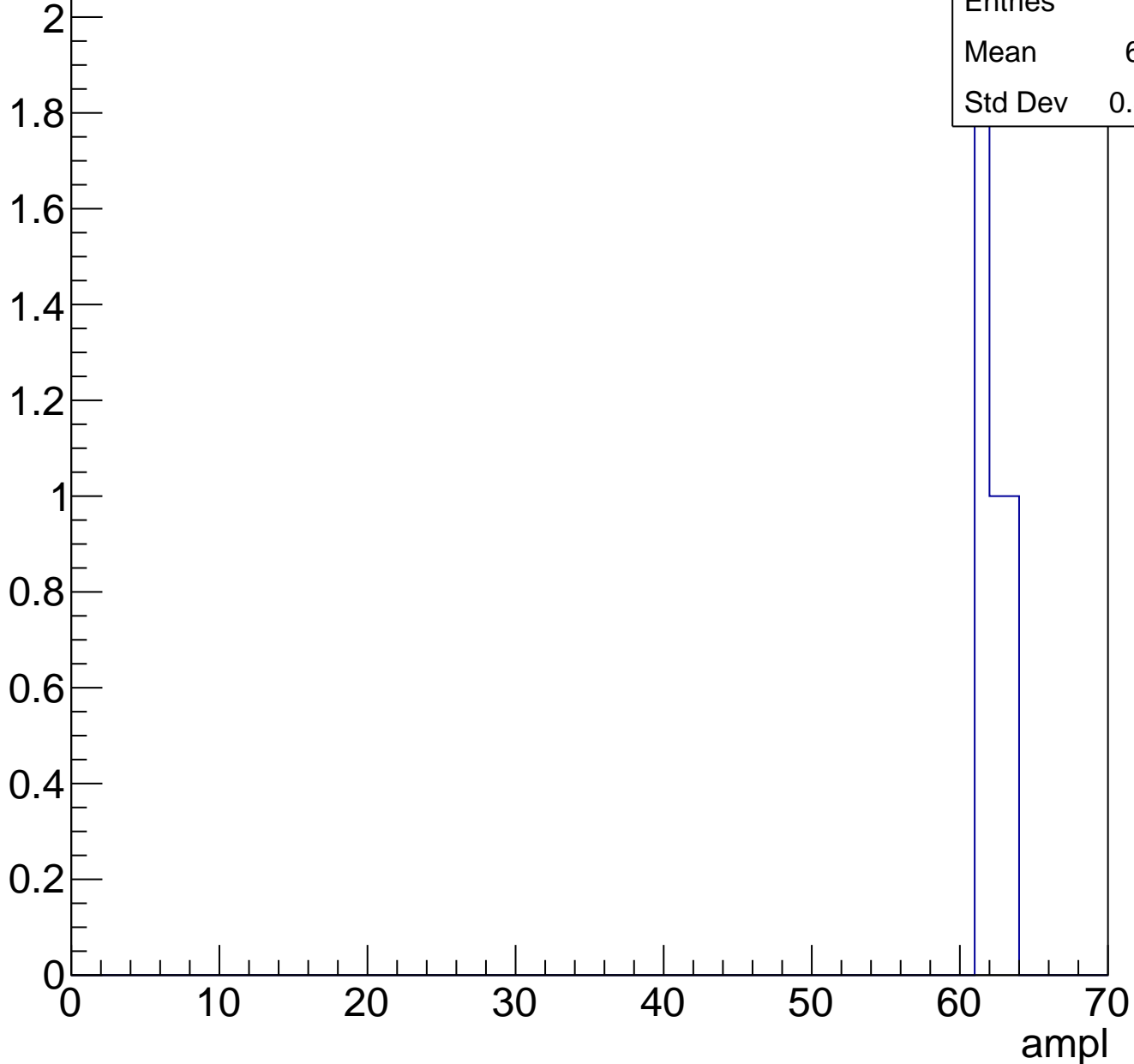
Entries	45
Mean	58.76
Std Dev	9.17



B1L103S, U10-ch81, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch81, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch82, adc0

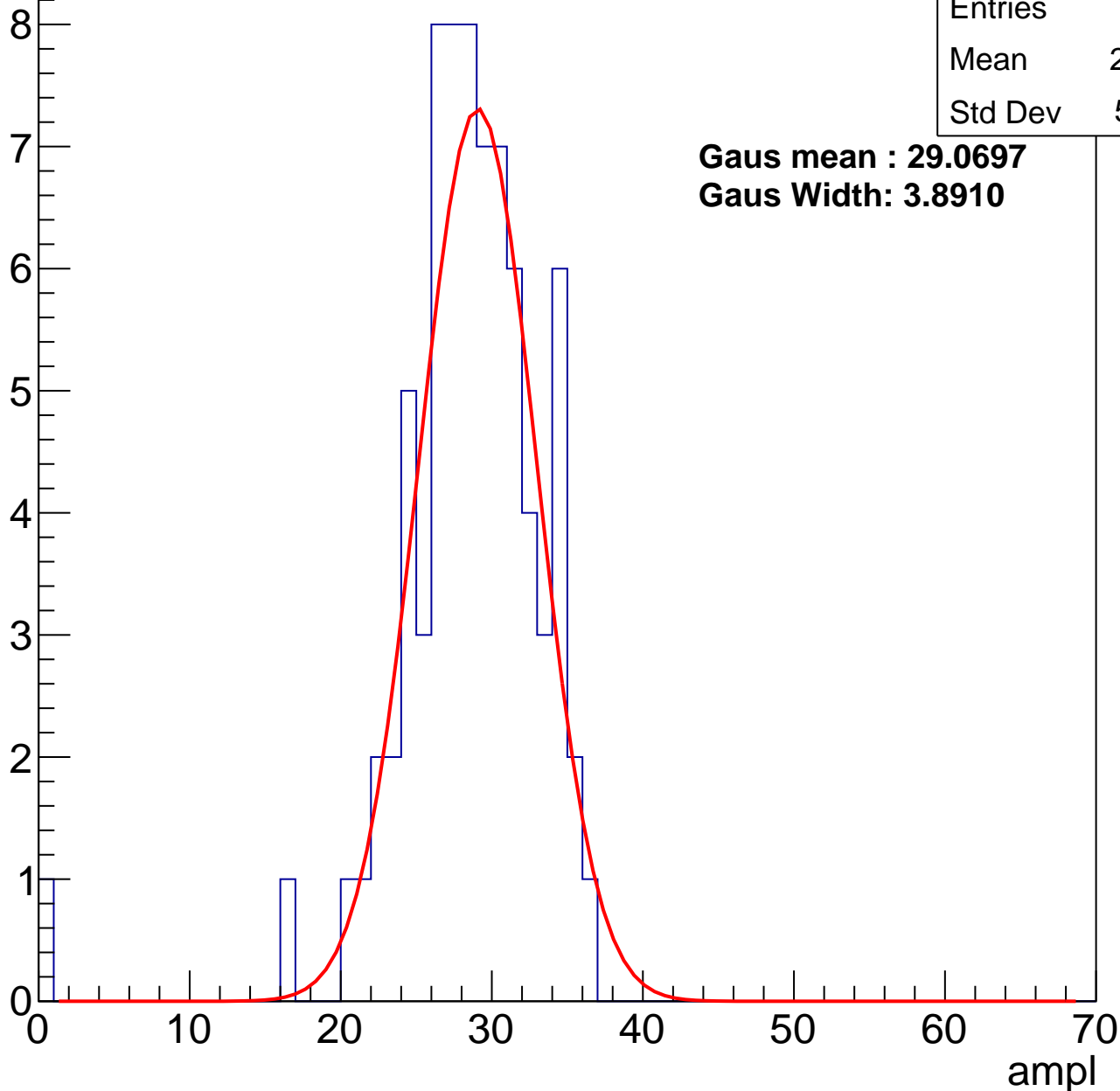
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	27.97
Std Dev	5.021

Gaus mean : 29.0697

Gaus Width: 3.8910



B1L103S, U10-ch82, adc1

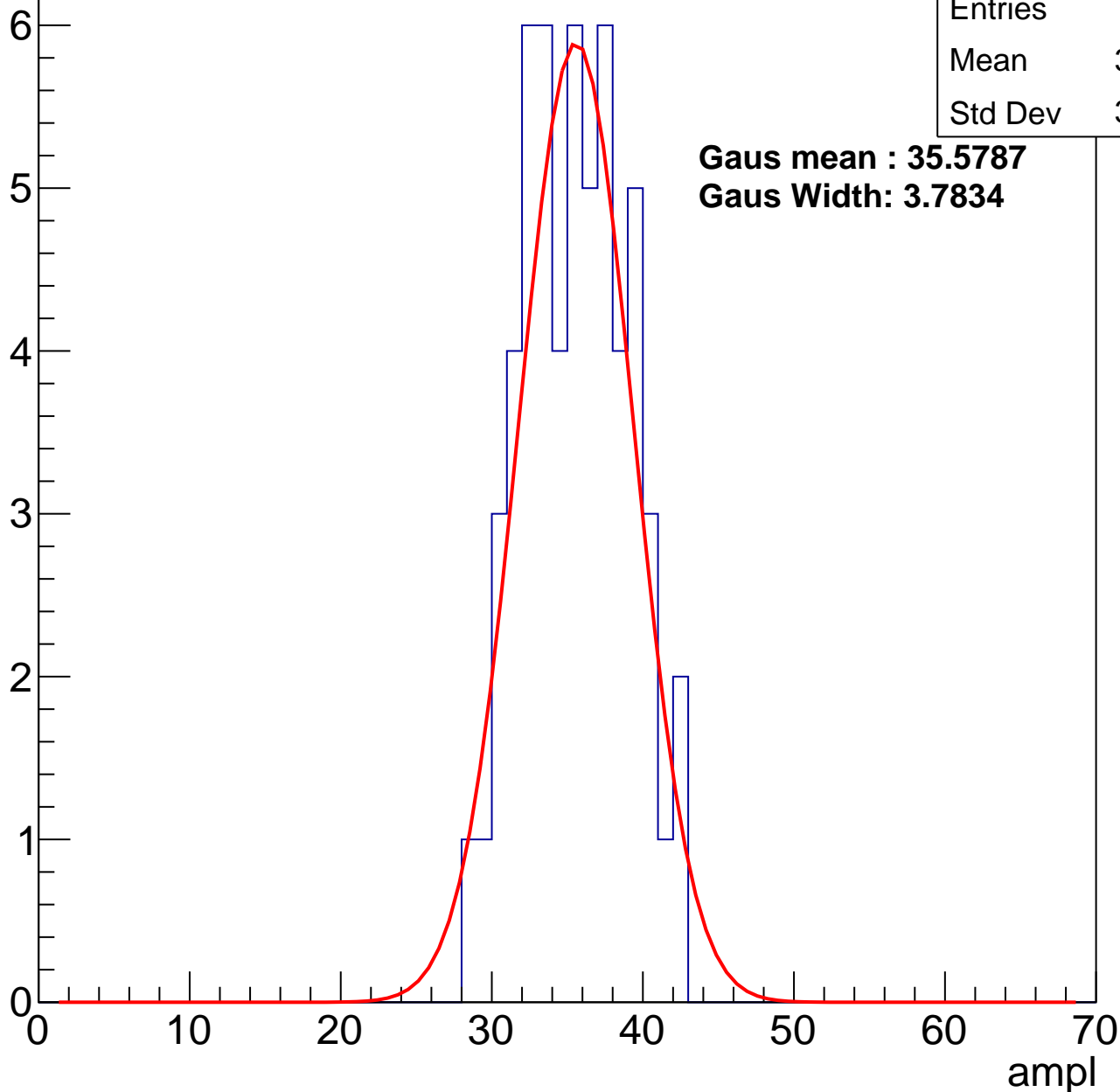
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	35.11
Std Dev	3.401

Gaus mean : 35.5787

Gaus Width: 3.7834



B1L103S, U10-ch82, adc2

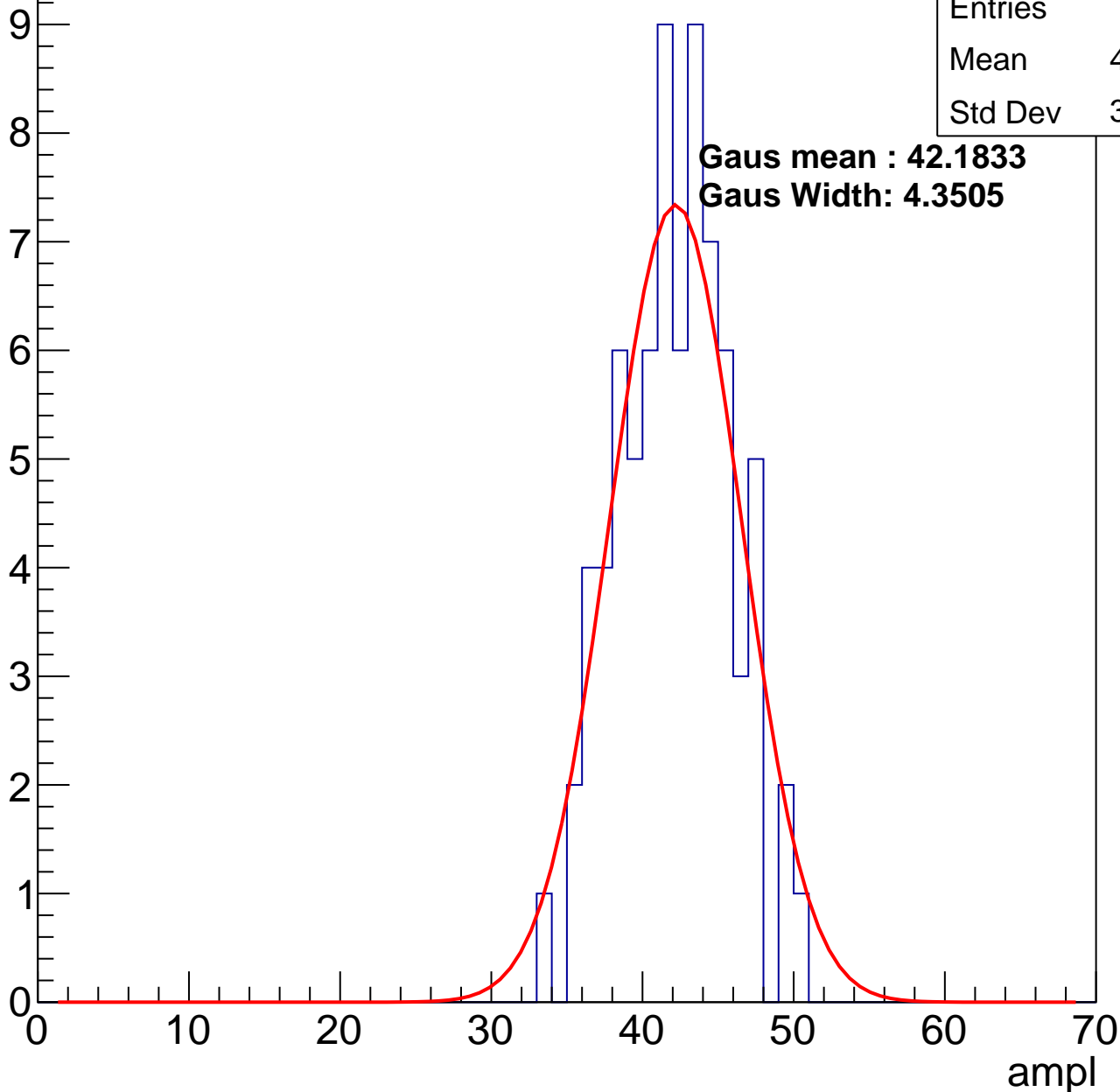
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	41.64
Std Dev	3.652

Gaus mean : 42.1833

Gaus Width: 4.3505

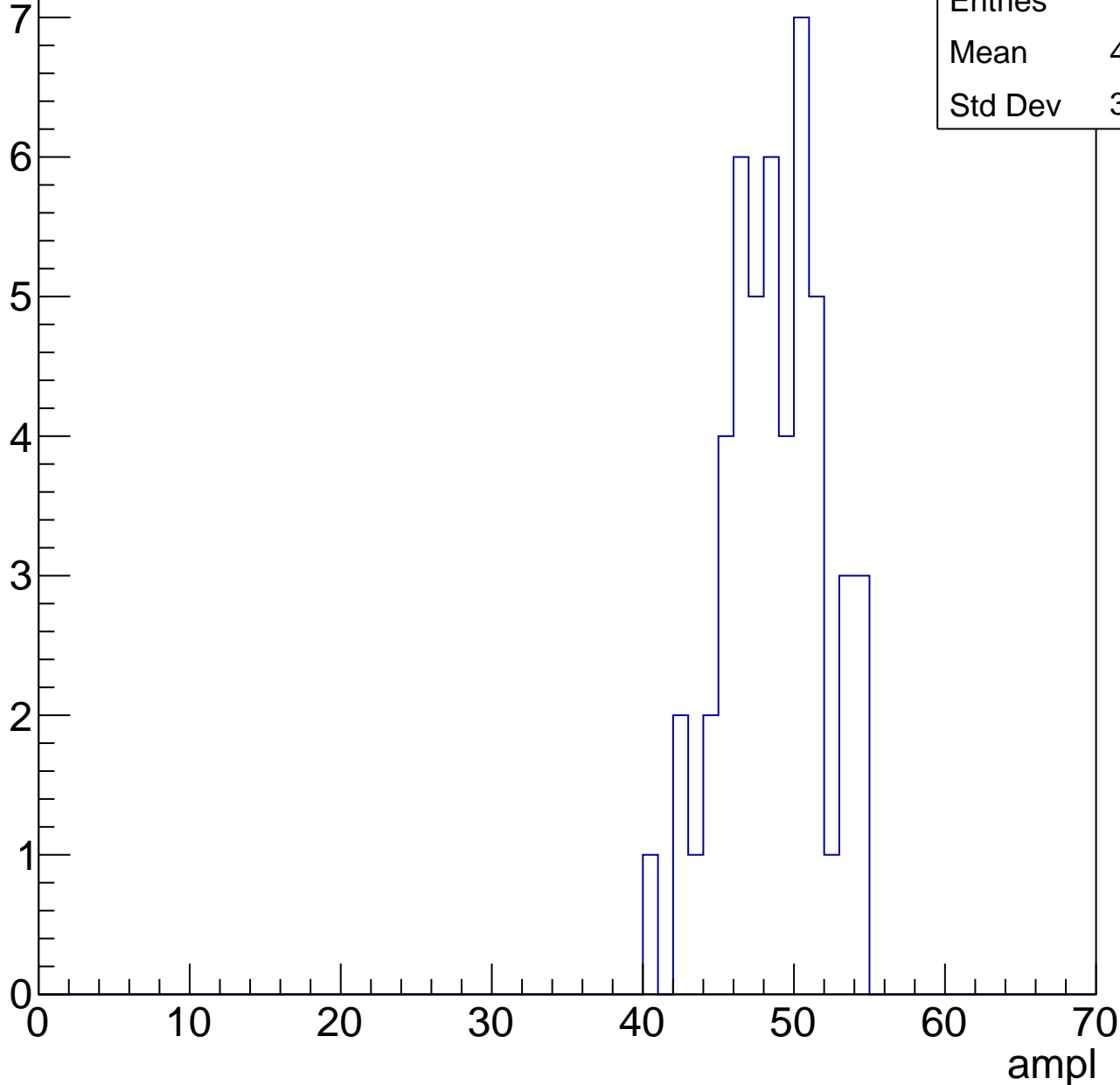


B1L103S, U10-ch82, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	48.16
Std Dev	3.264

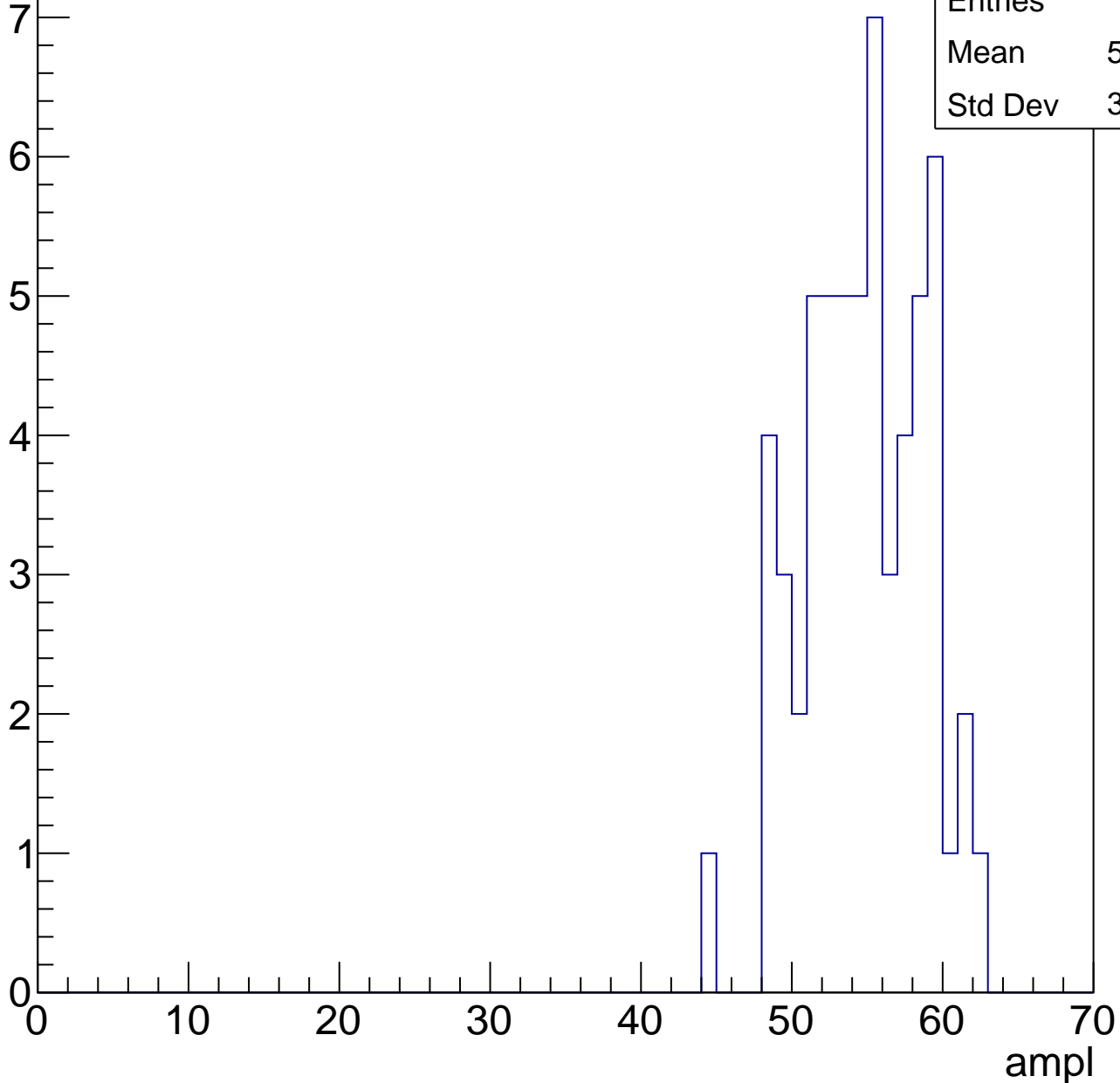


B1L103S, U10-ch82, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	54.27
Std Dev	3.905

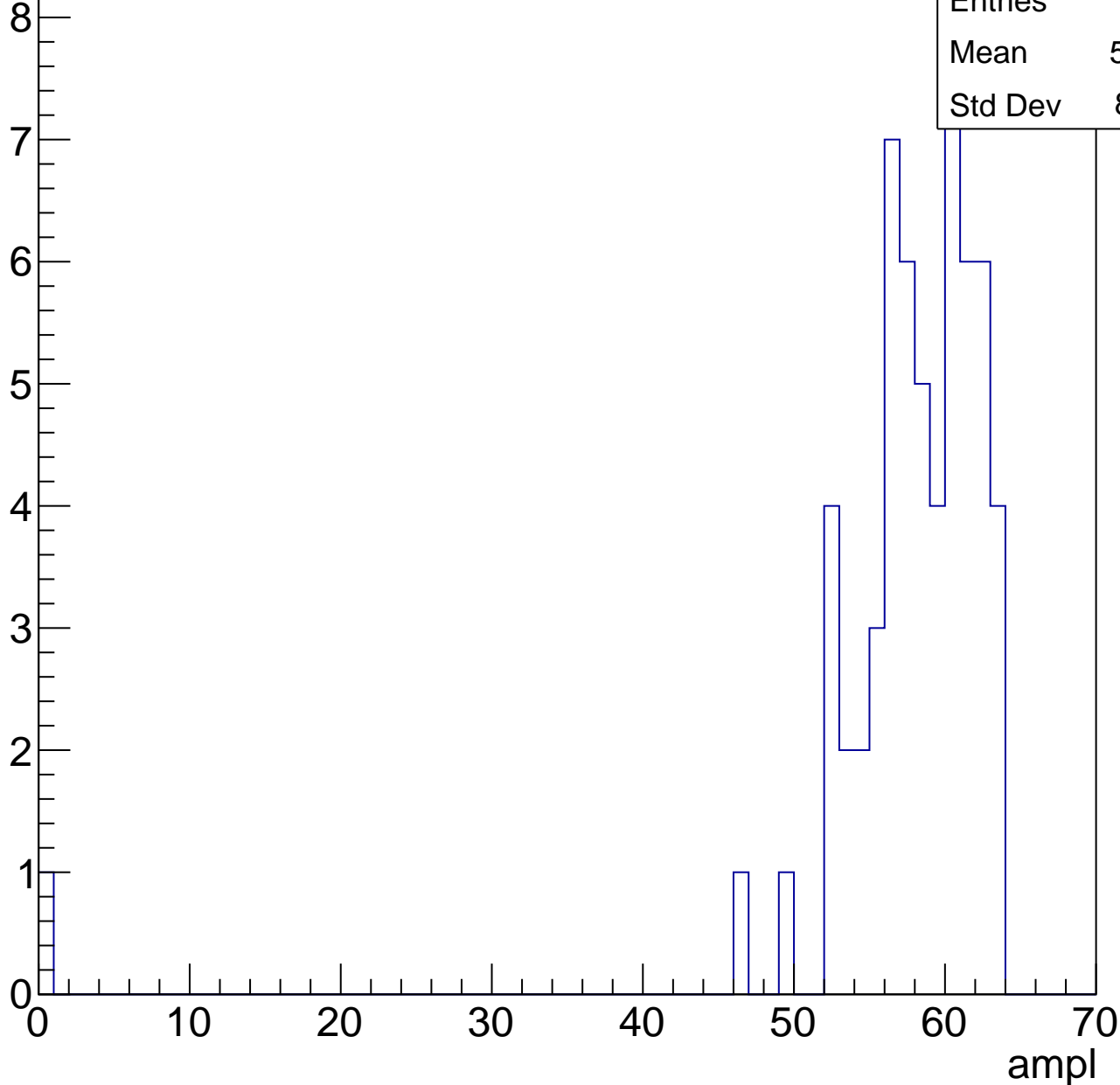


B1L103S, U10-ch82, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	56.87
Std Dev	8.251

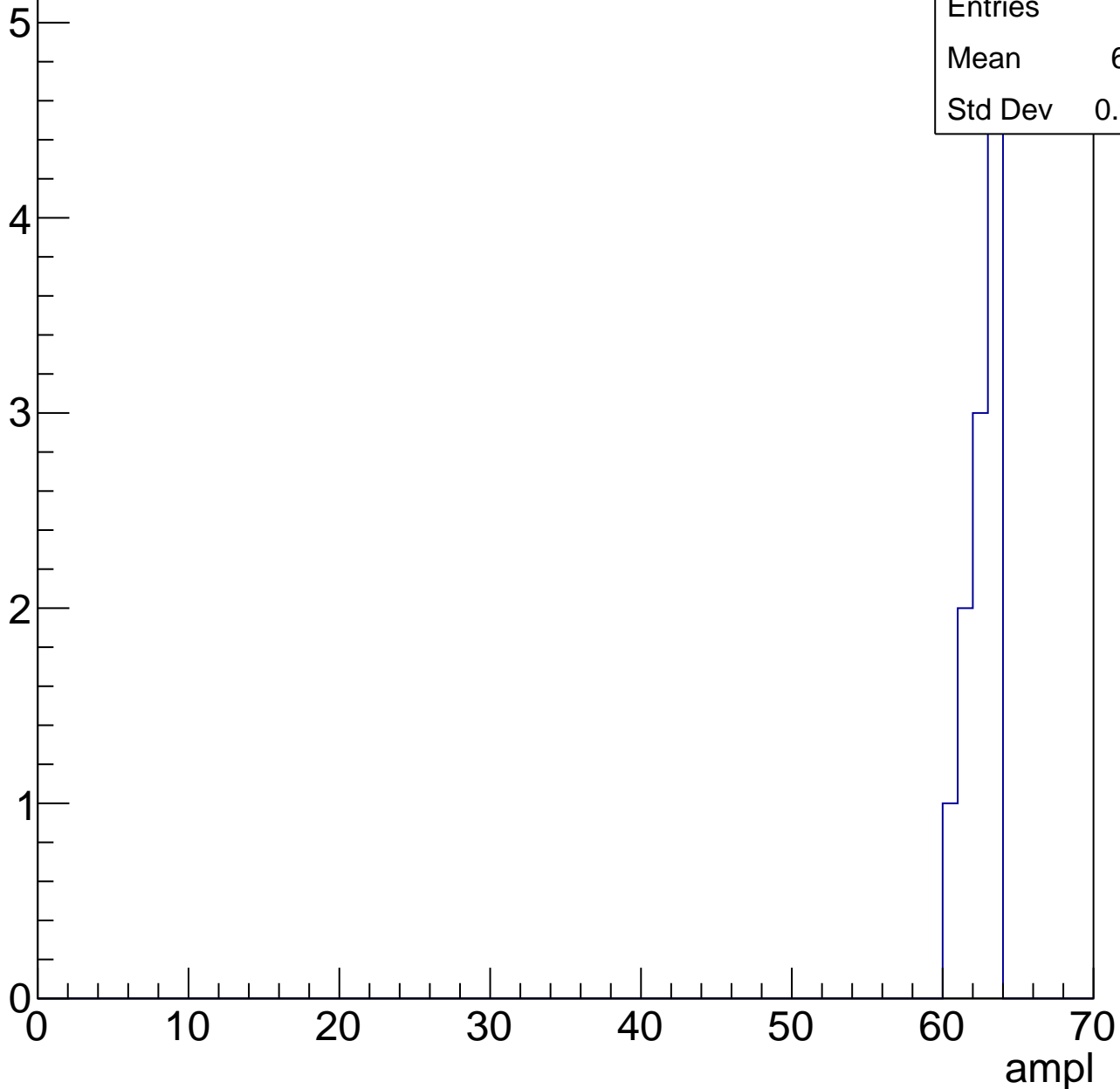


B1L103S, U10-ch82, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	11
Mean	62.09
Std Dev	0.9959



B1L103S, U10-ch82, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch83, adc0

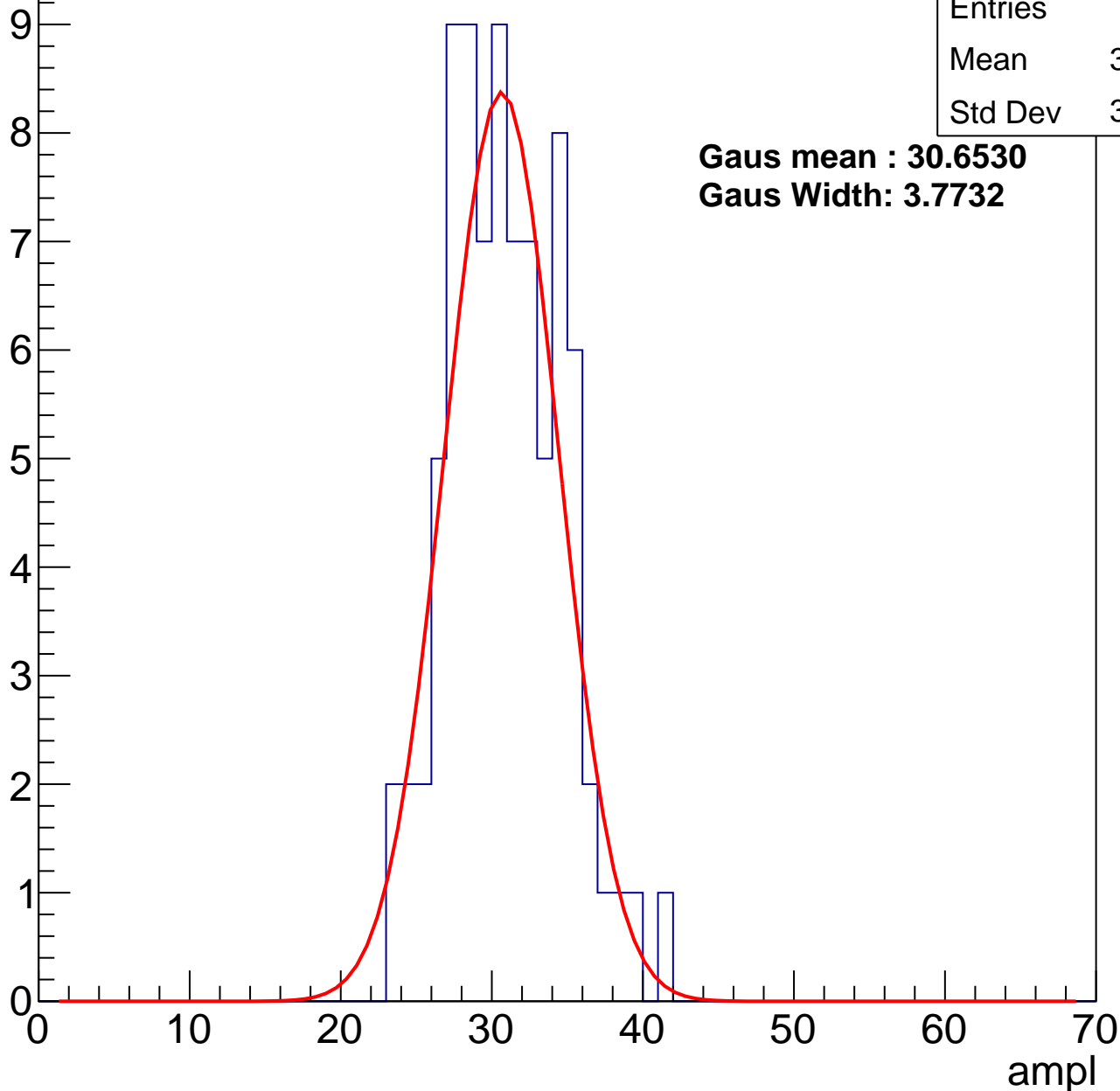
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	30.44
Std Dev	3.704

Gaus mean : 30.6530

Gaus Width: 3.7732



B1L103S, U10-ch83, adc1

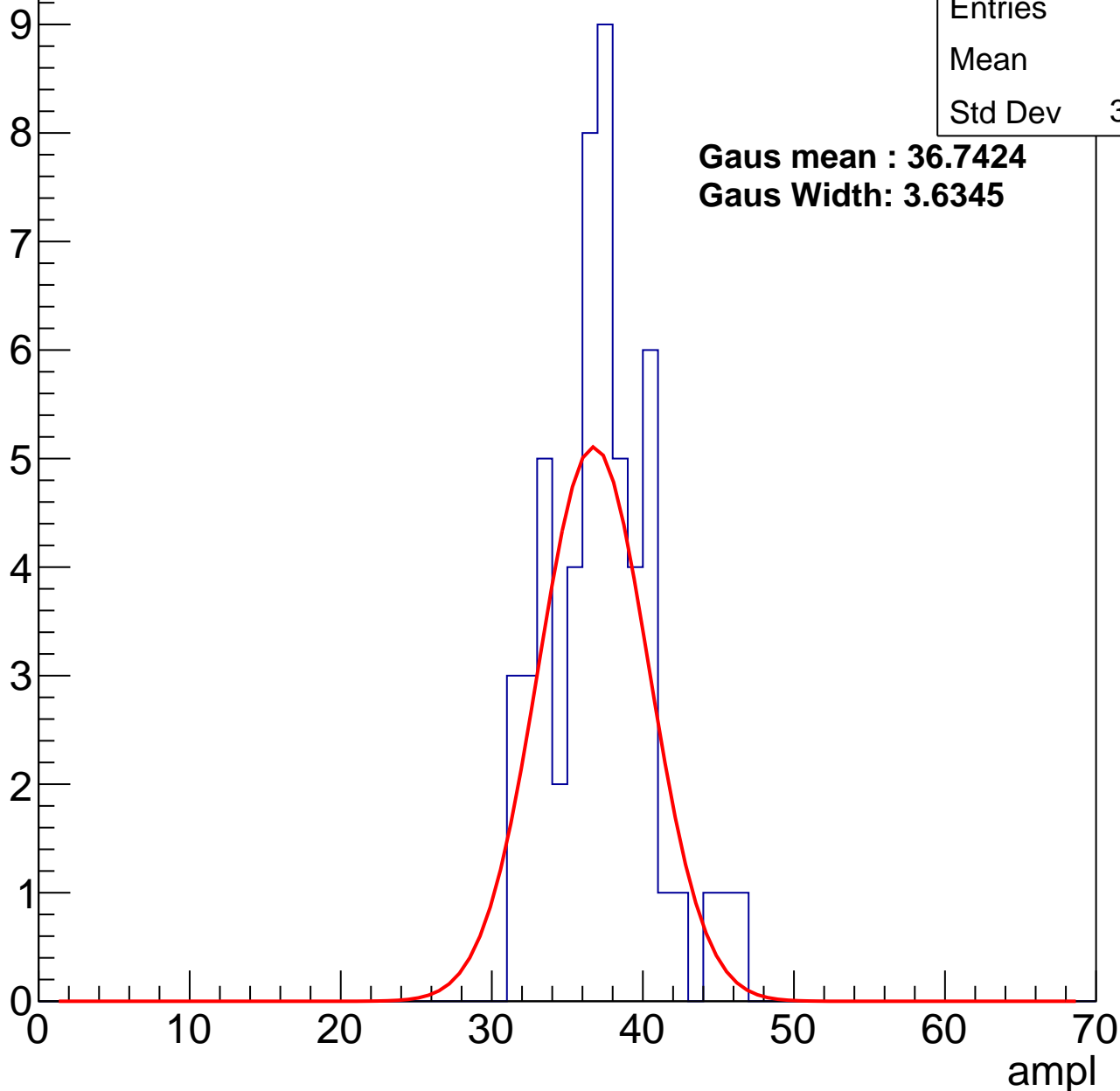
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	36.8
Std Dev	3.369

Gaus mean : 36.7424

Gaus Width: 3.6345



B1L103S, U10-ch83, adc2

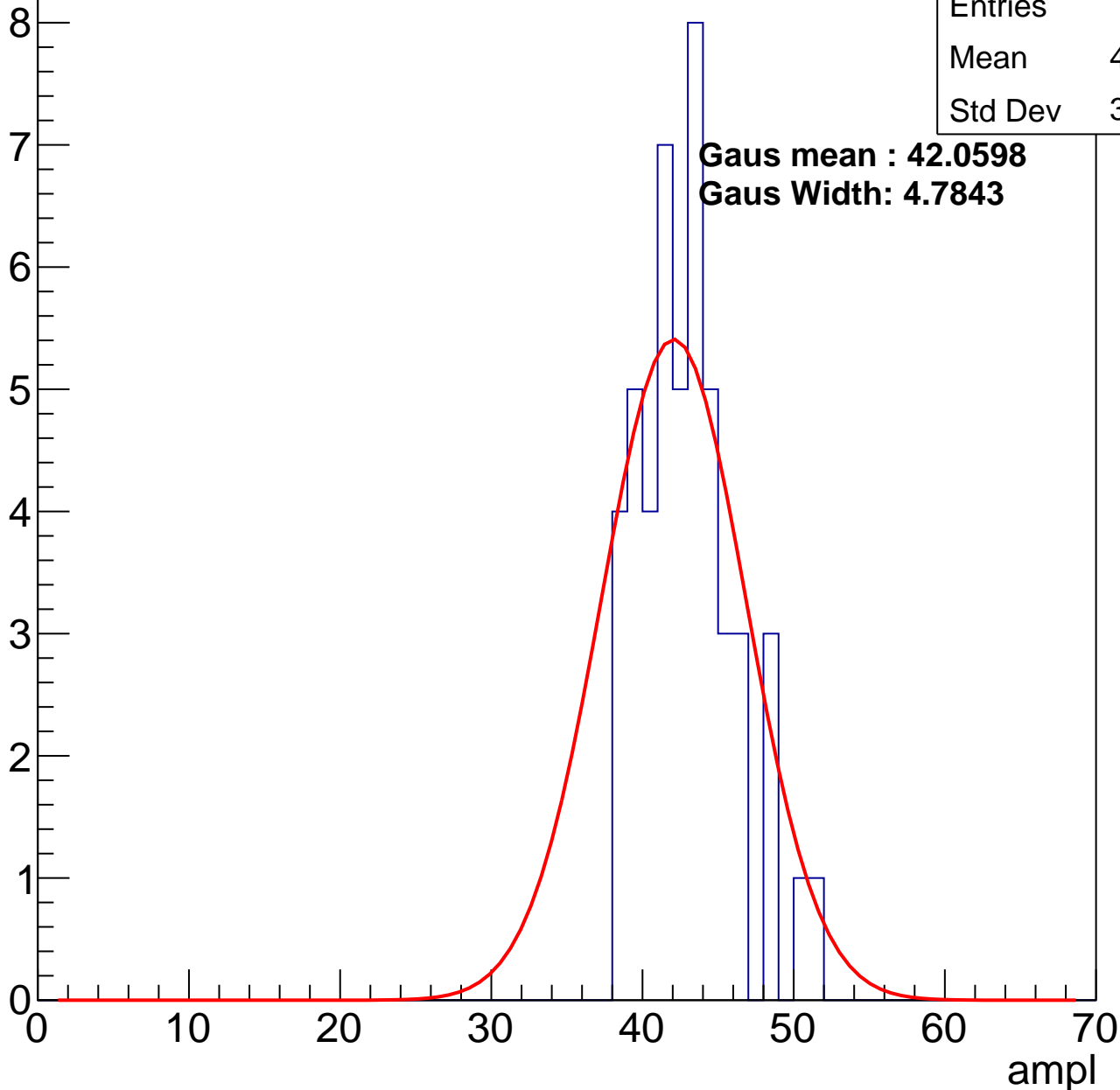
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	42.57
Std Dev	3.104

Gaus mean : 42.0598

Gaus Width: 4.7843

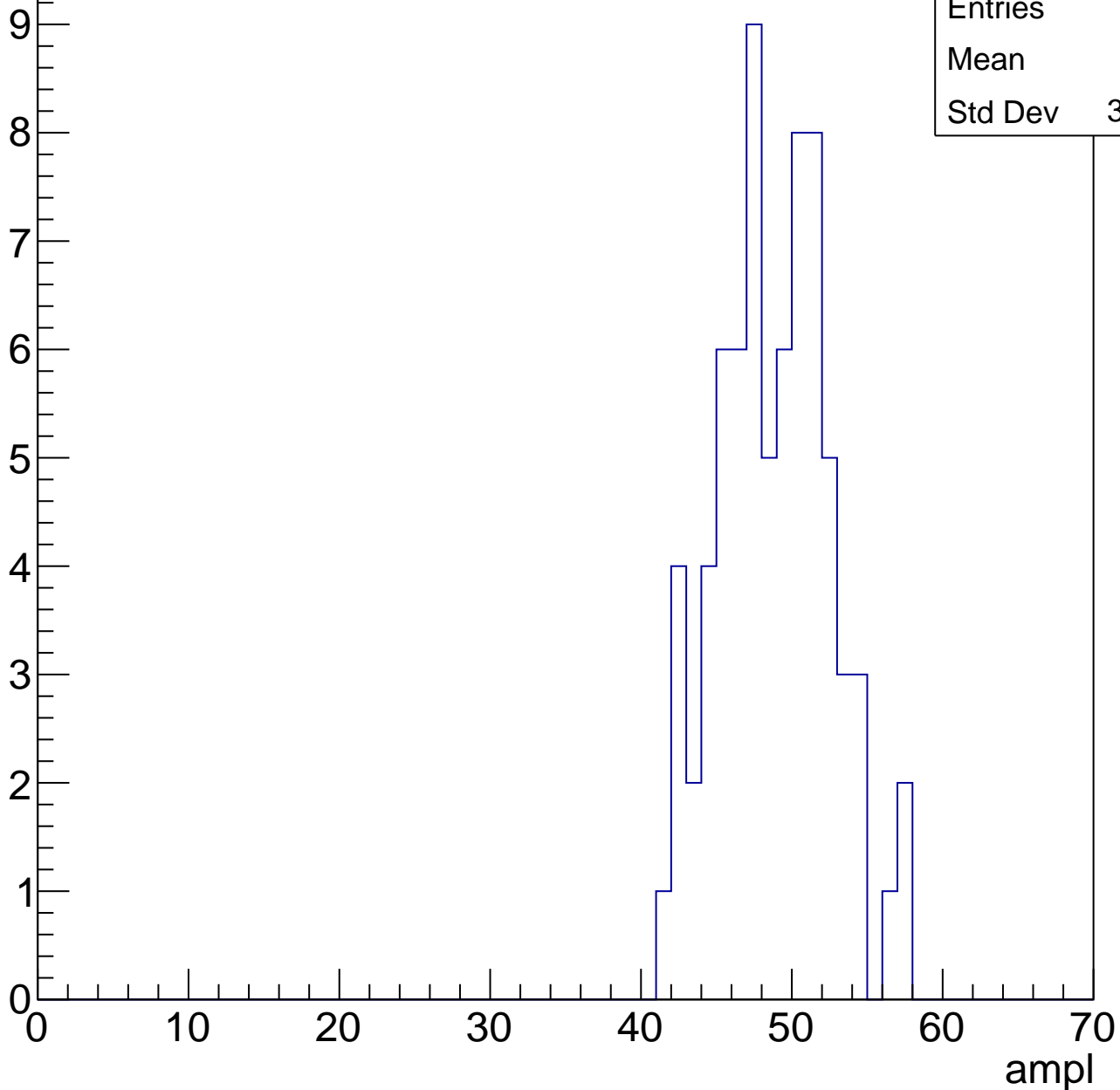


B1L103S, U10-ch83, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	48.4
Std Dev	3.667

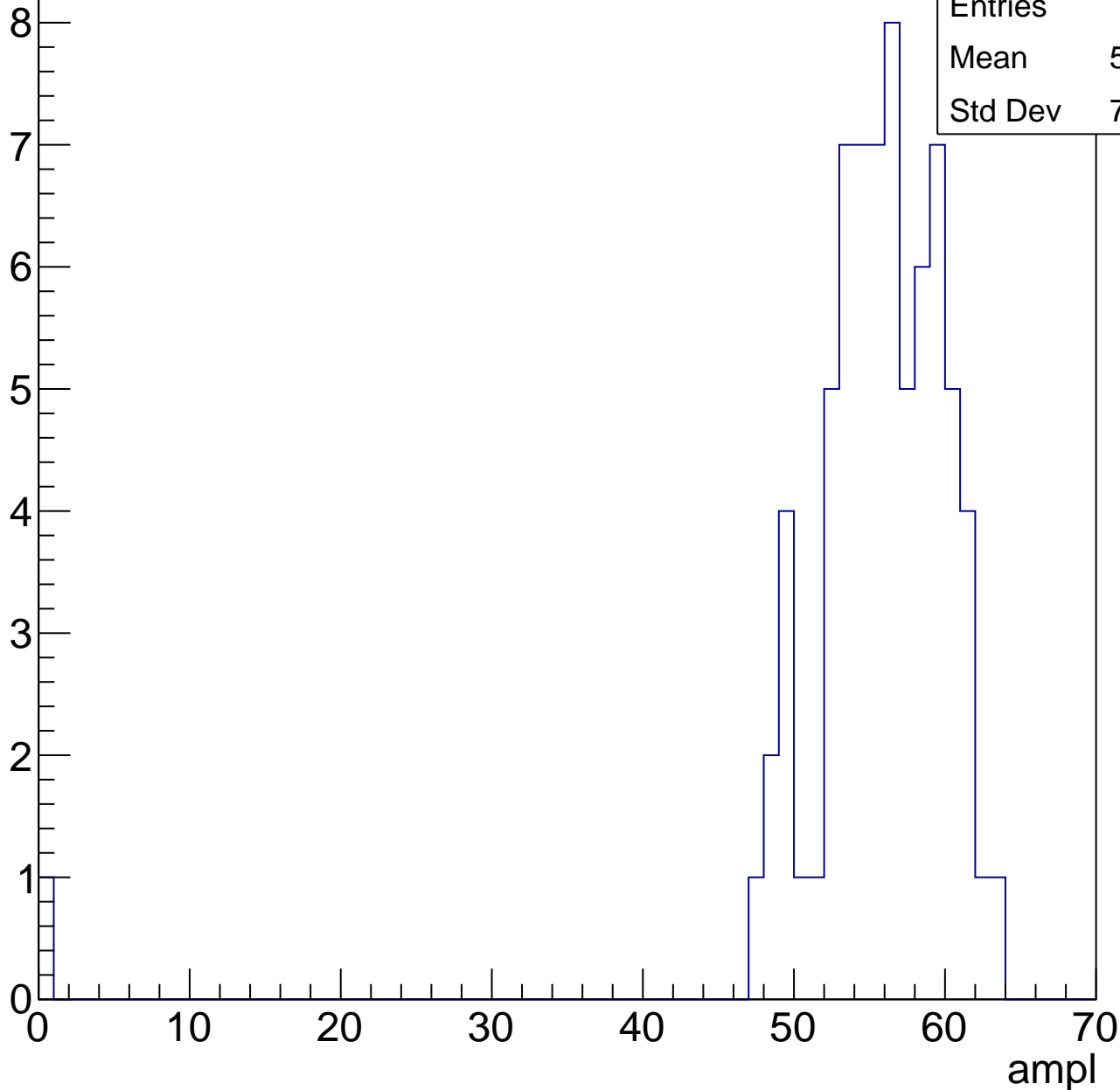


B1L103S, U10-ch83, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	54.75
Std Dev	7.424

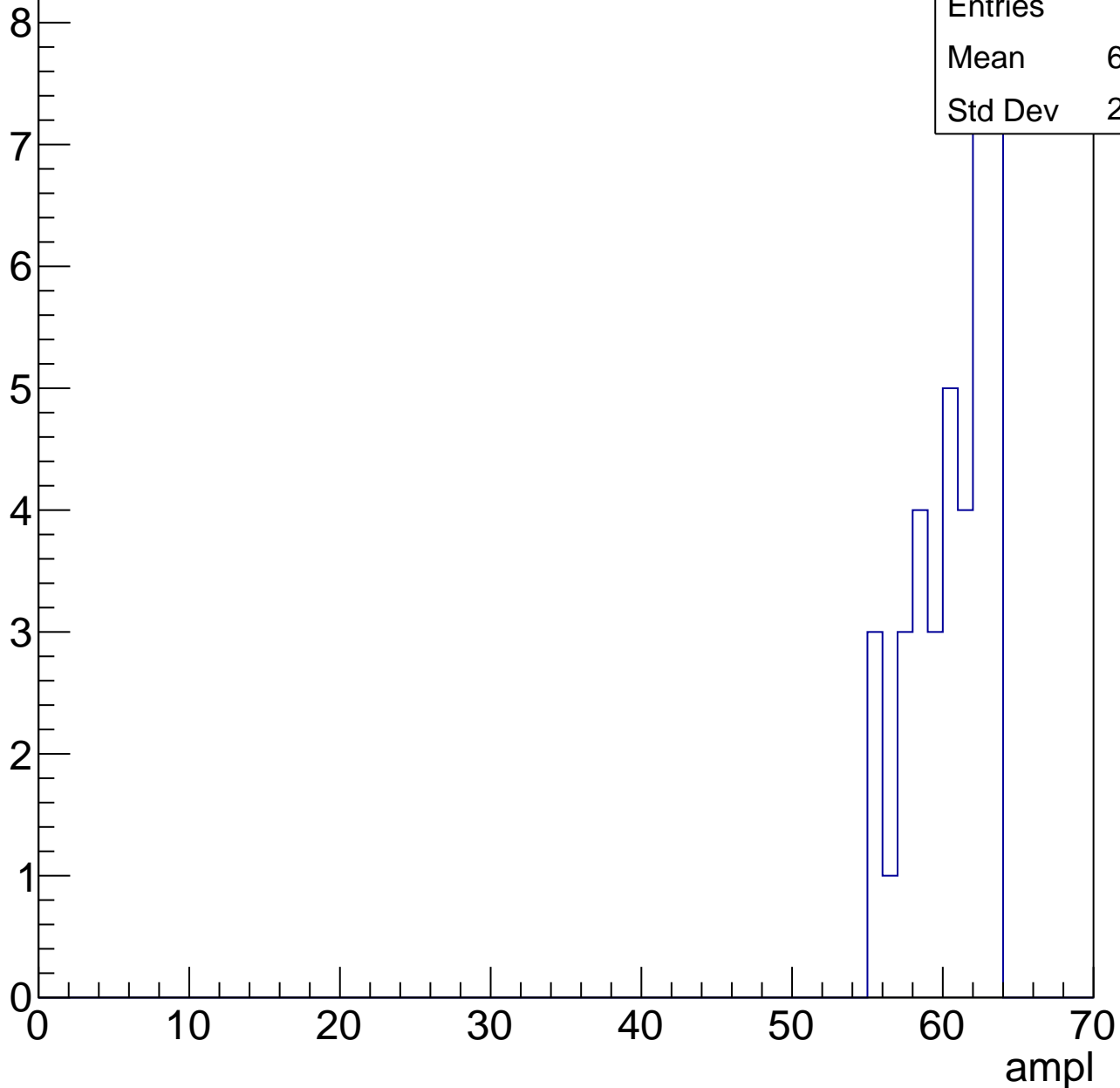


B1L103S, U10-ch83, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	39
Mean	60.13
Std Dev	2.503



B1L103S, U10-ch83, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	61.75
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch83, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

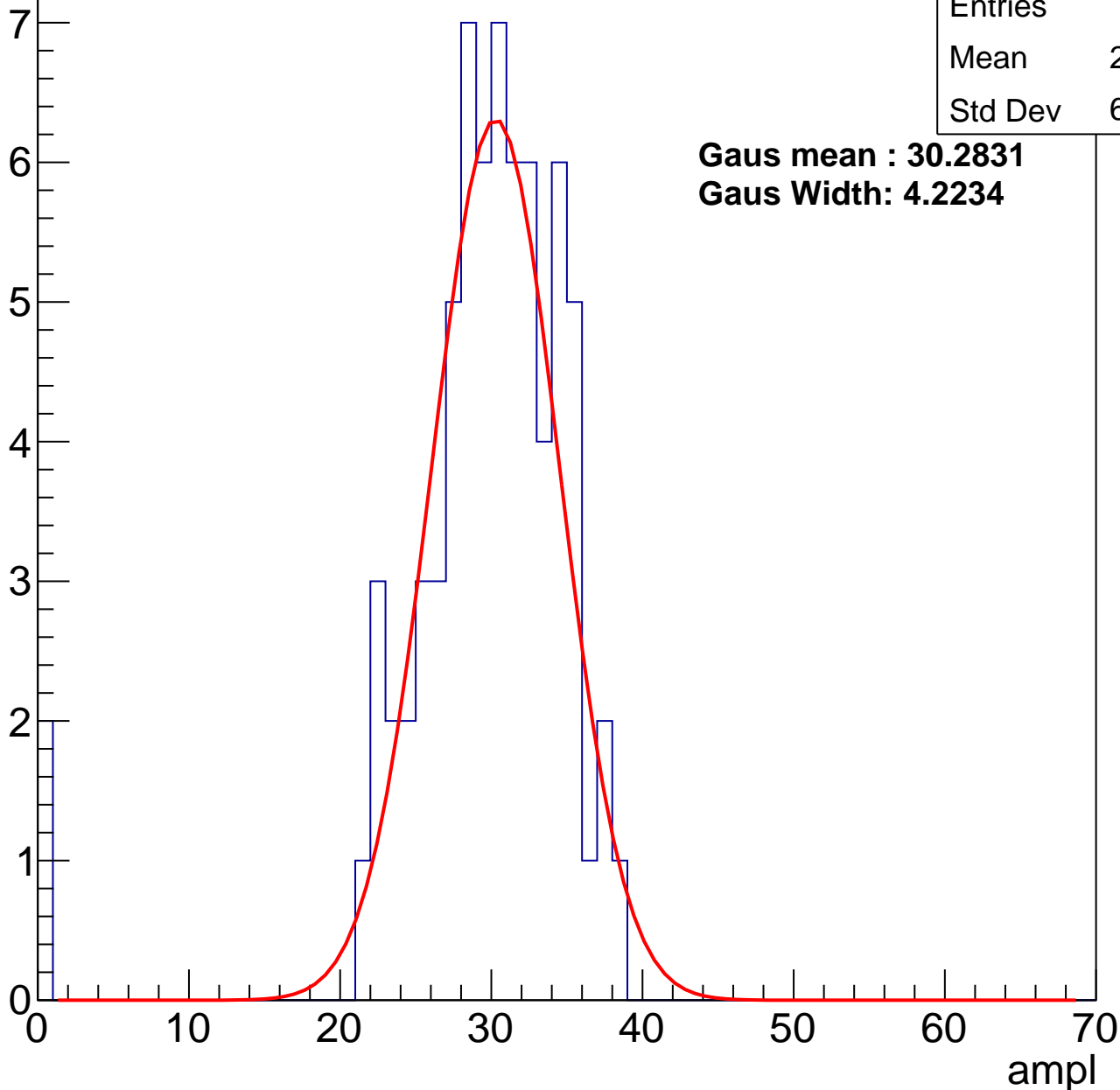
B1L103S, U10-ch84, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	28.97
Std Dev	6.296

Gaus mean : 30.2831
Gaus Width: 4.2234



B1L103S, U10-ch84, adc1

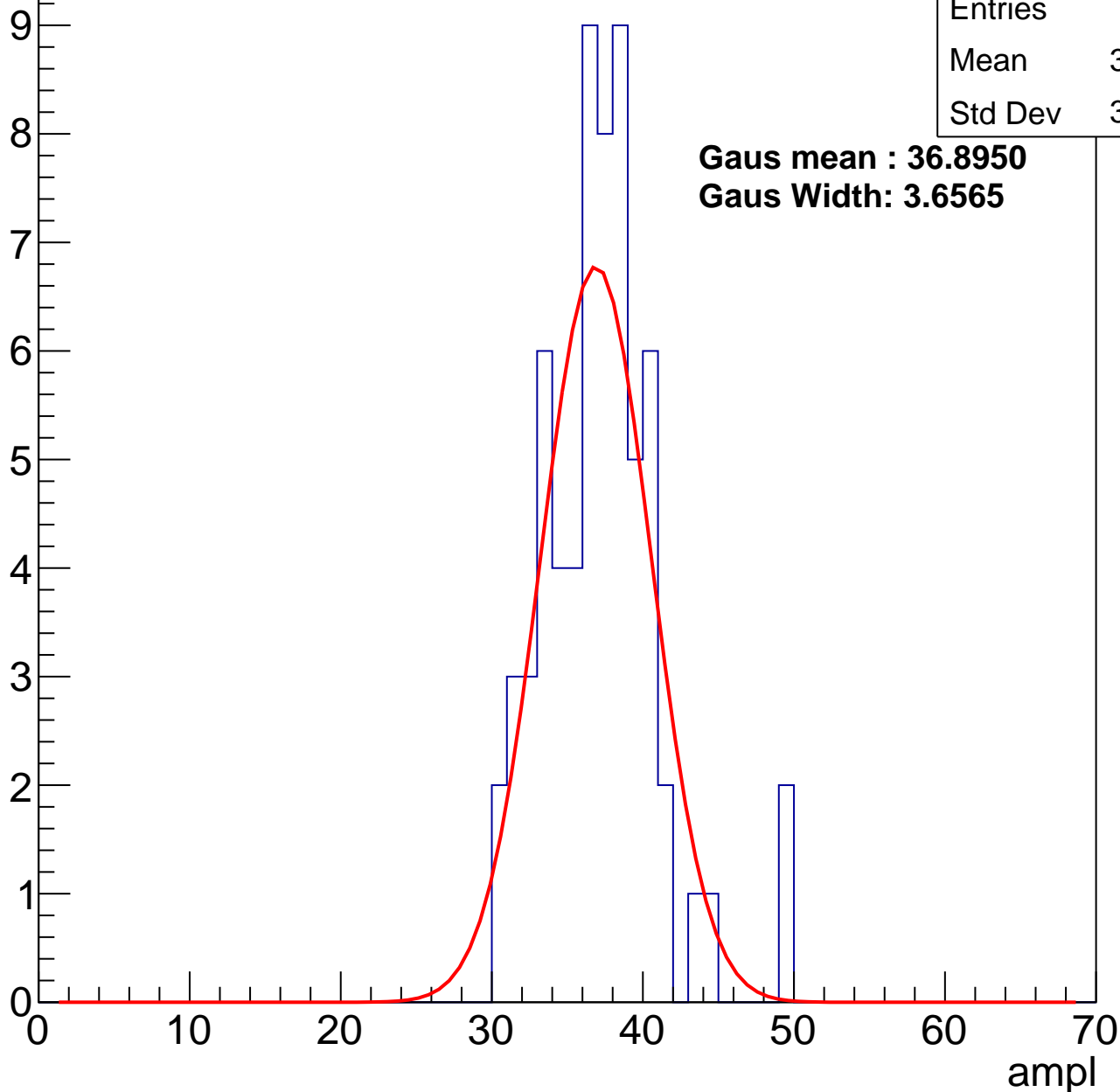
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.72
Std Dev	3.764

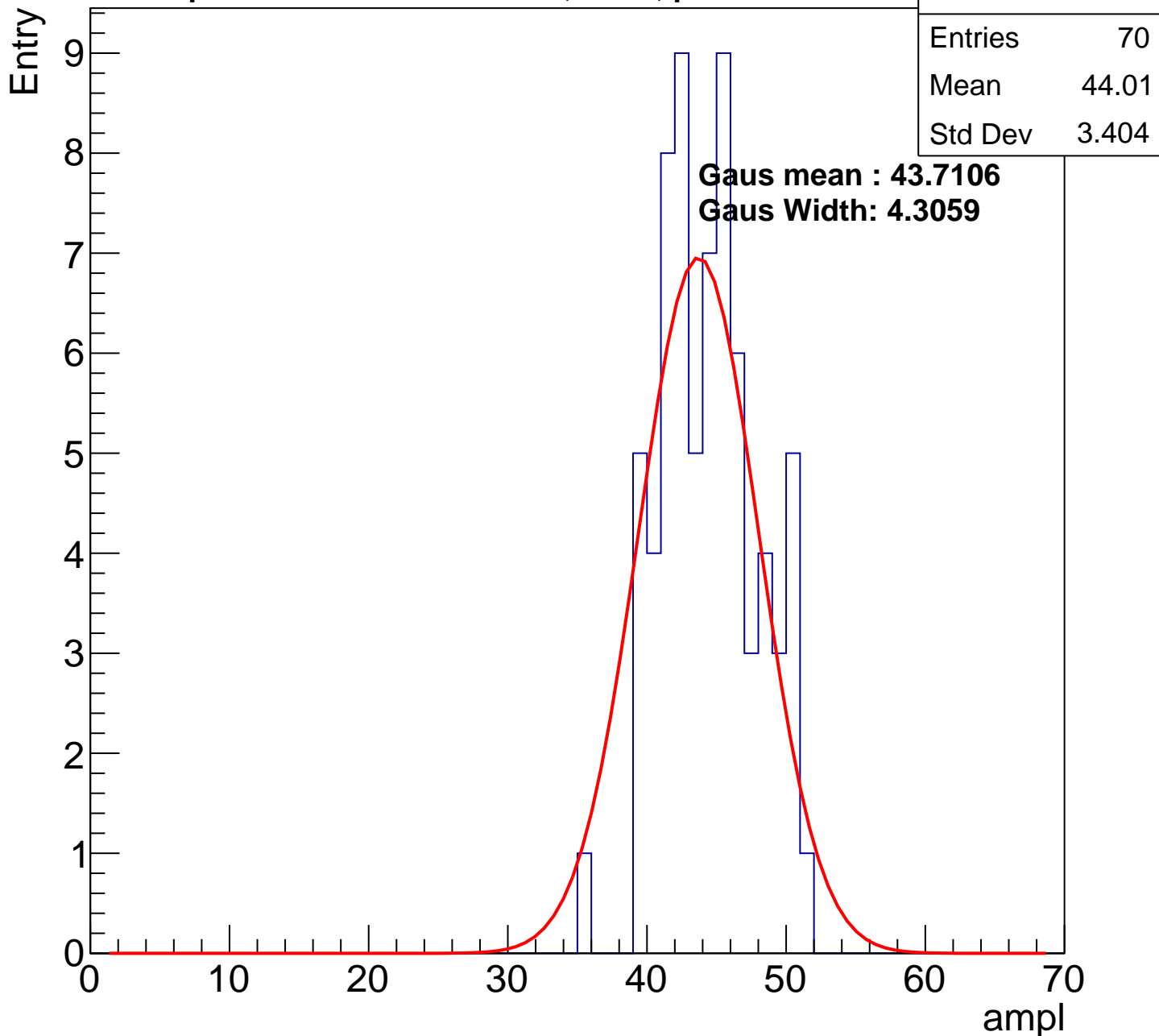
Gaus mean : 36.8950

Gaus Width: 3.6565



B1L103S, U10-ch84, adc2

calib_packv5_042523_0143.root, FC#7, port C2

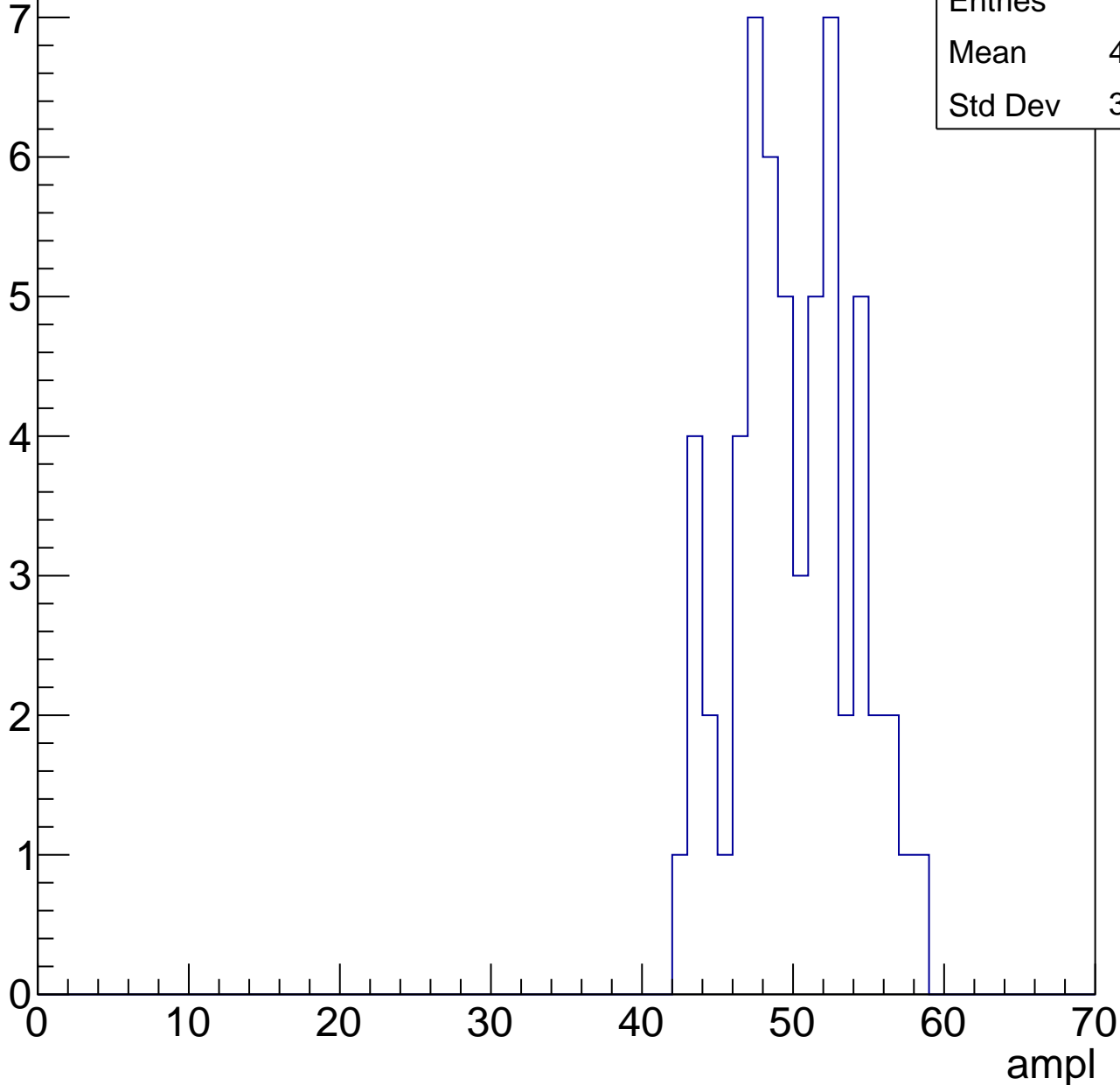


B1L103S, U10-ch84, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	49.57
Std Dev	3.869

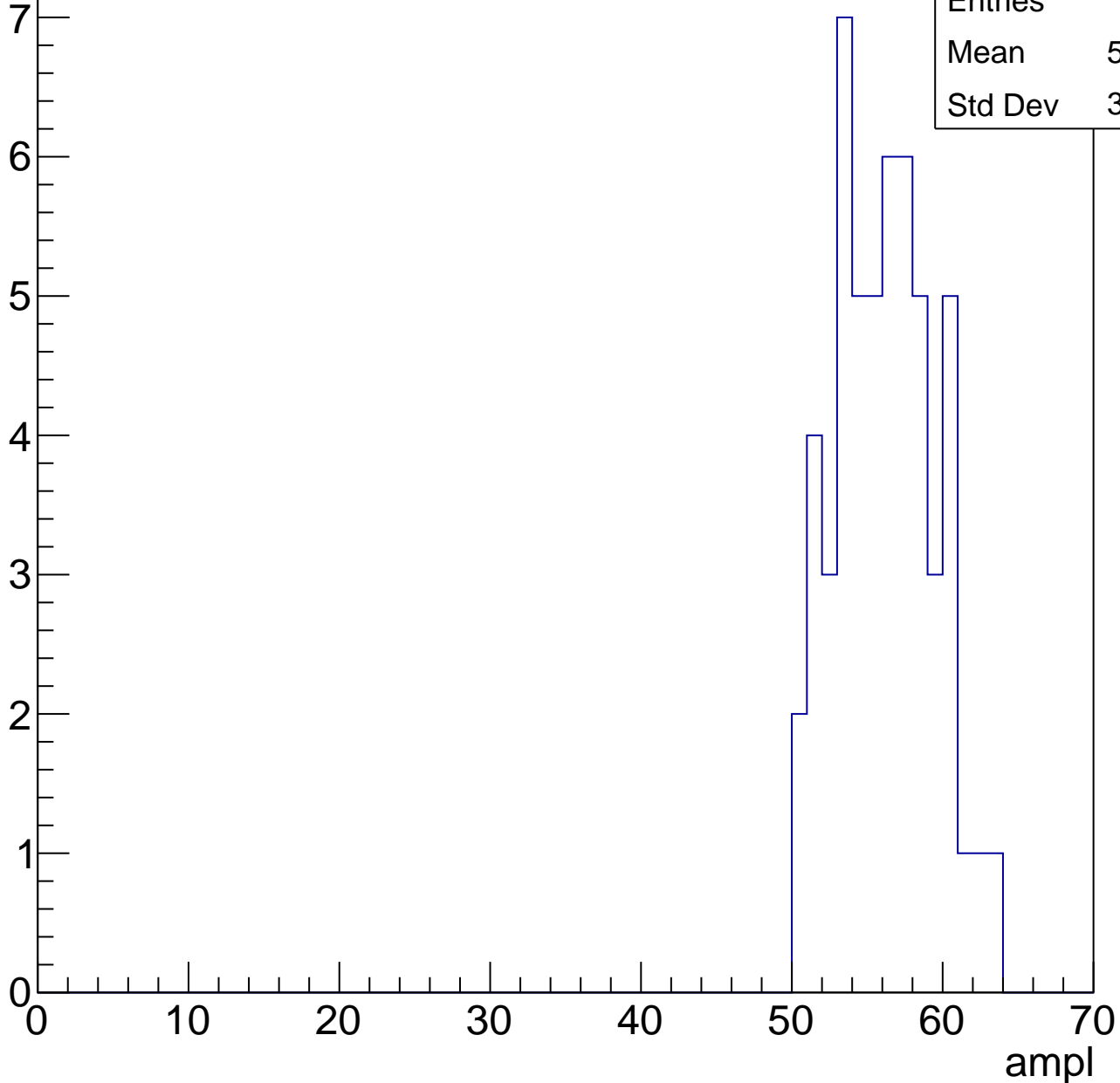


B1L103S, U10-ch84, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	55.69
Std Dev	3.179

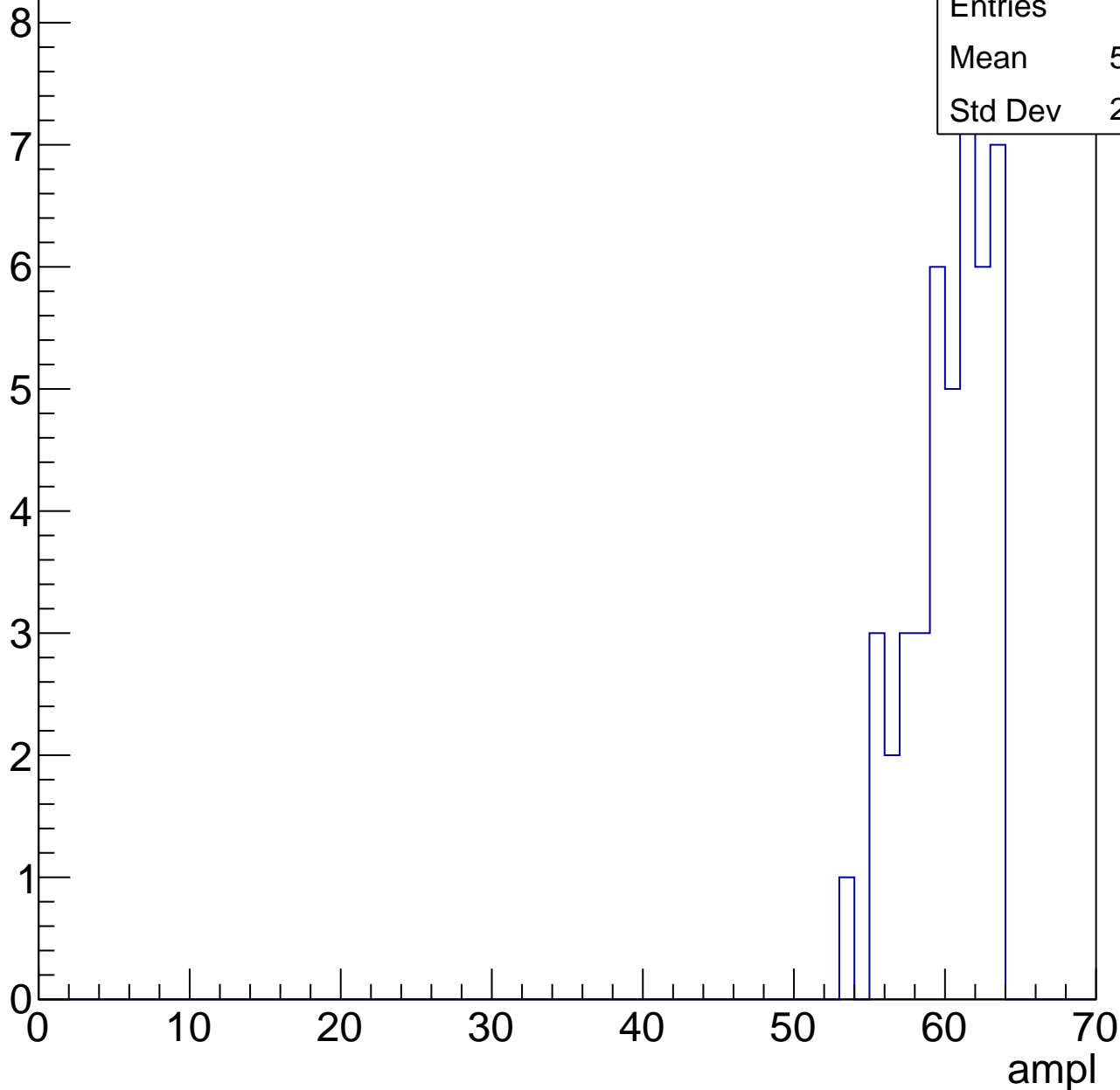


B1L103S, U10-ch84, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	44
Mean	59.77
Std Dev	2.584



B1L103S, U10-ch84, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

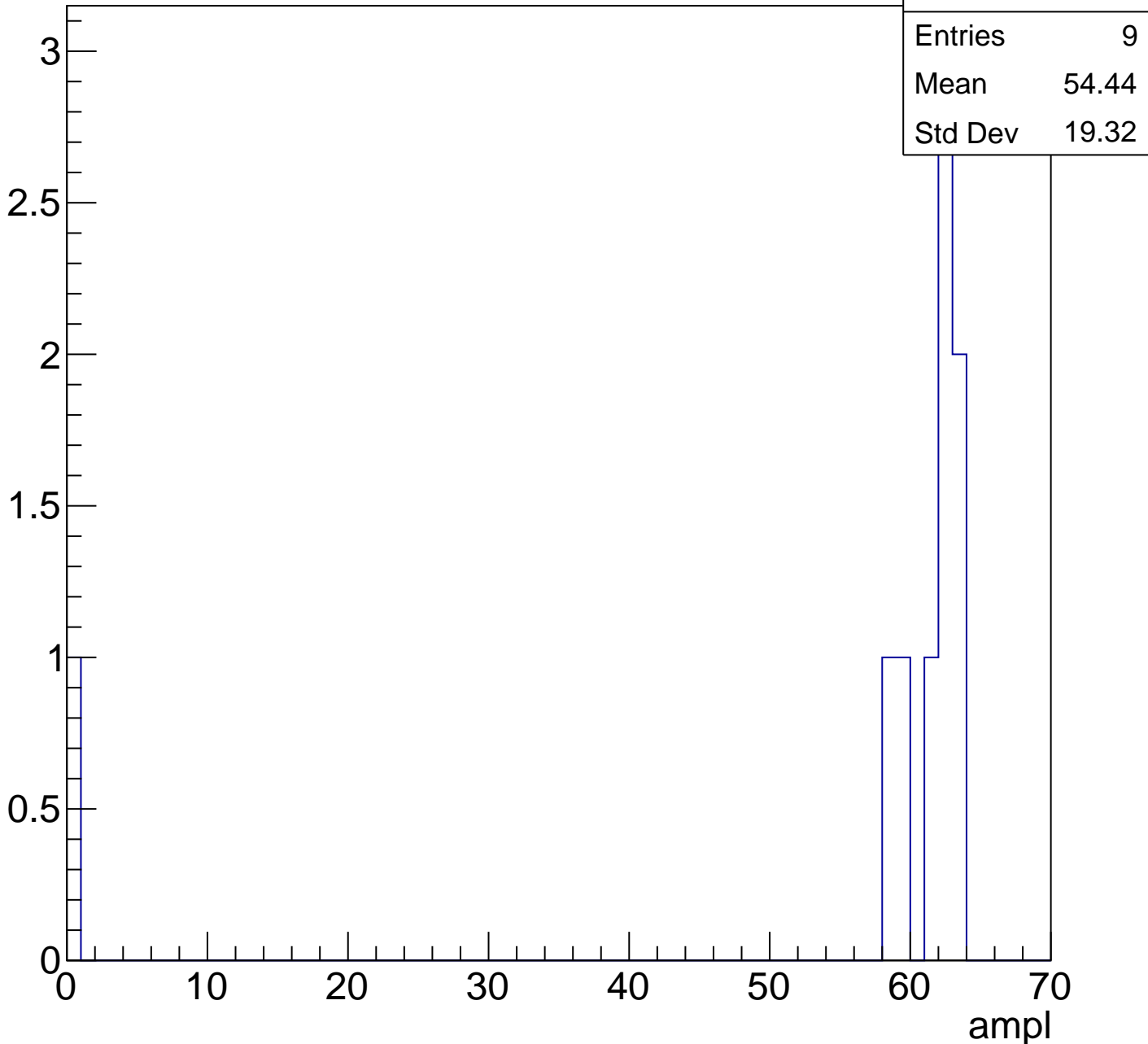
1

0.5

0

ampl

Entries	9
Mean	54.44
Std Dev	19.32



B1L103S, U10-ch84, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch85, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	28.46
Std Dev	3.363

Gaus mean : 28.8129

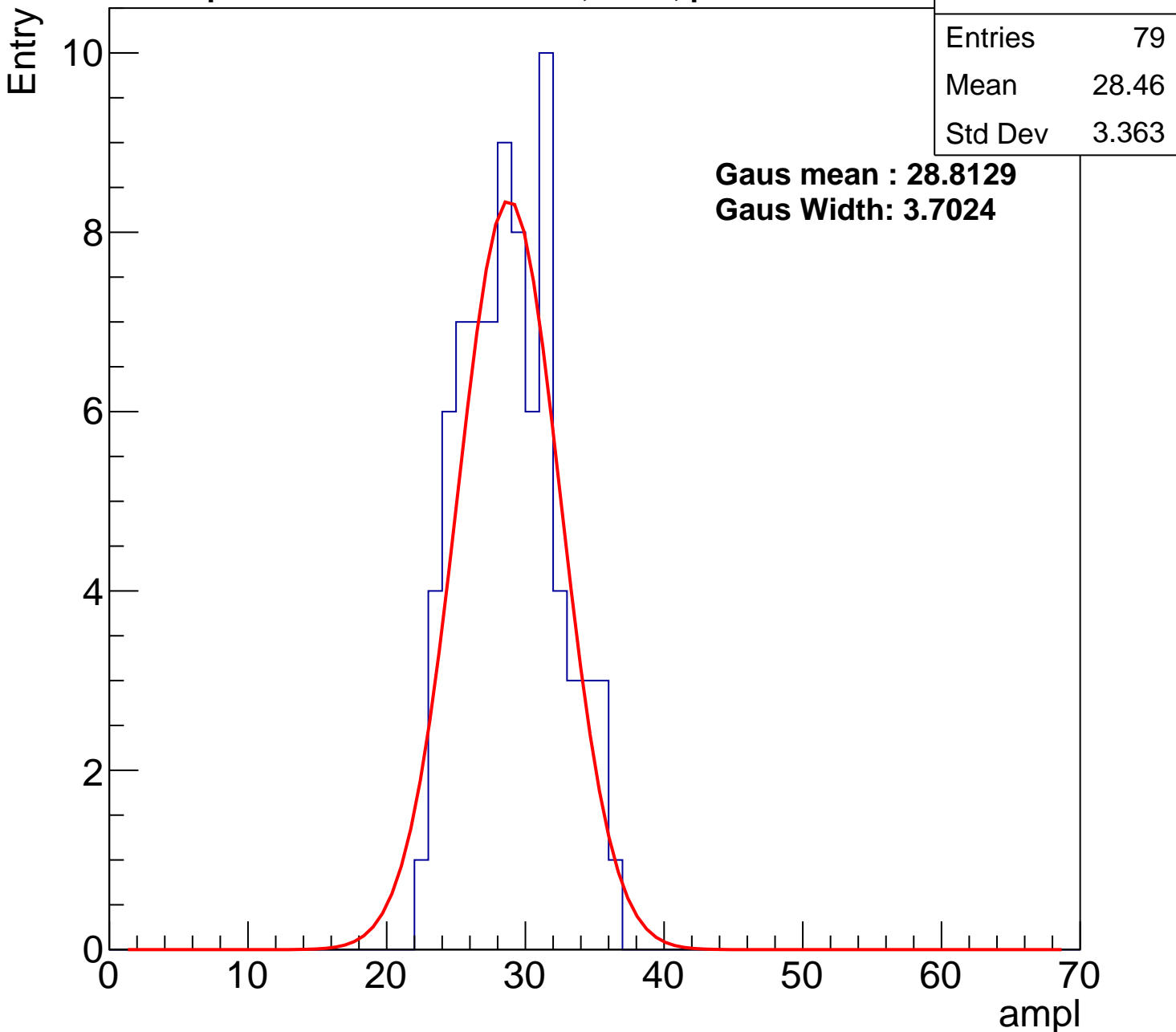
Gaus Width: 3.7024

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



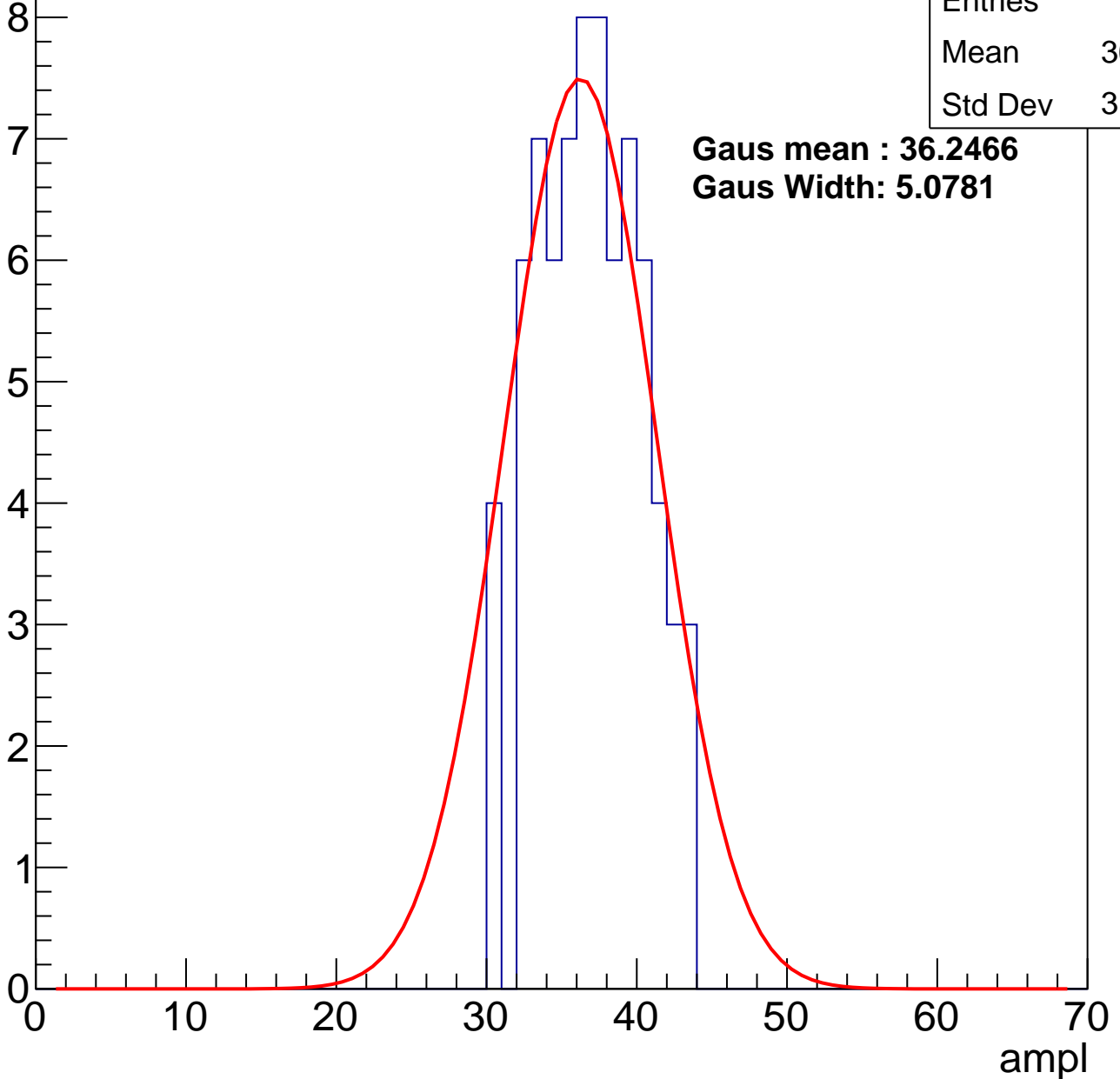
B1L103S, U10-ch85, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.48
Std Dev	3.392

Gaus mean : 36.2466
Gaus Width: 5.0781



B1L103S, U10-ch85, adc2

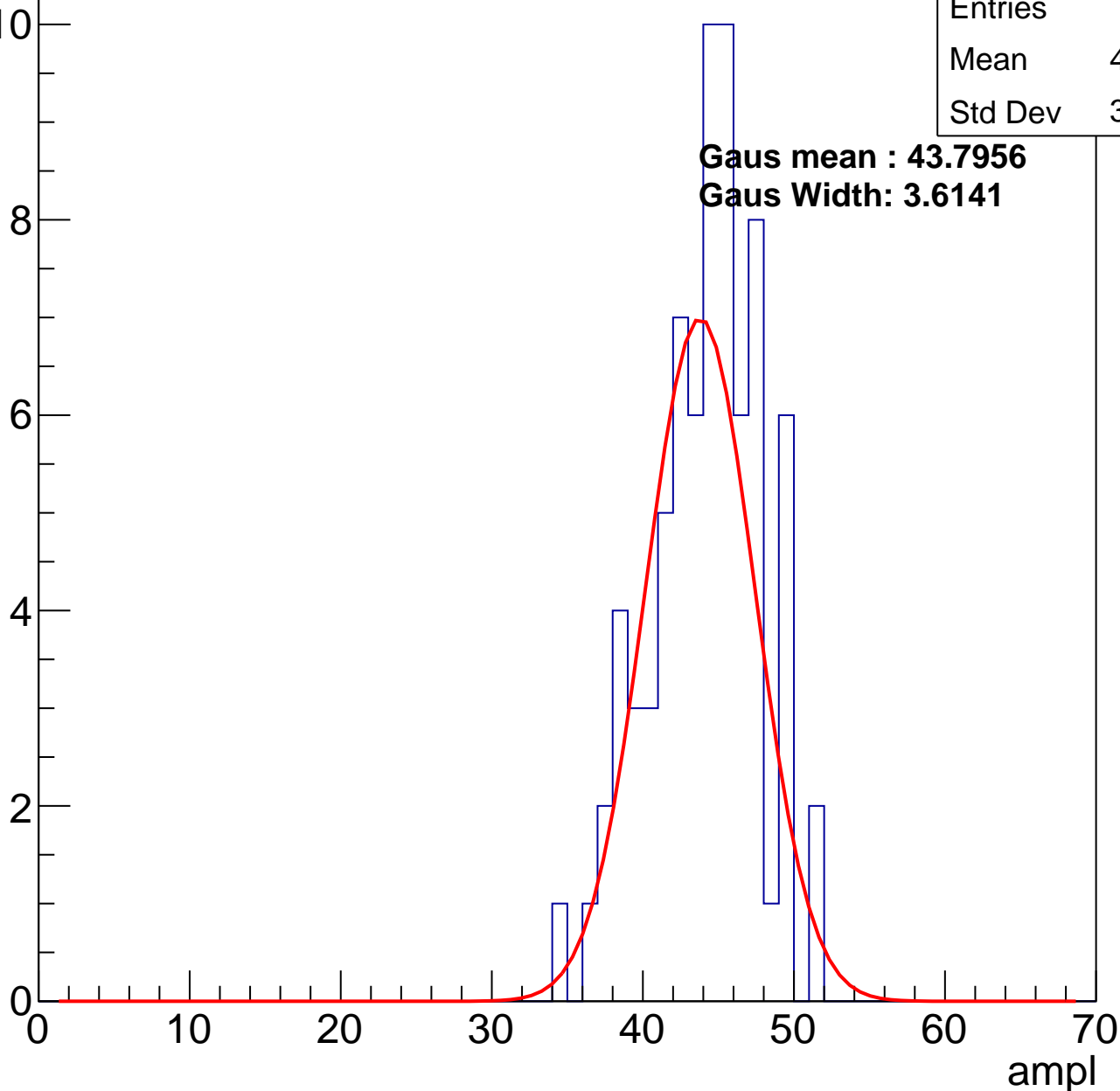
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	43.68
Std Dev	3.597

Gaus mean : 43.7956

Gaus Width: 3.6141

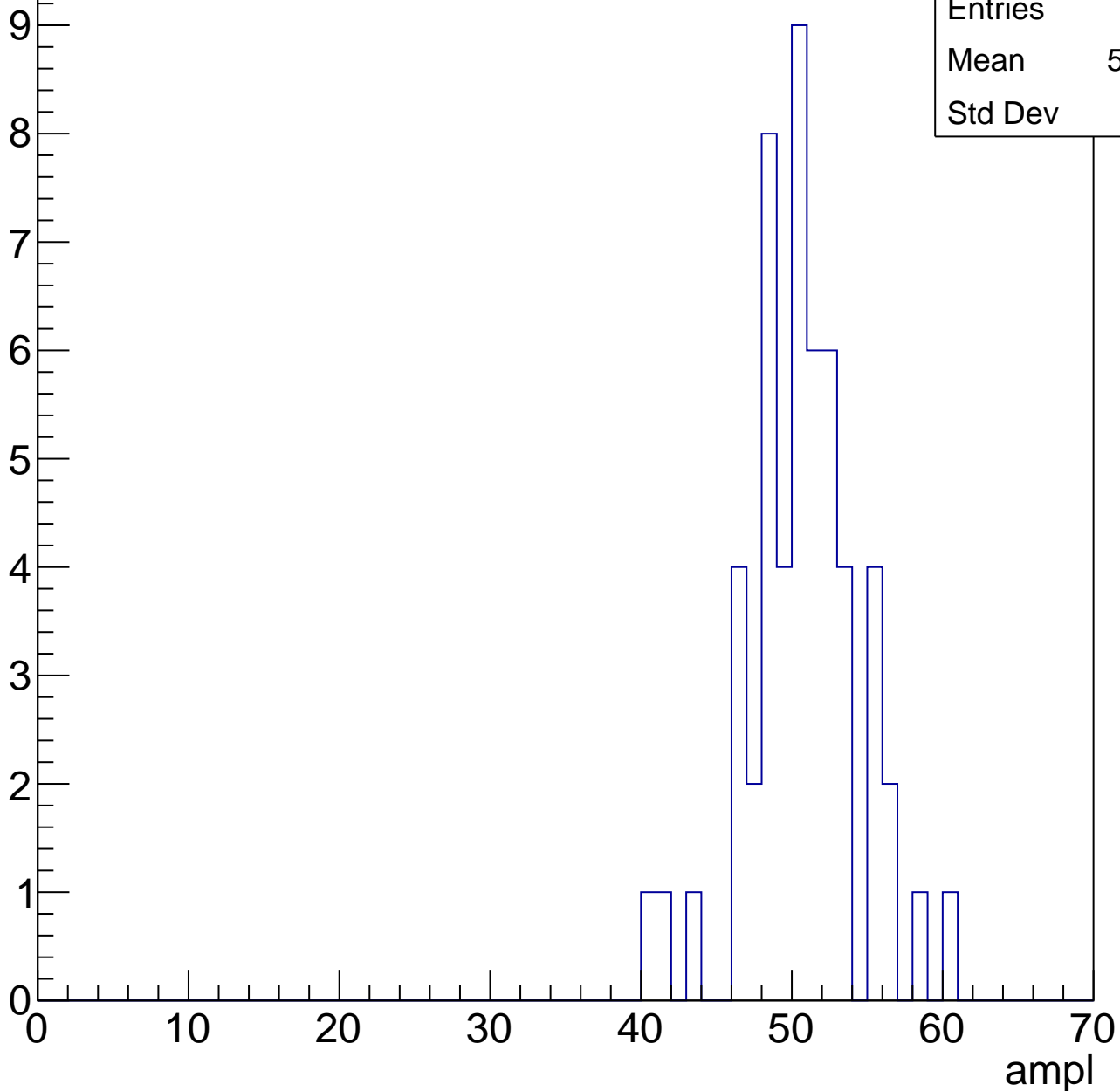


B1L103S, U10-ch85, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	50.22
Std Dev	3.72



B1L103S, U10-ch85, adc4

calib_packv5_042523_0143.root, FC#7, port C2

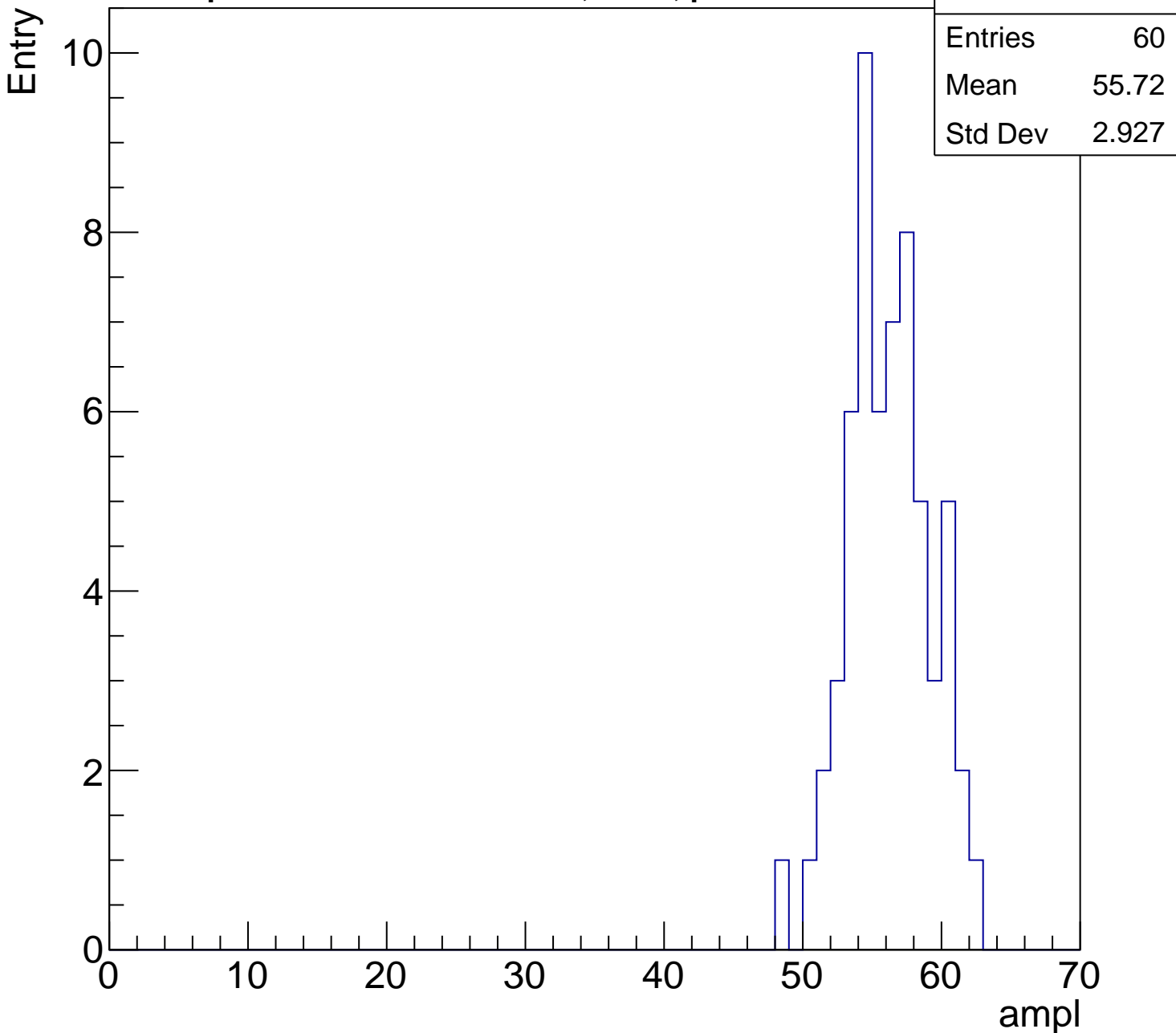
Entries	60
Mean	55.72
Std Dev	2.927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

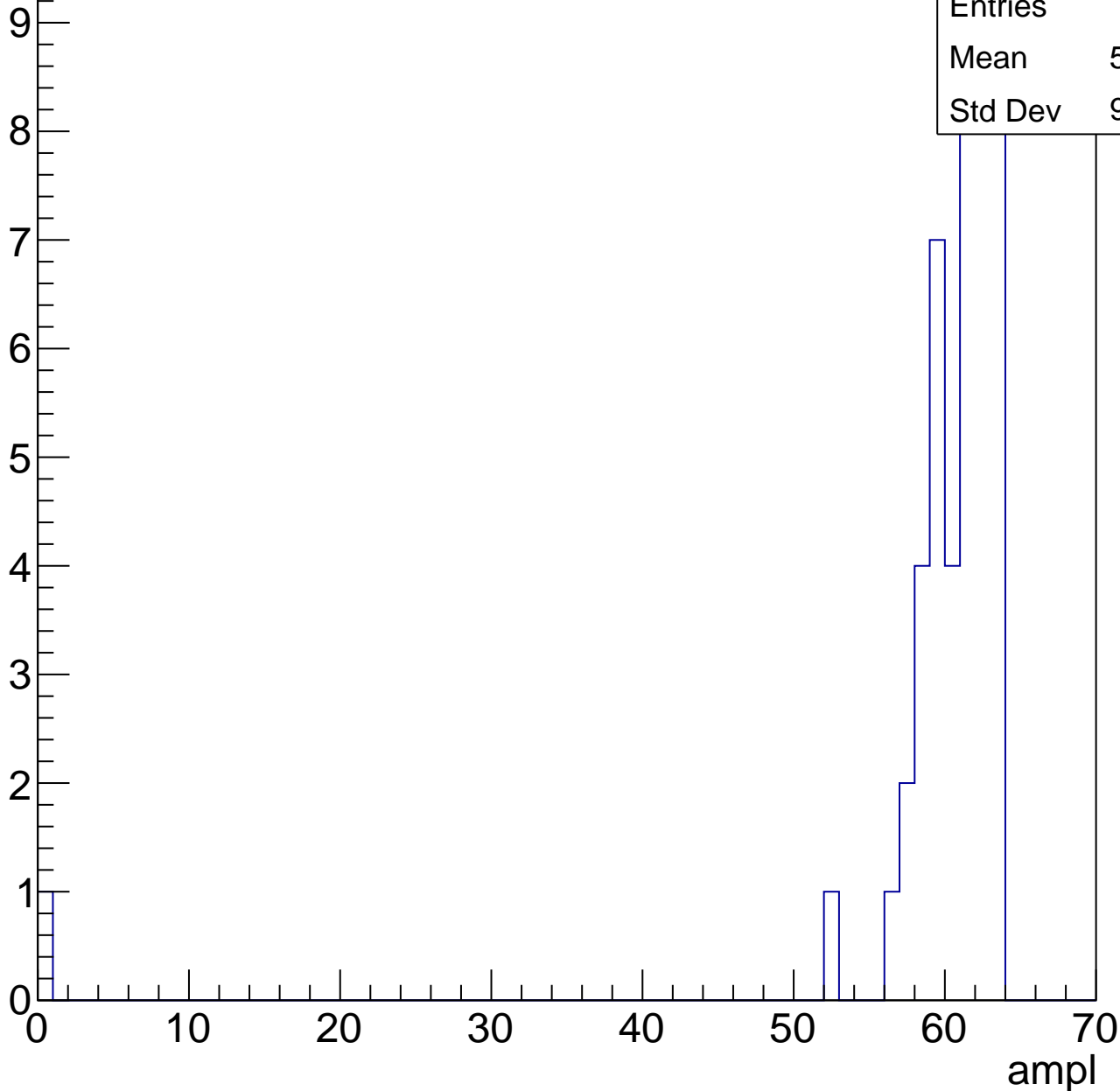


B1L103S, U10-ch85, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	59.07
Std Dev	9.193



B1L103S, U10-ch85, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch85, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch86, adc0

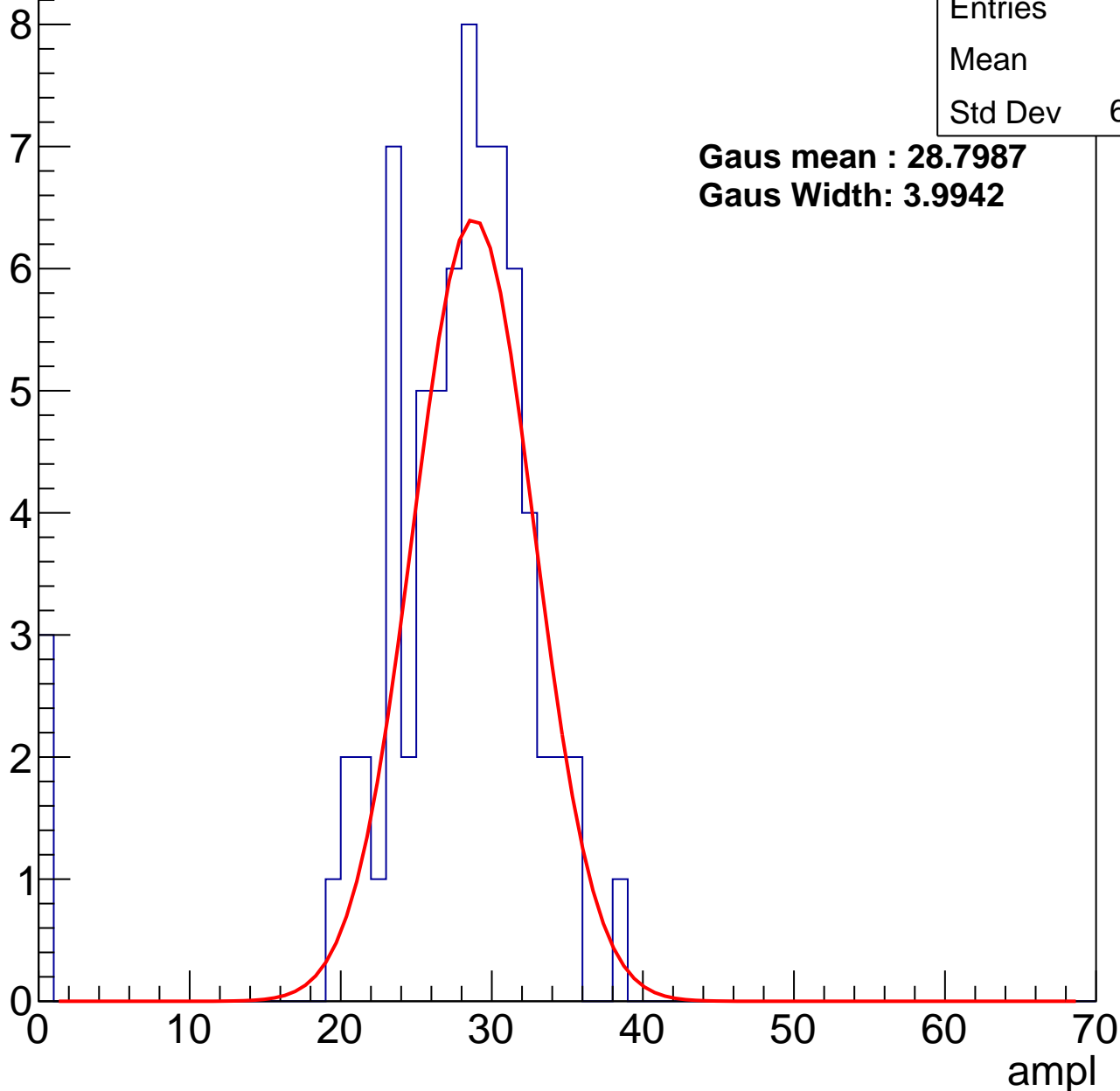
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	26.6
Std Dev	6.727

Gaus mean : 28.7987

Gaus Width: 3.9942



B1L103S, U10-ch86, adc1

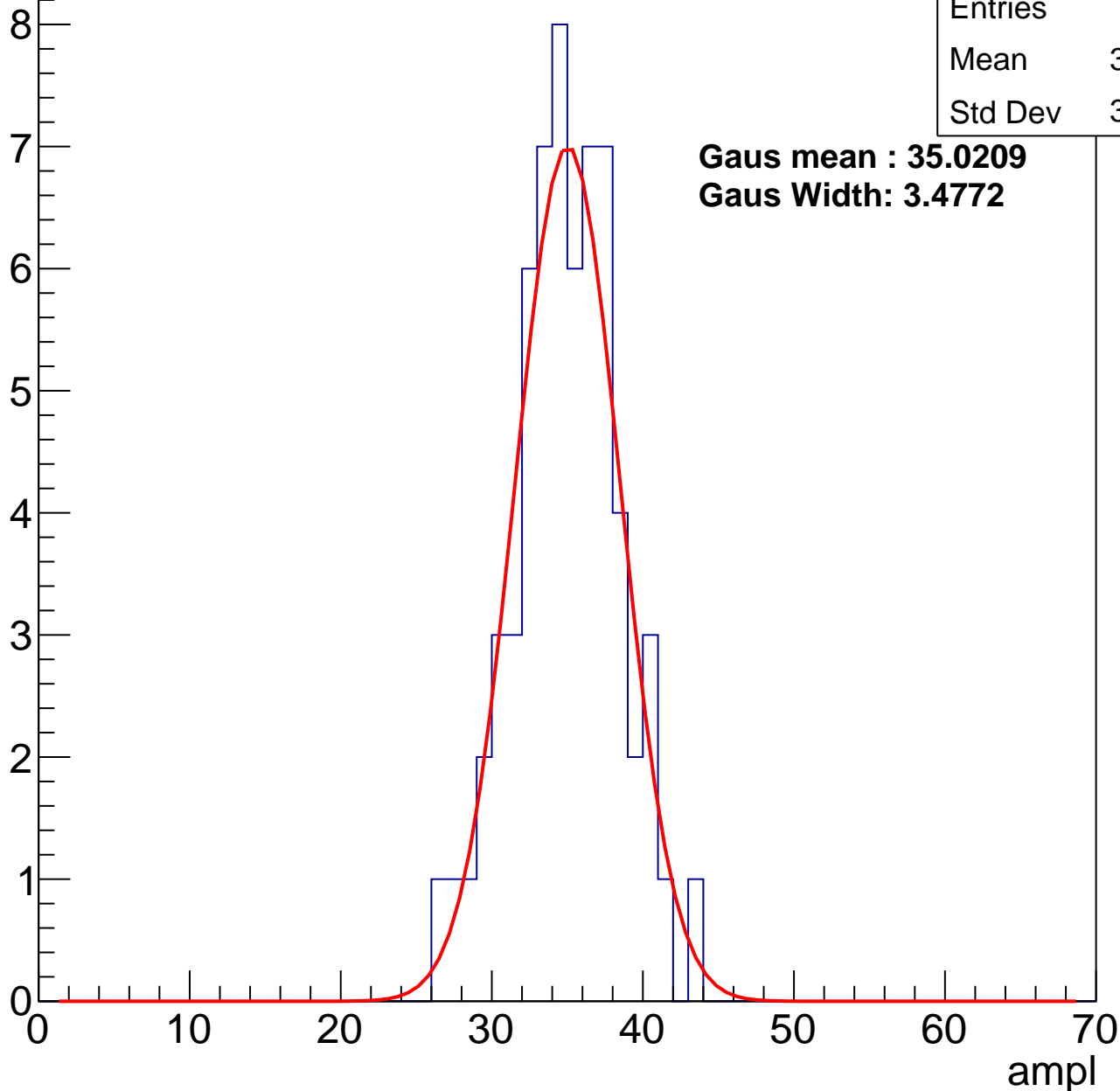
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	34.48
Std Dev	3.427

Gaus mean : 35.0209

Gaus Width: 3.4772



B1L103S, U10-ch86, adc2

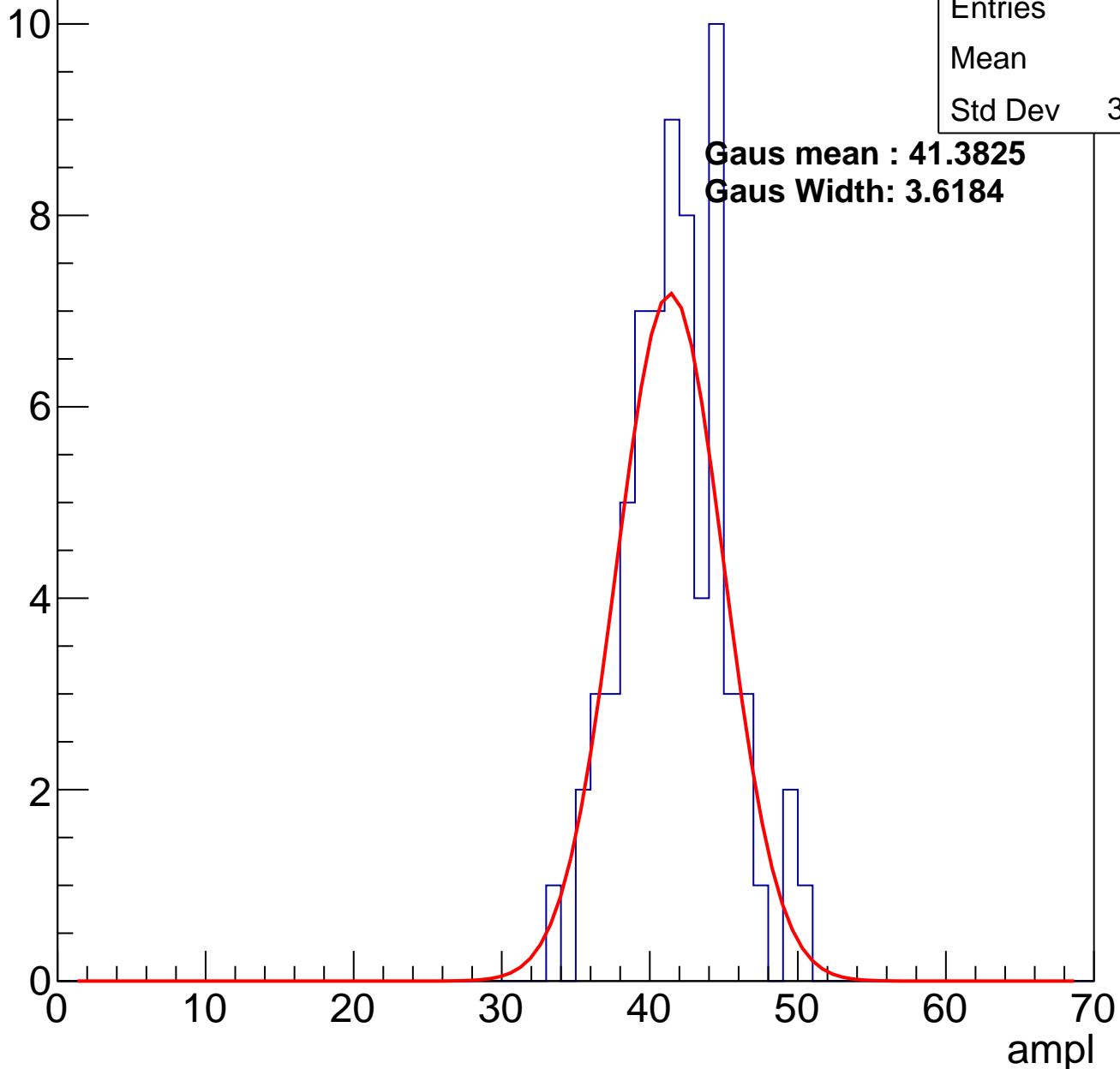
calib_packv5_042523_0143.root, FC#7, port C2

Entries	69
Mean	41.3
Std Dev	3.436

Gaus mean : 41.3825

Gaus Width: 3.6184

Entry

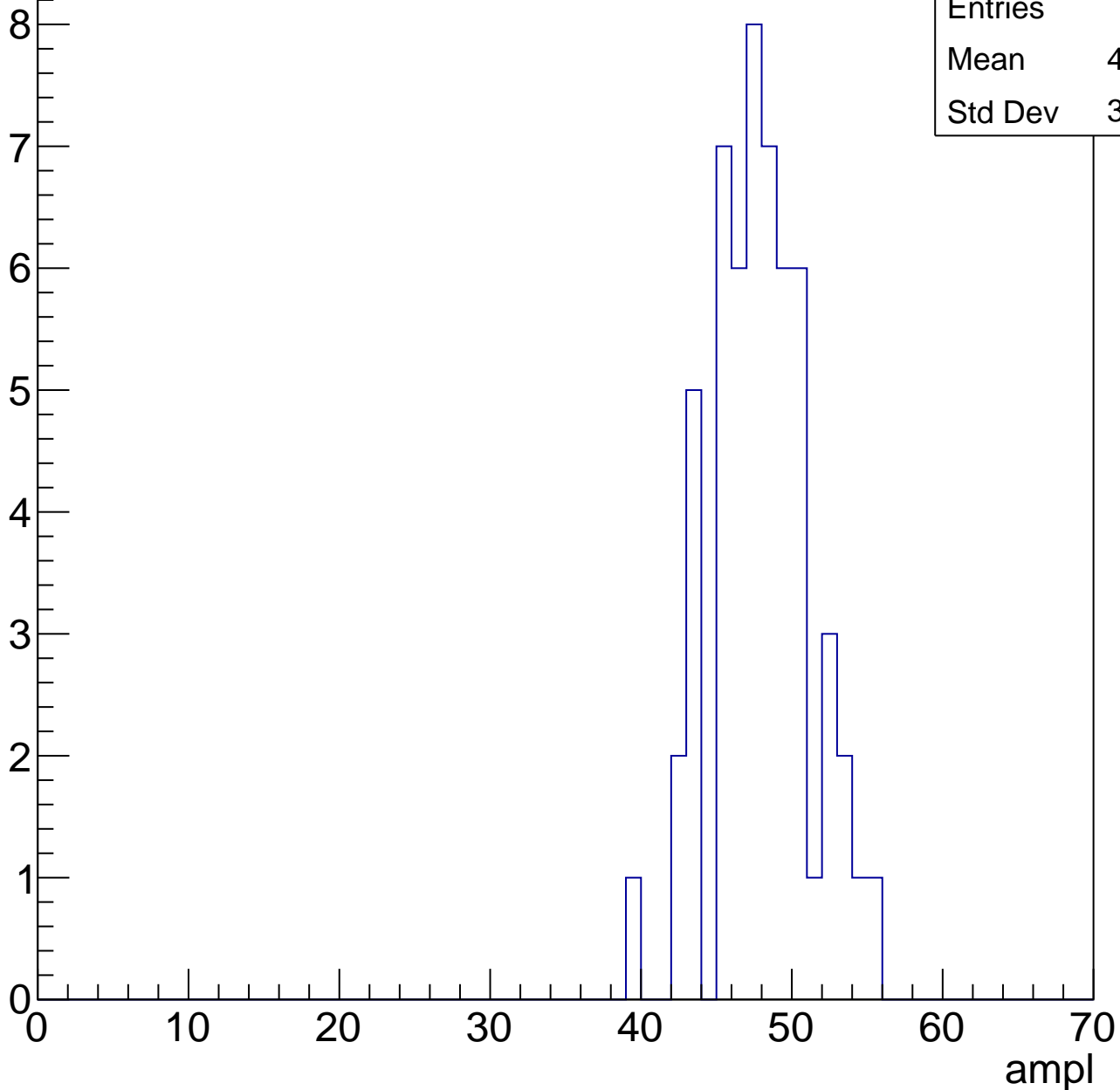


B1L103S, U10-ch86, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	47.45
Std Dev	3.212

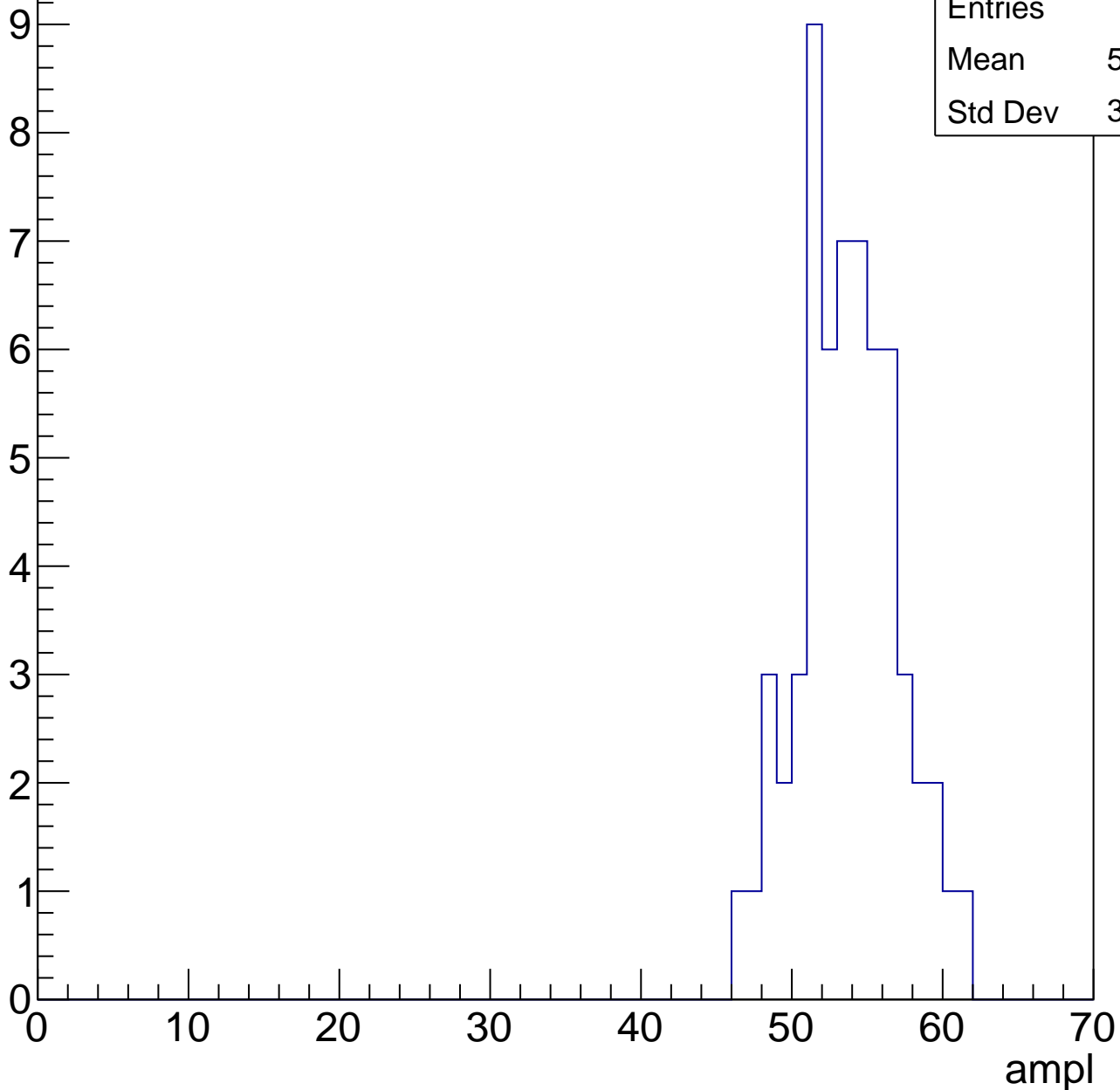


B1L103S, U10-ch86, adc4

calib_packv5_042523_0143.root, FC#7, port C2

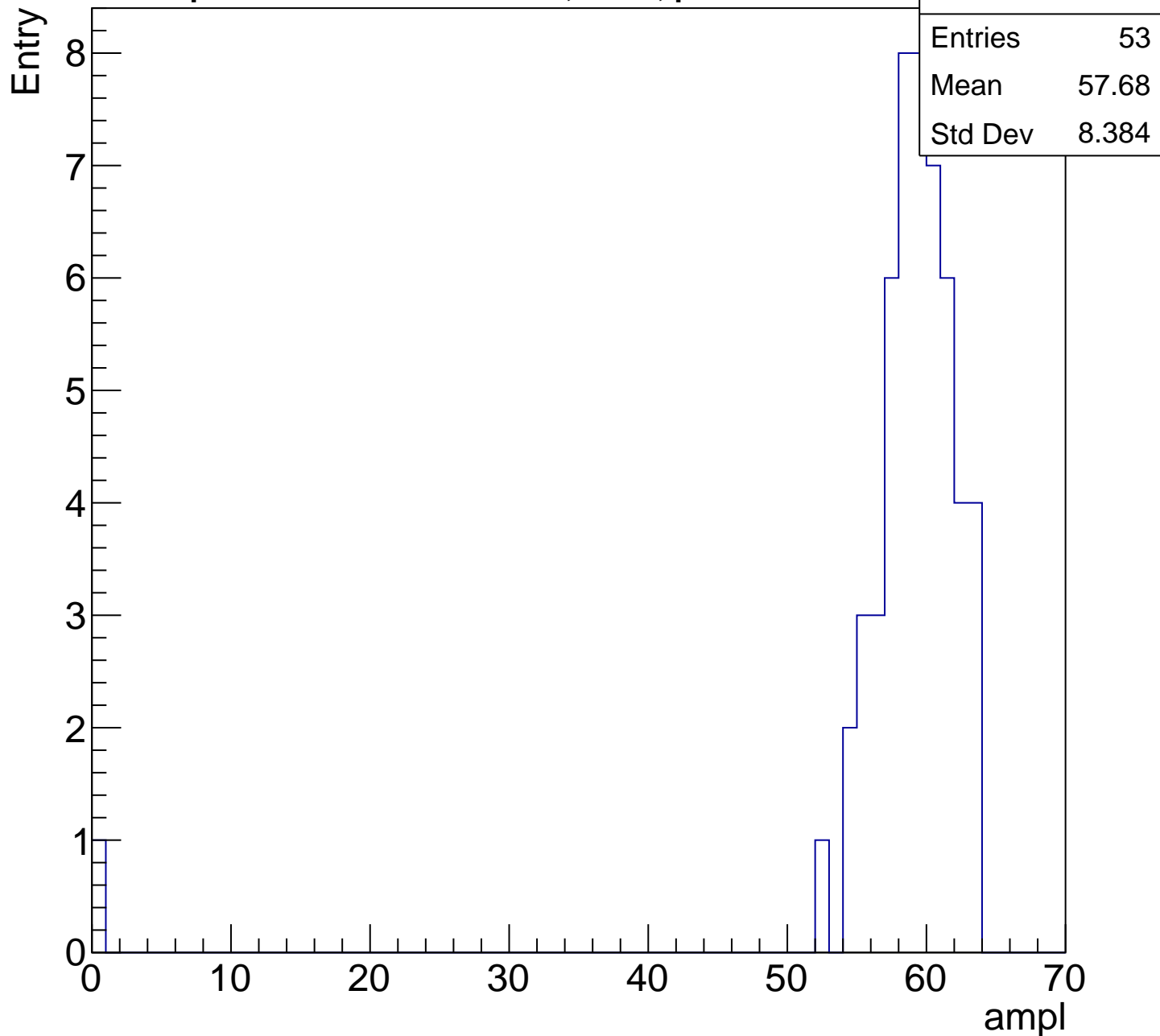
Entry

Entries	60
Mean	53.28
Std Dev	3.225



B1L103S, U10-ch86, adc5

calib_packv5_042523_0143.root, FC#7, port C2

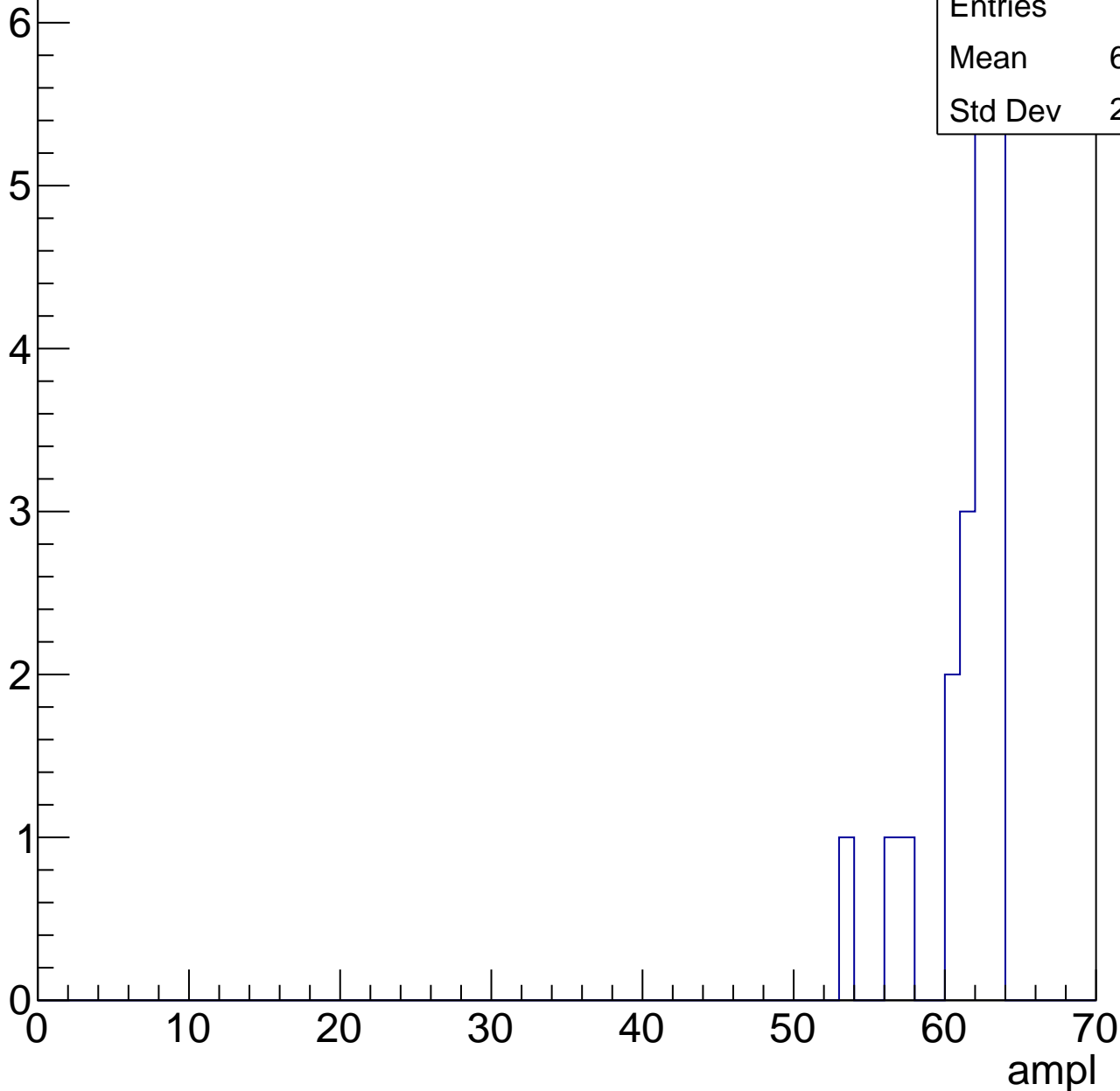


B1L103S, U10-ch86, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	20
Mean	60.95
Std Dev	2.617



B1L103S, U10-ch86, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch87, adc0

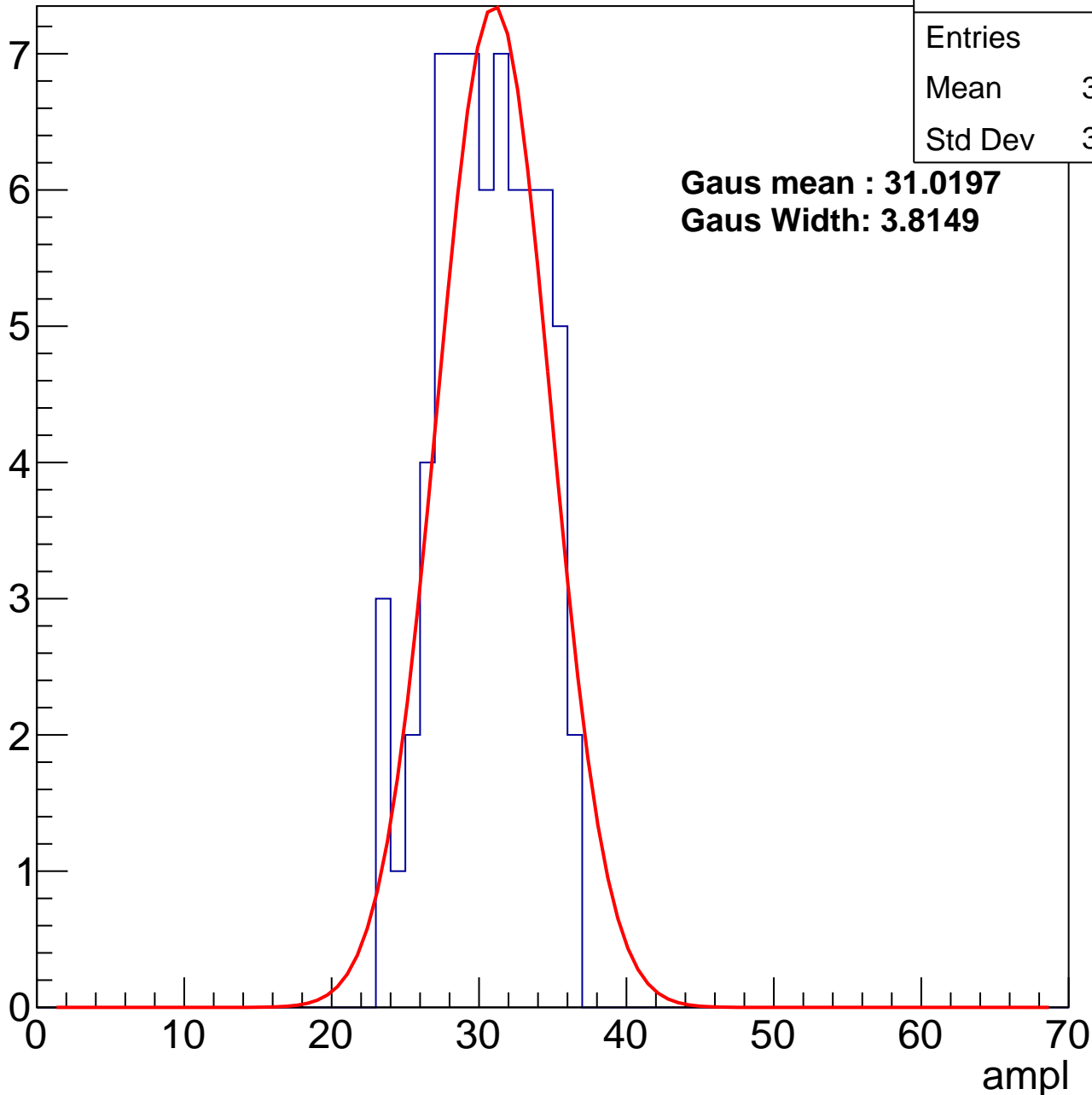
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	30.04
Std Dev	3.347

Gaus mean : 31.0197

Gaus Width: 3.8149



B1L103S, U10-ch87, adc1

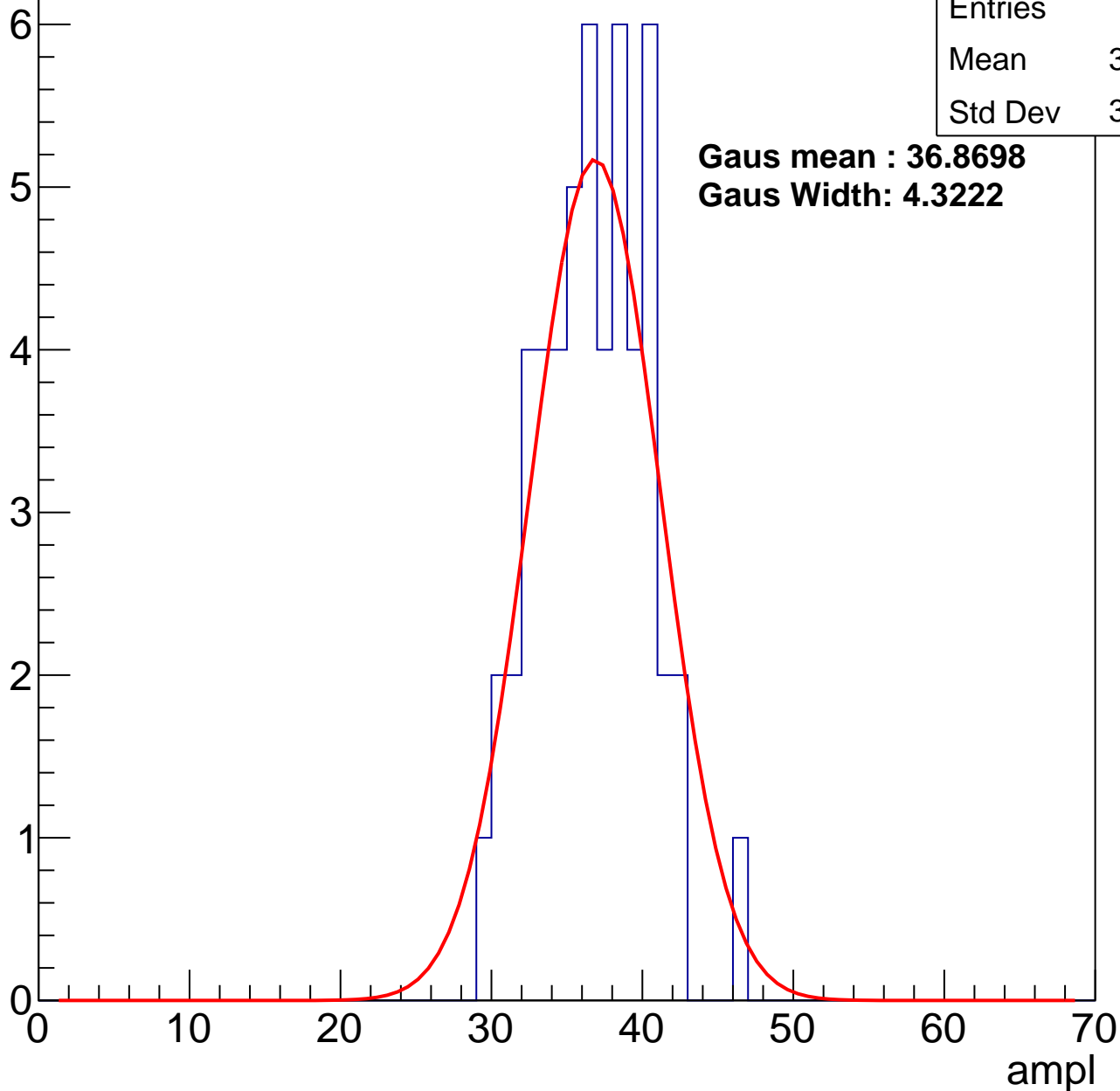
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	36.26
Std Dev	3.556

Gaus mean : 36.8698

Gaus Width: 4.3222



B1L103S, U10-ch87, adc2

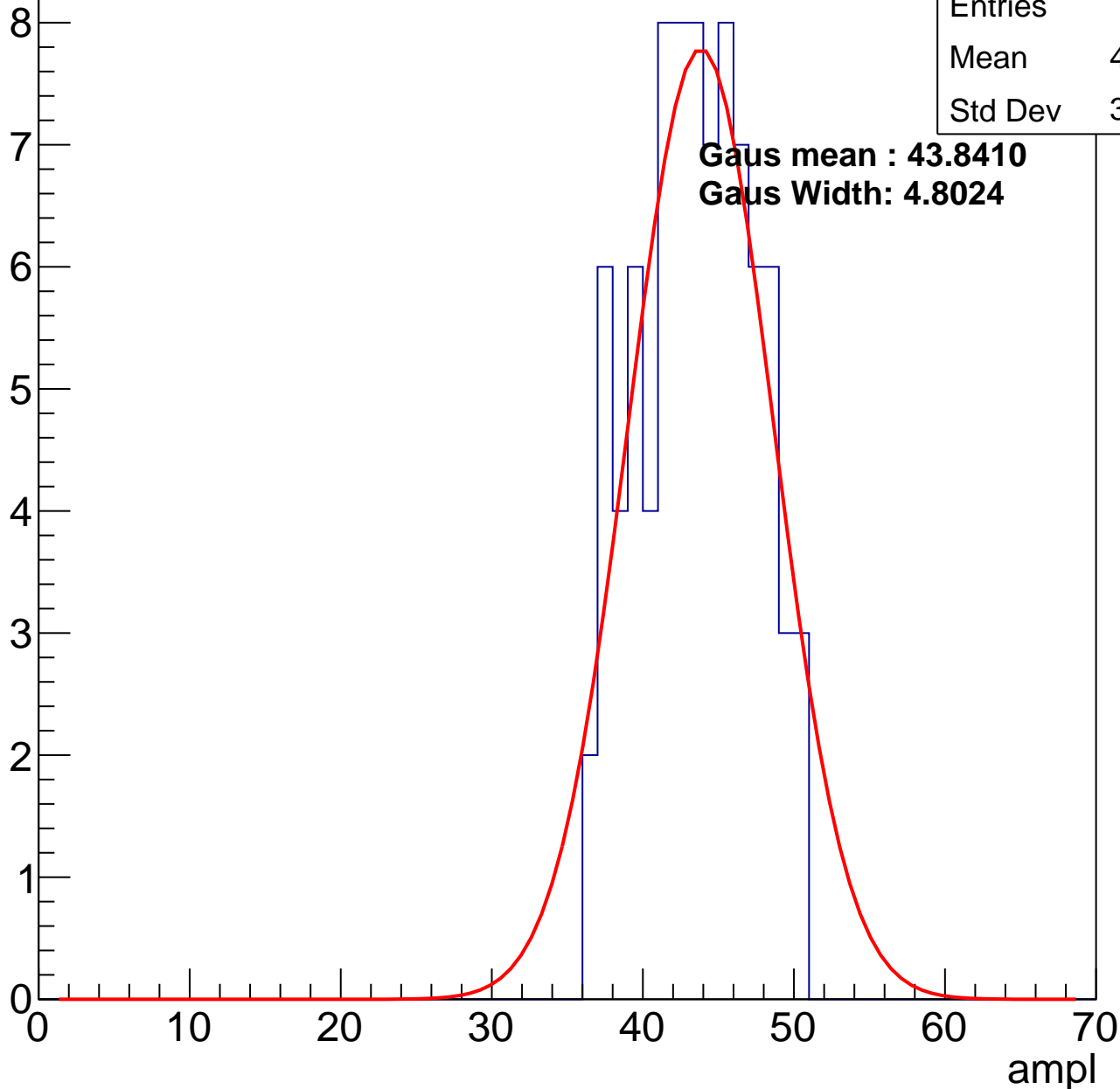
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	43.08
Std Dev	3.717

Gaus mean : 43.8410

Gaus Width: 4.8024

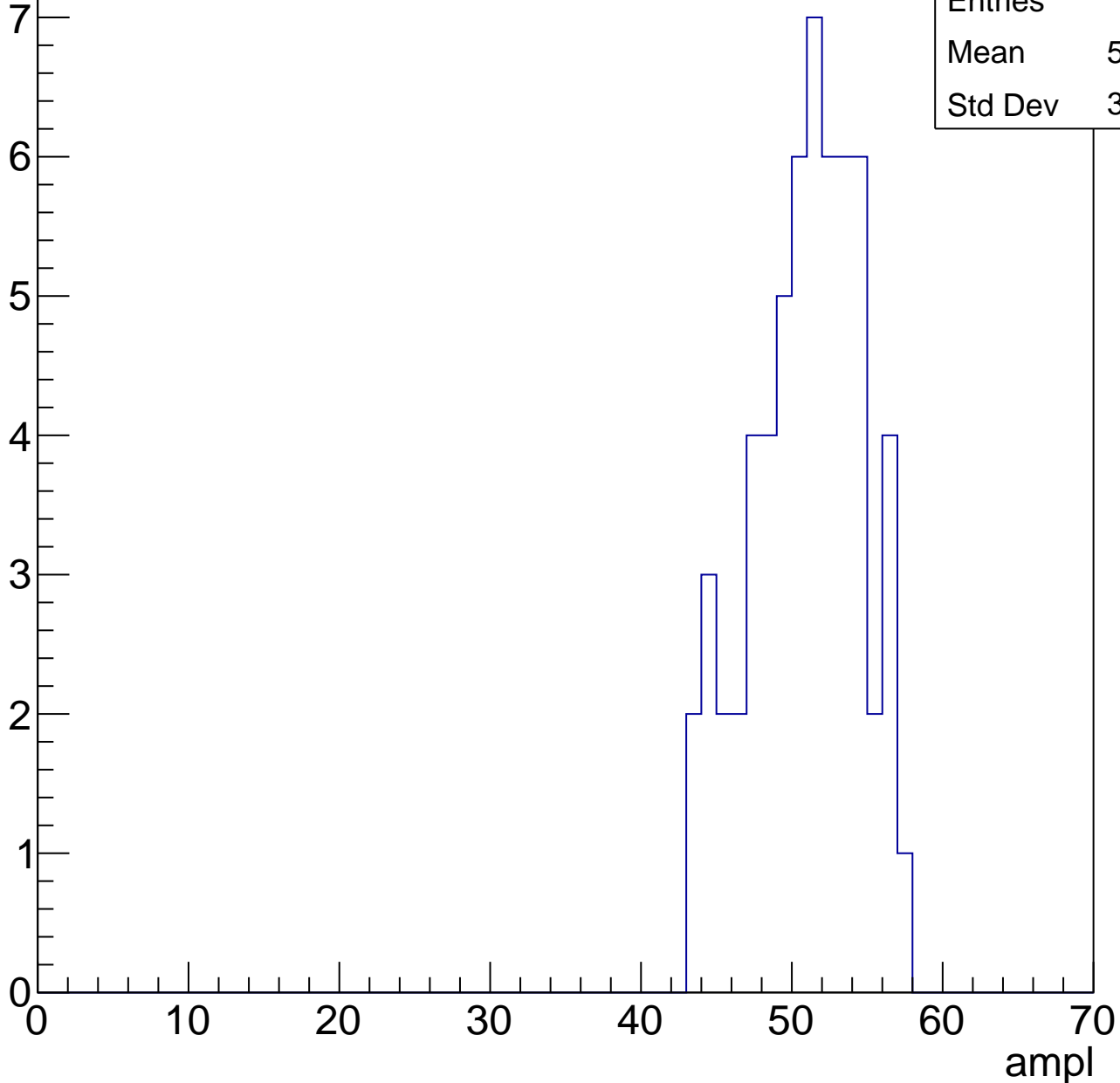


B1L103S, U10-ch87, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

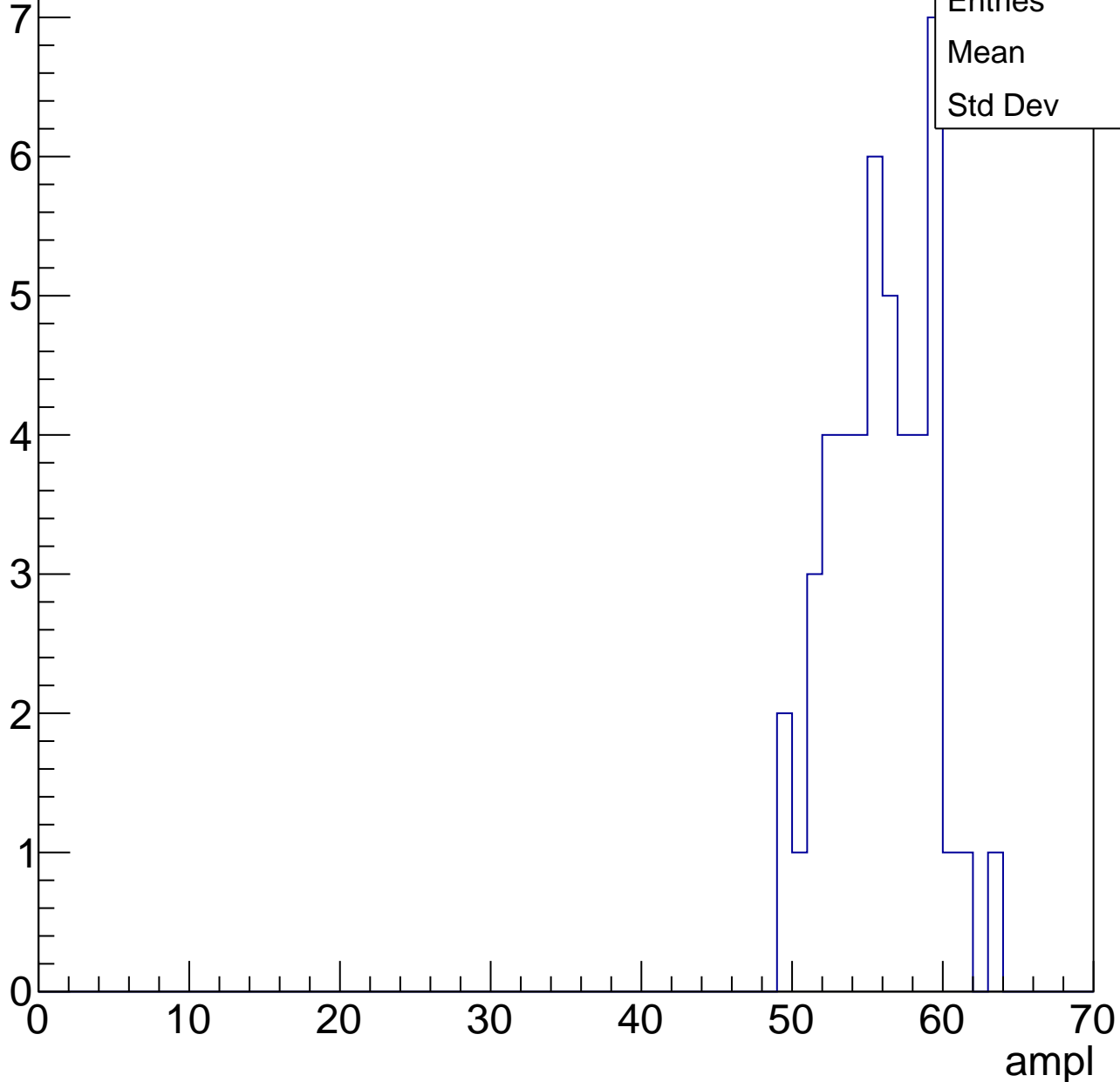
Entries	60
Mean	50.45
Std Dev	3.552



B1L103S, U10-ch87, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



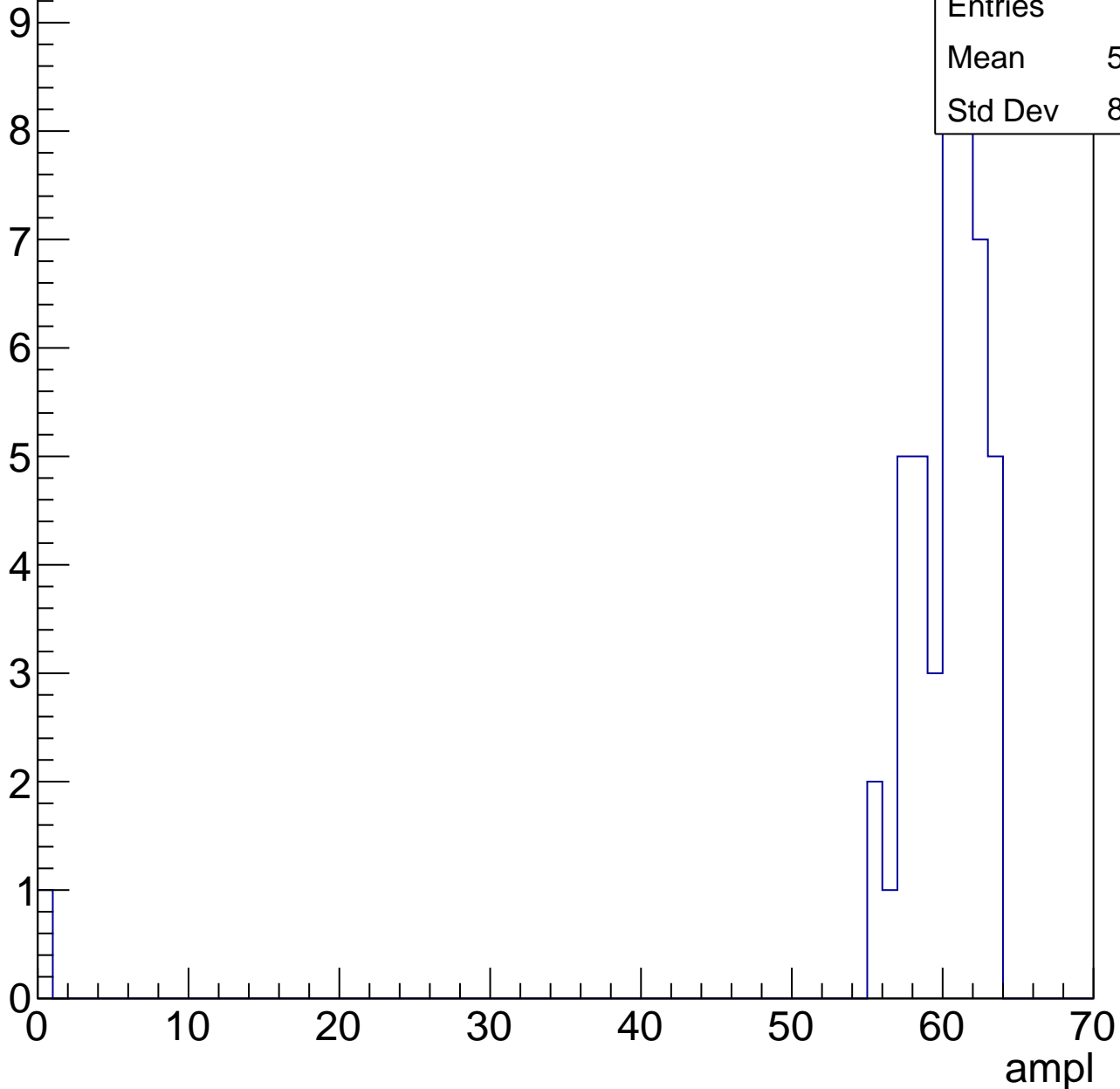
Entries	47
Mean	55.4
Std Dev	3.22

B1L103S, U10-ch87, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

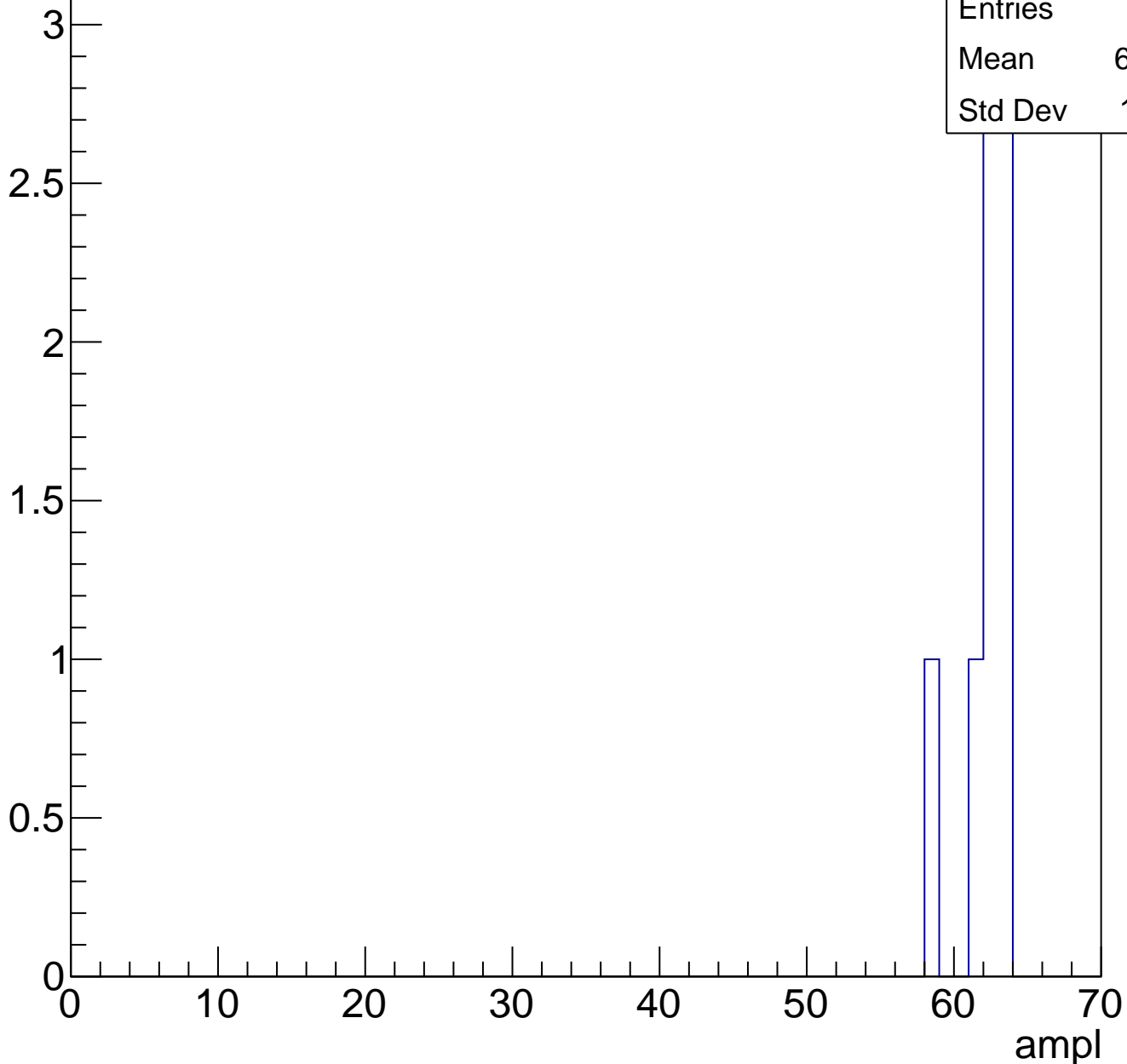
Entries	46
Mean	58.59
Std Dev	8.997



B1L103S, U10-ch87, adc6

calib_packv5_042523_0143.root, FC#7, port C2

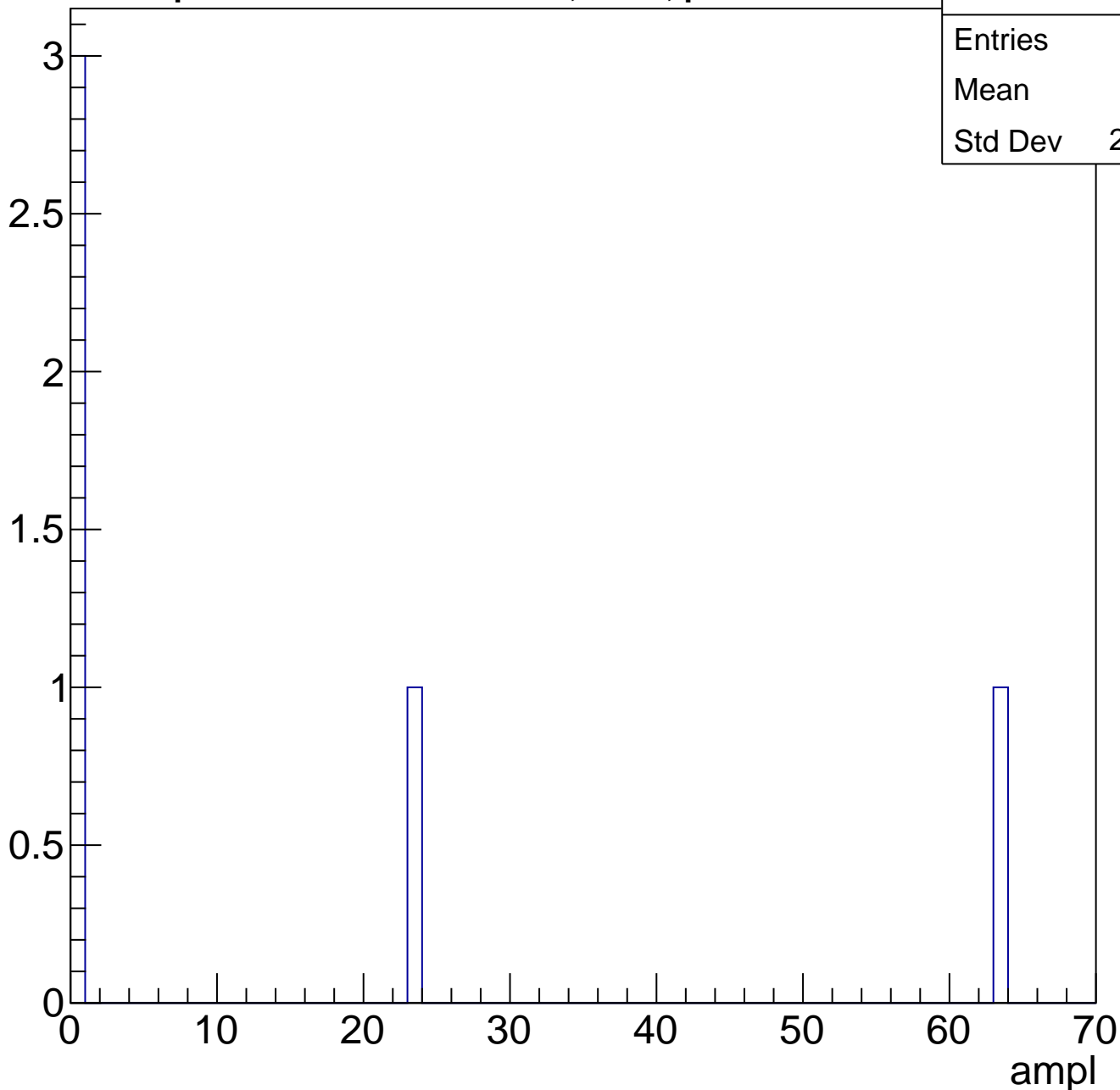
Entry



B1L103S, U10-ch87, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	17.2
Std Dev	24.57

B1L103S, U10-ch88, adc0

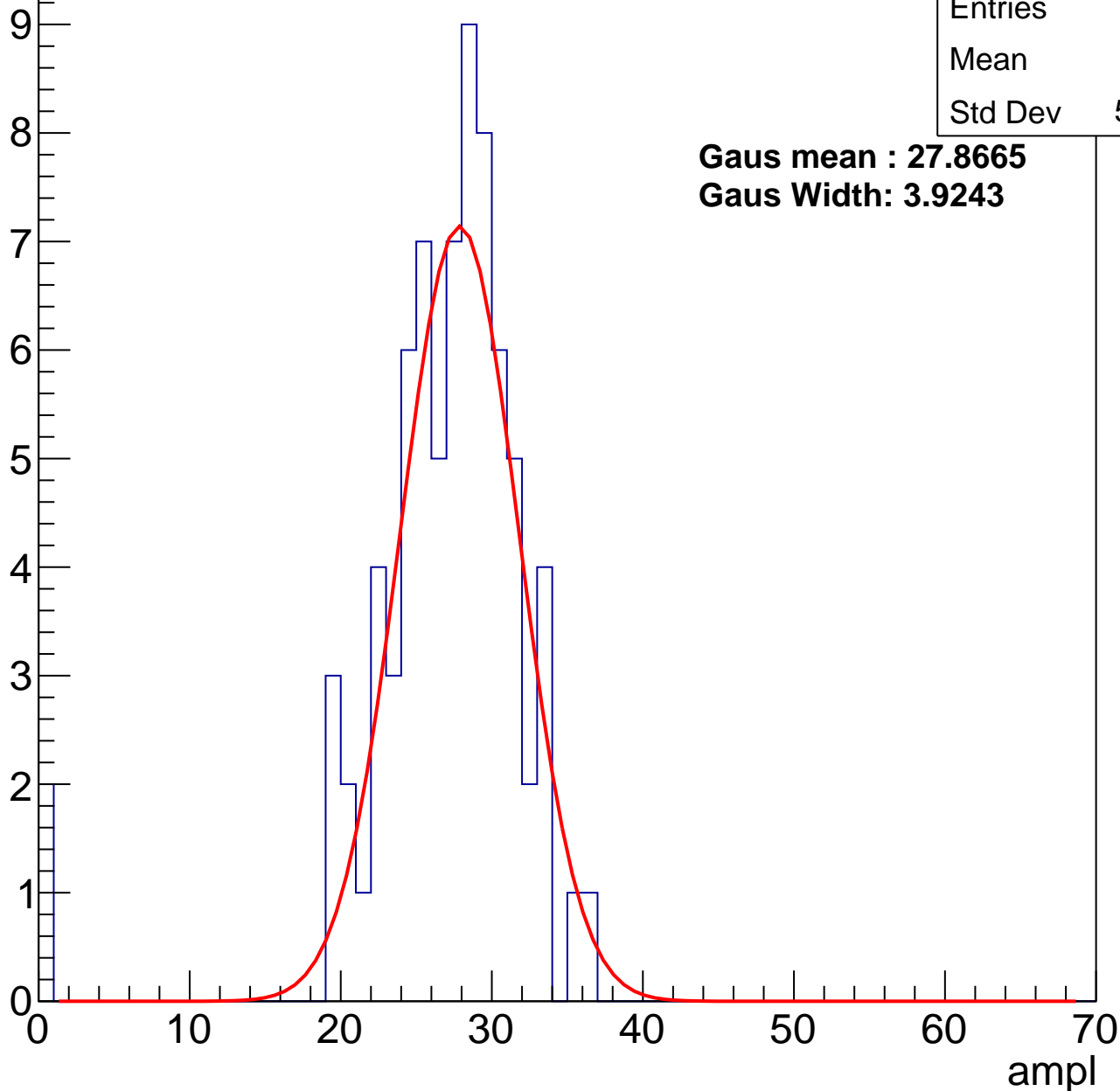
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	26.3
Std Dev	5.731

Gaus mean : 27.8665

Gaus Width: 3.9243



B1L103S, U10-ch88, adc1

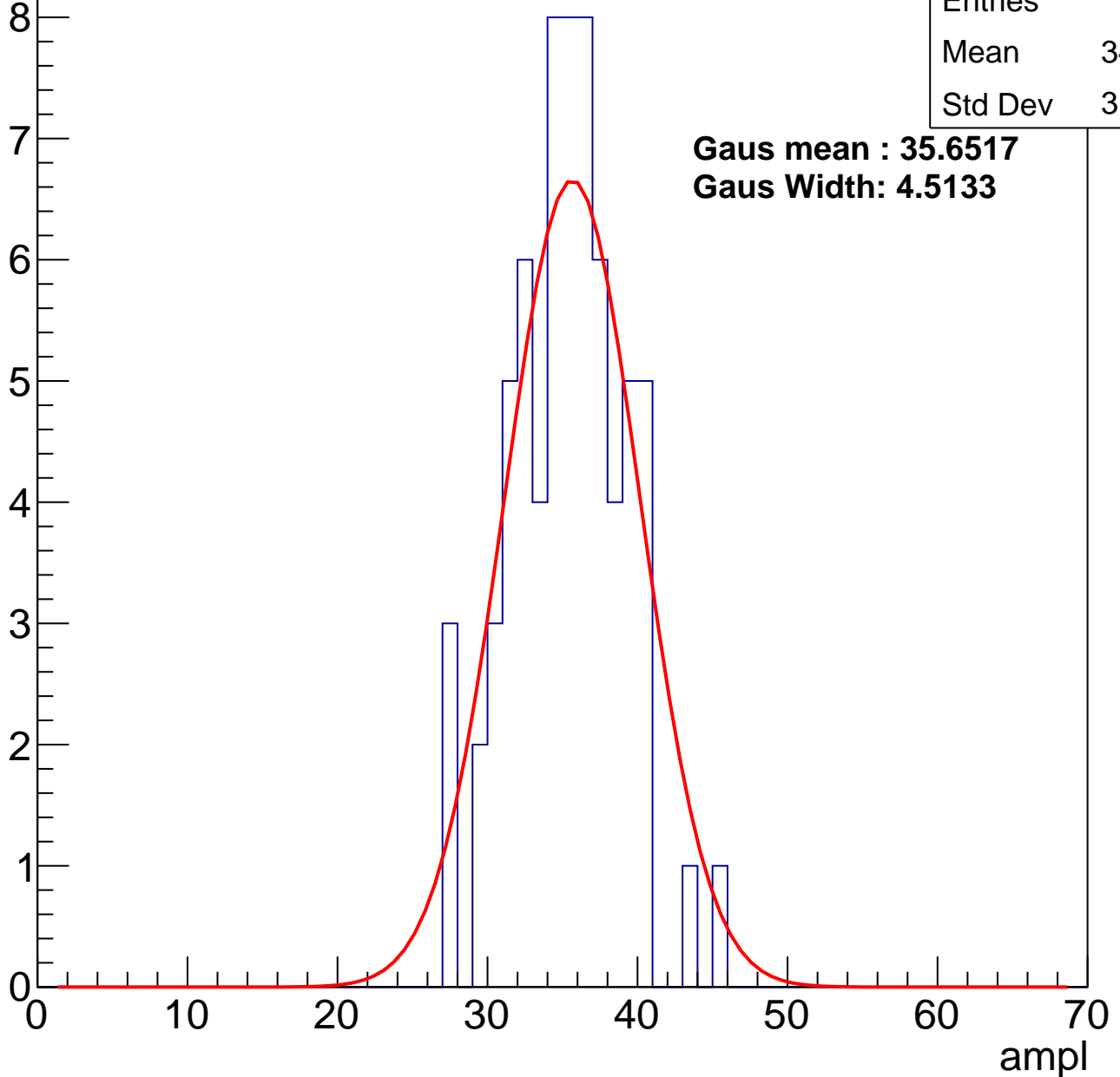
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.86
Std Dev	3.676

Gaus mean : 35.6517

Gaus Width: 4.5133



B1L103S, U10-ch88, adc2

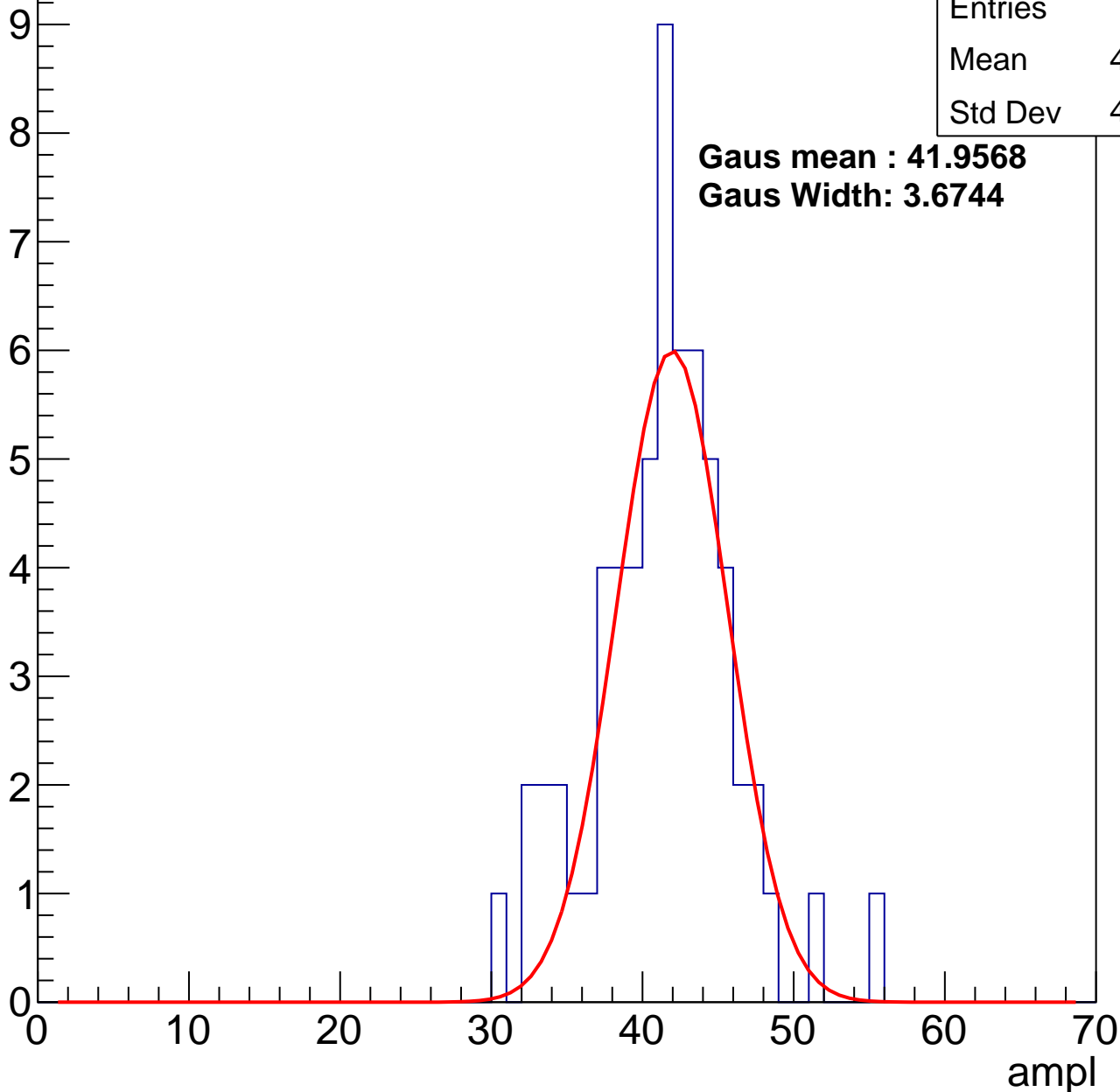
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	40.86
Std Dev	4.535

Gaus mean : 41.9568

Gaus Width: 3.6744

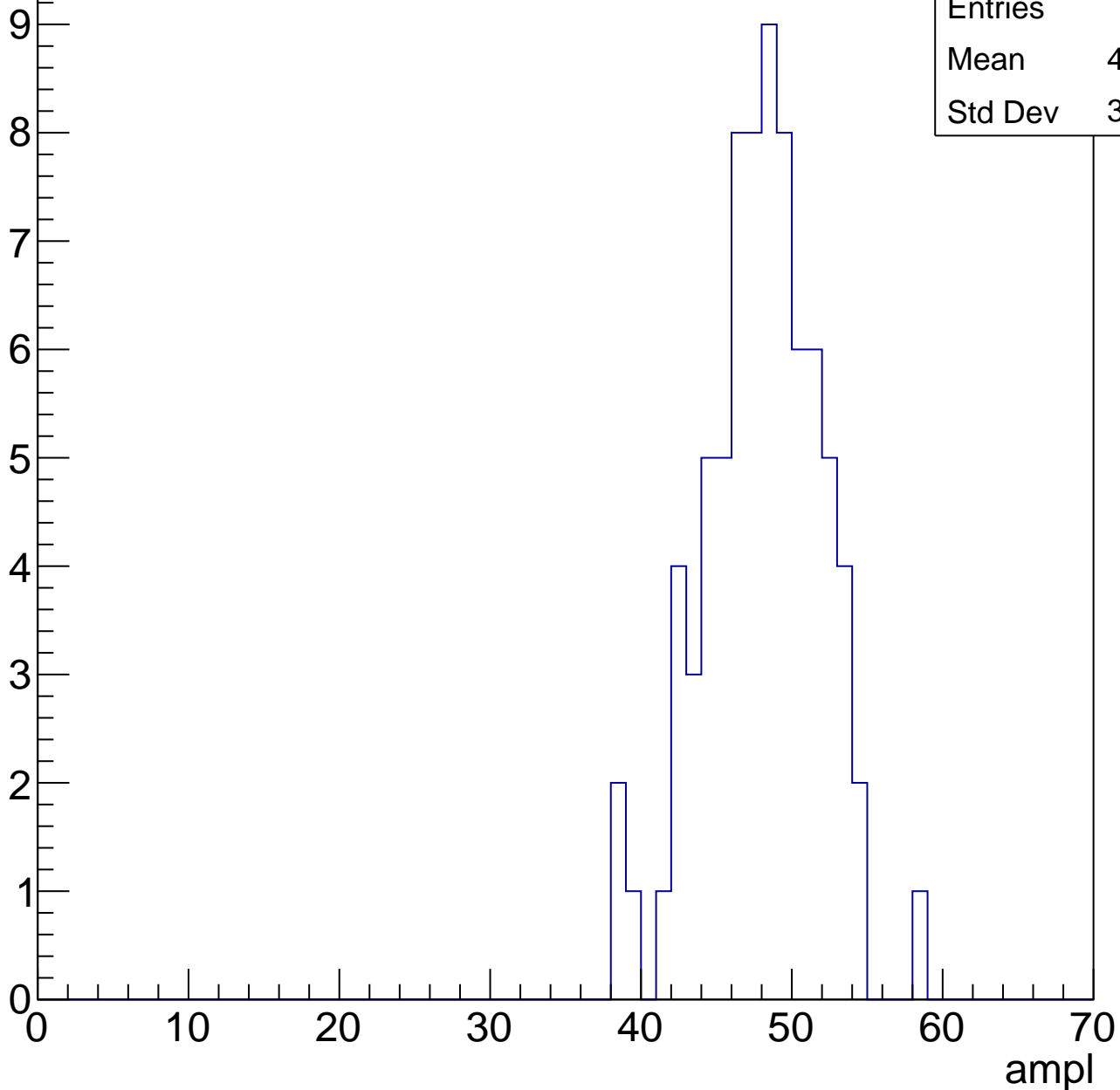


B1L103S, U10-ch88, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	47.56
Std Dev	3.818

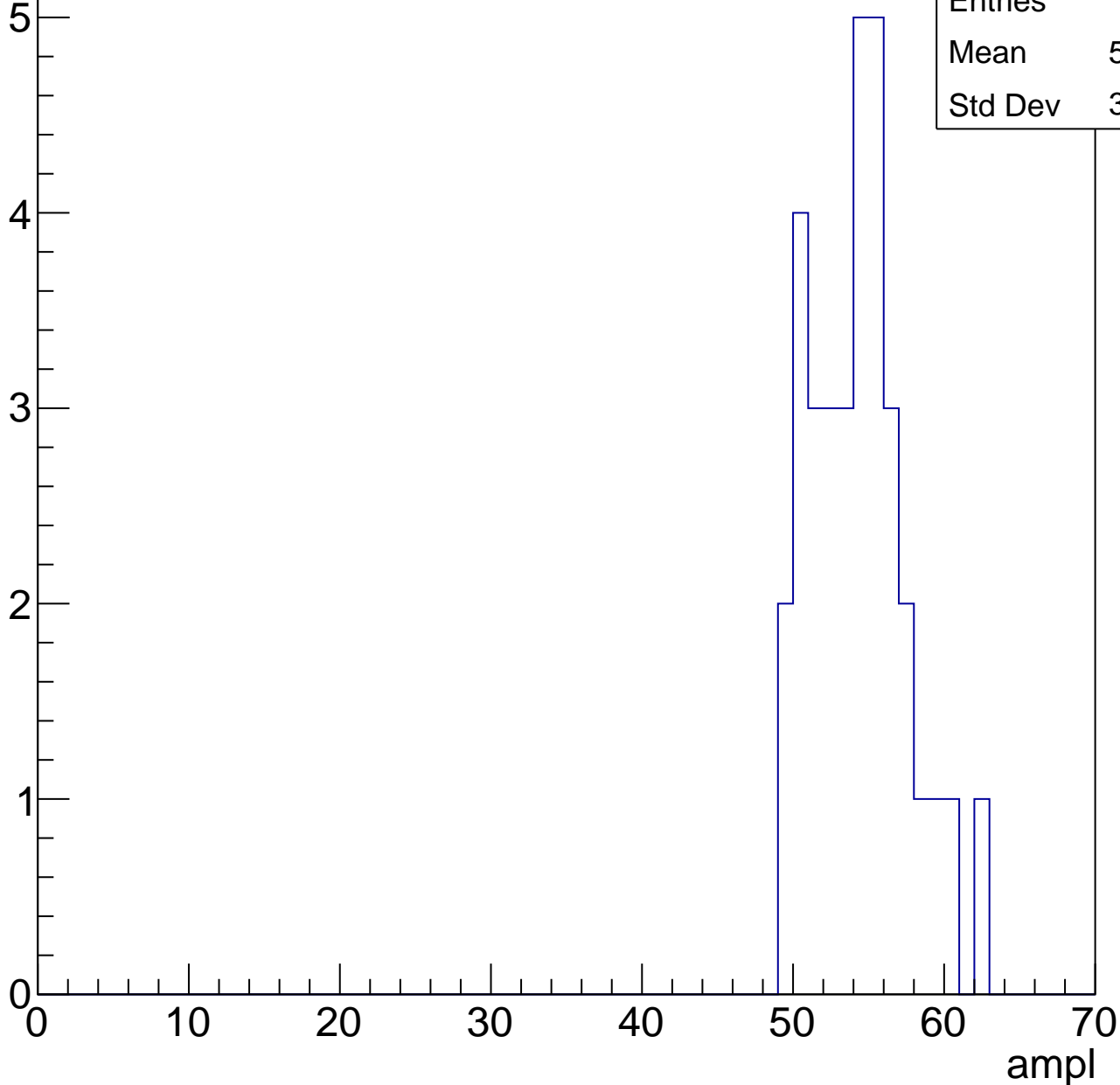


B1L103S, U10-ch88, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	53.88
Std Dev	3.123



B1L103S, U10-ch88, adc5

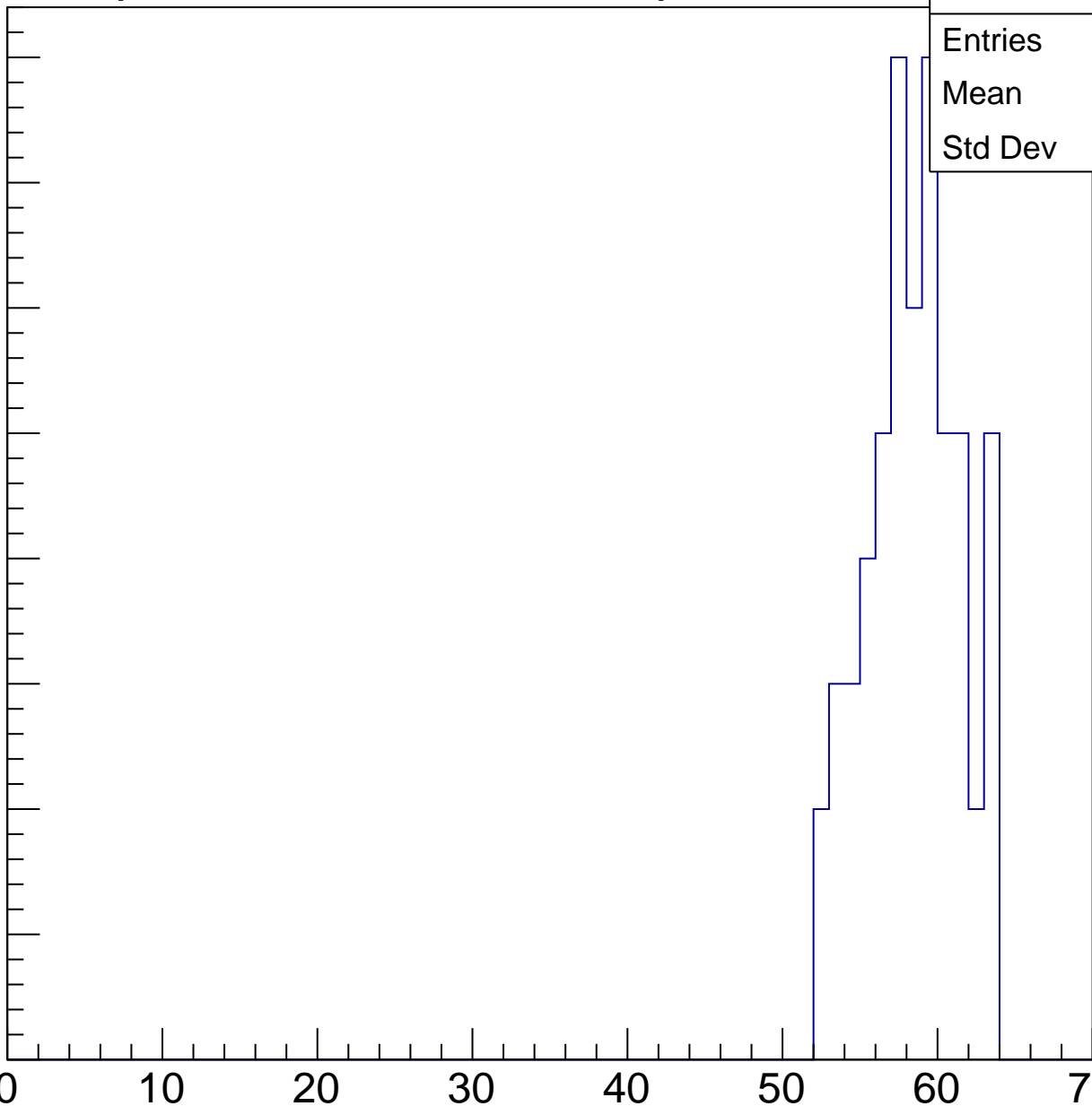
calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	56
Mean	57.95
Std Dev	2.955

ampl

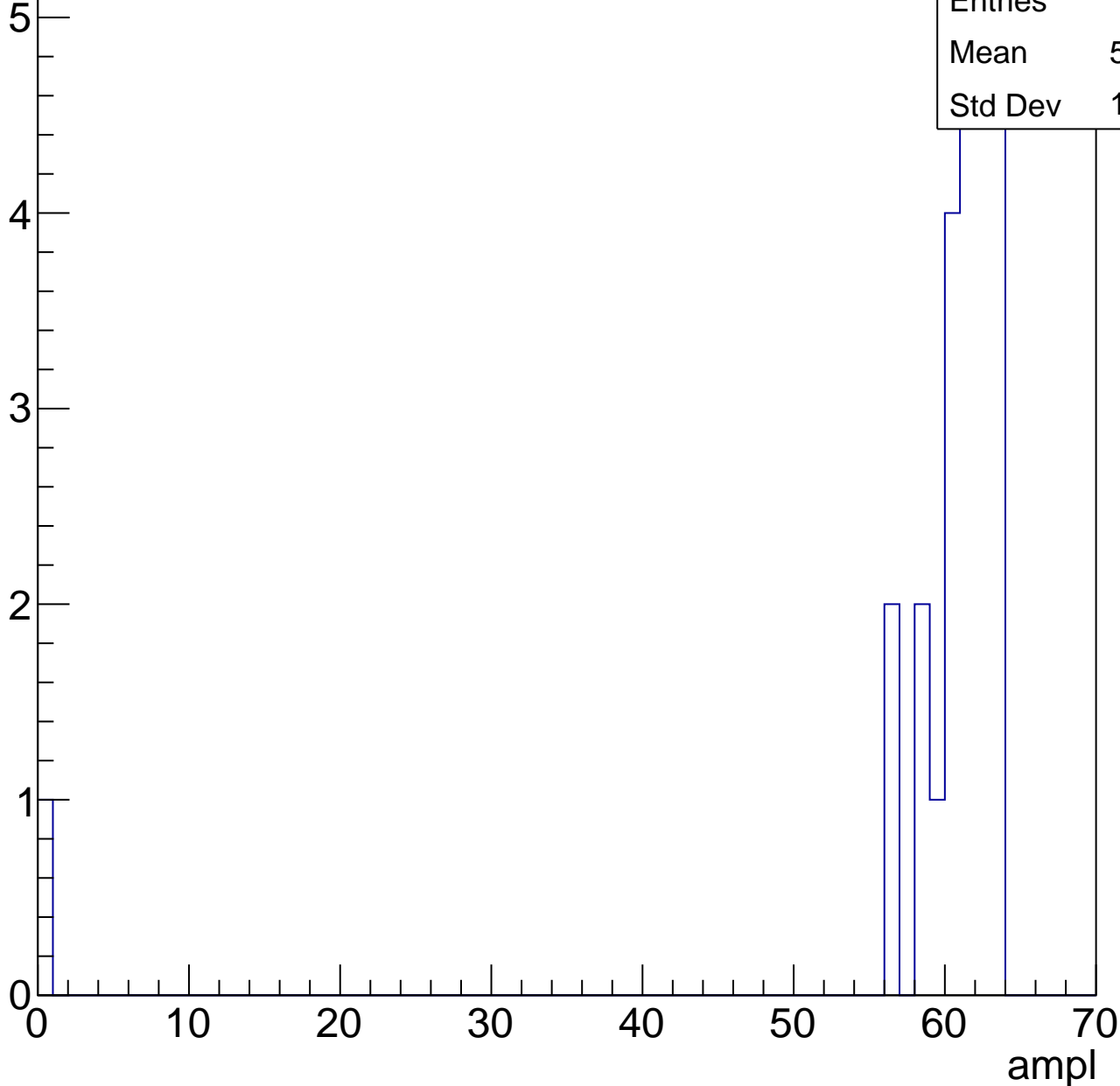


B1L103S, U10-ch88, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

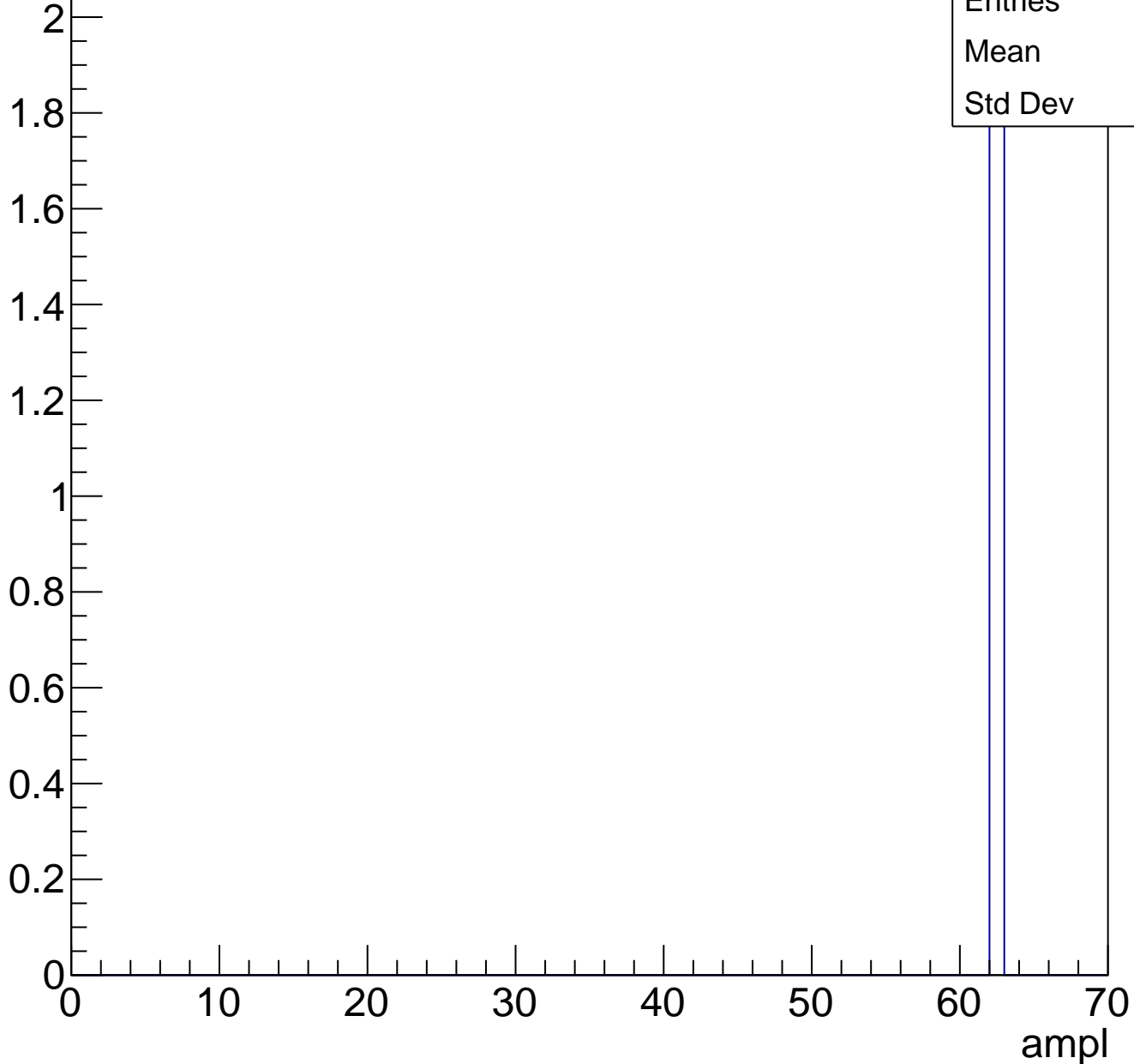
Entries	25
Mean	58.28
Std Dev	12.06



B1L103S, U10-ch88, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch89, adc0

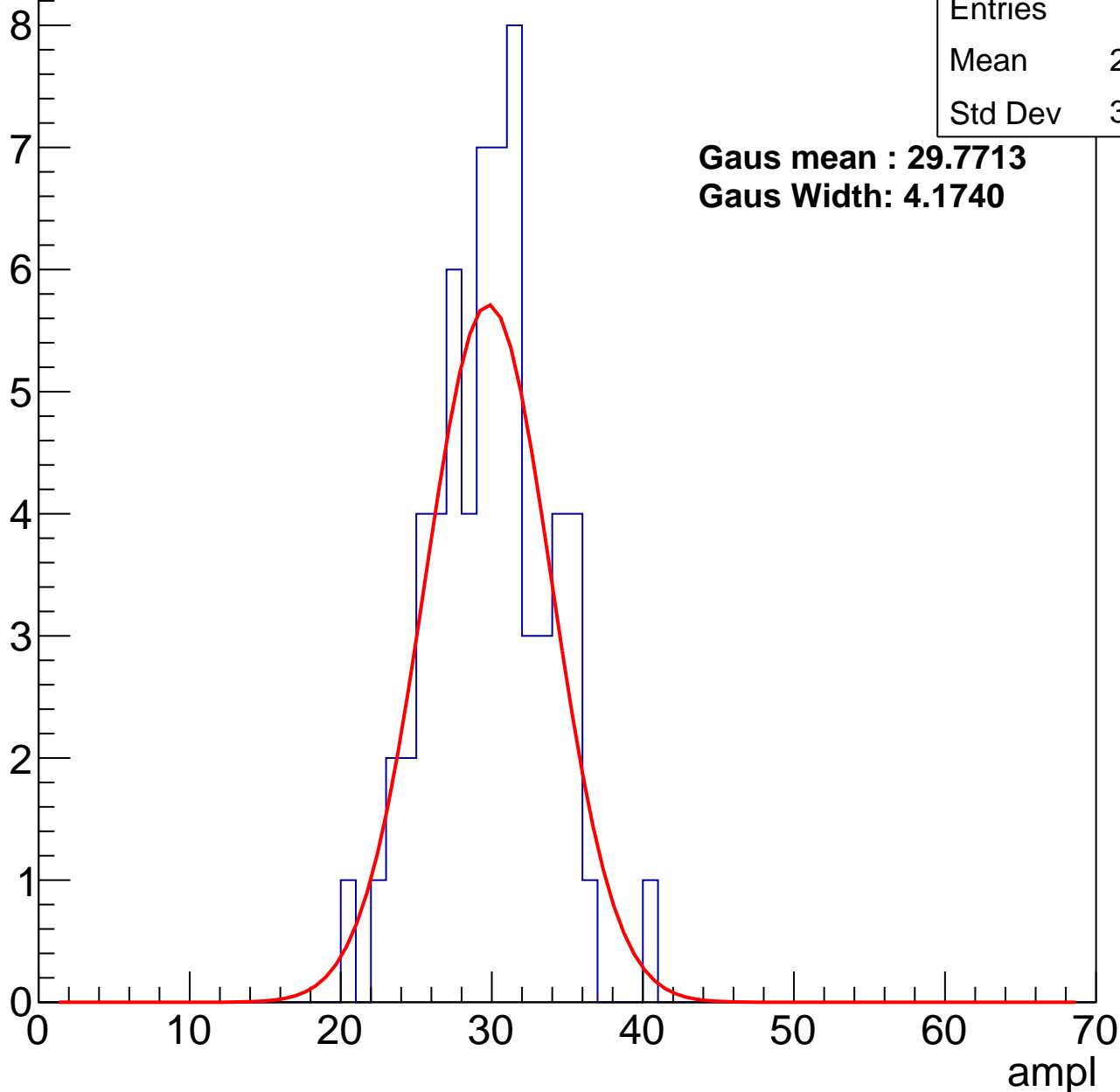
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	29.39
Std Dev	3.807

Gaus mean : 29.7713

Gaus Width: 4.1740



B1L103S, U10-ch89, adc1

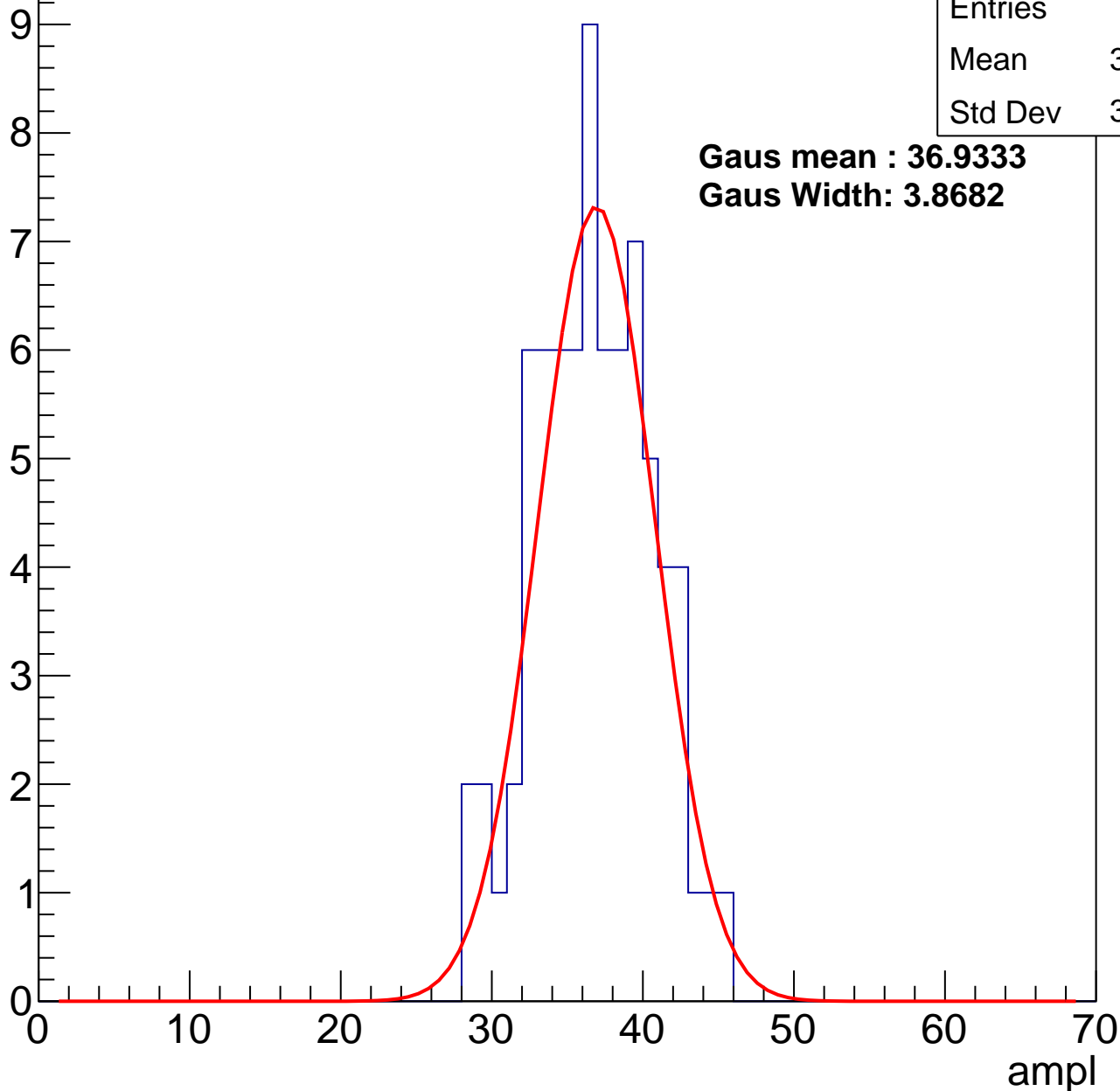
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	36.28
Std Dev	3.825

Gaus mean : 36.9333

Gaus Width: 3.8682



B1L103S, U10-ch89, adc2

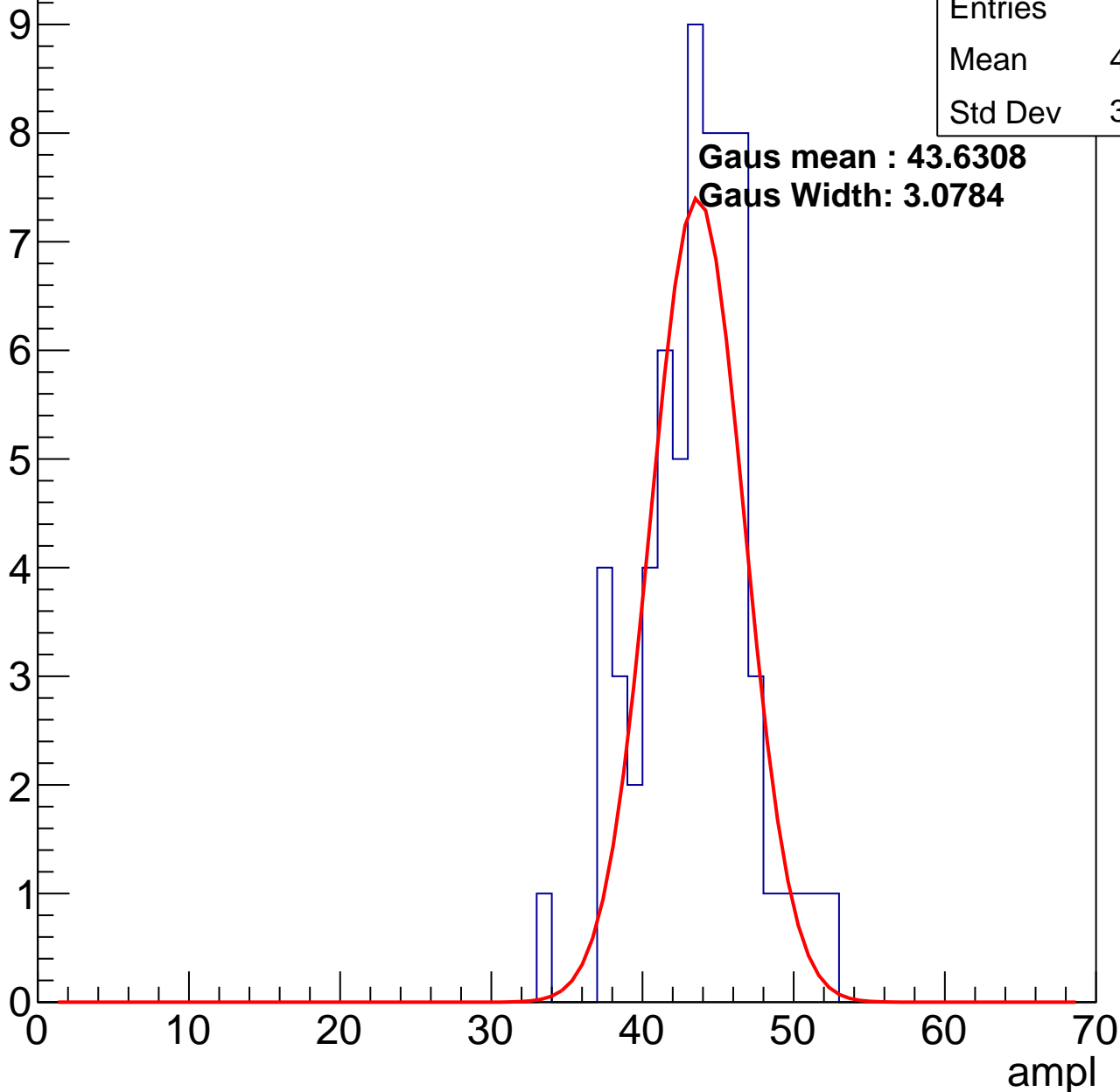
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	43.14
Std Dev	3.546

Gaus mean : 43.6308

Gaus Width: 3.0784

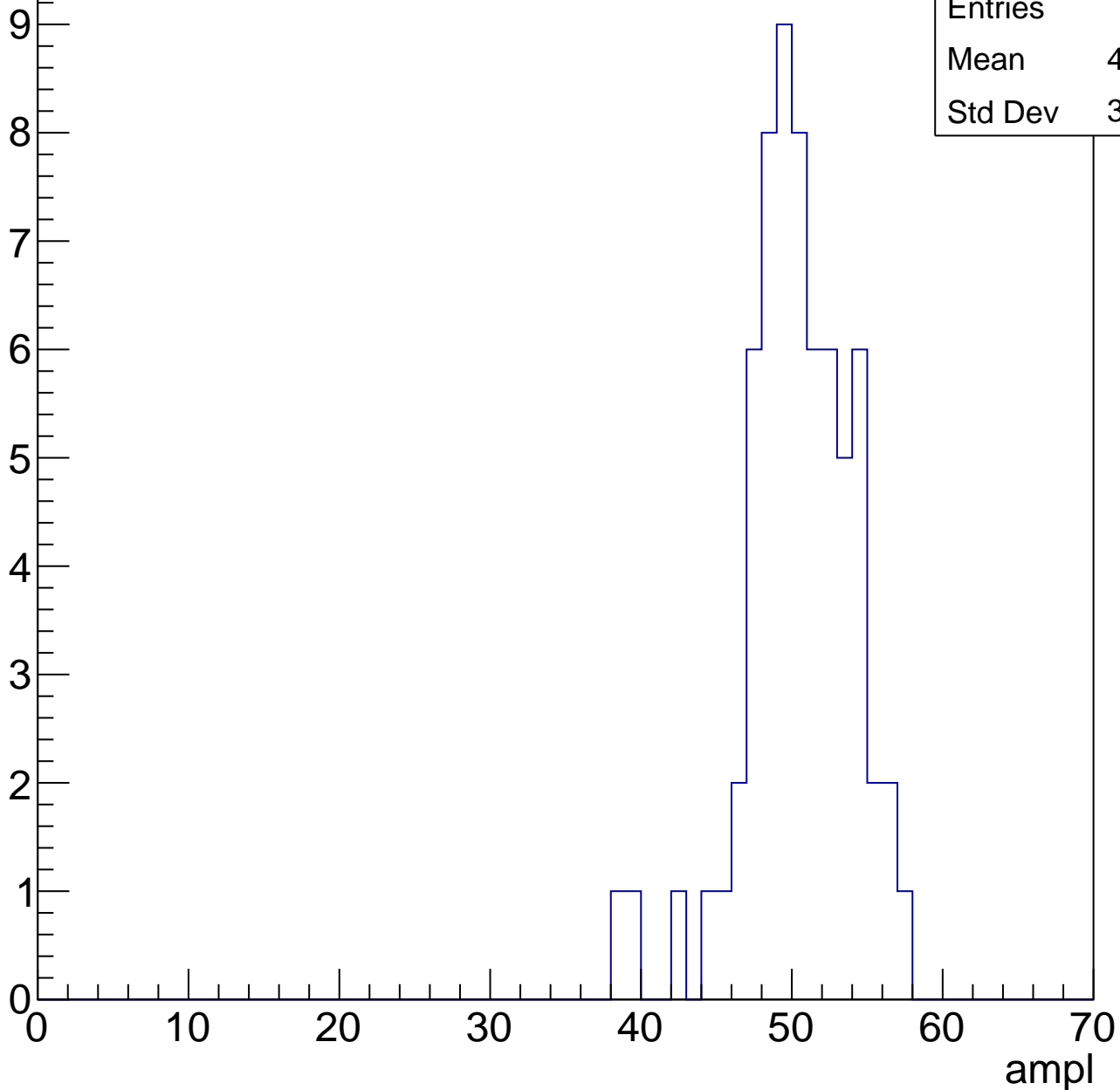


B1L103S, U10-ch89, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	49.89
Std Dev	3.606

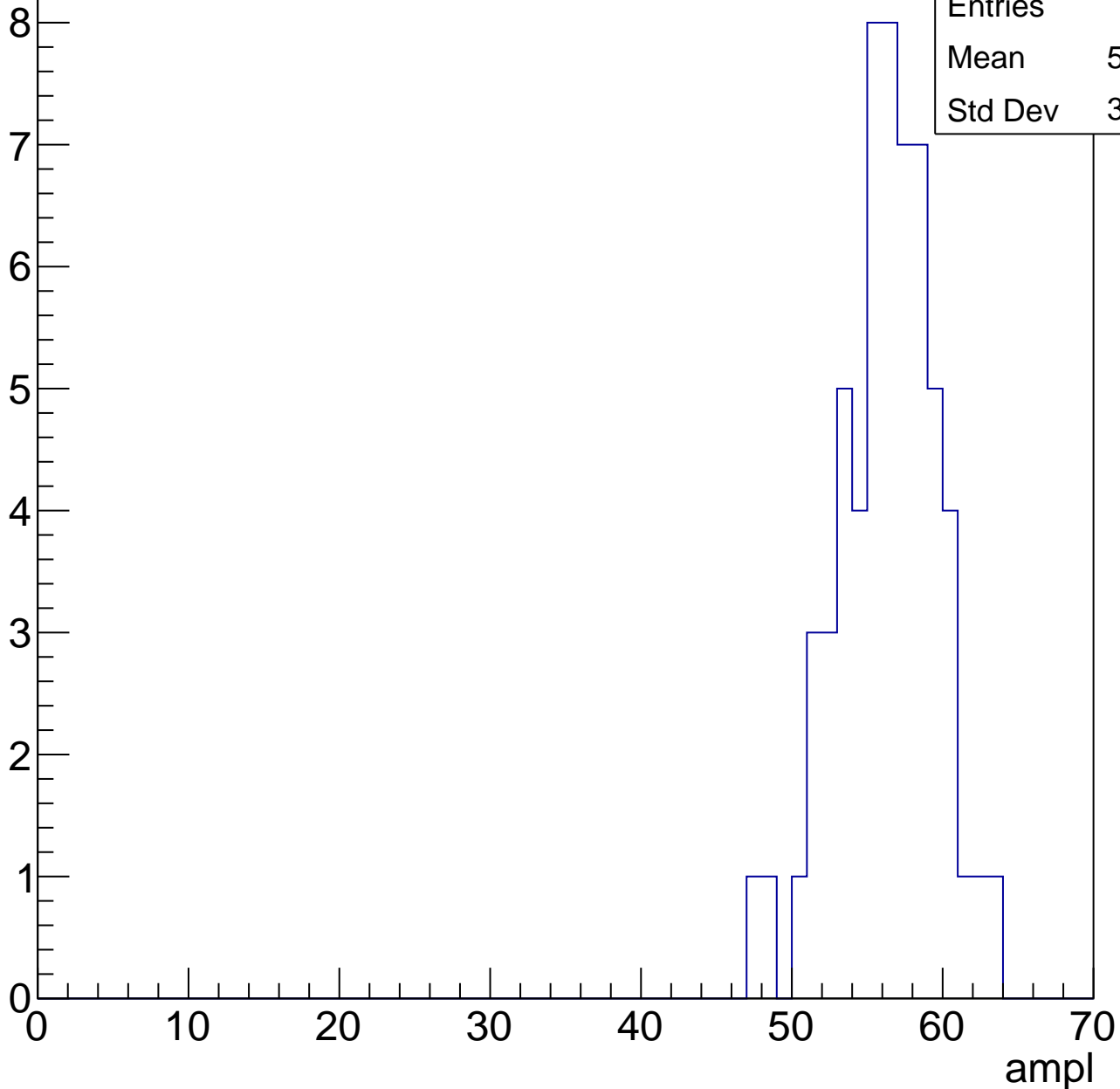


B1L103S, U10-ch89, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	55.82
Std Dev	3.227

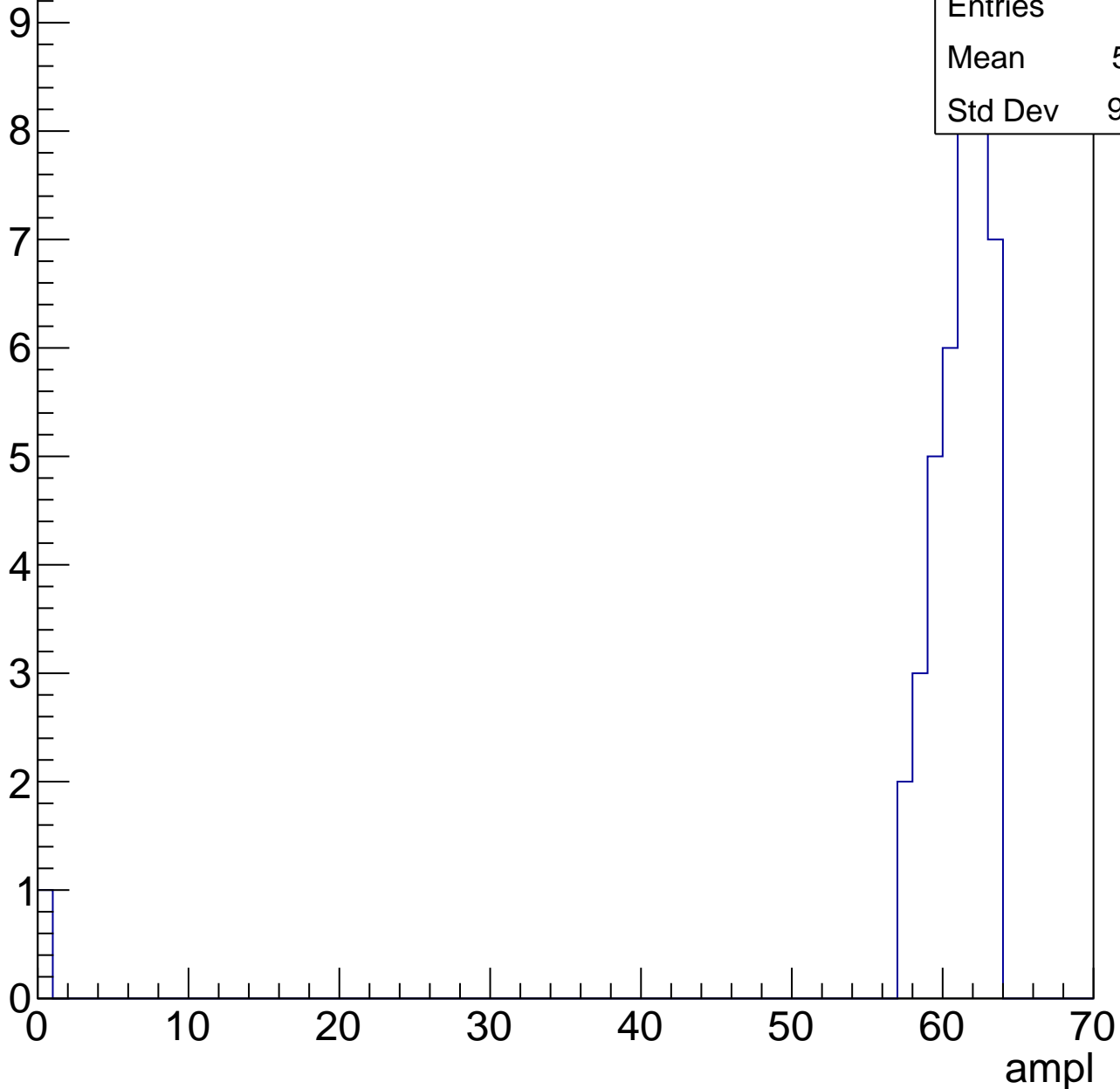


B1L103S, U10-ch89, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.31
Std Dev	9.415



B1L103S, U10-ch89, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch89, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	0
Std Dev	0

B1L103S, U10-ch90, adc0

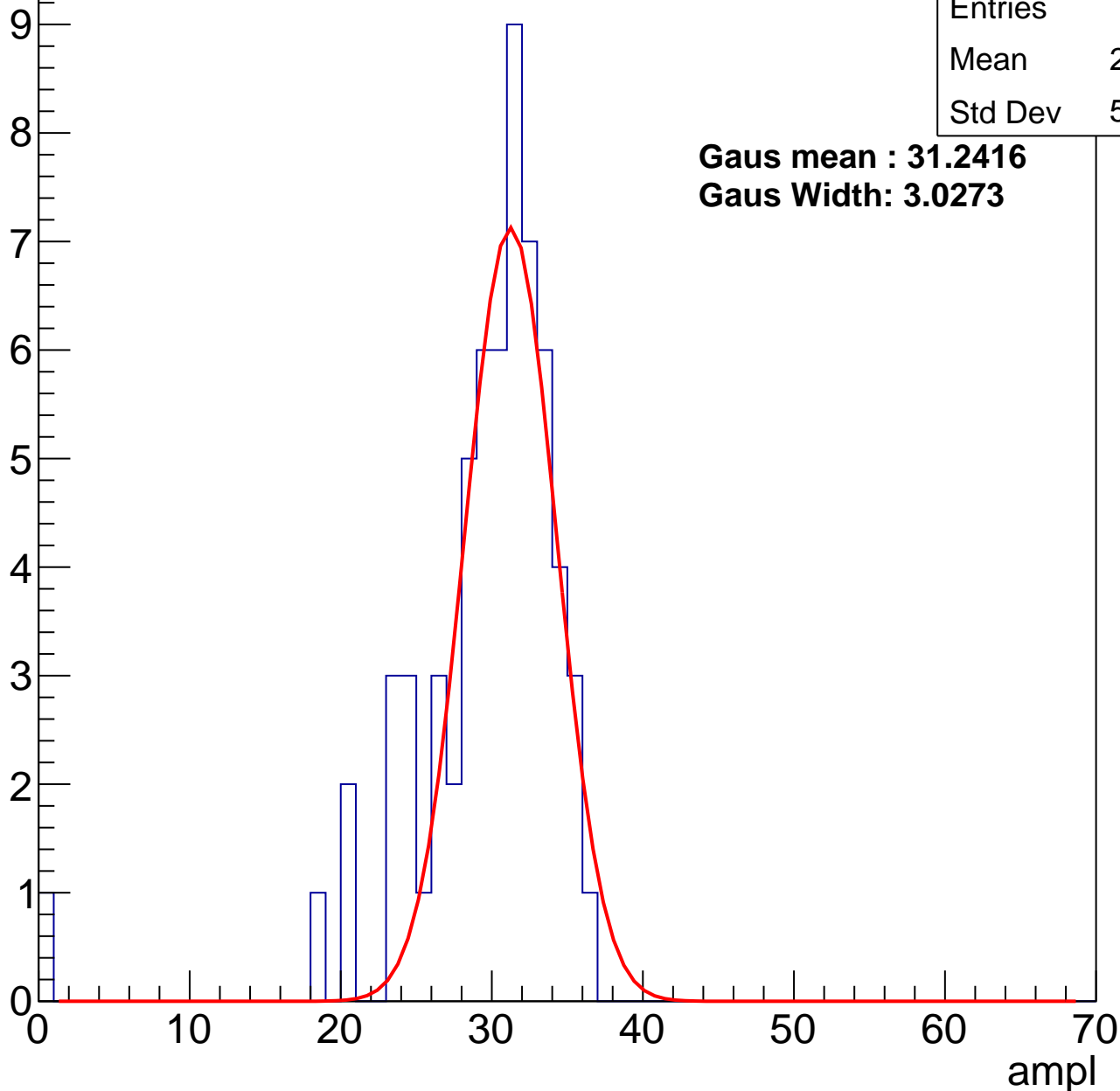
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	29.02
Std Dev	5.376

Gaus mean : 31.2416

Gaus Width: 3.0273



B1L103S, U10-ch90, adc1

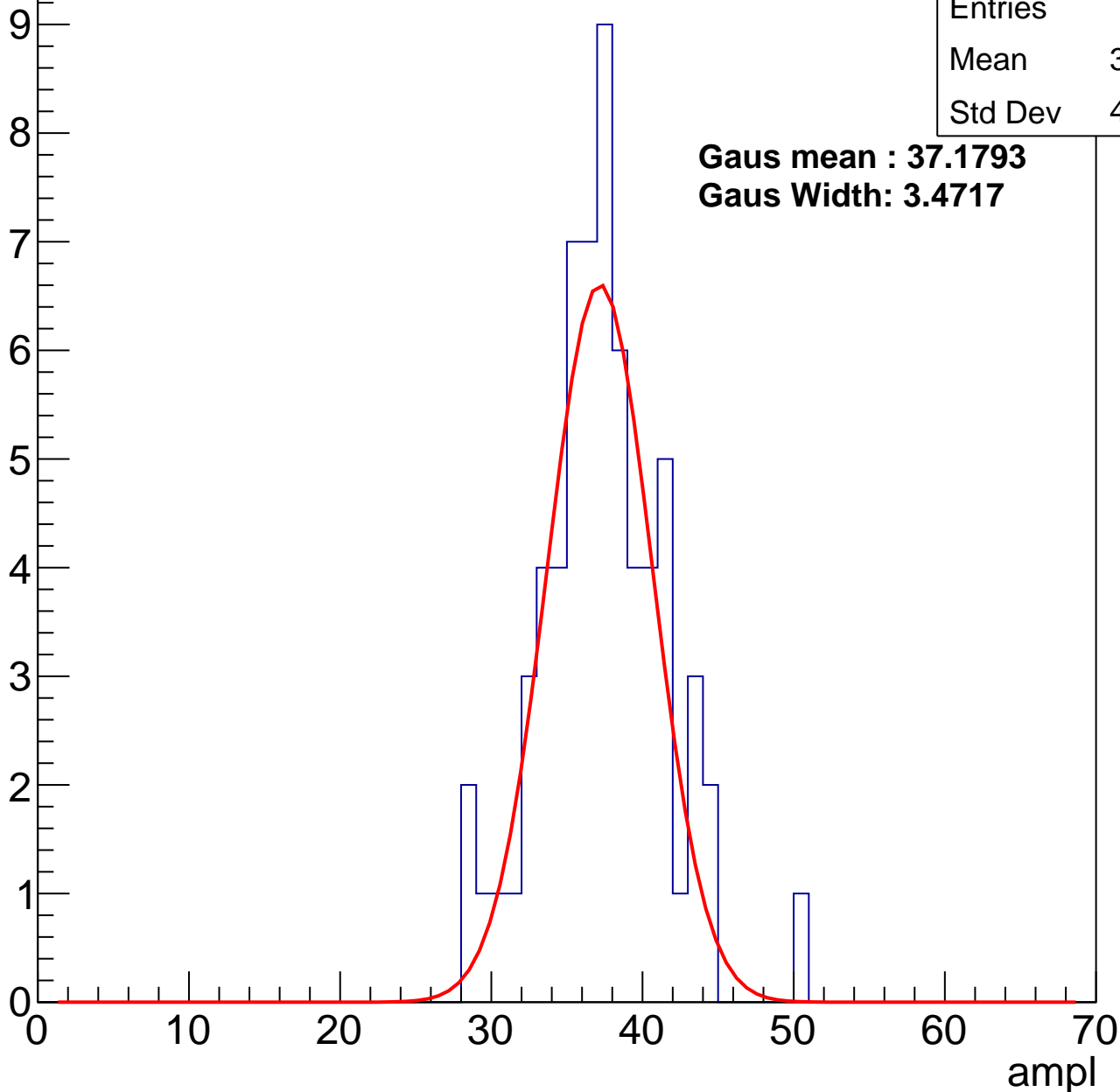
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	36.89
Std Dev	4.035

Gaus mean : 37.1793

Gaus Width: 3.4717



B1L103S, U10-ch90, adc2

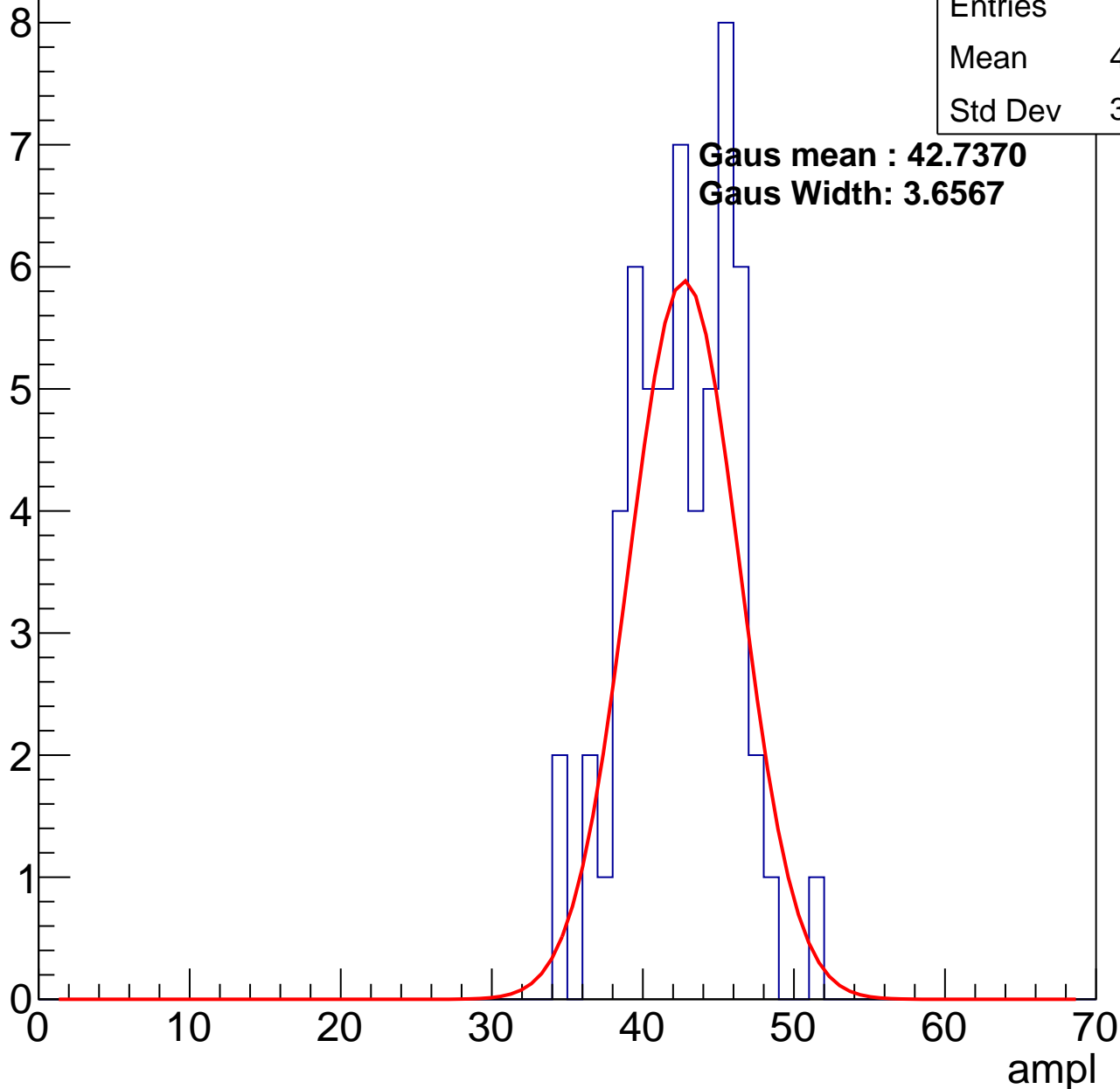
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	42.08
Std Dev	3.509

Gaus mean : 42.7370

Gaus Width: 3.6567



B1L103S, U10-ch90, adc3

calib_packv5_042523_0143.root, FC#7, port C2

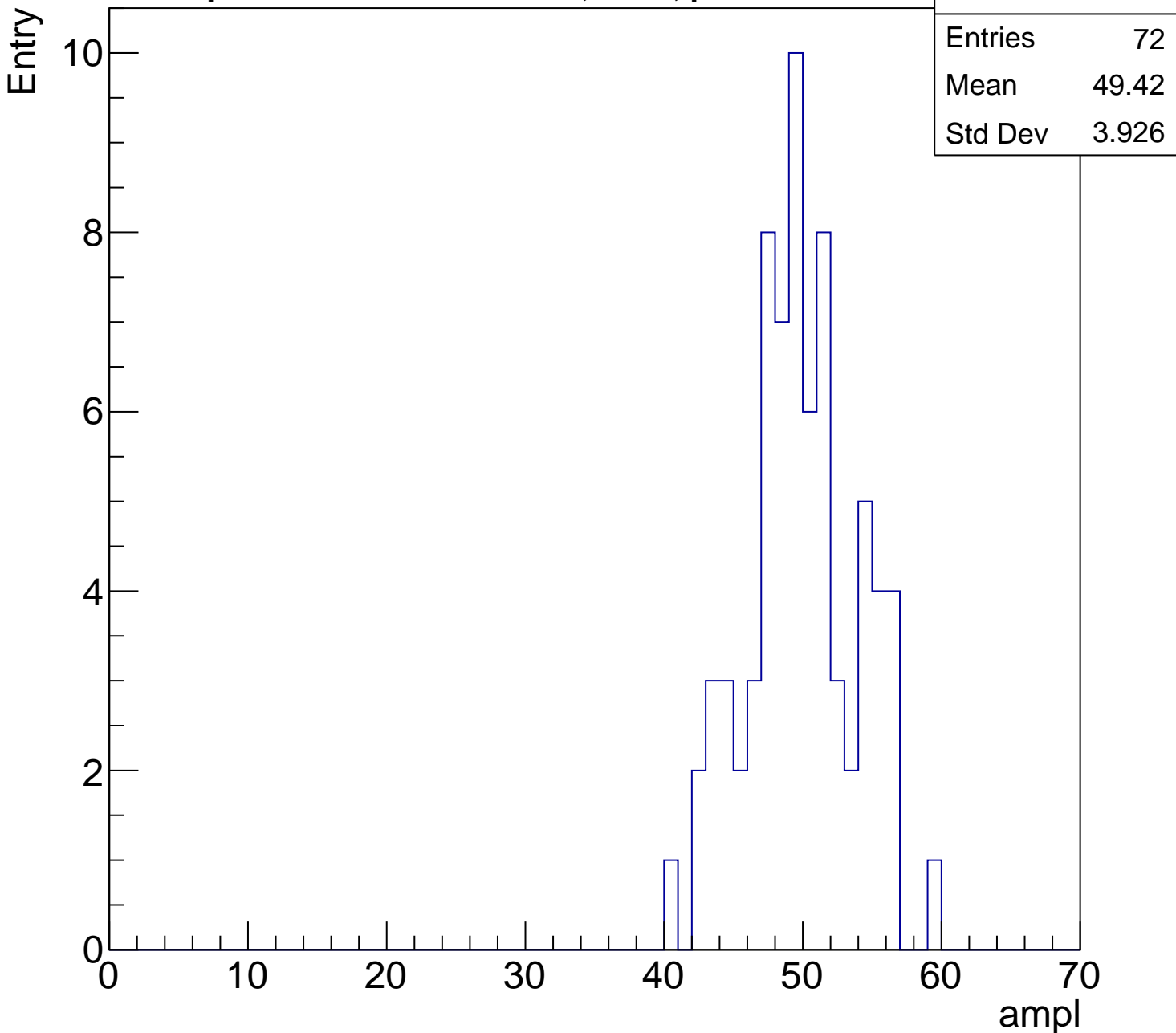
Entries	72
Mean	49.42
Std Dev	3.926

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

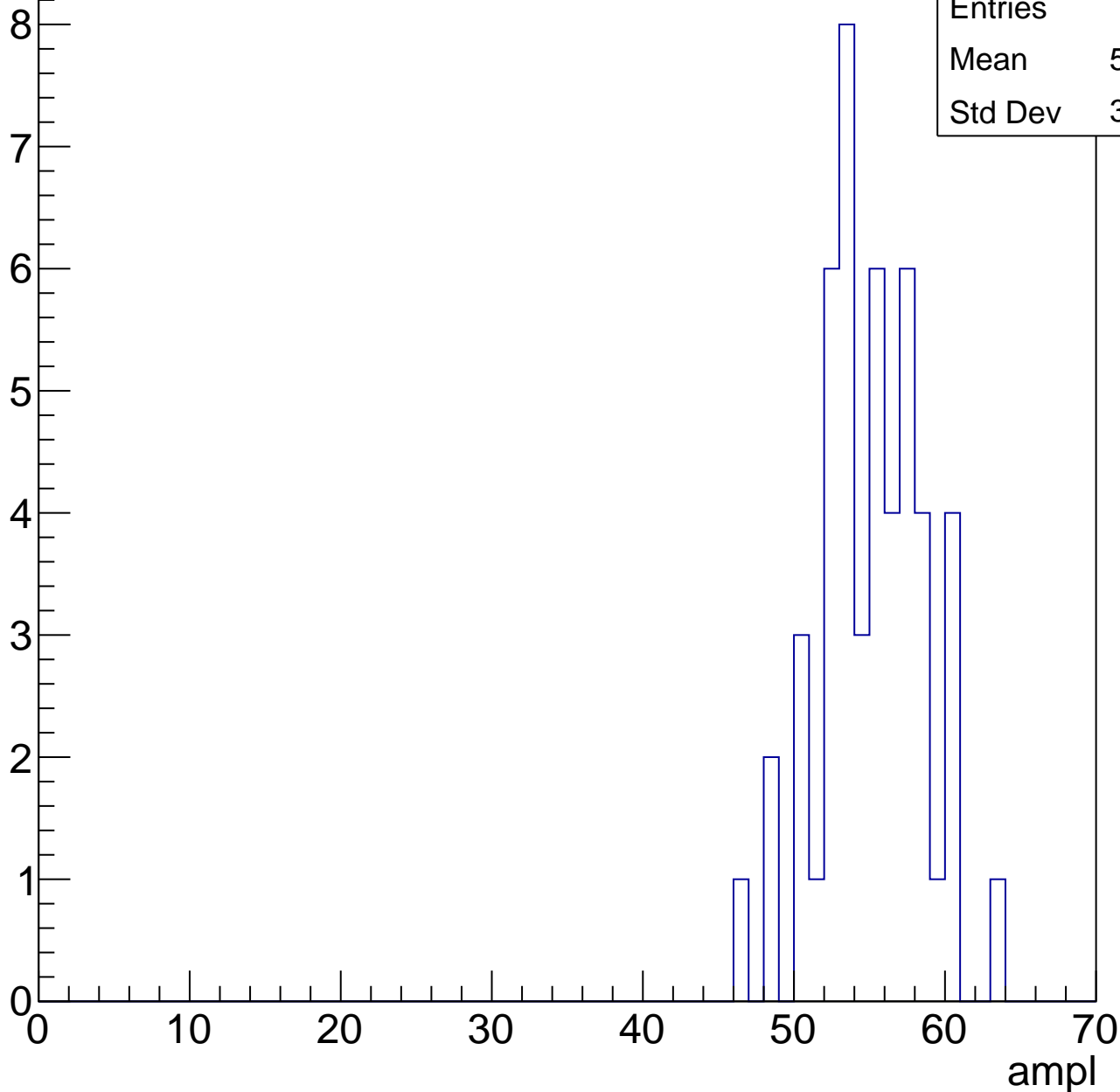


B1L103S, U10-ch90, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	54.62
Std Dev	3.452

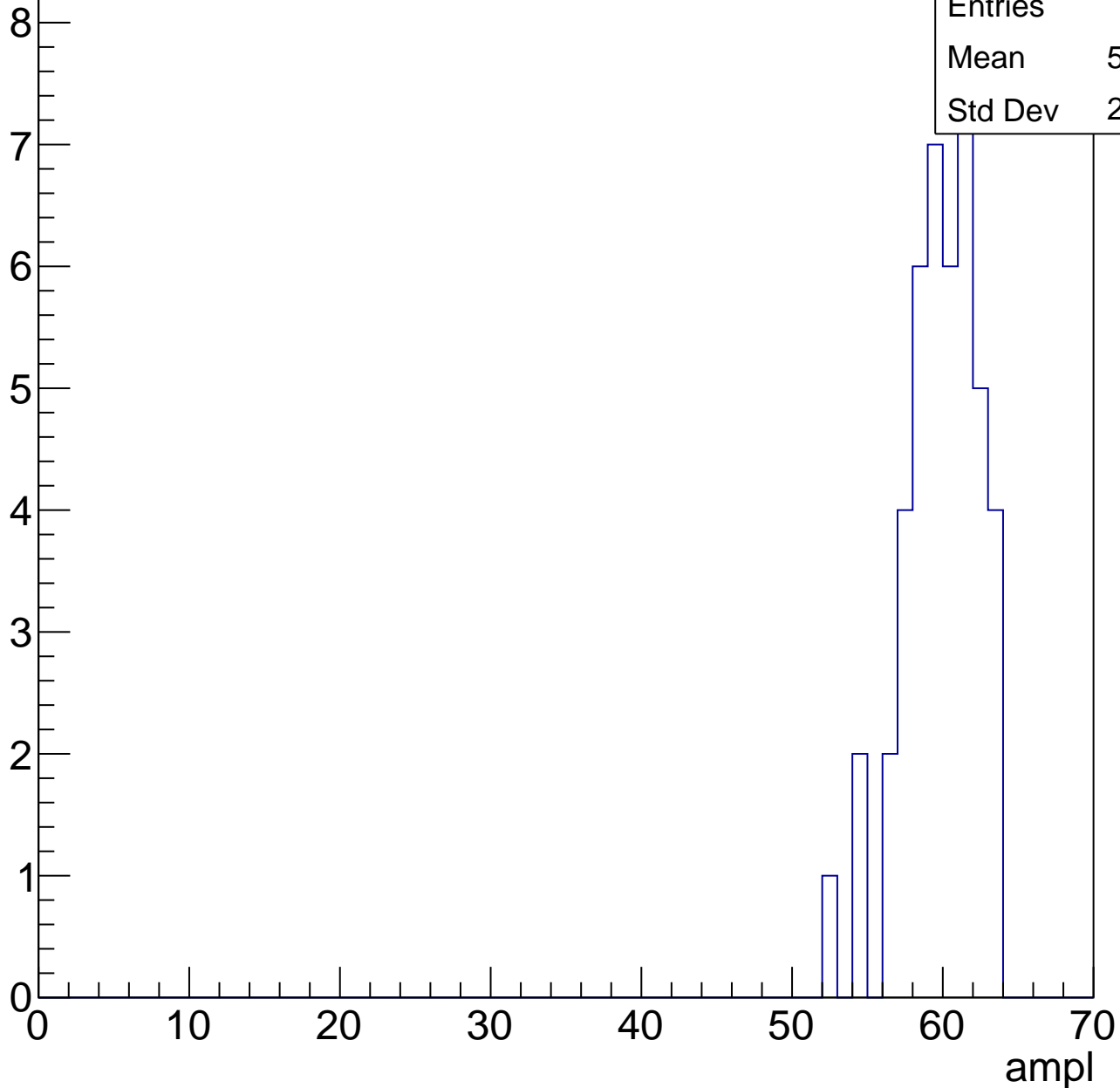


B1L103S, U10-ch90, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	59.36
Std Dev	2.496

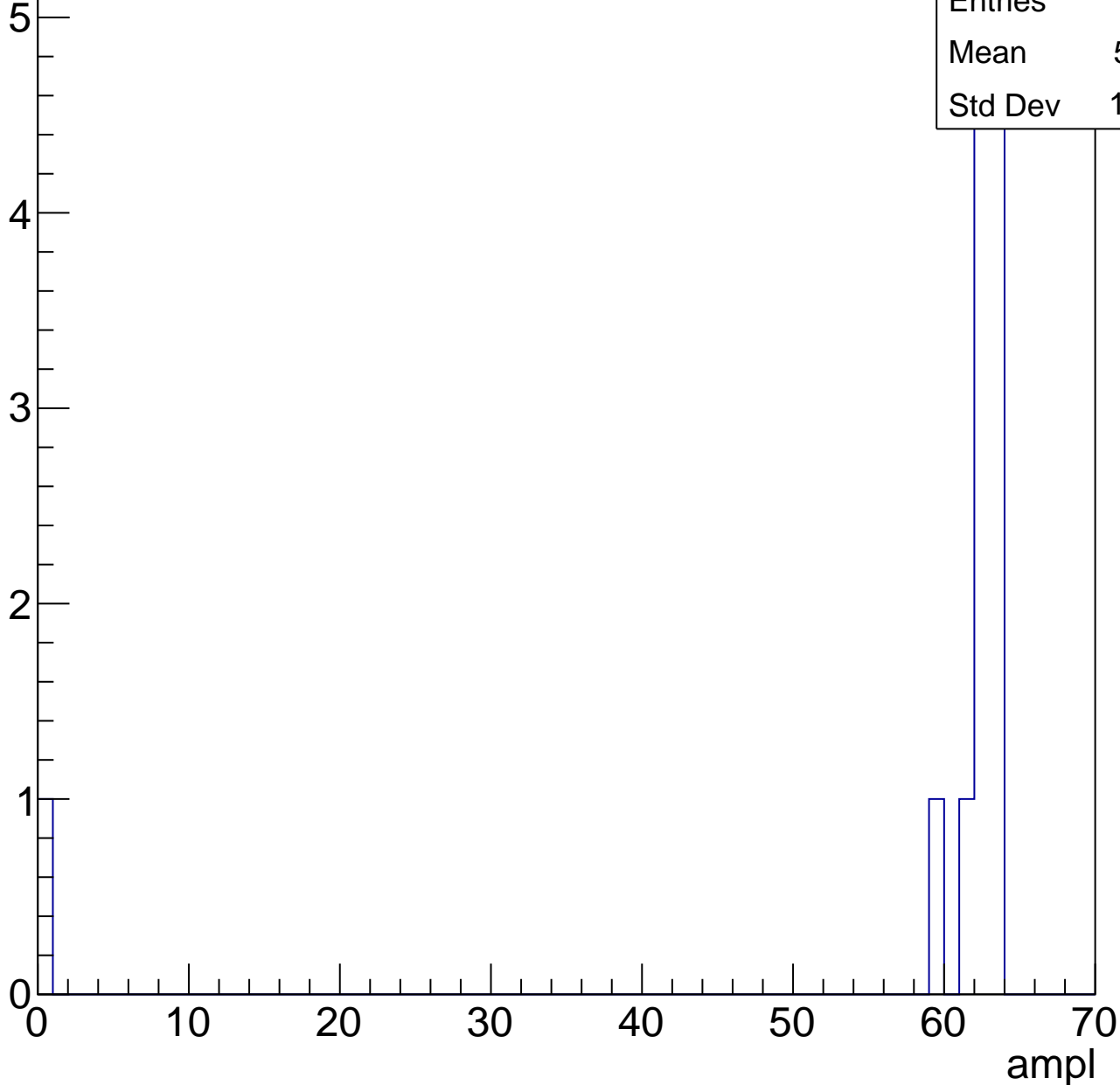


B1L103S, U10-ch90, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	13
Mean	57.31
Std Dev	16.58



B1L103S, U10-ch90, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch91, adc0

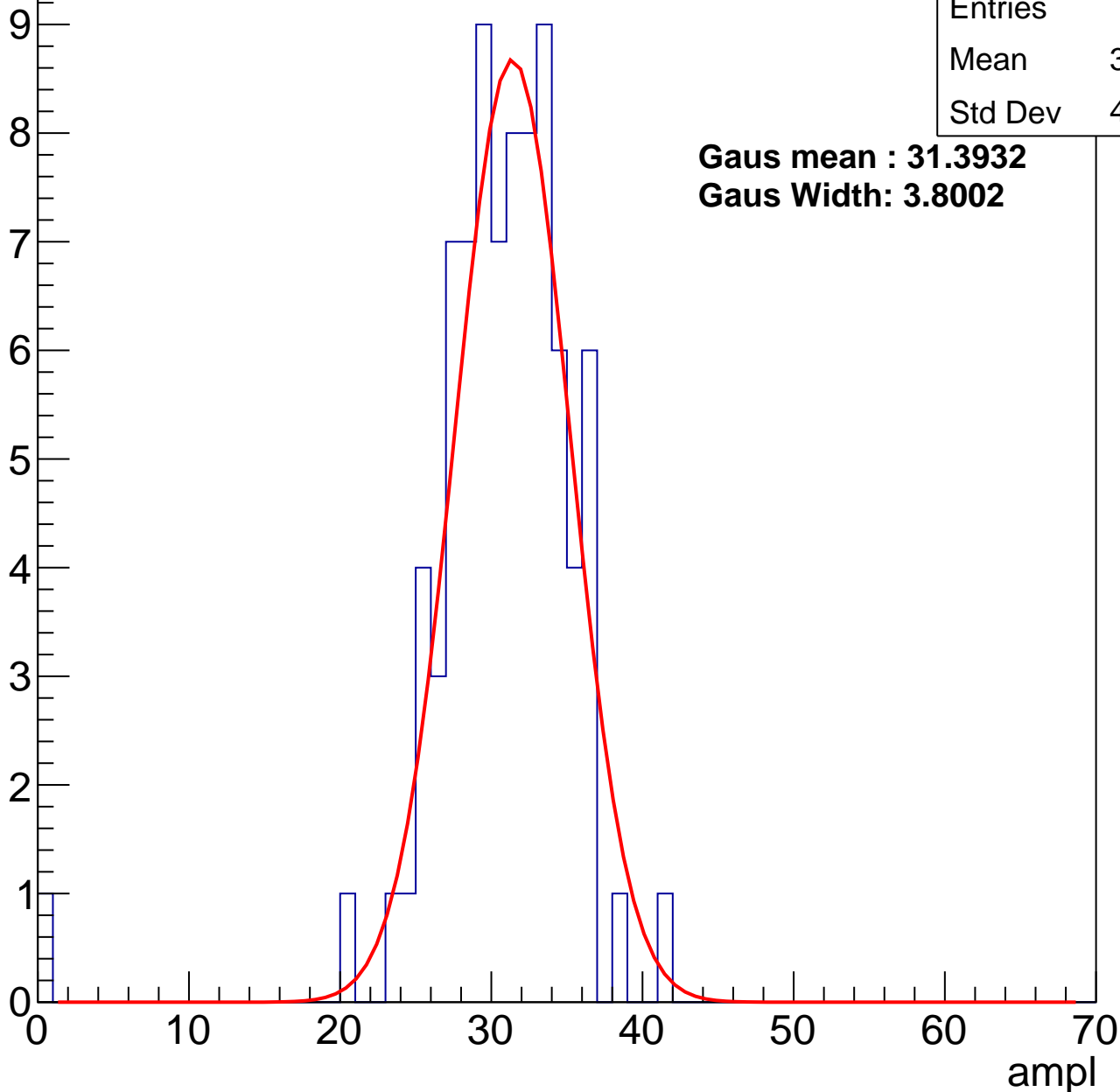
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	30.25
Std Dev	4.935

Gaus mean : 31.3932

Gaus Width: 3.8002



B1L103S, U10-ch91, adc1

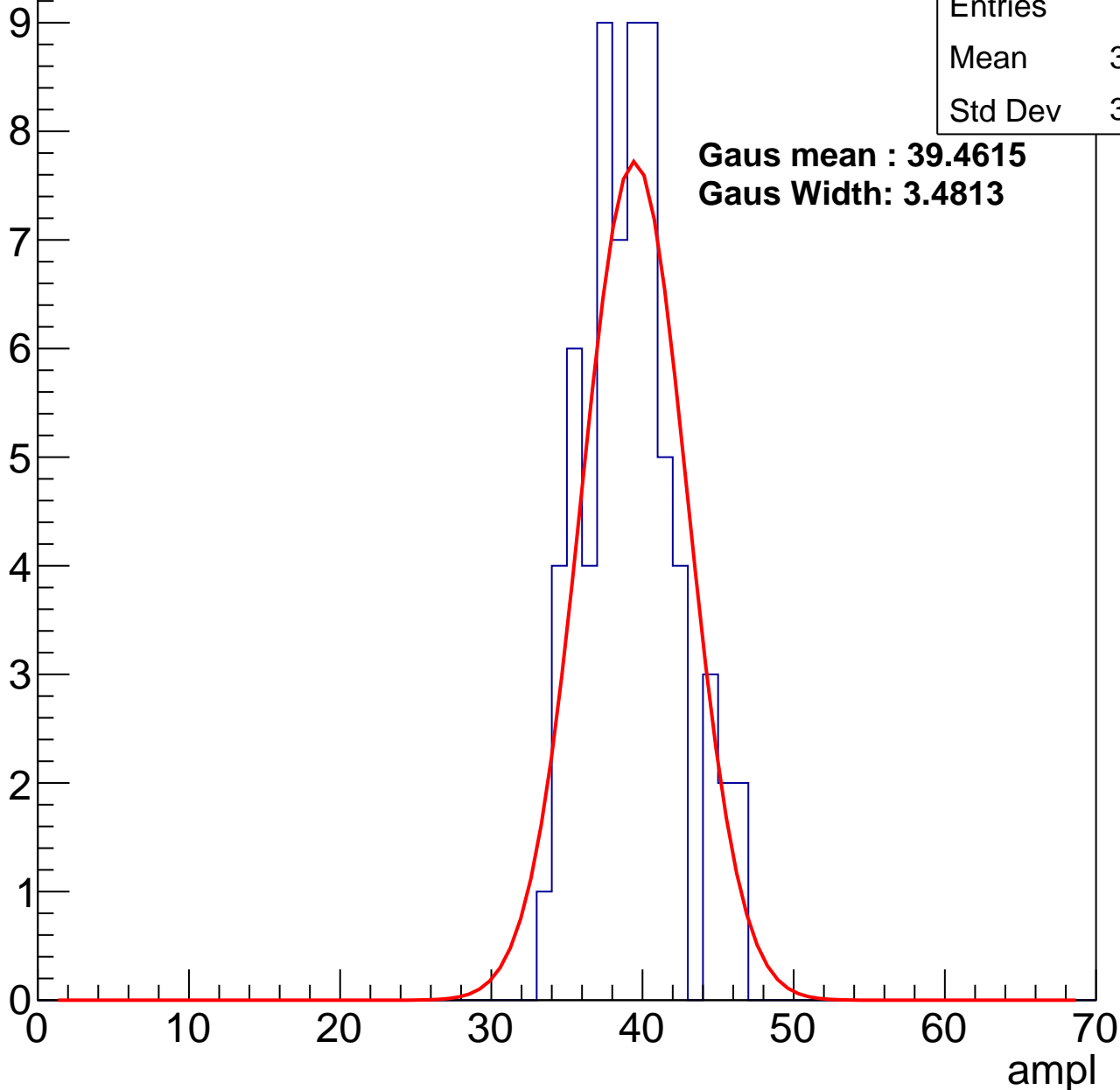
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	38.77
Std Dev	3.082

Gaus mean : 39.4615

Gaus Width: 3.4813



B1L103S, U10-ch91, adc2

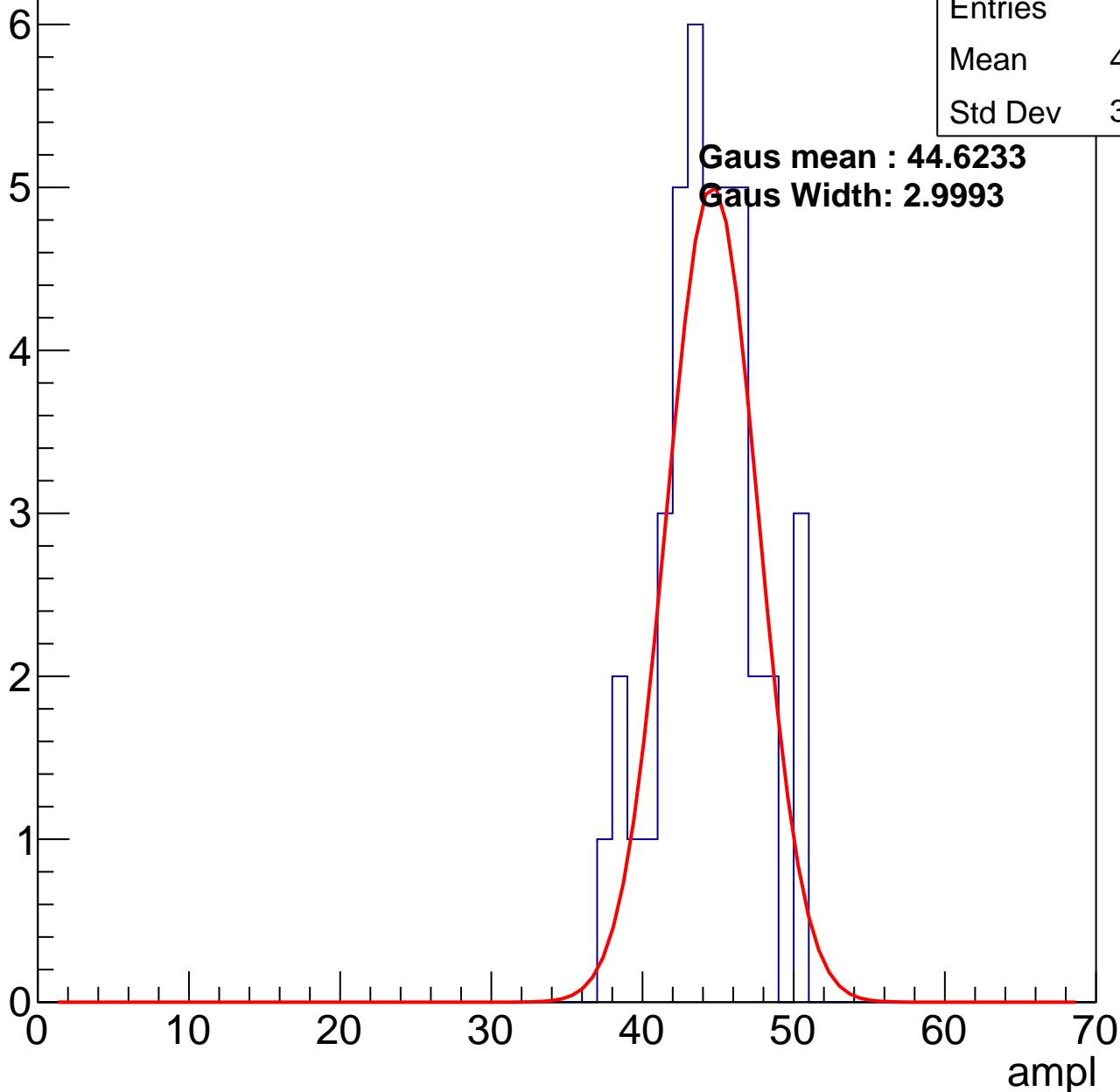
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	41
Mean	43.85
Std Dev	3.112

Gaus mean : 44.6233

Gaus Width: 2.9993

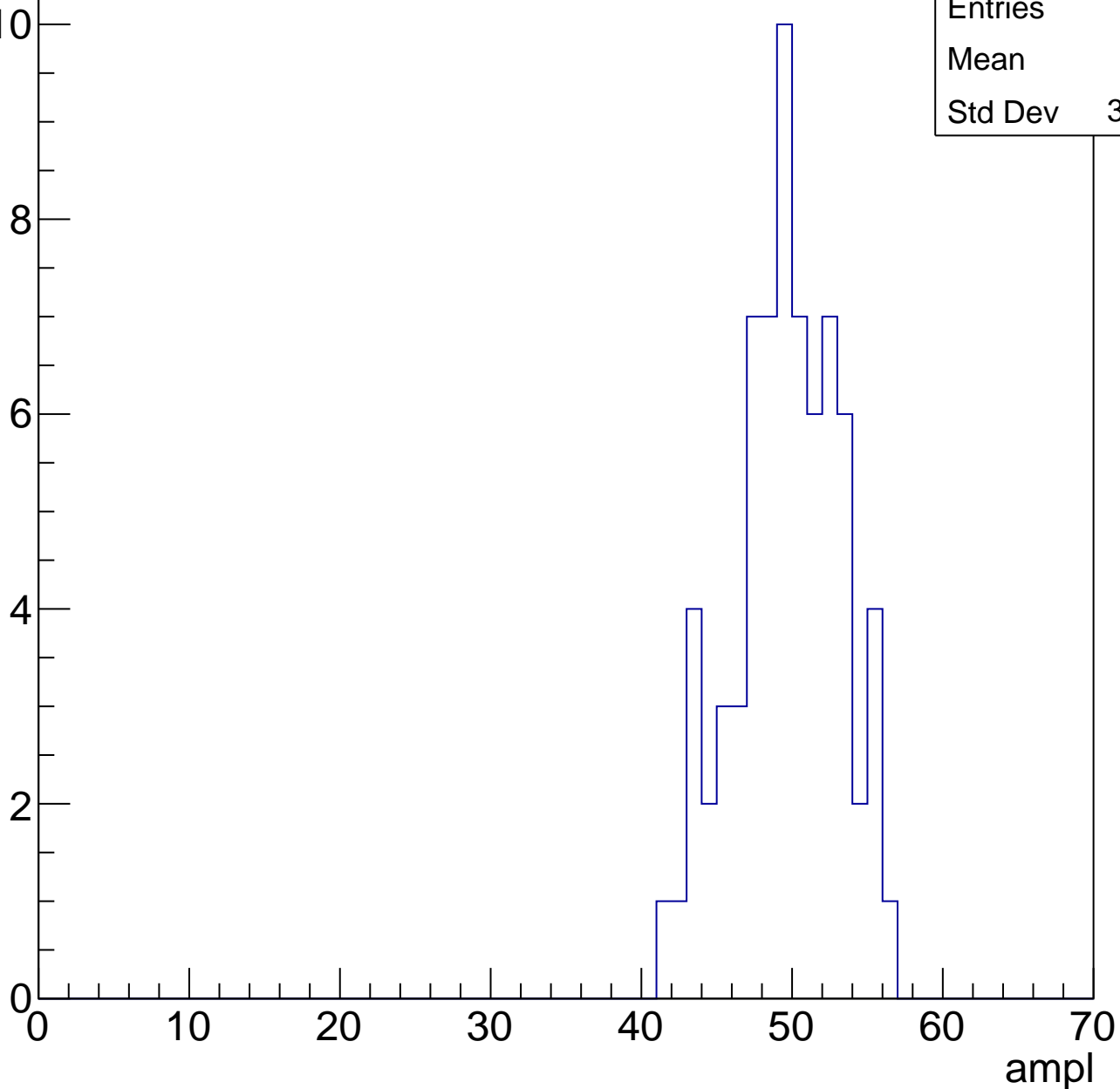


B1L103S, U10-ch91, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	49.2
Std Dev	3.454

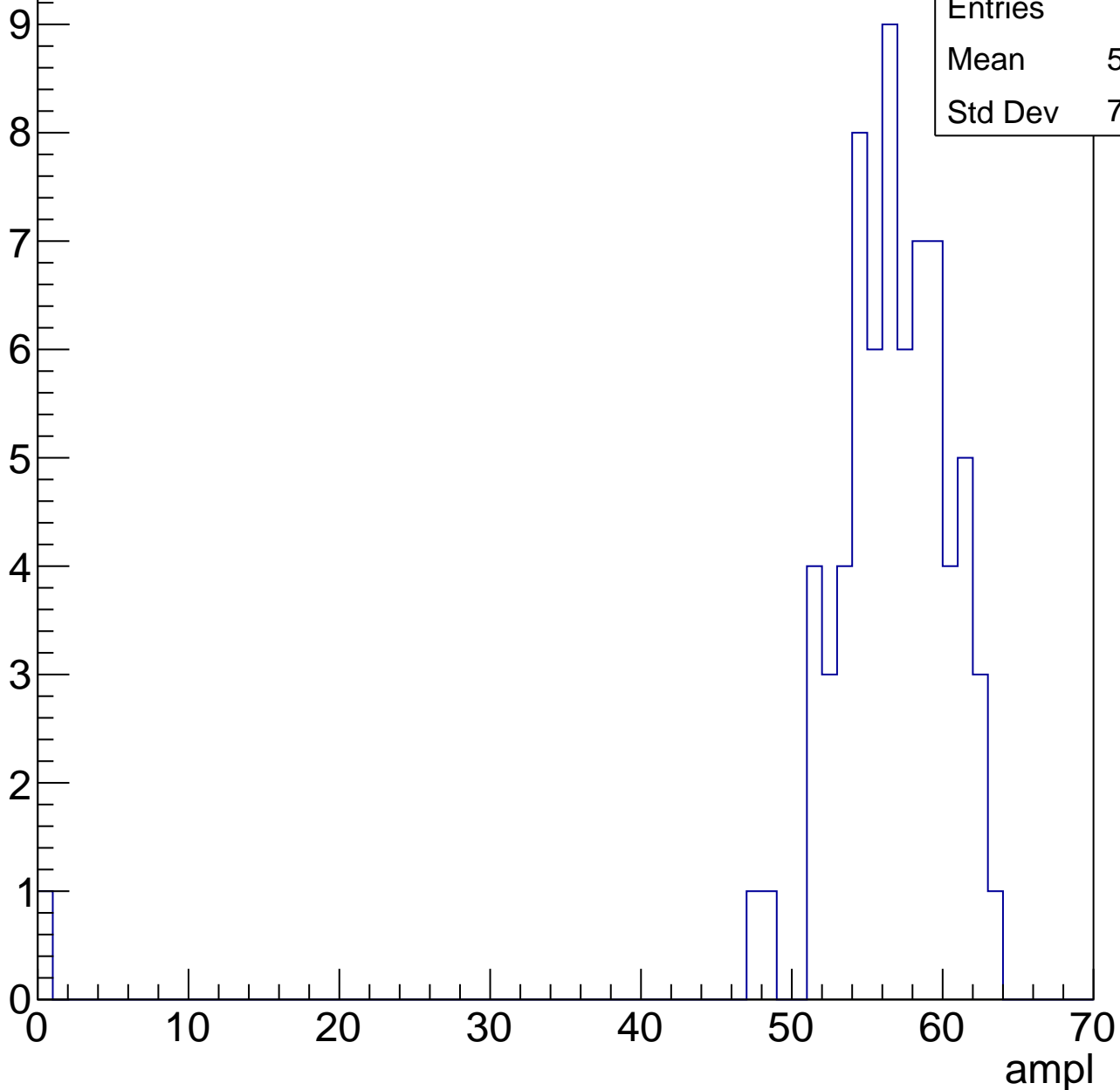


B1L103S, U10-ch91, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

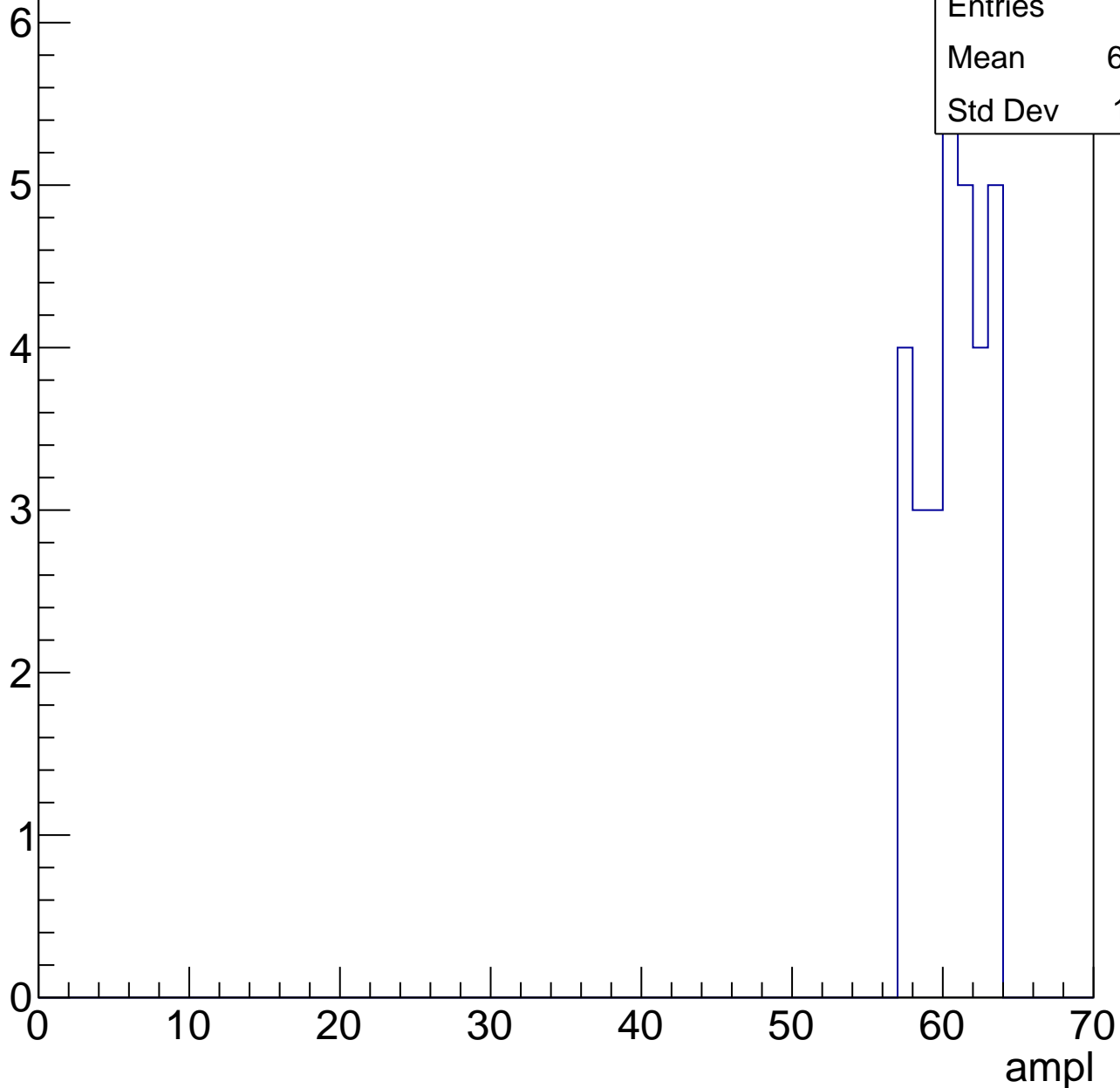
Entries	70
Mean	55.54
Std Dev	7.494



B1L103S, U10-ch91, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch91, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch91, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch92, adc0

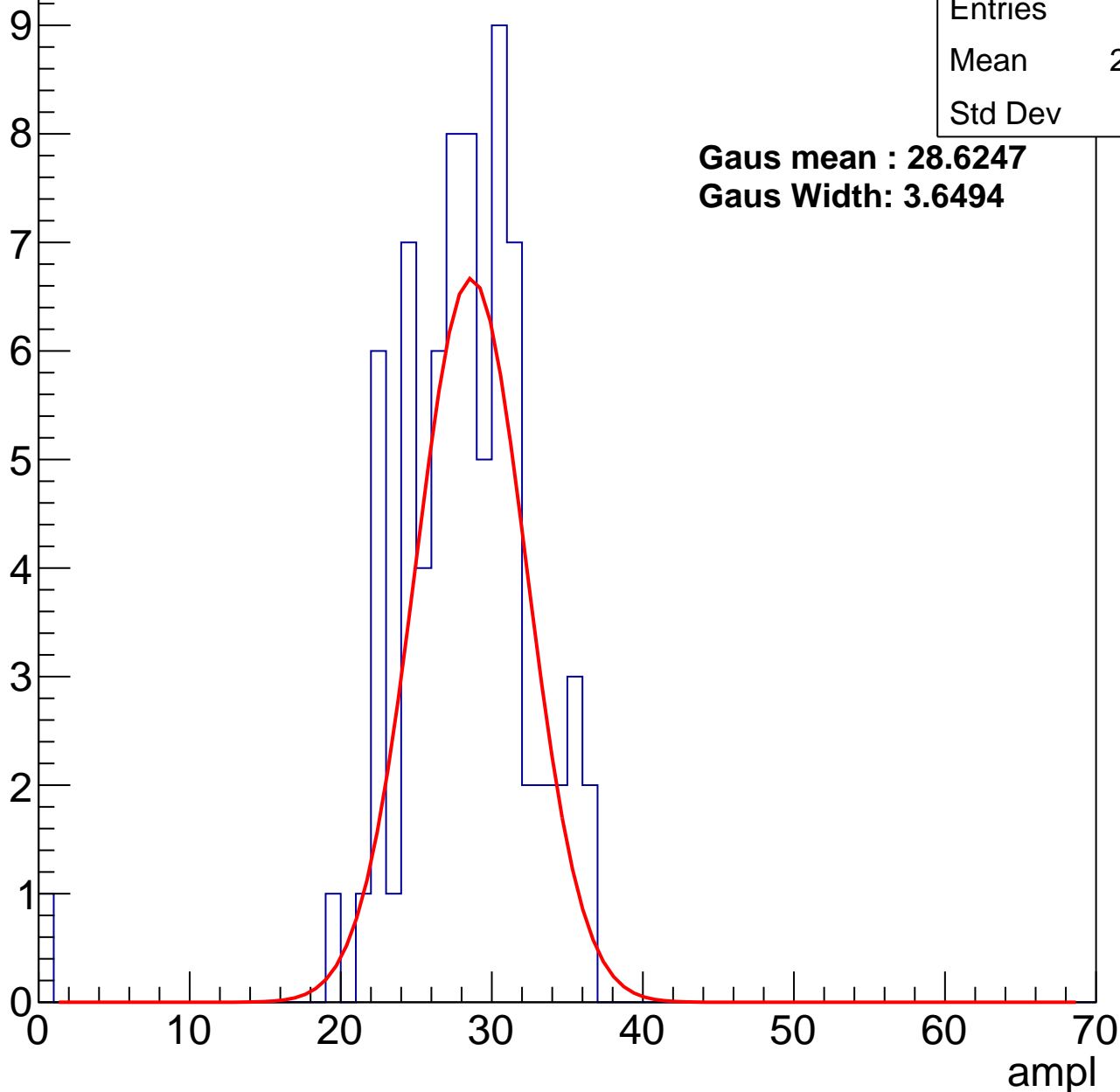
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	27.55
Std Dev	4.97

Gaus mean : 28.6247

Gaus Width: 3.6494



B1L103S, U10-ch92, adc1

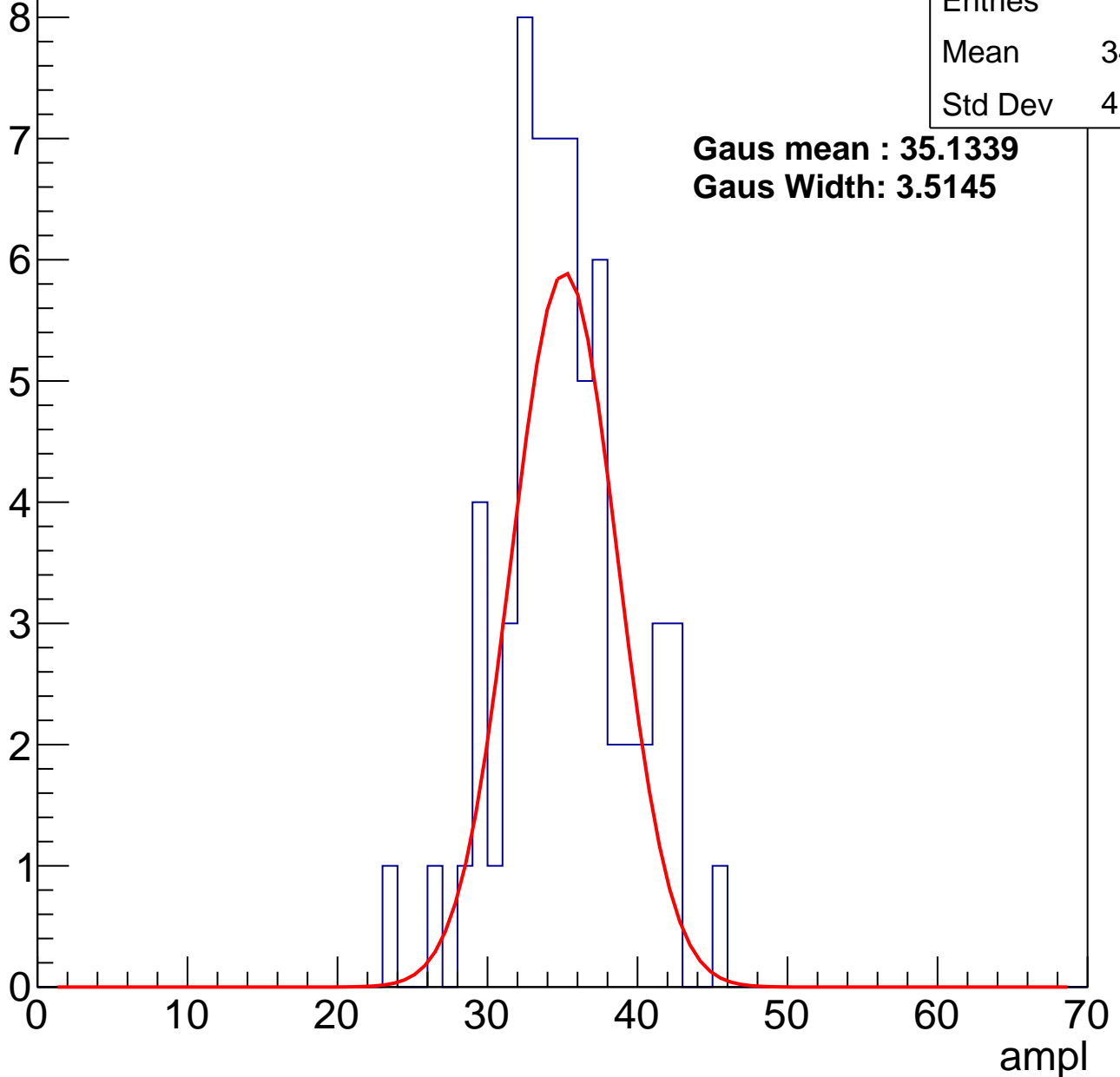
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	34.62
Std Dev	4.095

Gaus mean : 35.1339

Gaus Width: 3.5145



B1L103S, U10-ch92, adc2

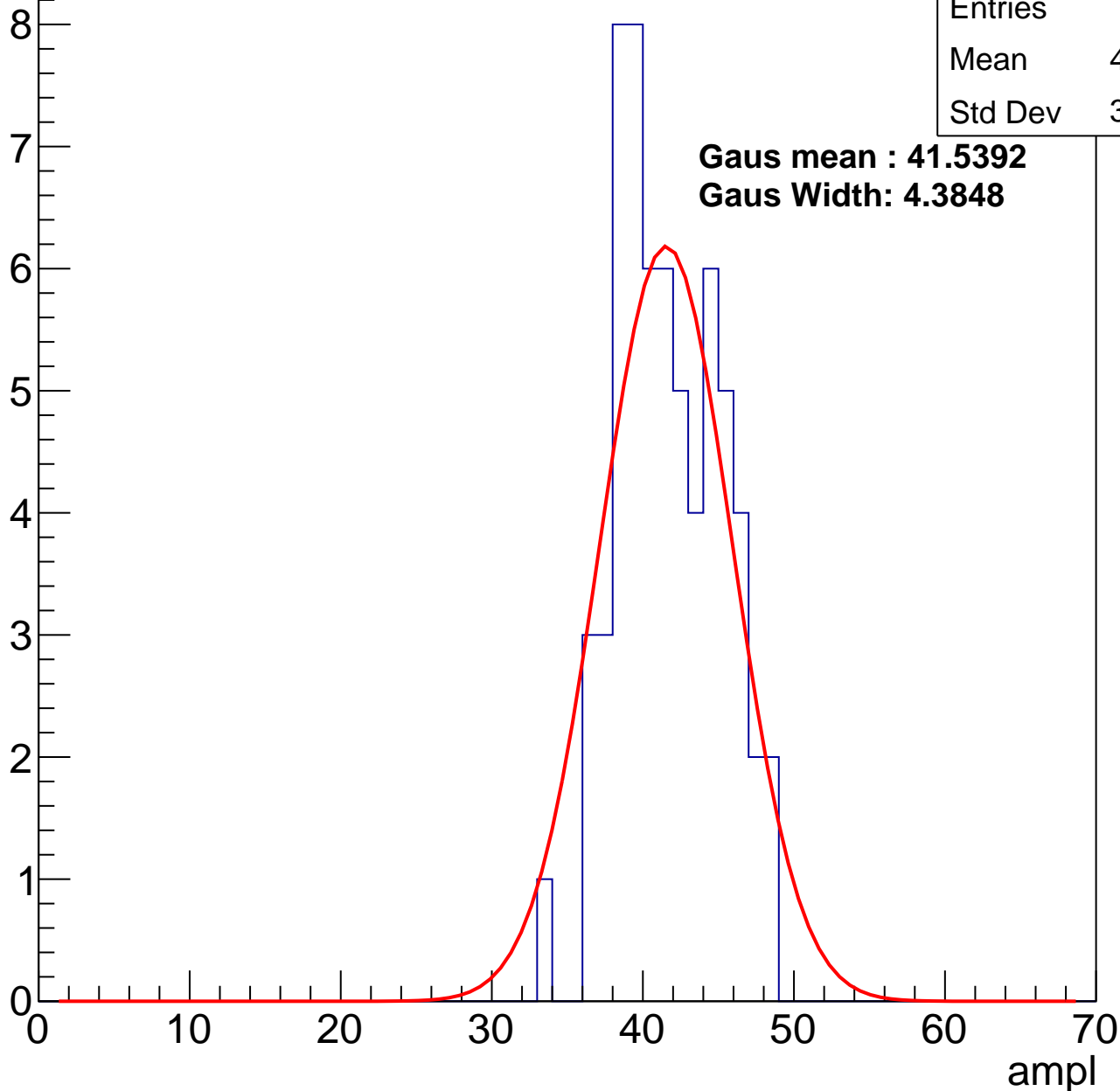
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.25
Std Dev	3.376

Gaus mean : 41.5392

Gaus Width: 4.3848

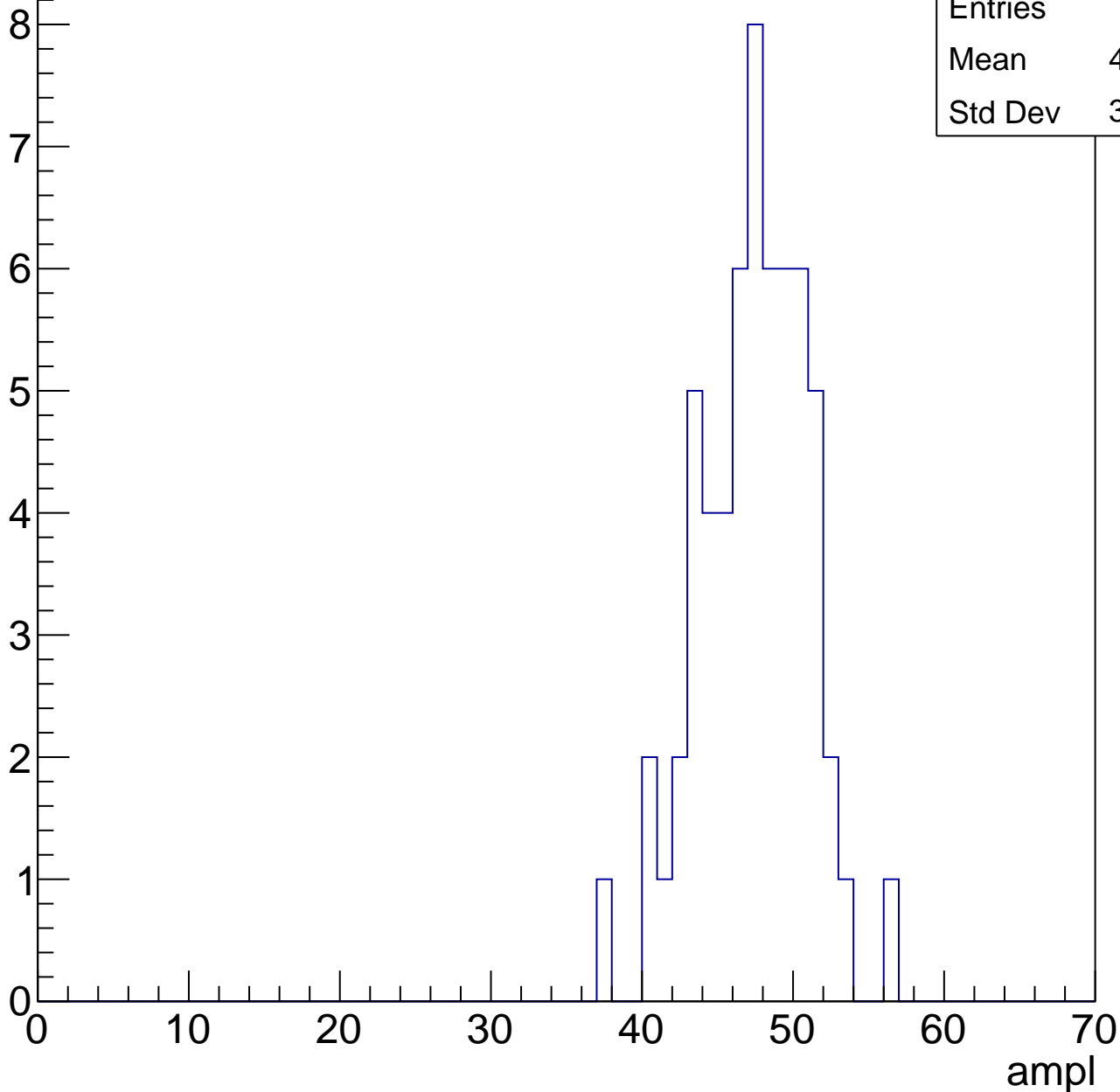


B1L103S, U10-ch92, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

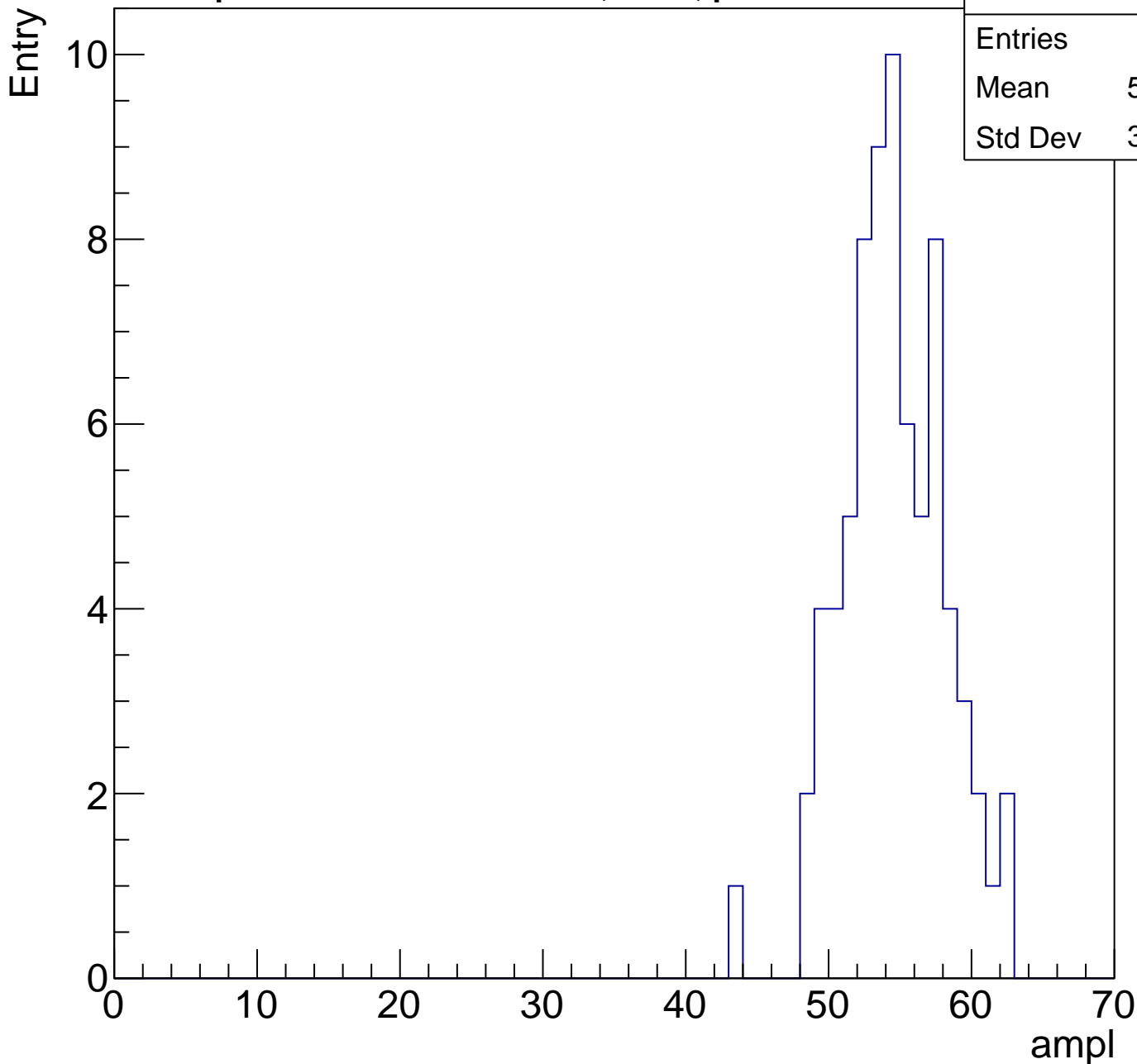
Entries	60
Mean	46.92
Std Dev	3.542



B1L103S, U10-ch92, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	54.09
Std Dev	3.542

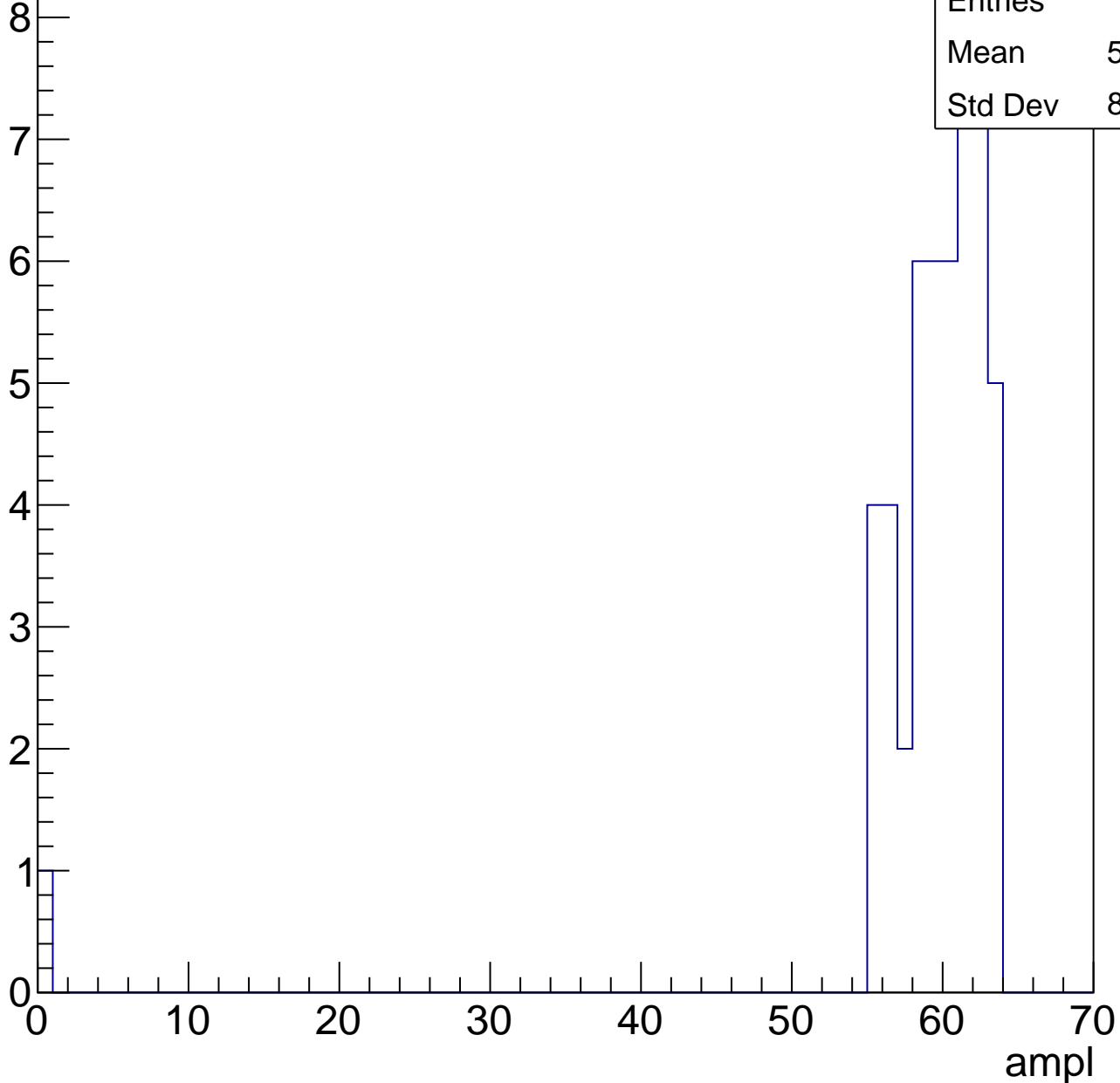


B1L103S, U10-ch92, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	58.38
Std Dev	8.678



B1L103S, U10-ch92, adc6

calib_packv5_042523_0143.root, FC#7, port C2

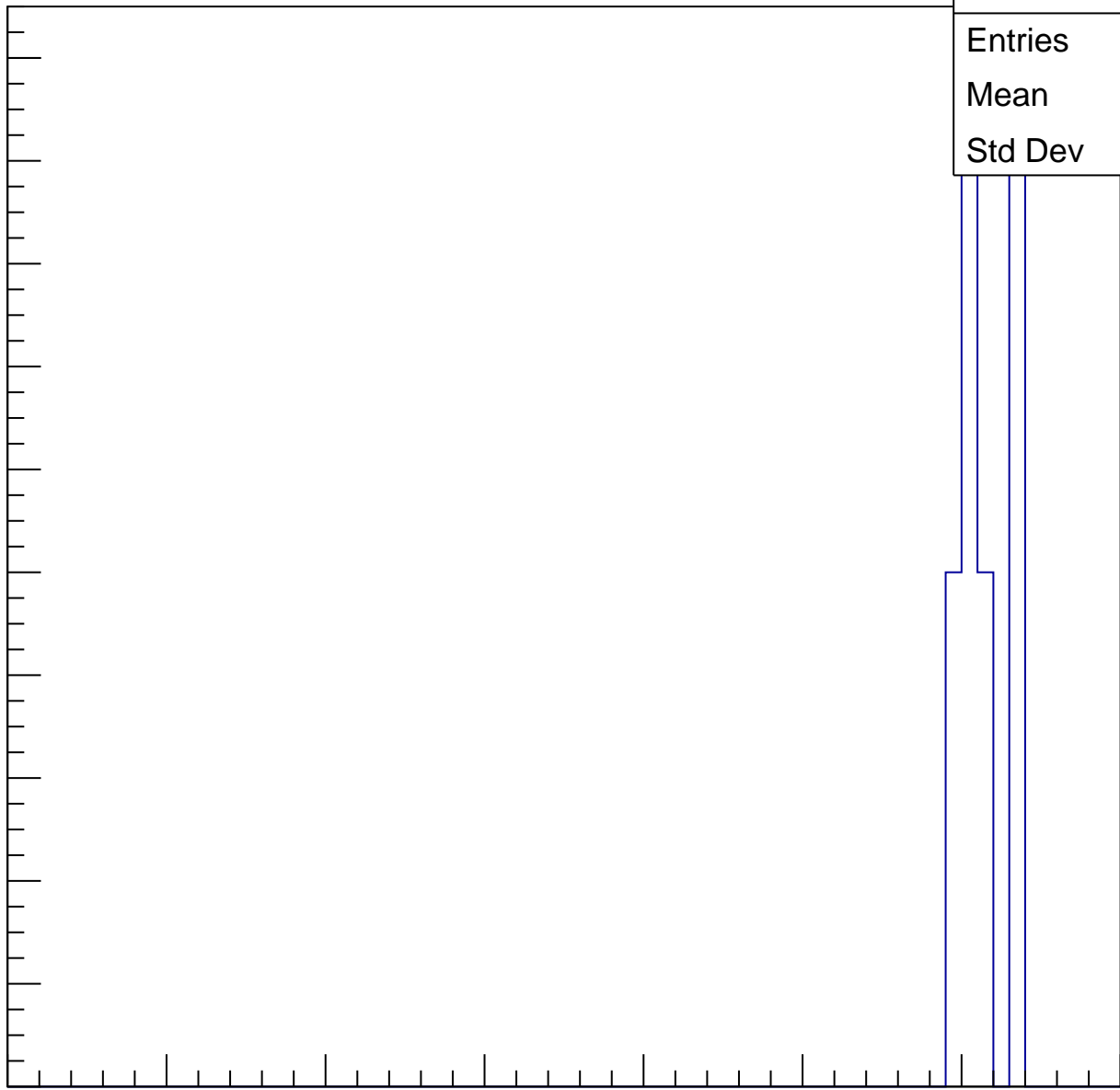
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	61
Std Dev	1.528

ampl

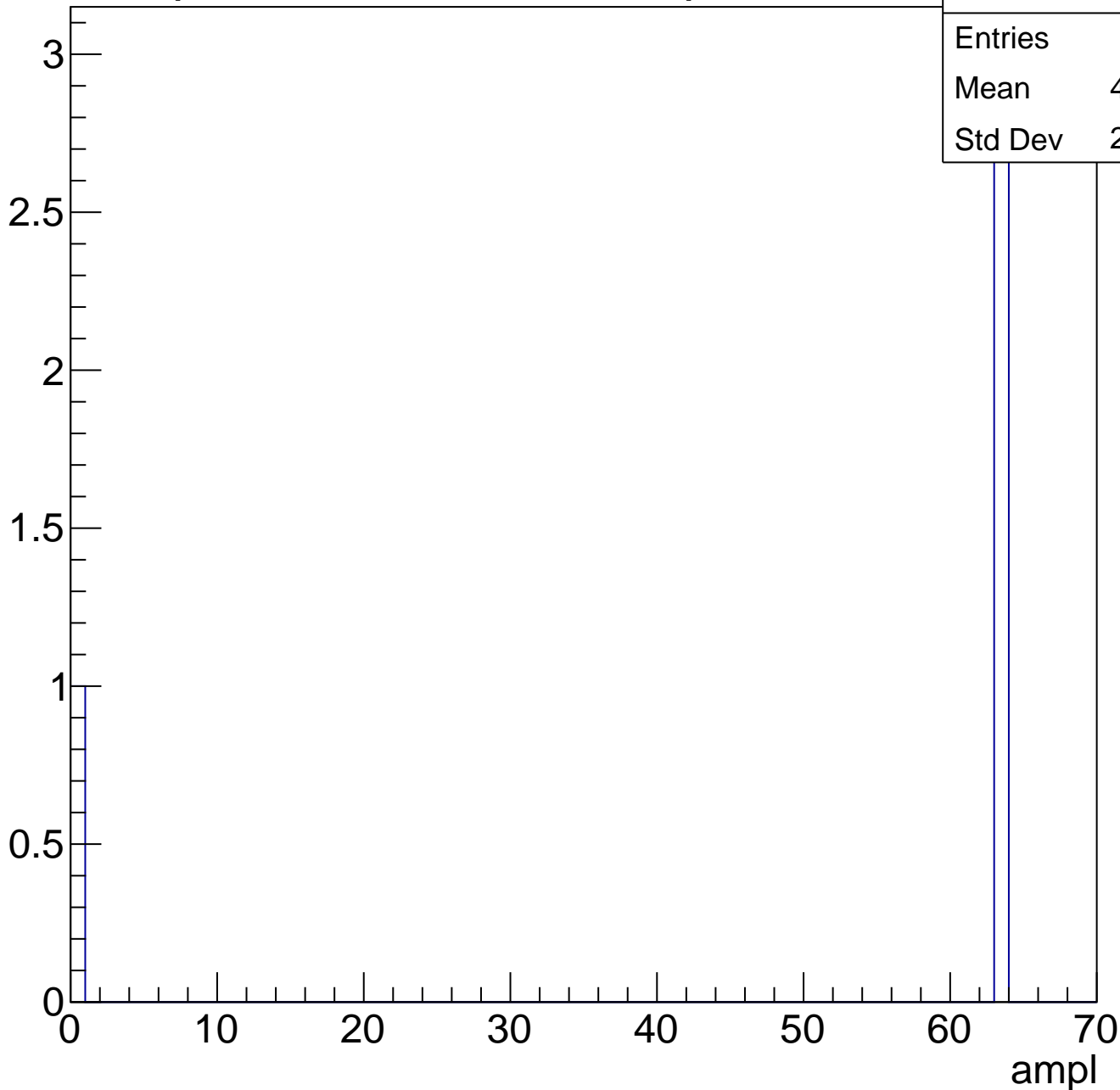
0 10 20 30 40 50 60 70



B1L103S, U10-ch92, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch93, adc0

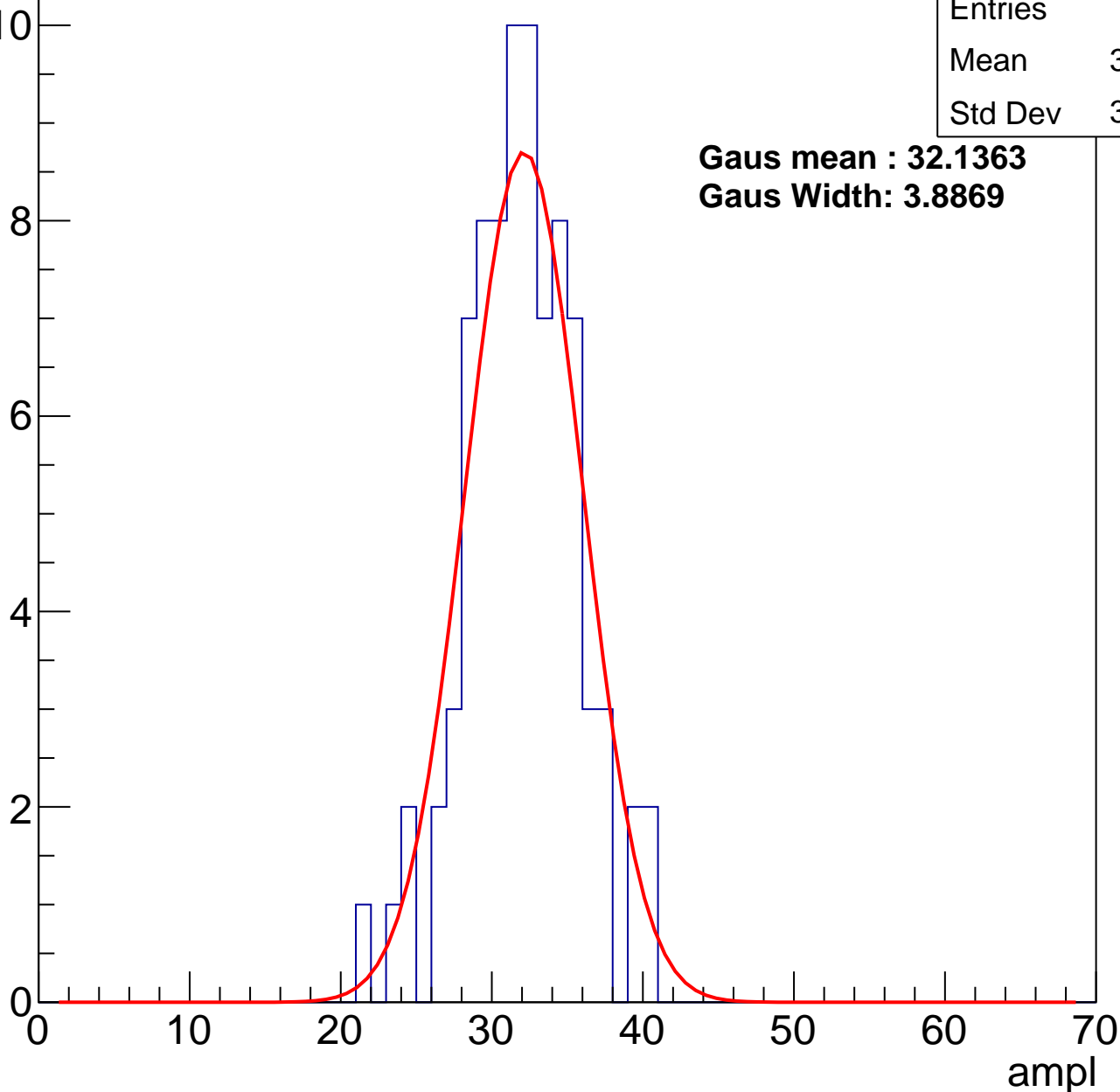
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	31.52
Std Dev	3.676

Gaus mean : 32.1363

Gaus Width: 3.8869



B1L103S, U10-ch93, adc1

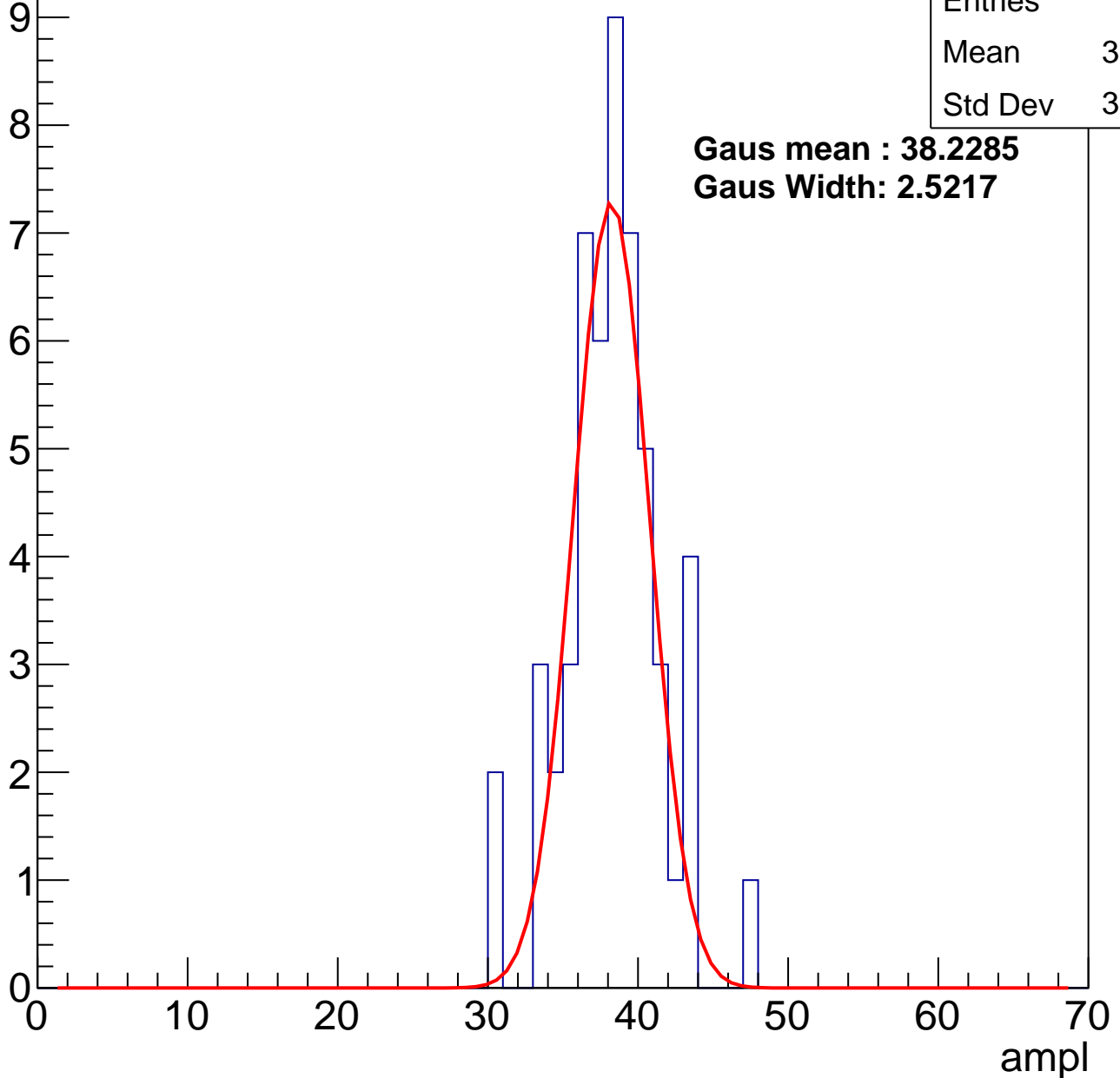
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	37.83
Std Dev	3.208

Gaus mean : 38.2285

Gaus Width: 2.5217



B1L103S, U10-ch93, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	45.08
Std Dev	3.597

Gaus mean : 45.6600

Gaus Width: 3.7598

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

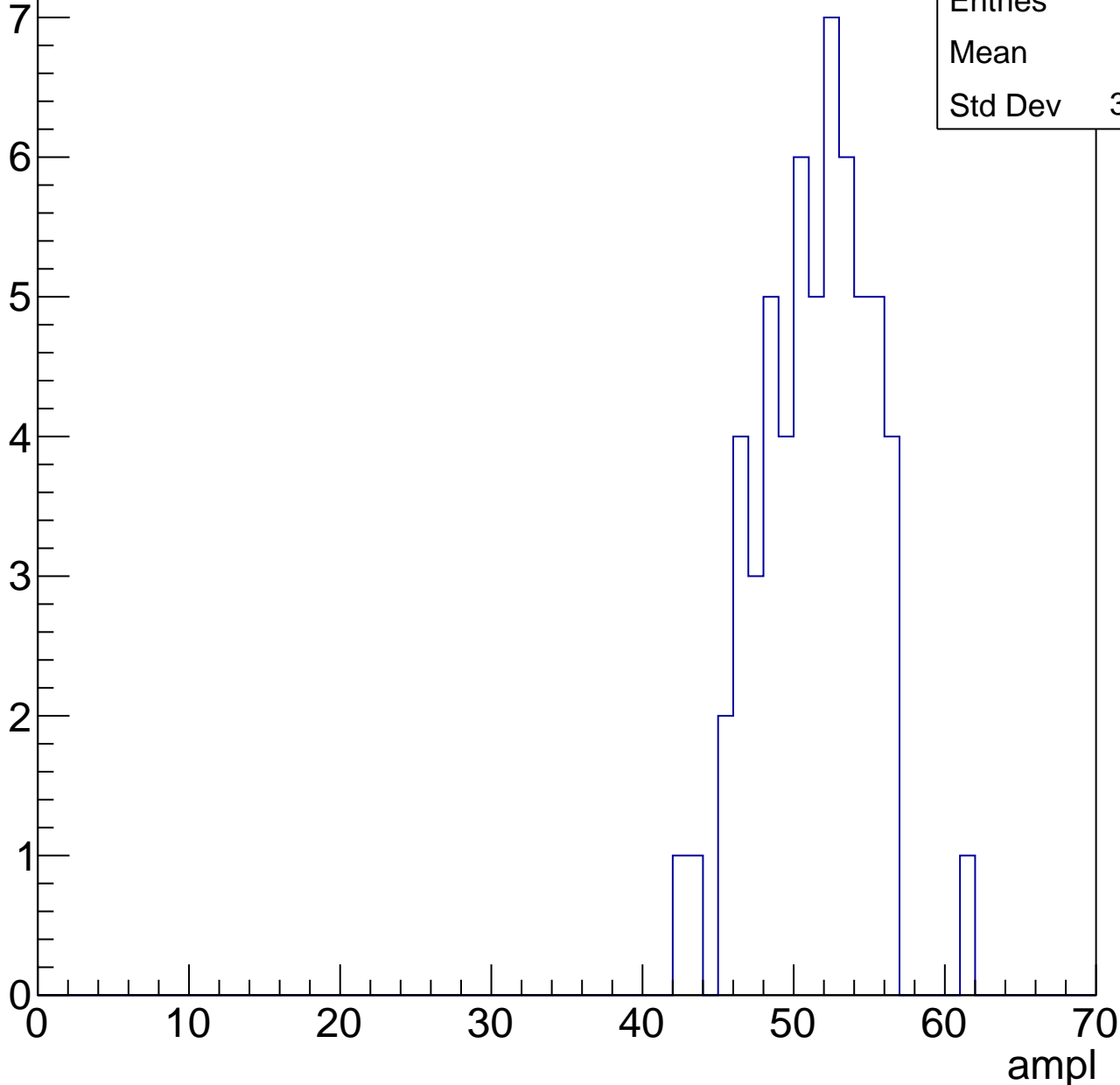
70

B1L103S, U10-ch93, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	50.9
Std Dev	3.653

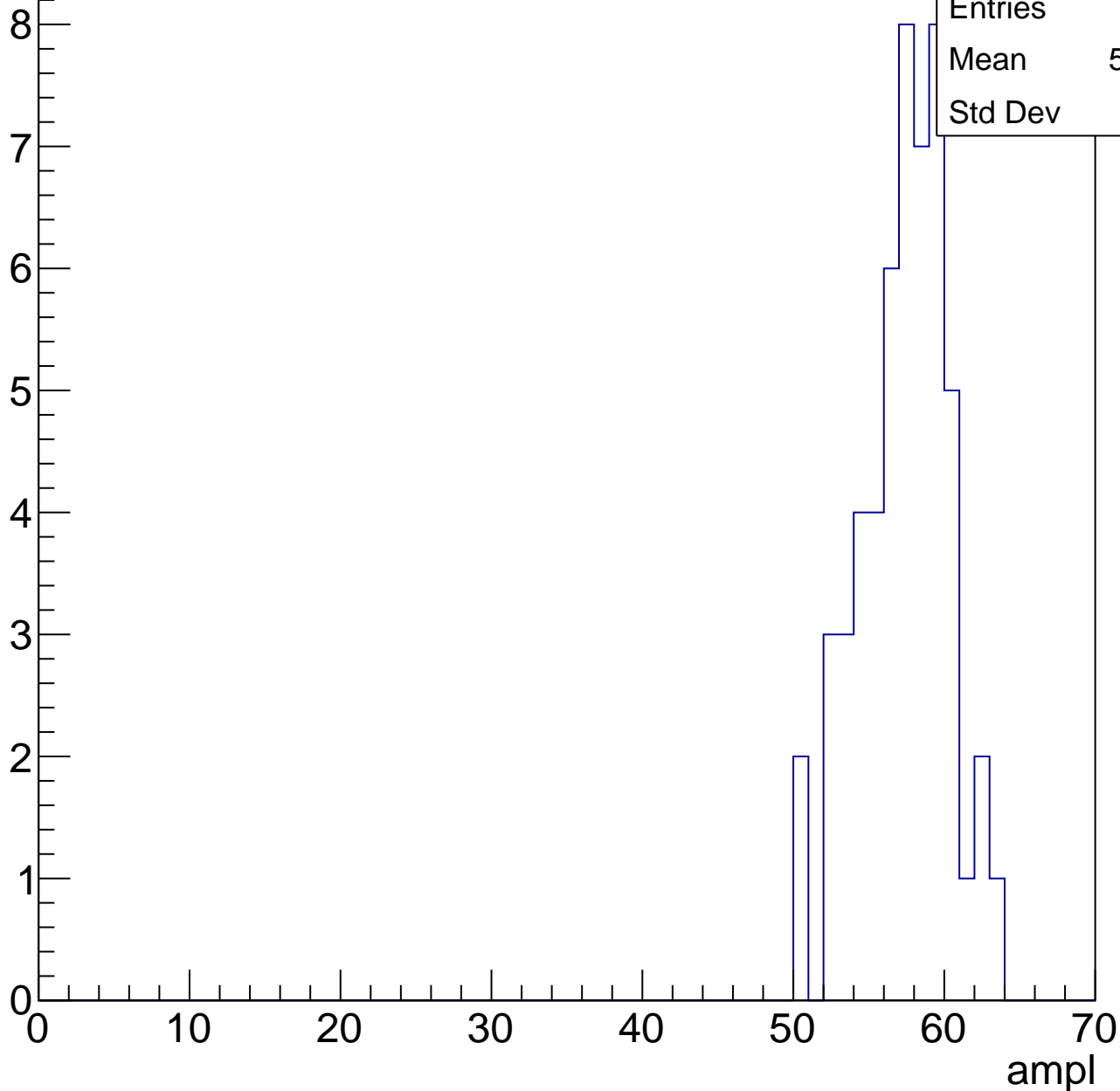


B1L103S, U10-ch93, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	56.83
Std Dev	2.93

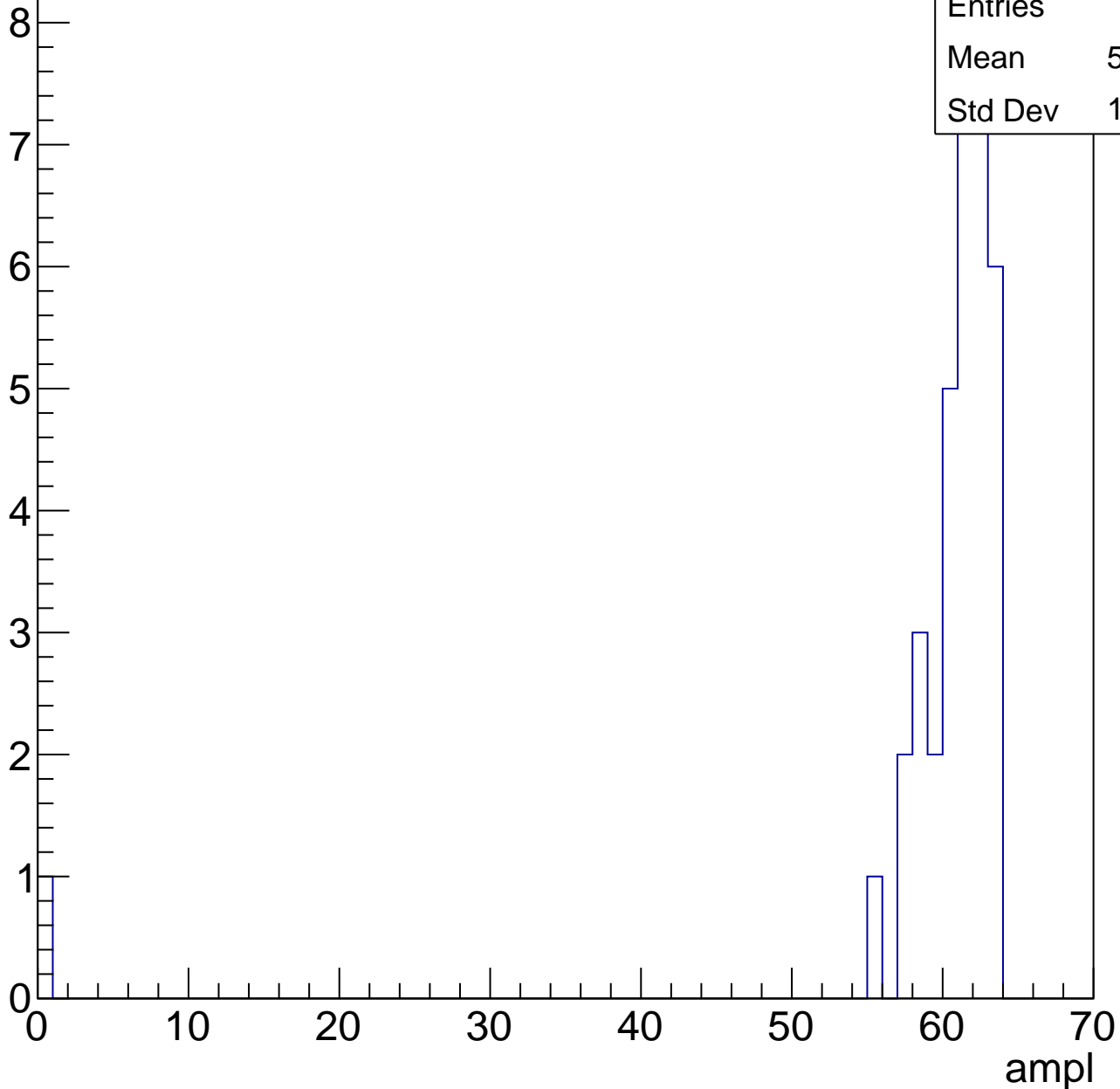


B1L103S, U10-ch93, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	36
Mean	58.97
Std Dev	10.16



B1L103S, U10-ch93, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch93, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch94, adc0

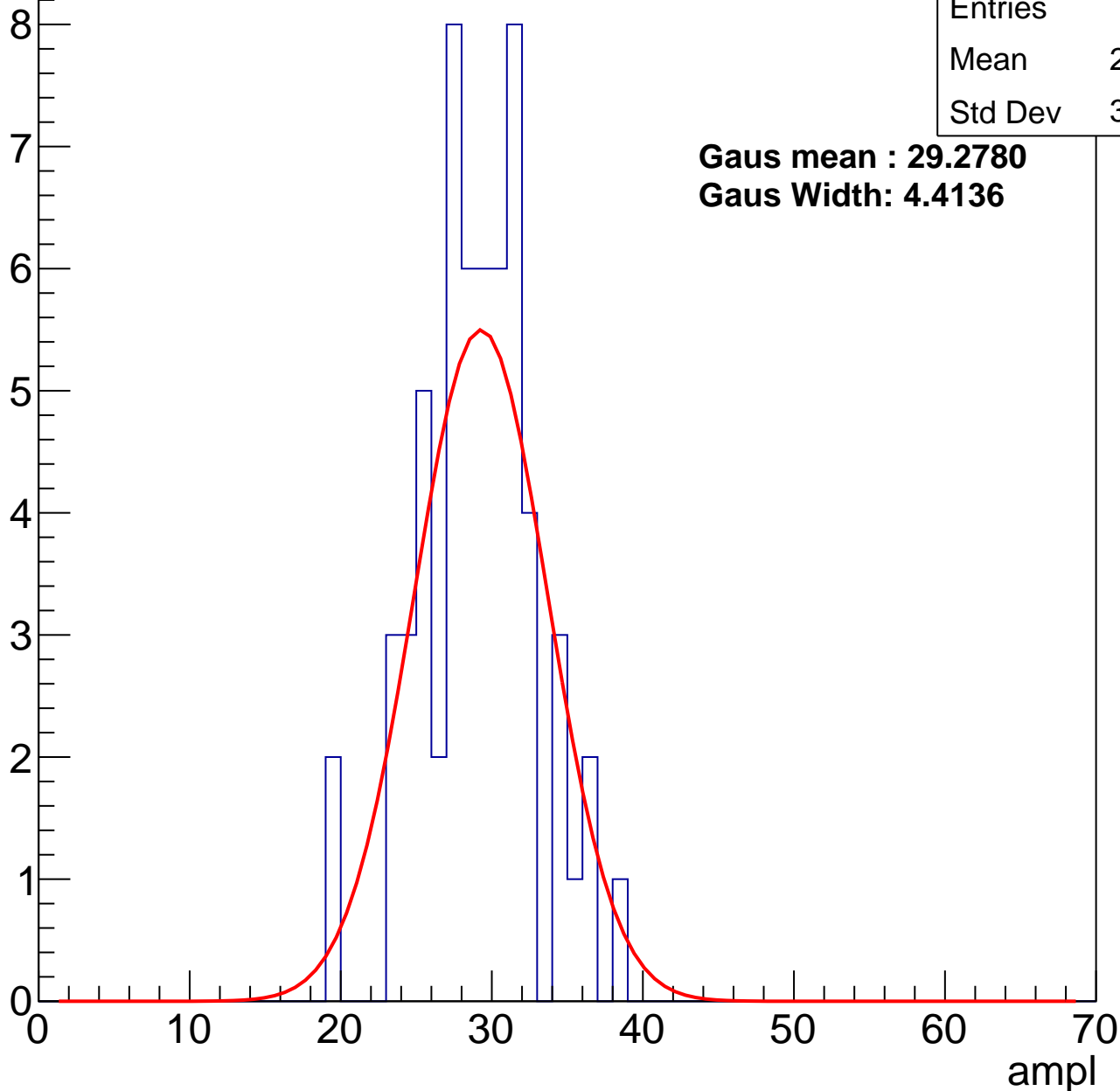
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	28.62
Std Dev	3.817

Gaus mean : 29.2780

Gaus Width: 4.4136



B1L103S, U10-ch94, adc1

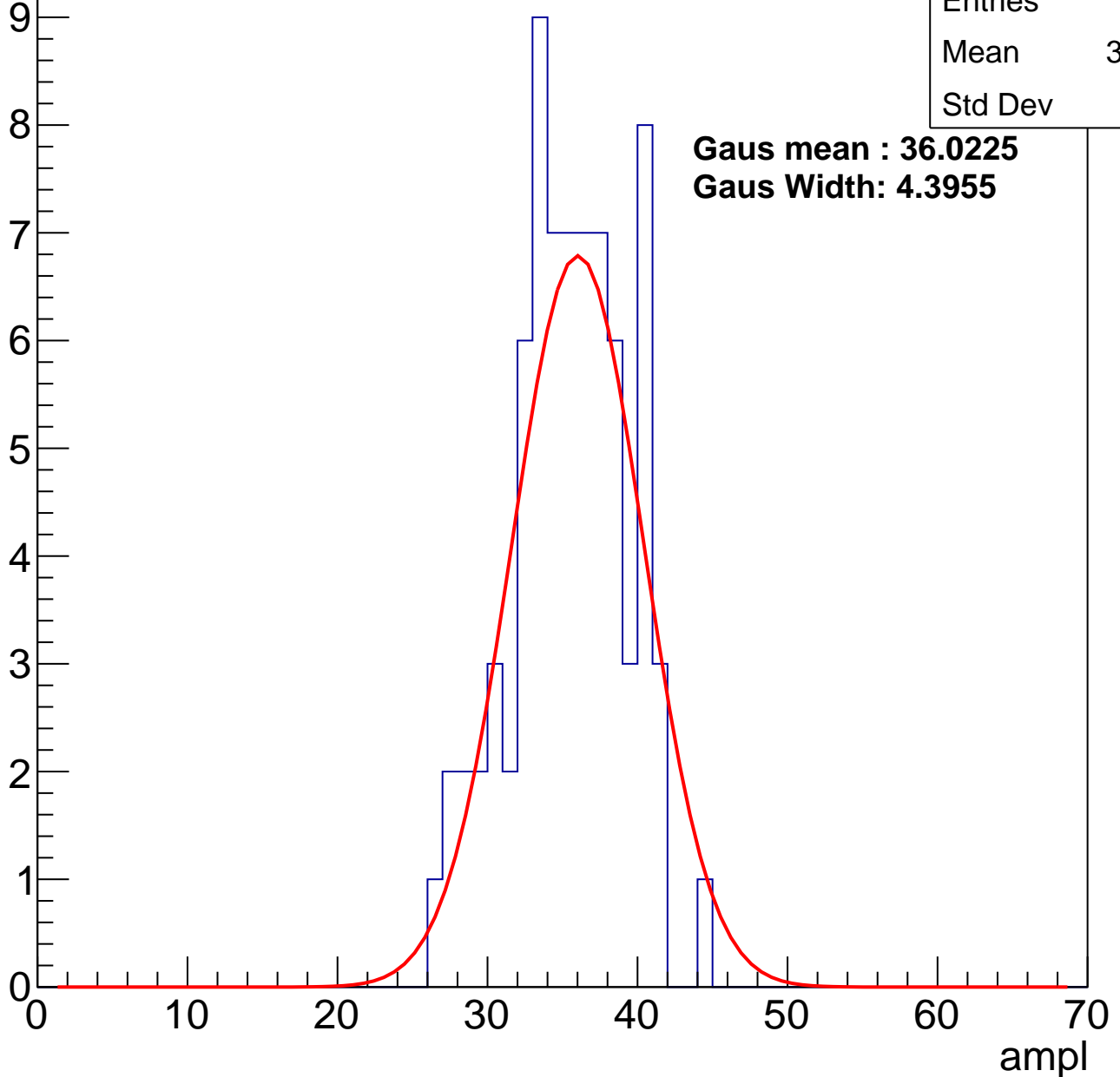
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	35.01
Std Dev	3.82

Gaus mean : 36.0225

Gaus Width: 4.3955



B1L103S, U10-ch94, adc2

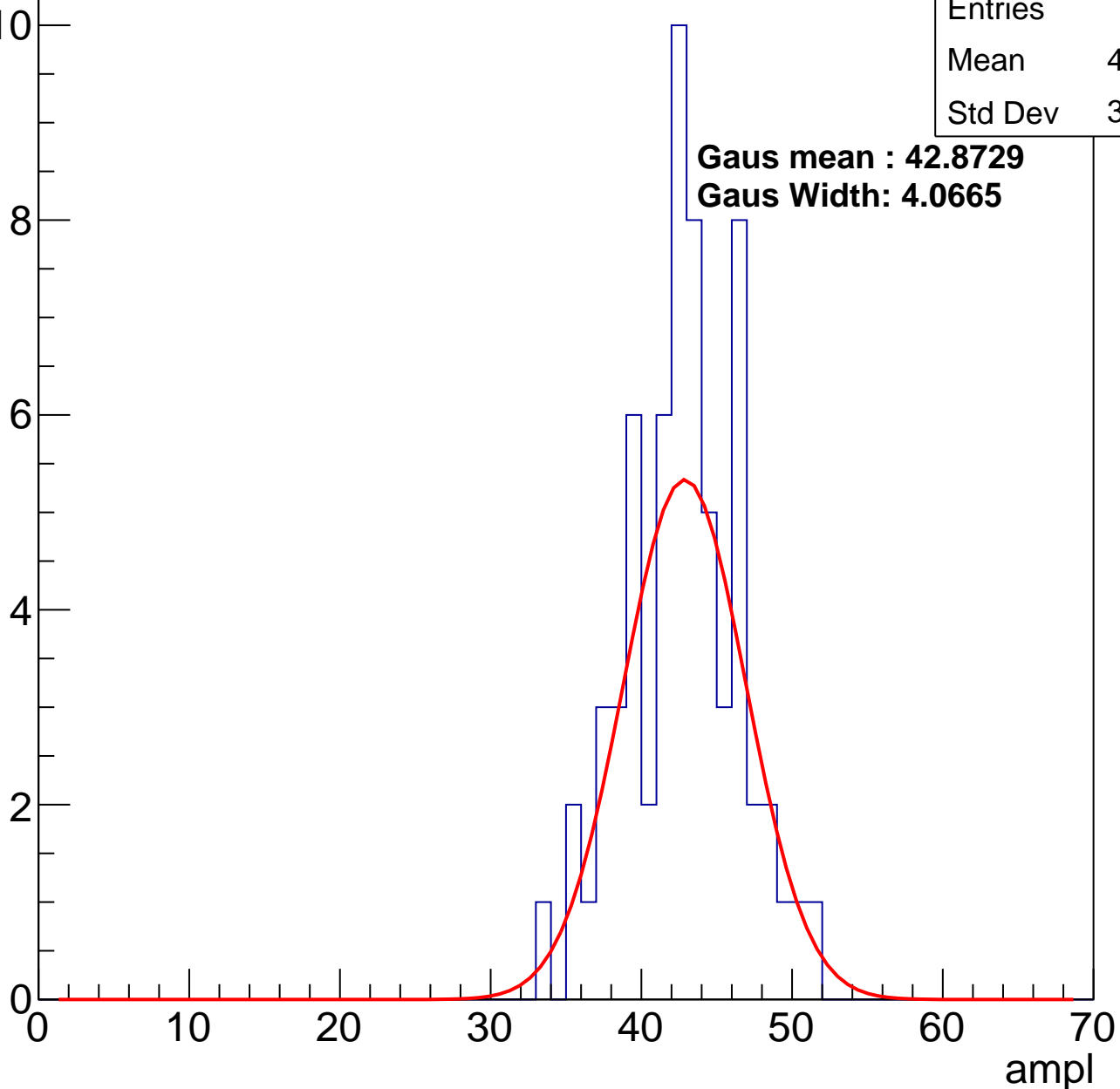
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	42.32
Std Dev	3.717

Gaus mean : 42.8729

Gaus Width: 4.0665

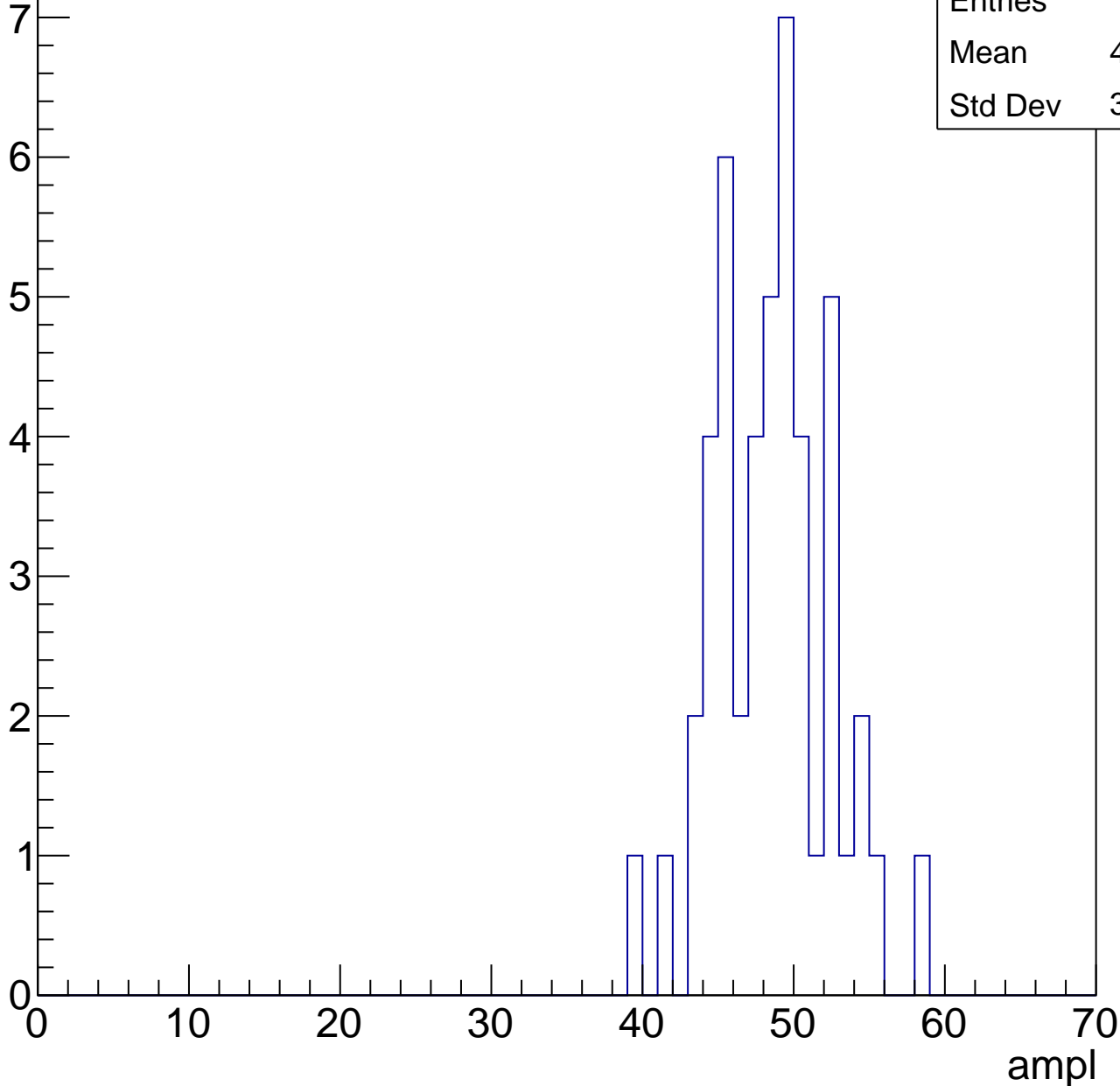


B1L103S, U10-ch94, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	48.09
Std Dev	3.769

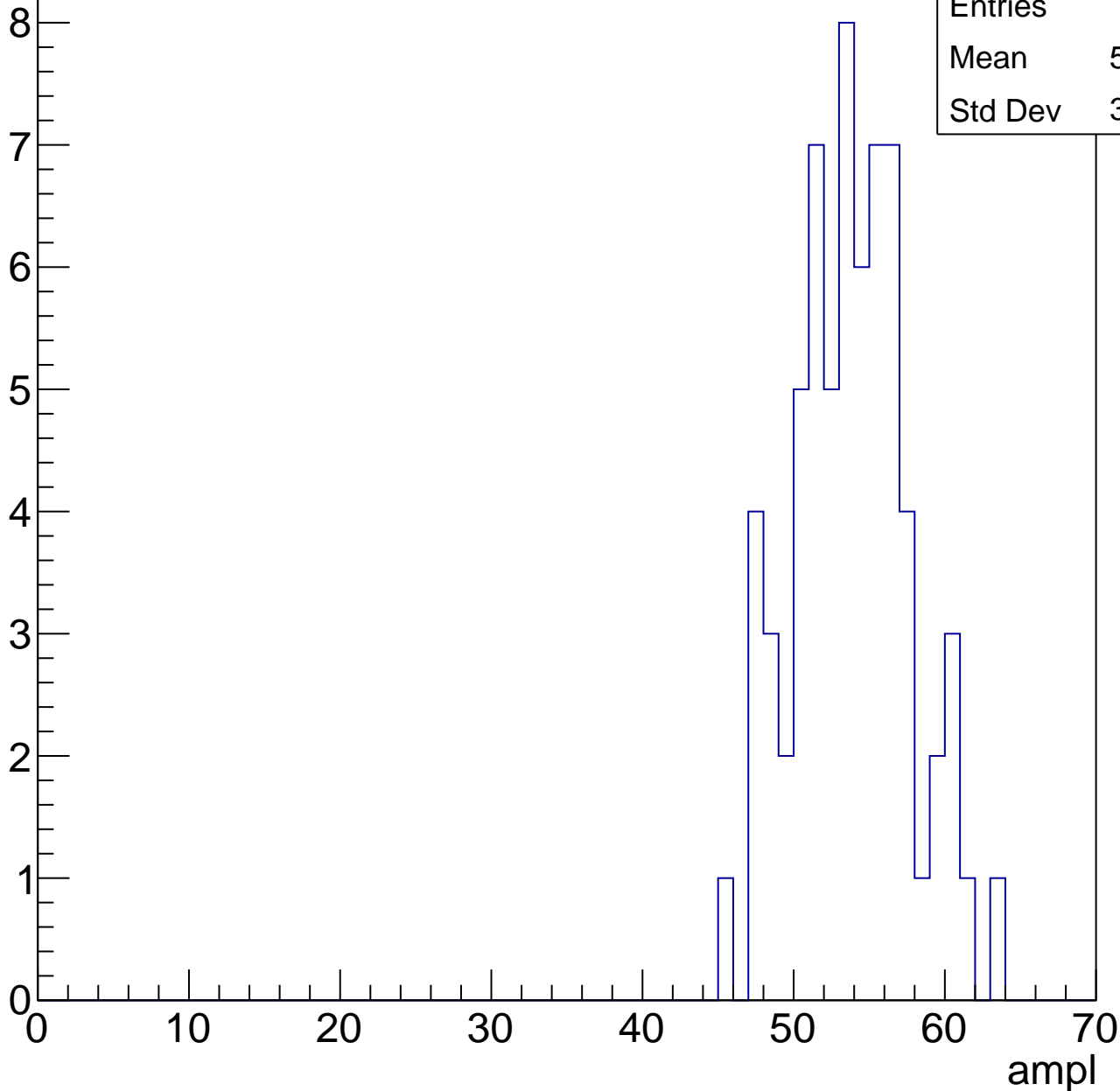


B1L103S, U10-ch94, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

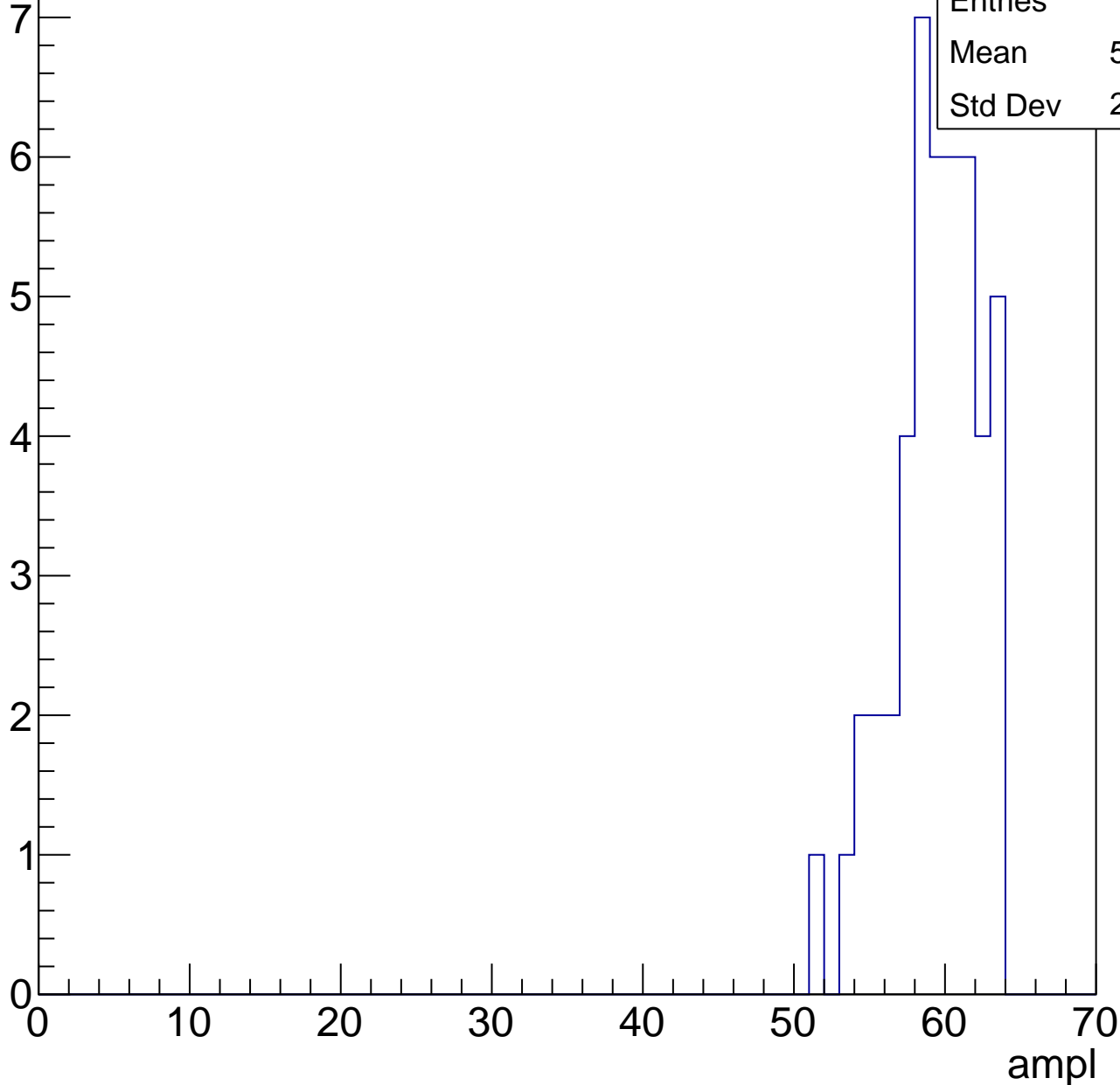
Entries	67
Mean	53.36
Std Dev	3.772



B1L103S, U10-ch94, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

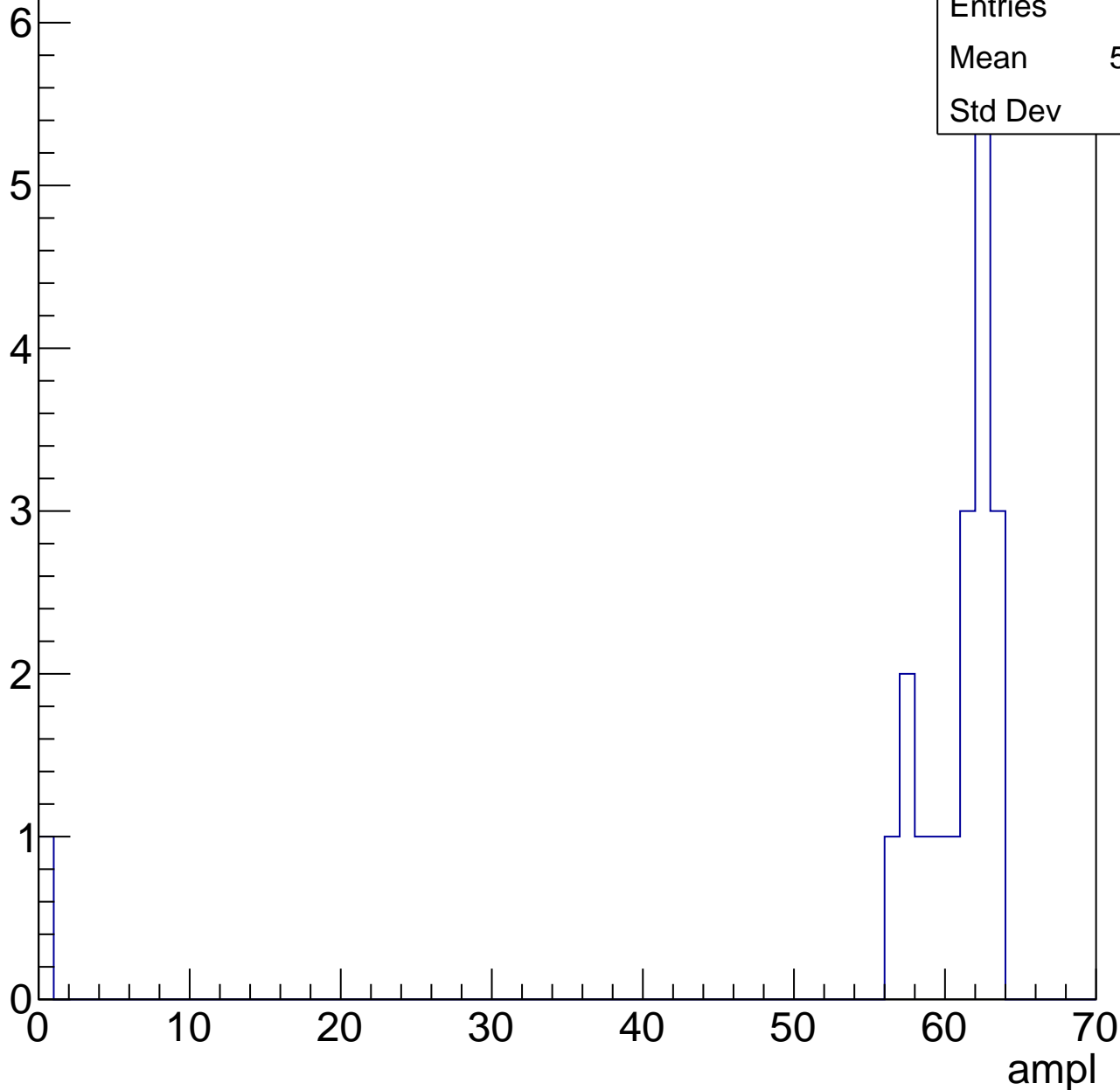


B1L103S, U10-ch94, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

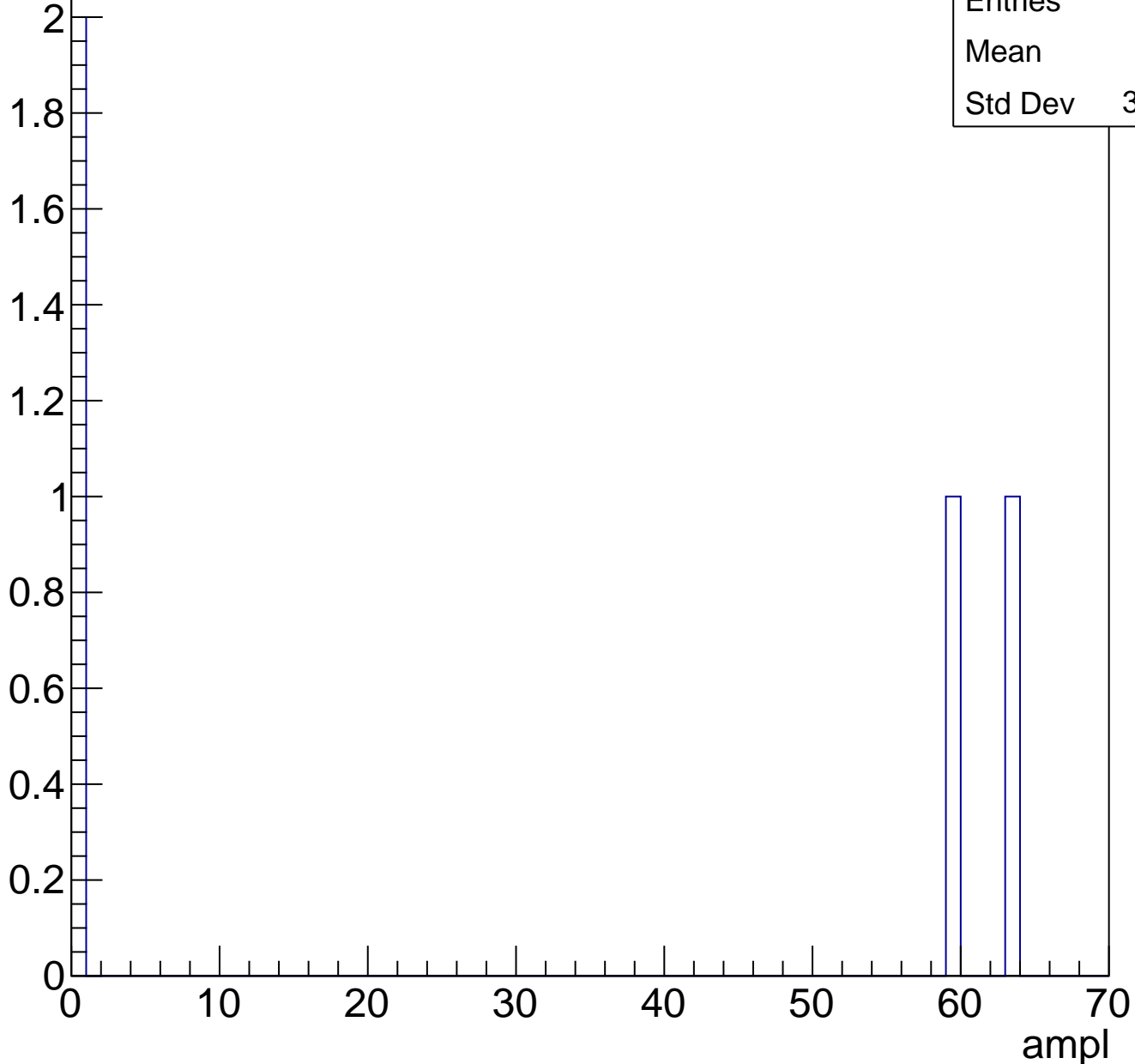
Entries	19
Mean	57.42
Std Dev	13.7



B1L103S, U10-ch94, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	4
Mean	30.5
Std Dev	30.53

B1L103S, U10-ch95, adc0

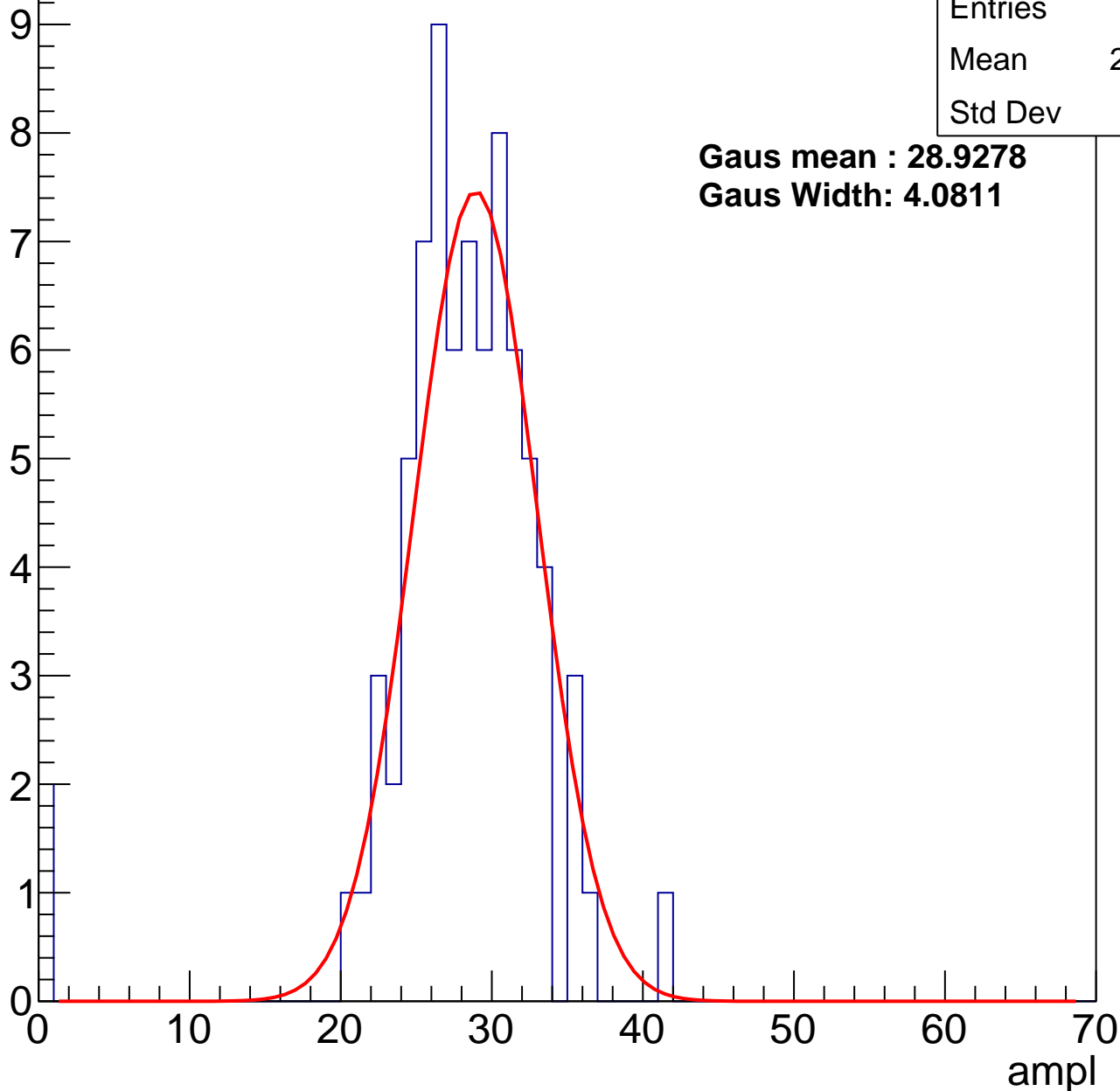
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	27.45
Std Dev	5.87

Gaus mean : 28.9278

Gaus Width: 4.0811



B1L103S, U10-ch95, adc1

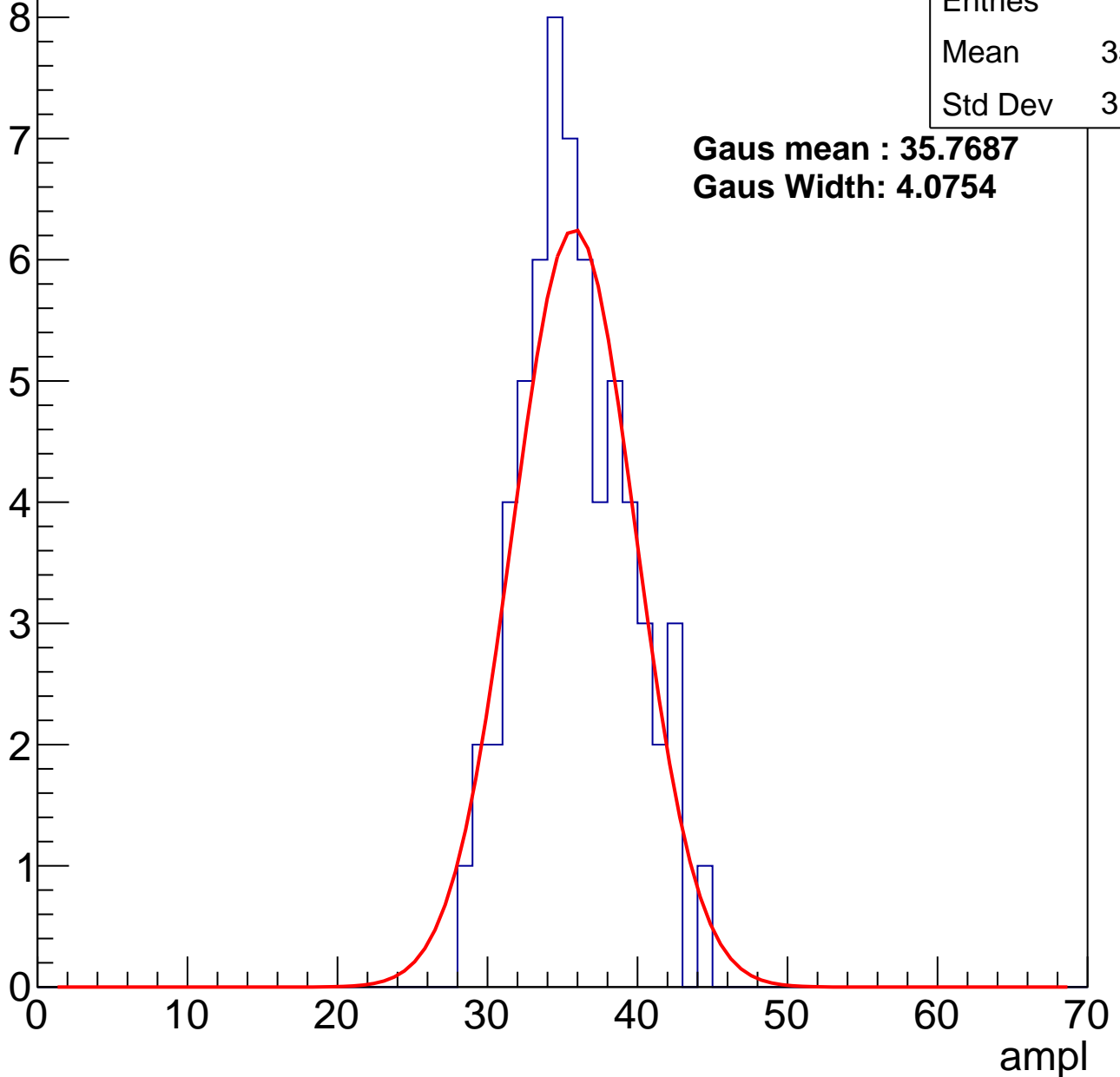
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	35.35
Std Dev	3.586

Gaus mean : 35.7687

Gaus Width: 4.0754



B1L103S, U10-ch95, adc2

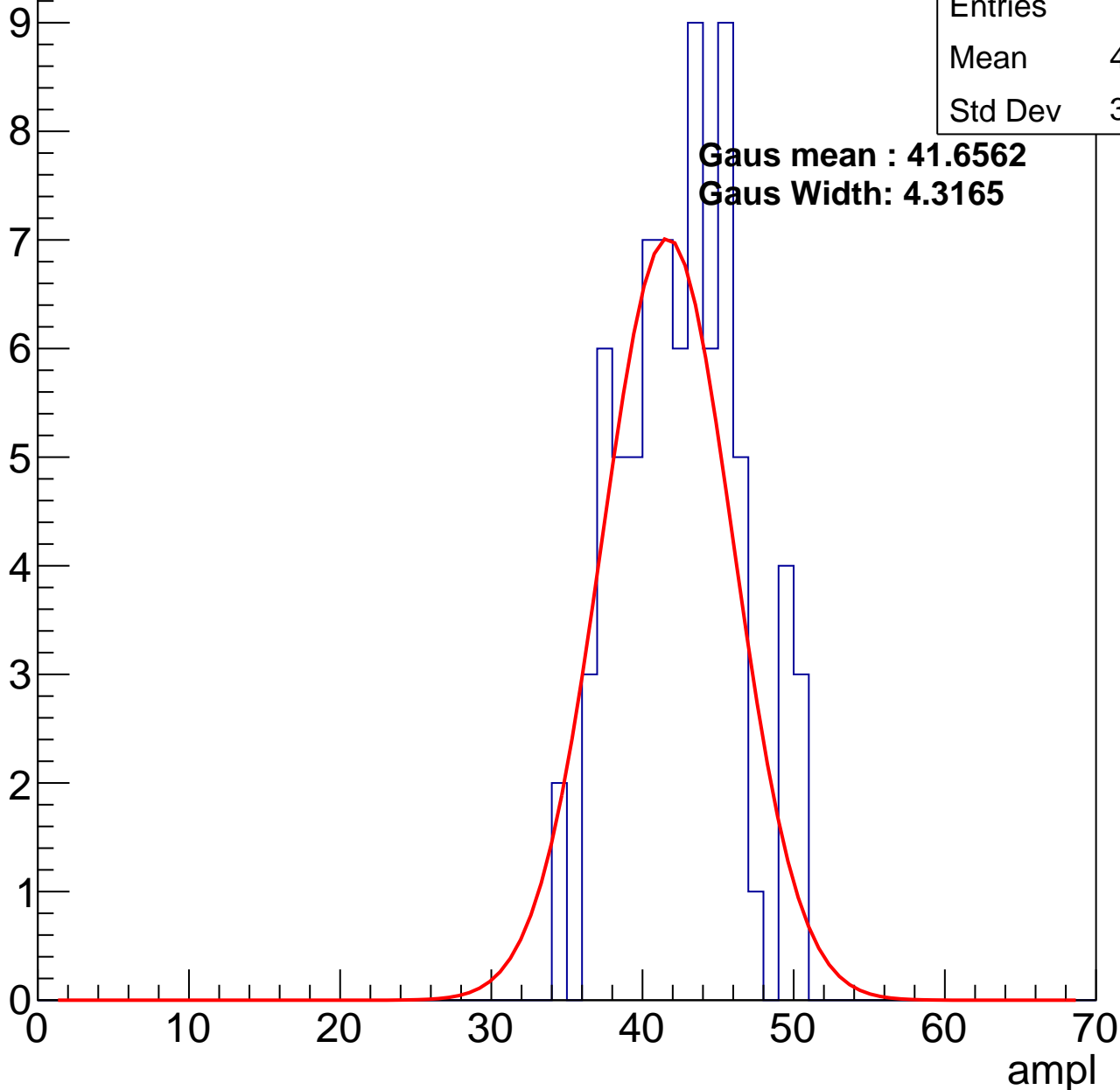
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	42.06
Std Dev	3.844

Gaus mean : 41.6562

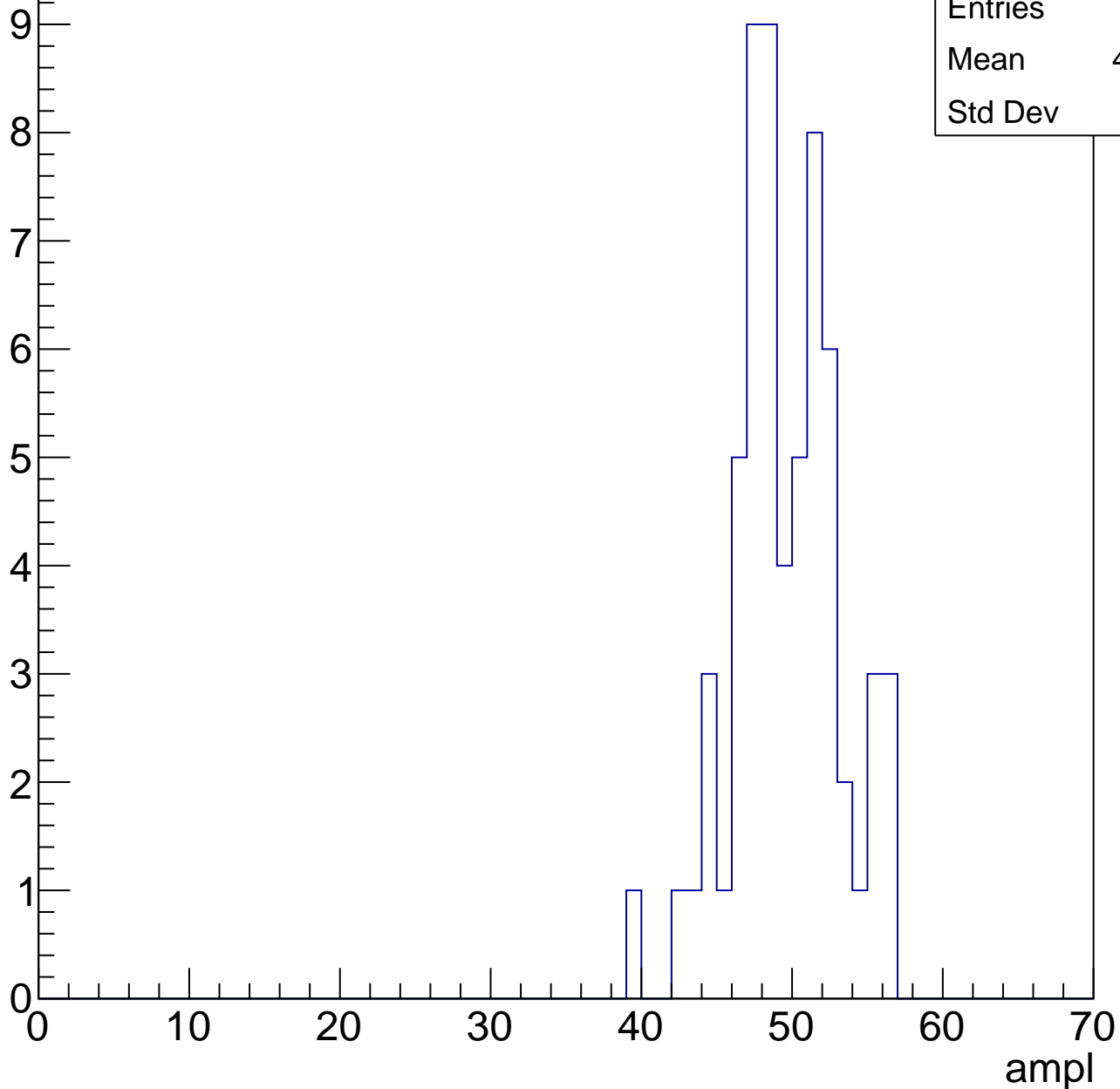
Gaus Width: 4.3165



B1L103S, U10-ch95, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry



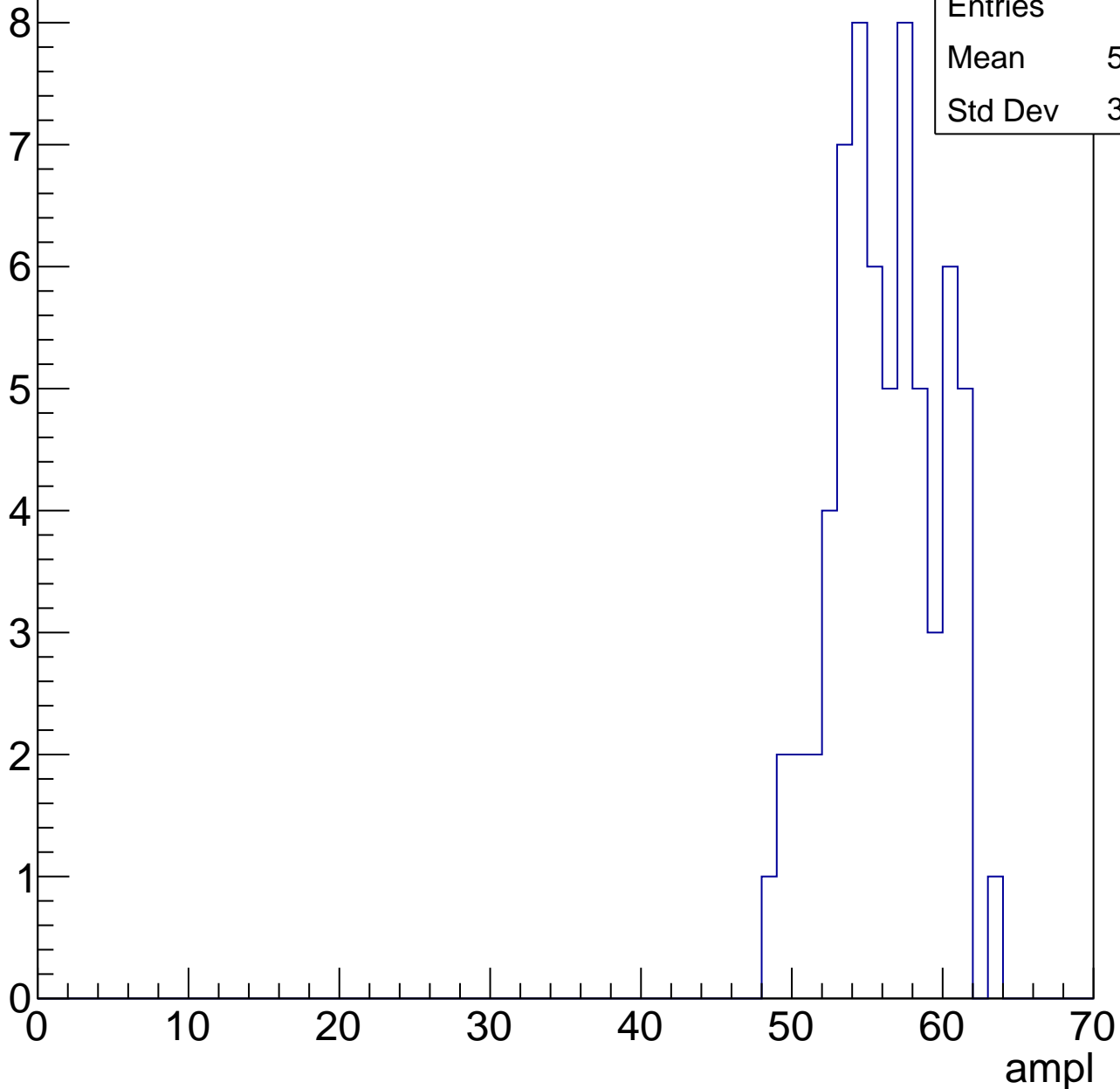
Entries	62
Mean	49.11
Std Dev	3.52

B1L103S, U10-ch95, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	55.69
Std Dev	3.437

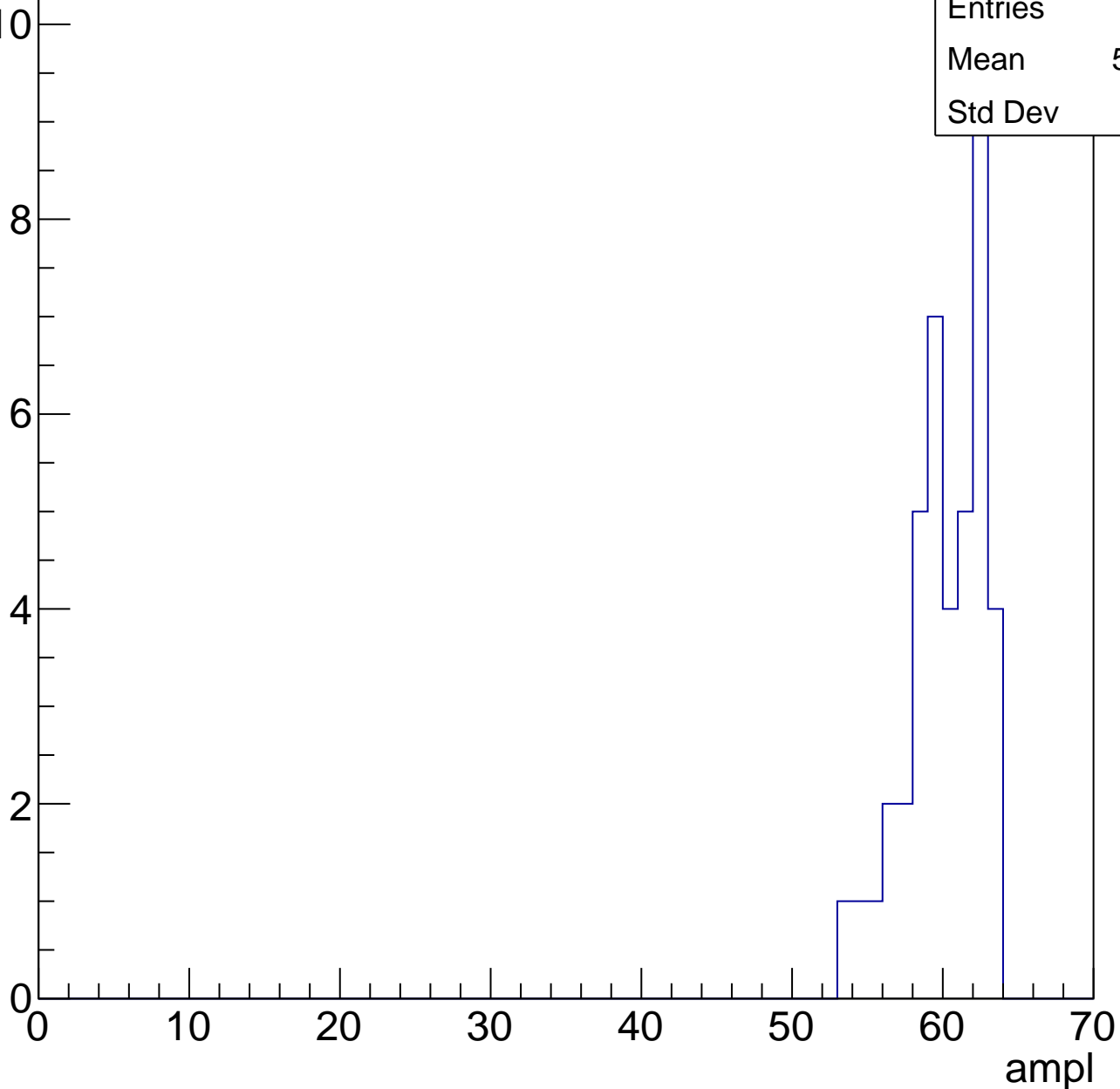


B1L103S, U10-ch95, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	59.71
Std Dev	2.51

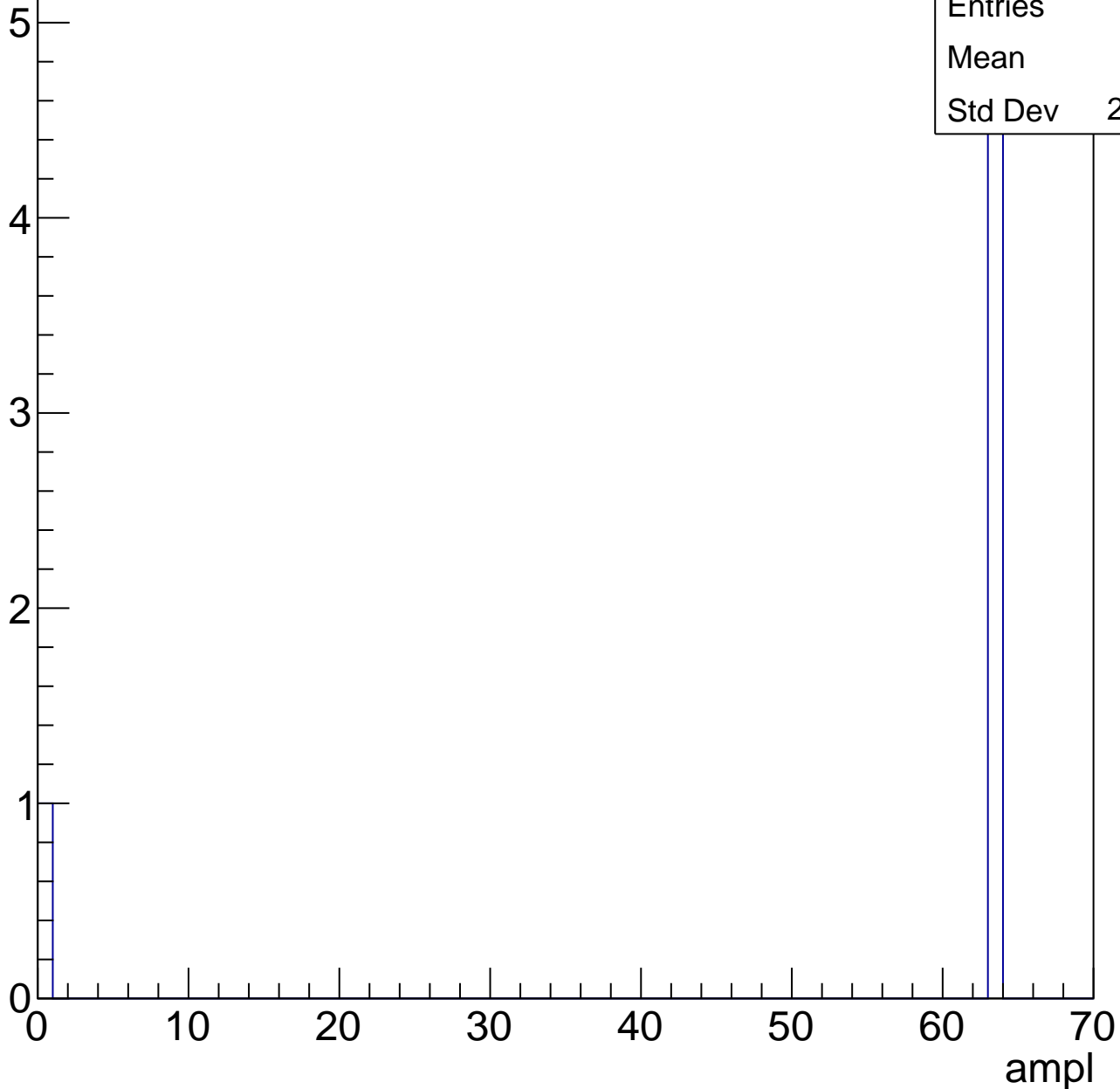


B1L103S, U10-ch95, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	6
Mean	52.5
Std Dev	23.48



B1L103S, U10-ch95, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch96, adc0

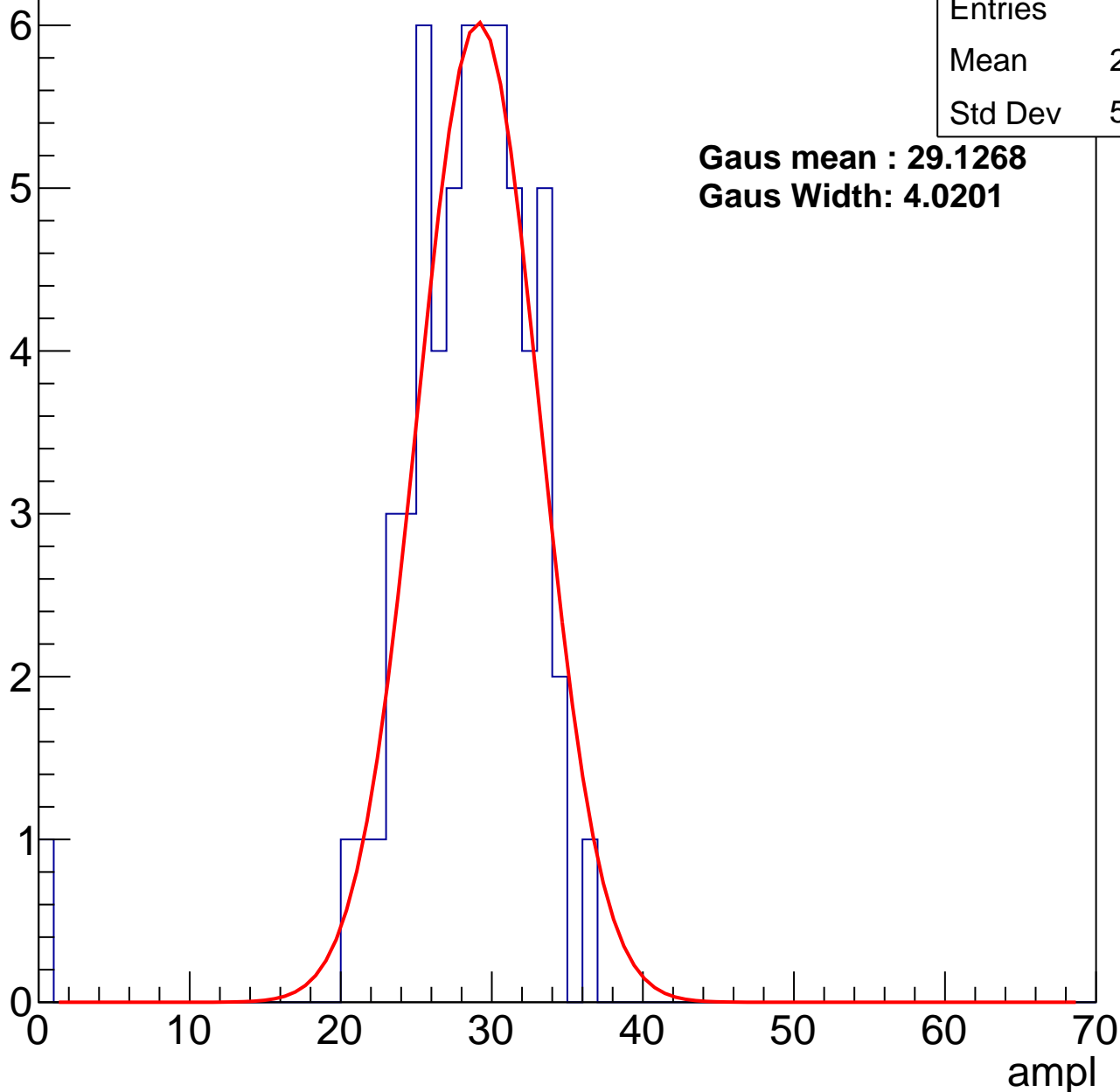
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	27.78
Std Dev	5.047

Gaus mean : 29.1268

Gaus Width: 4.0201



B1L103S, U10-ch96, adc1

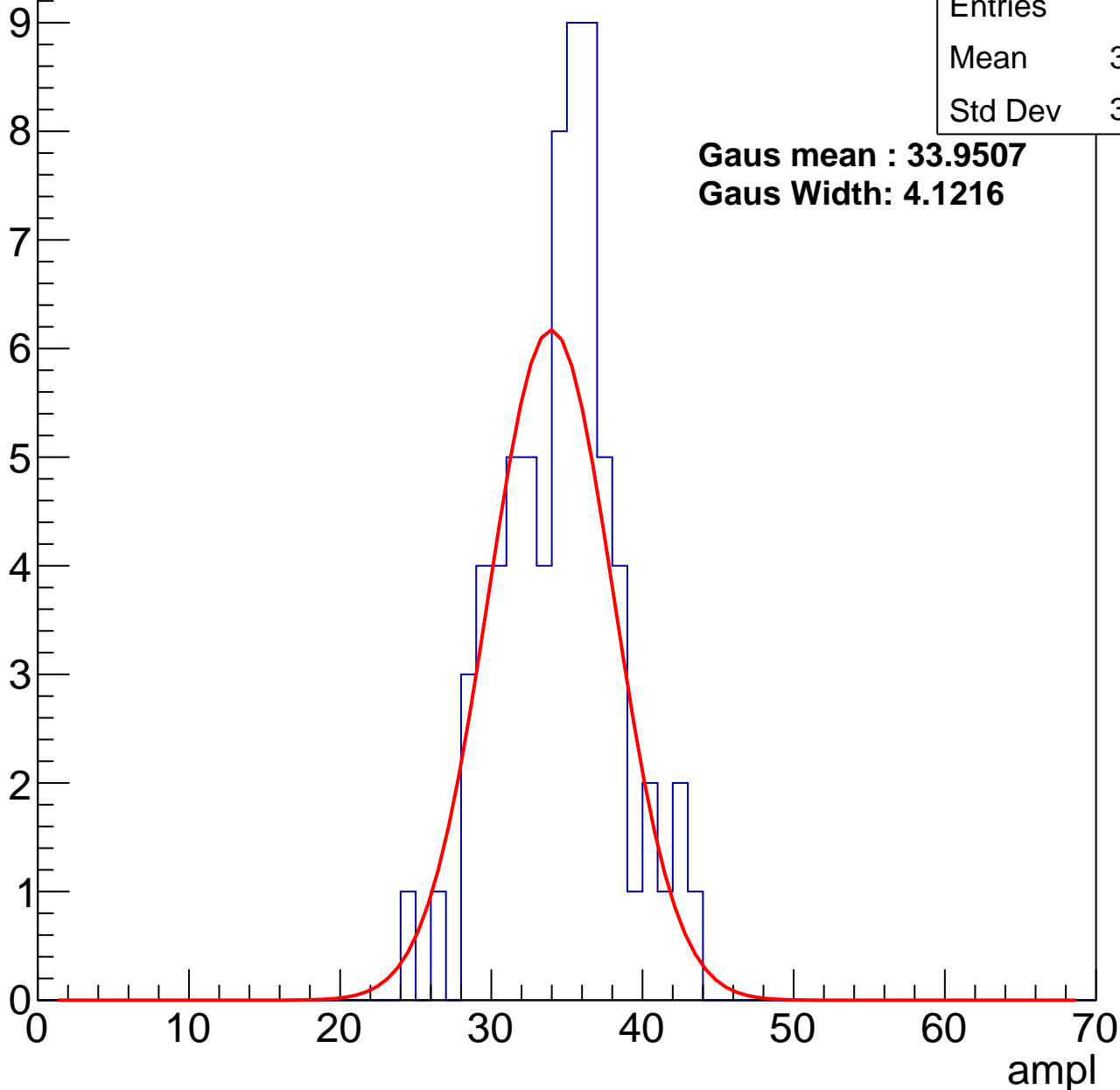
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	34.09
Std Dev	3.833

Gaus mean : 33.9507

Gaus Width: 4.1216



B1L103S, U10-ch96, adc2

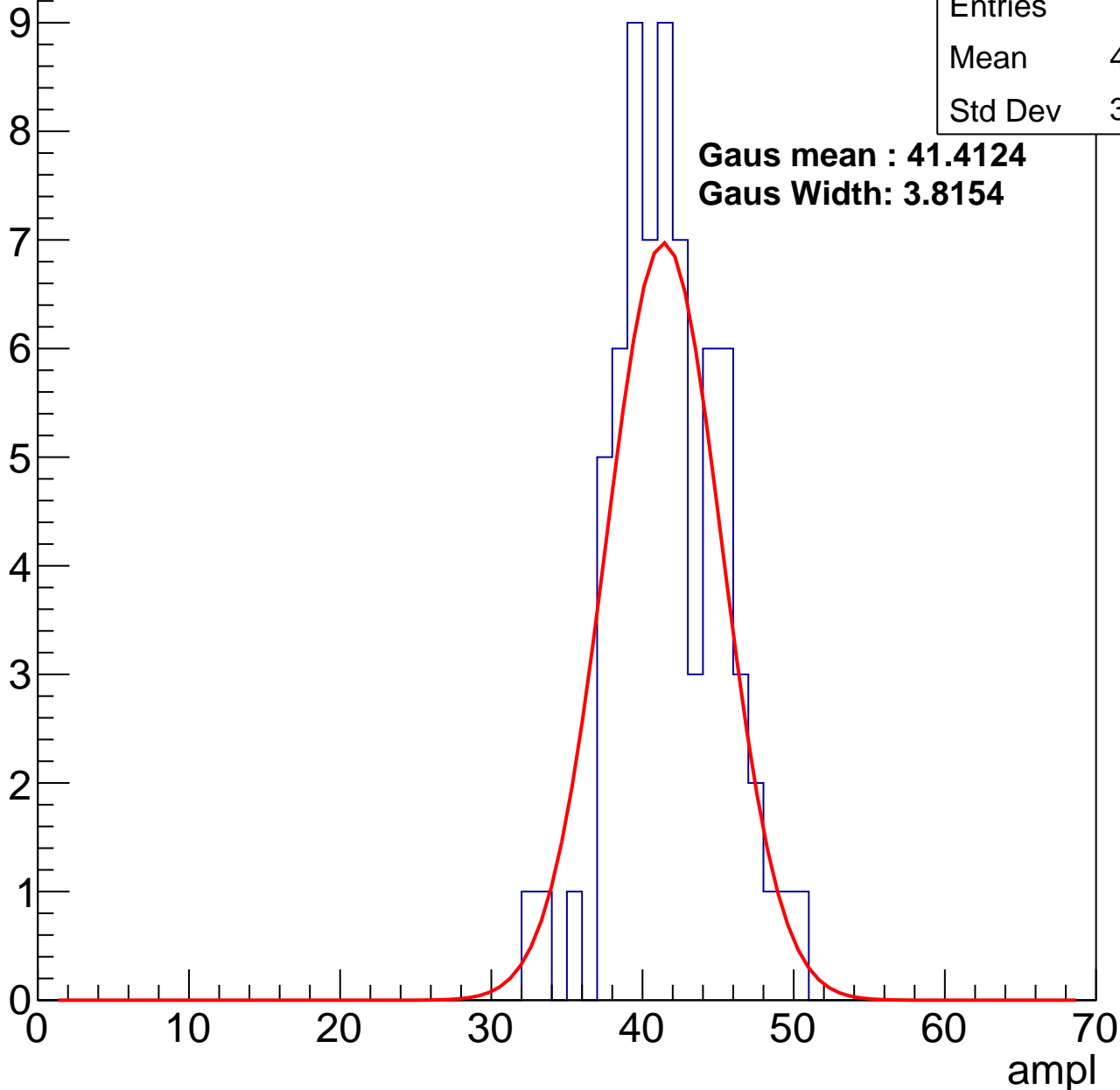
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	41.29
Std Dev	3.535

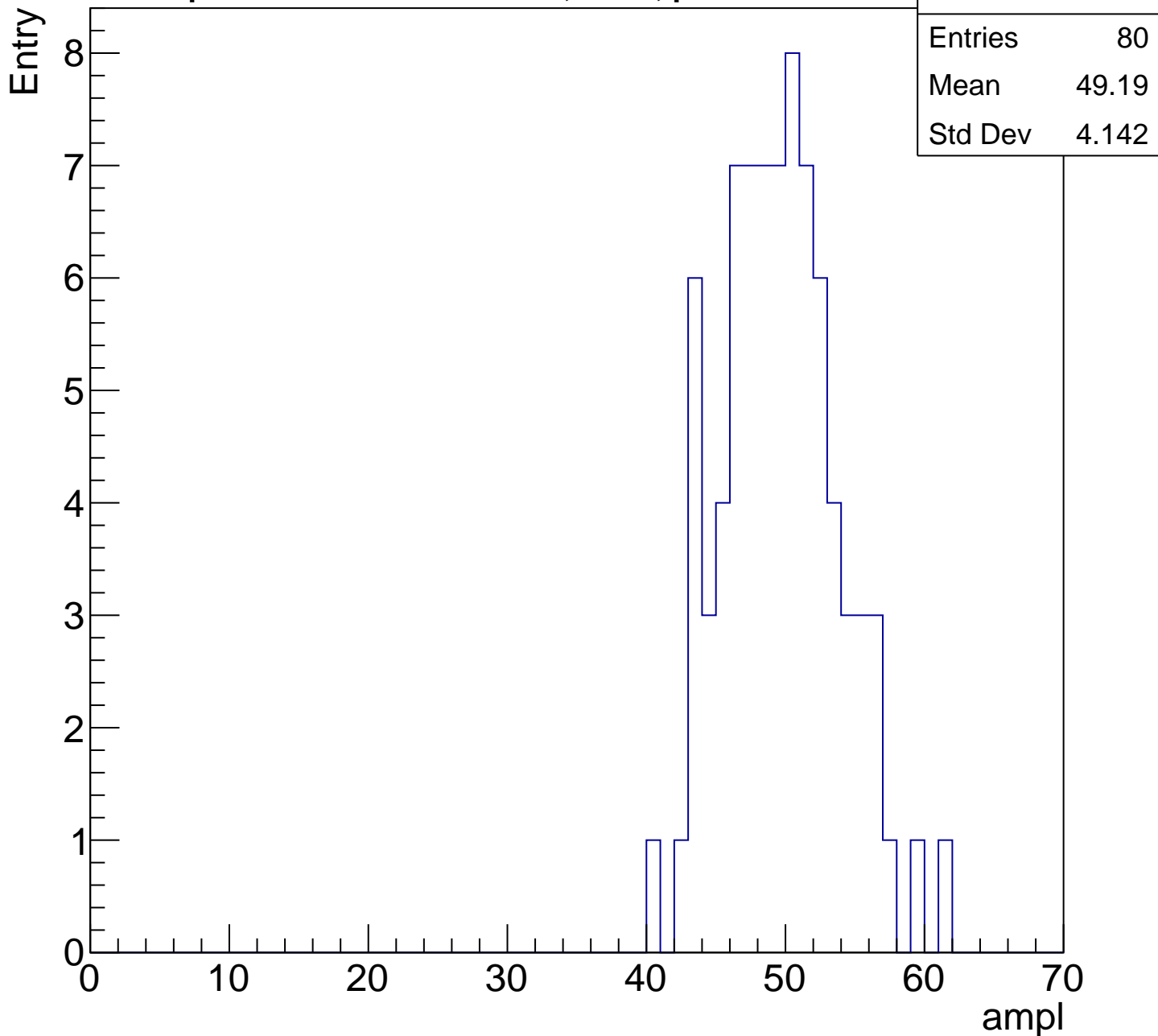
Gaus mean : 41.4124

Gaus Width: 3.8154



B1L103S, U10-ch96, adc3

calib_packv5_042523_0143.root, FC#7, port C2

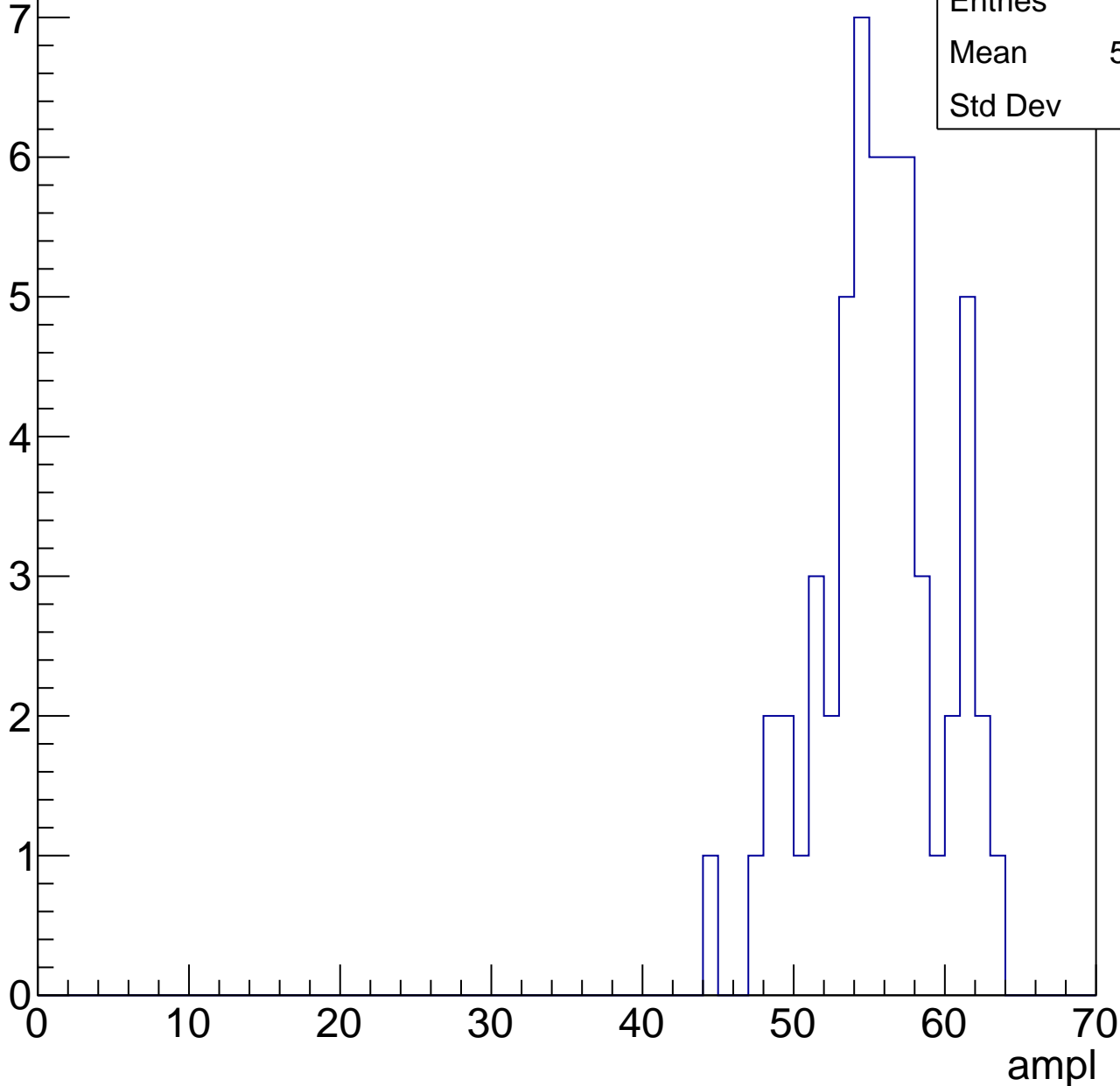


B1L103S, U10-ch96, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

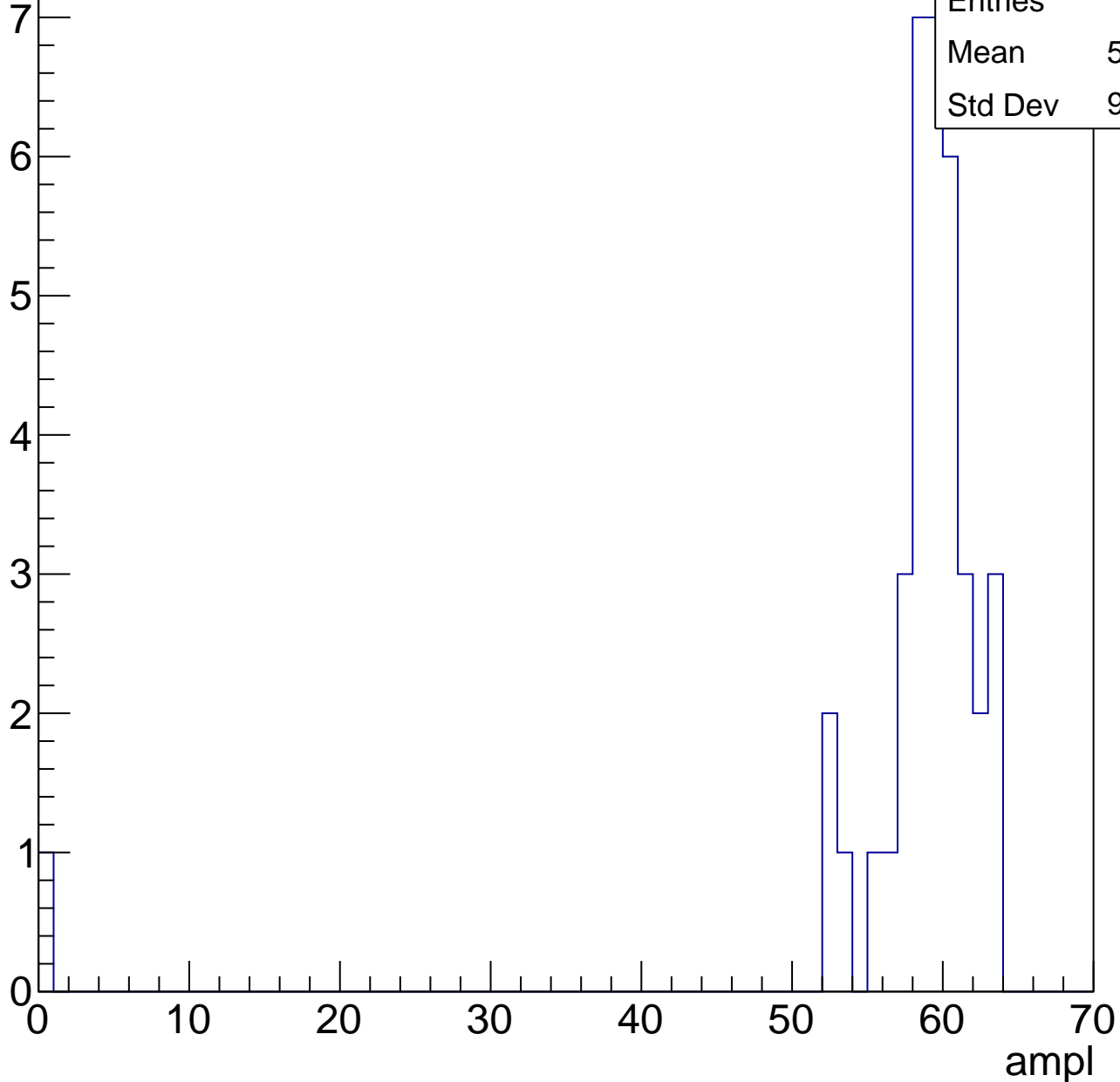
Entries	56
Mean	55.14
Std Dev	4.09



B1L103S, U10-ch96, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

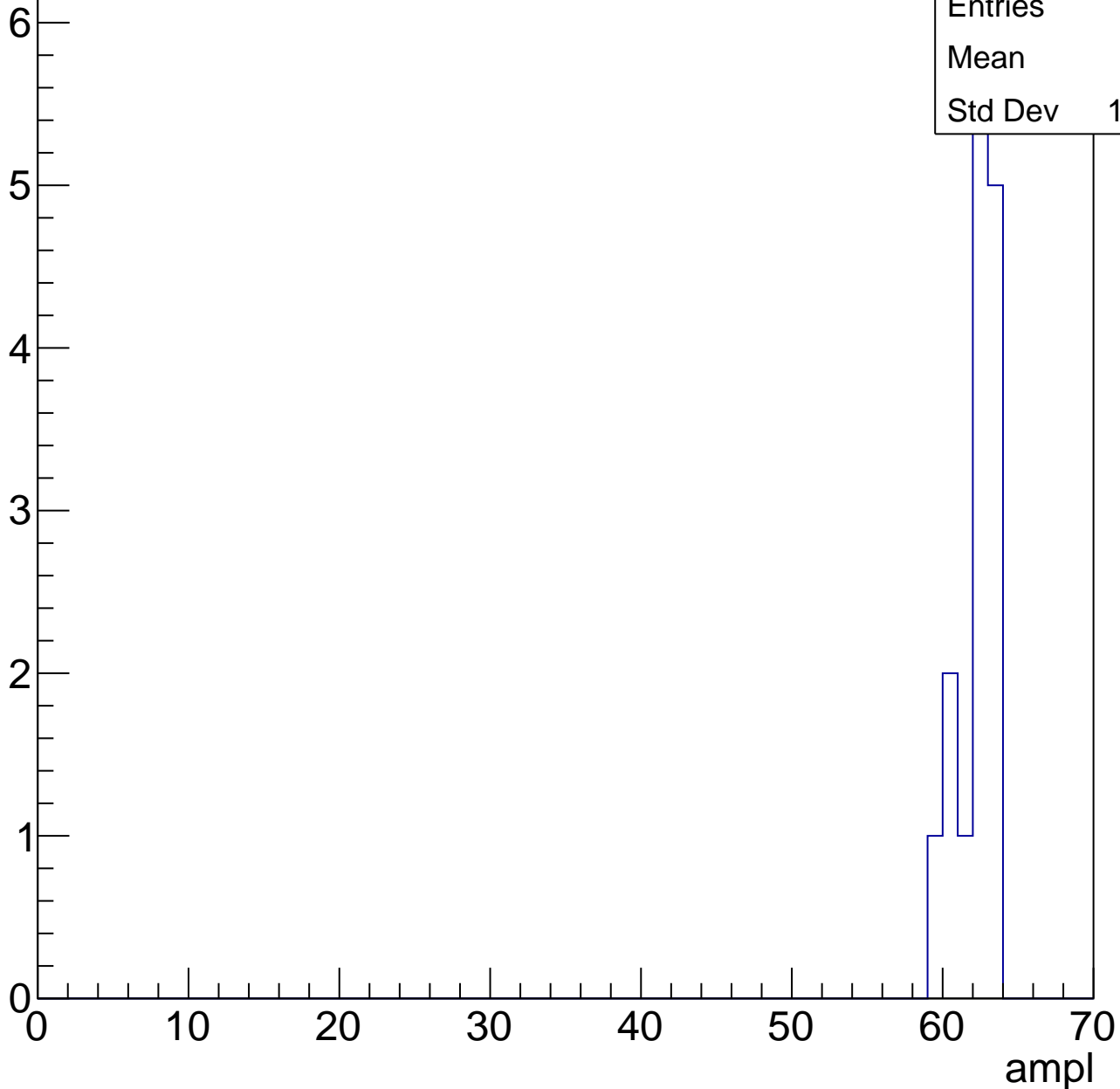


B1L103S, U10-ch96, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.8
Std Dev	1.222



B1L103S, U10-ch96, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch97, adc0

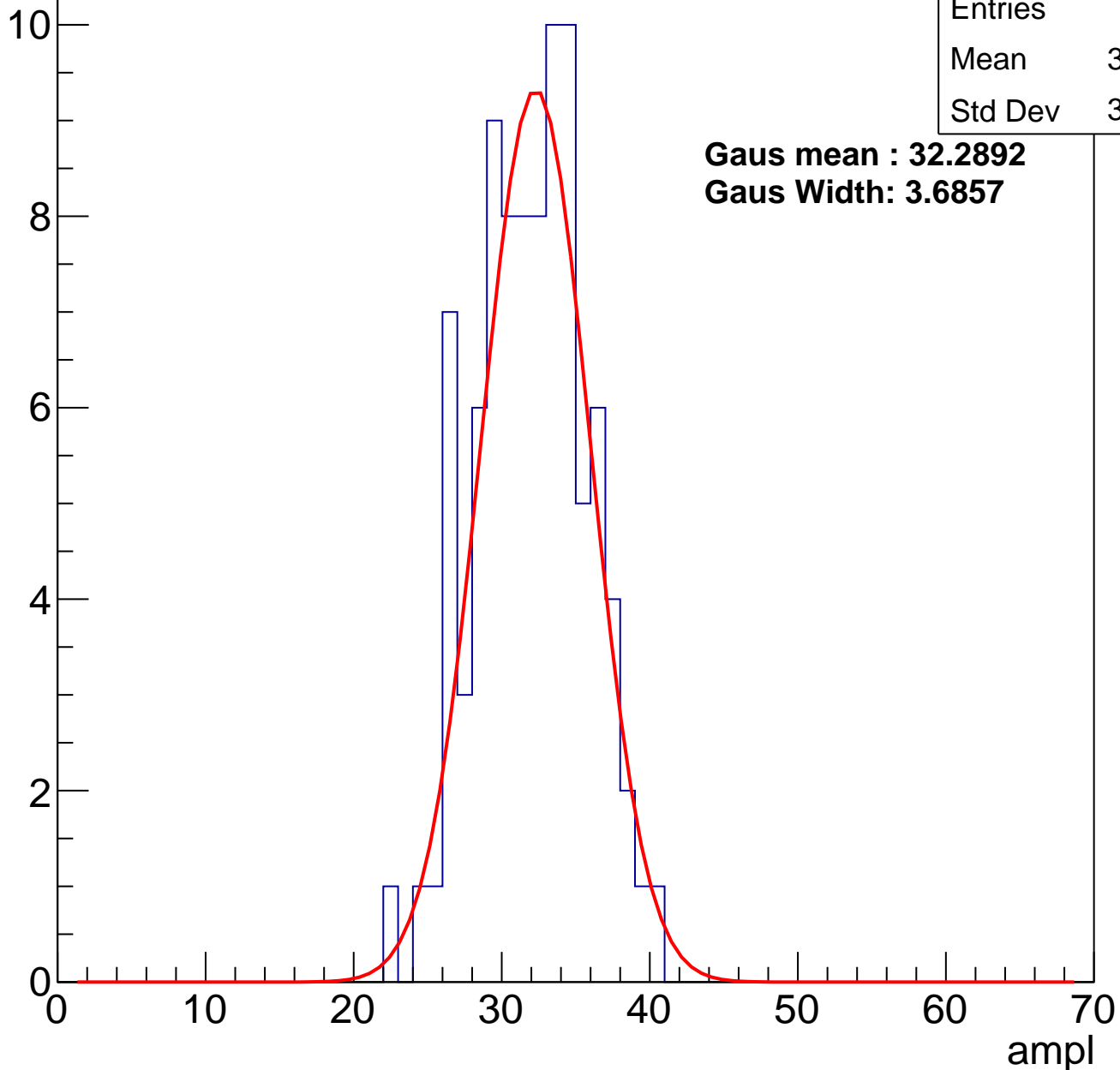
calib_packv5_042523_0143.root, FC#7, port C2

Entries	91
Mean	31.55
Std Dev	3.653

Gaus mean : 32.2892

Gaus Width: 3.6857

Entry



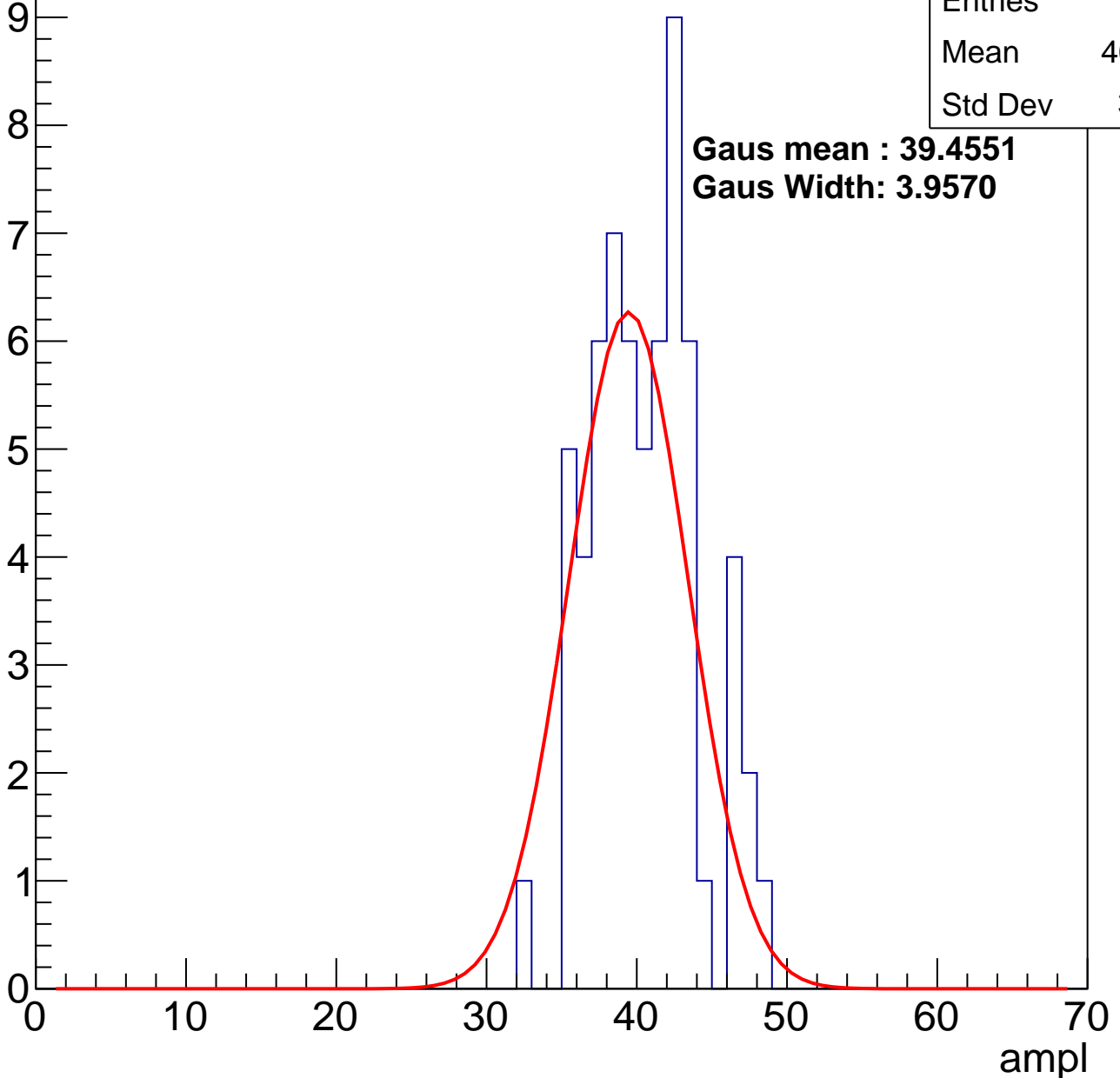
B1L103S, U10-ch97, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	40.08
Std Dev	3.47

Gaus mean : 39.4551
Gaus Width: 3.9570



B1L103S, U10-ch97, adc2

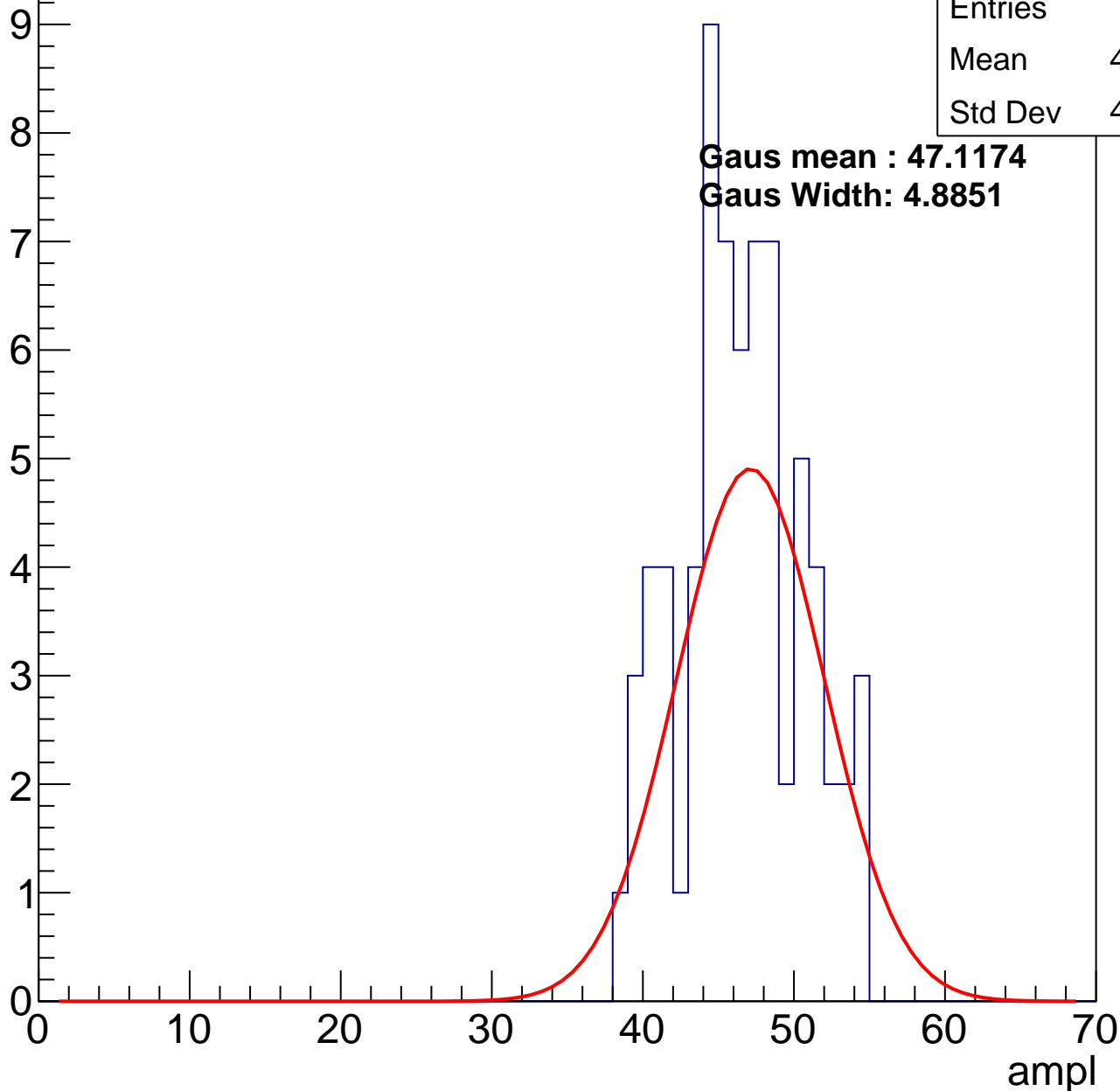
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	46.04
Std Dev	4.016

Gaus mean : 47.1174

Gaus Width: 4.8851

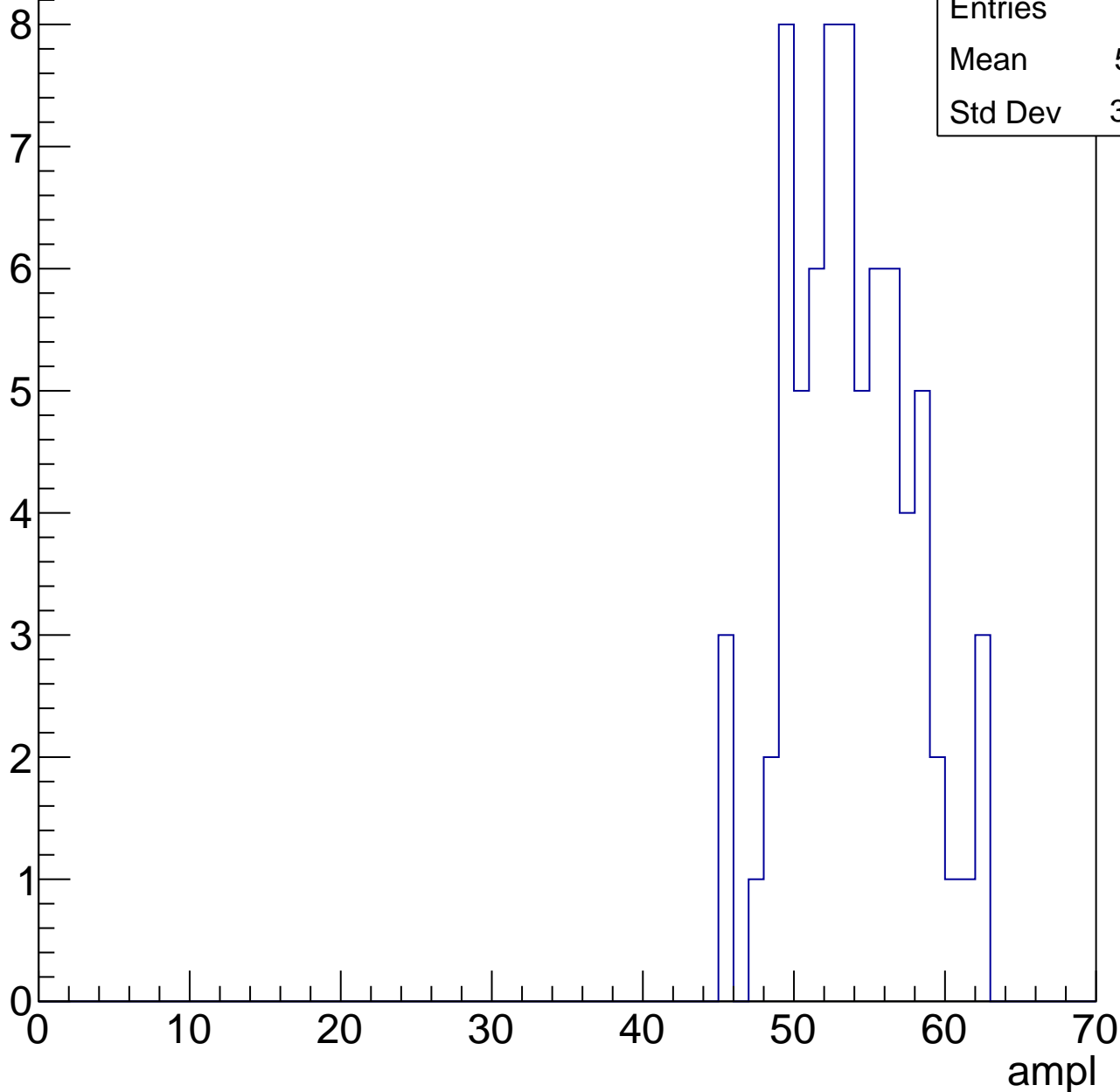


B1L103S, U10-ch97, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

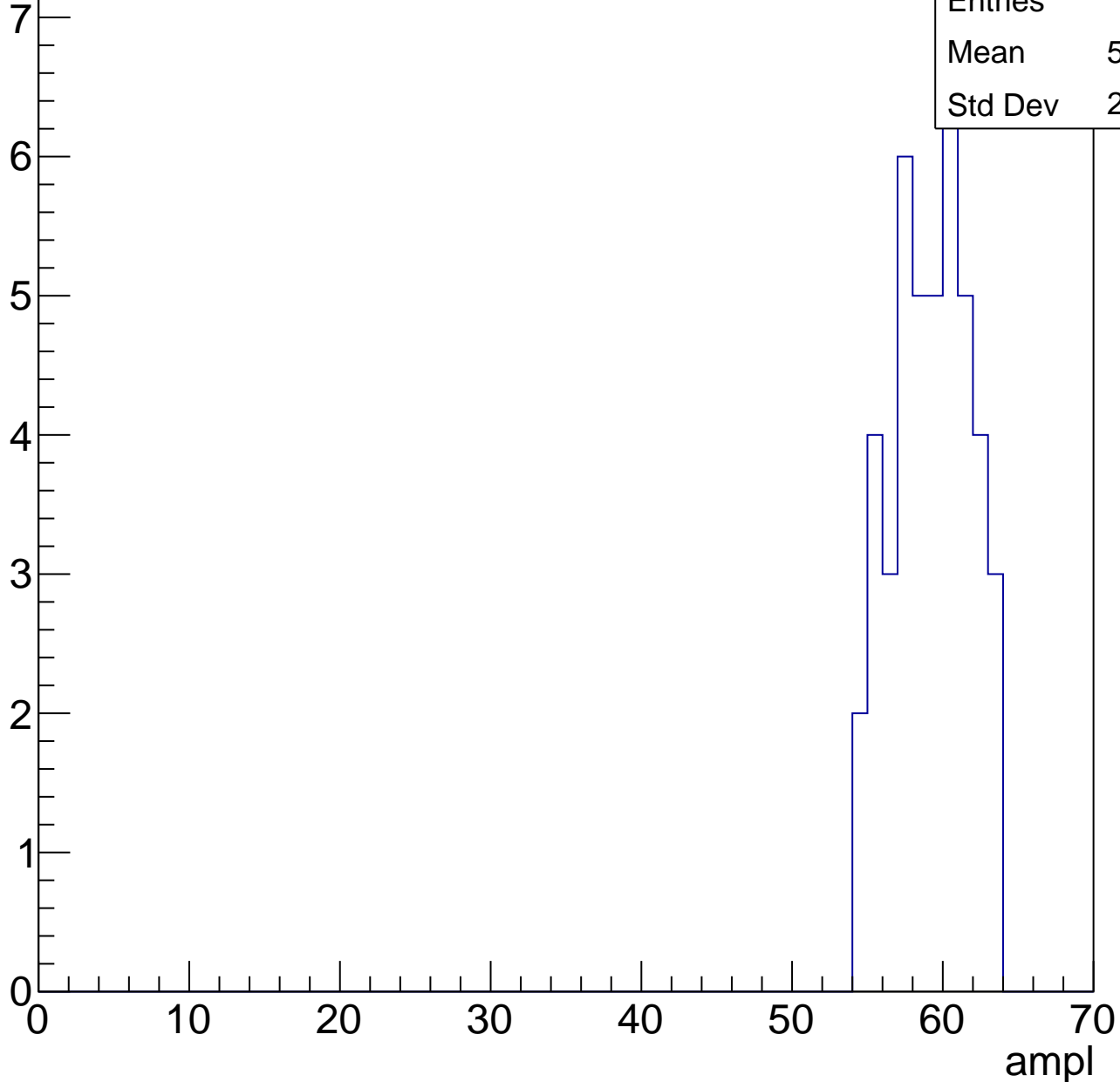
Entries	74
Mean	53.31
Std Dev	3.986



B1L103S, U10-ch97, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

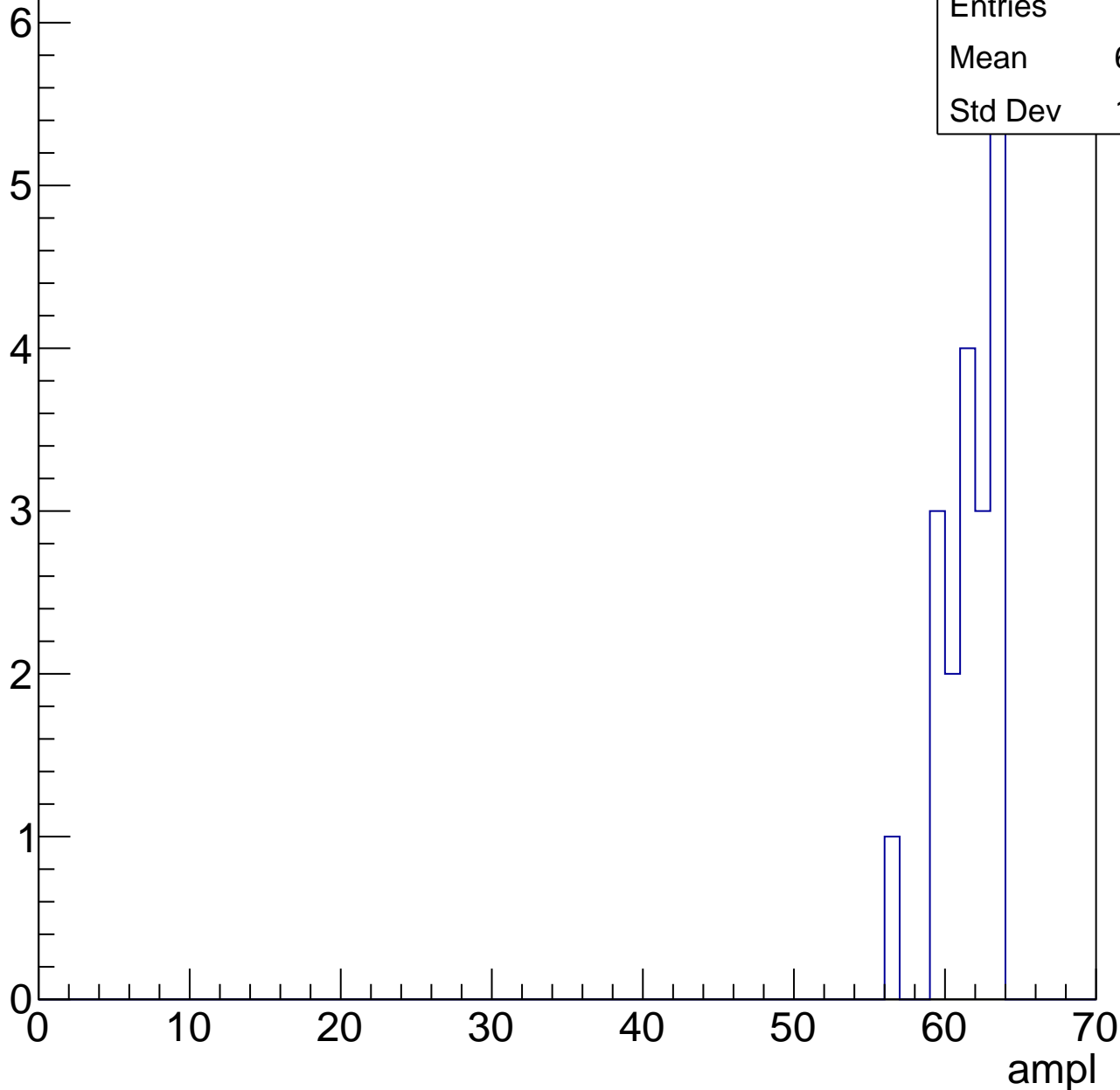


B1L103S, U10-ch97, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	19
Mean	61.11
Std Dev	1.861



B1L103S, U10-ch97, adc6

calib_packv5_042523_0143.root, FC#7, port C2

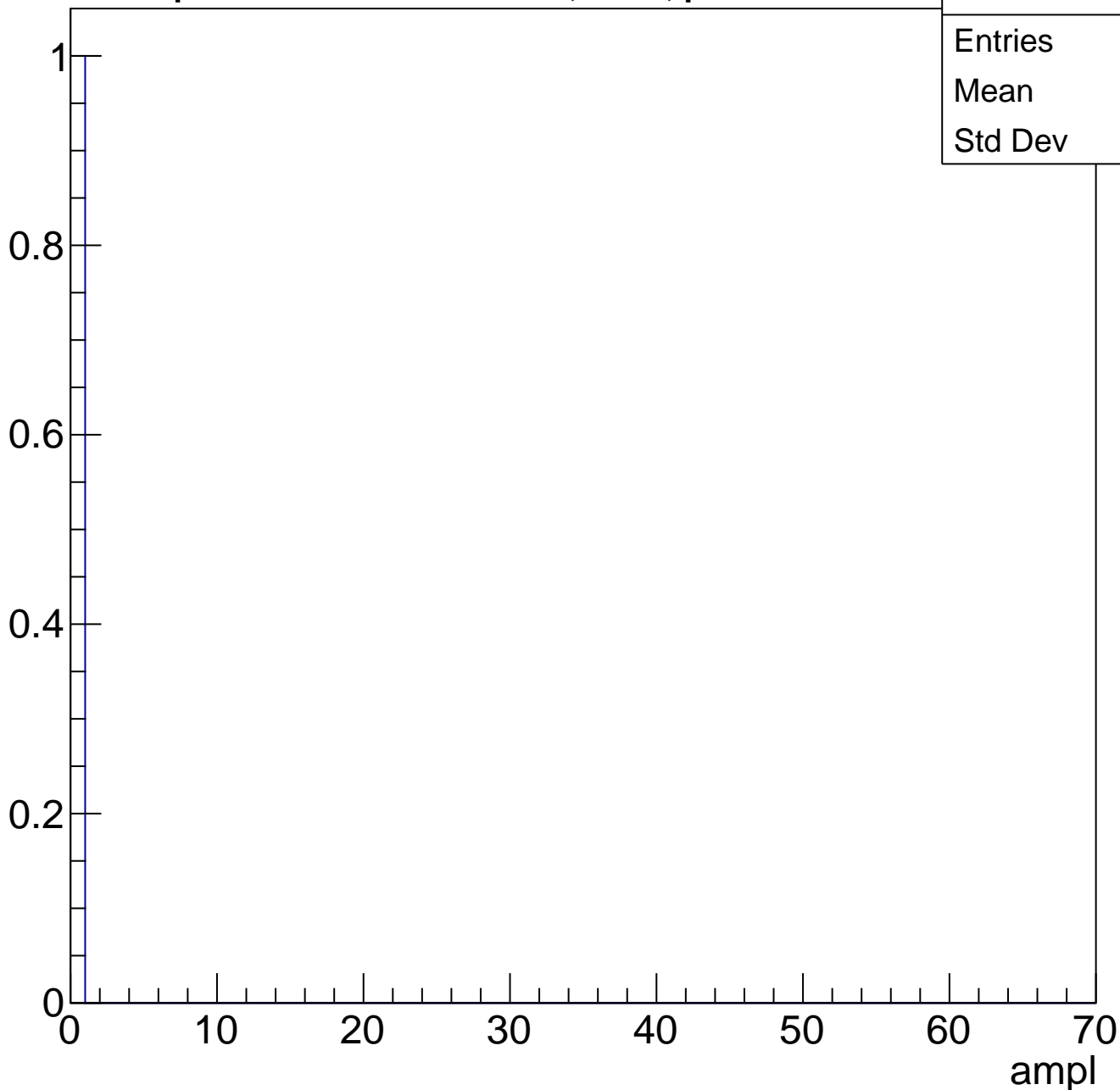
Entry



B1L103S, U10-ch97, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch98, adc0

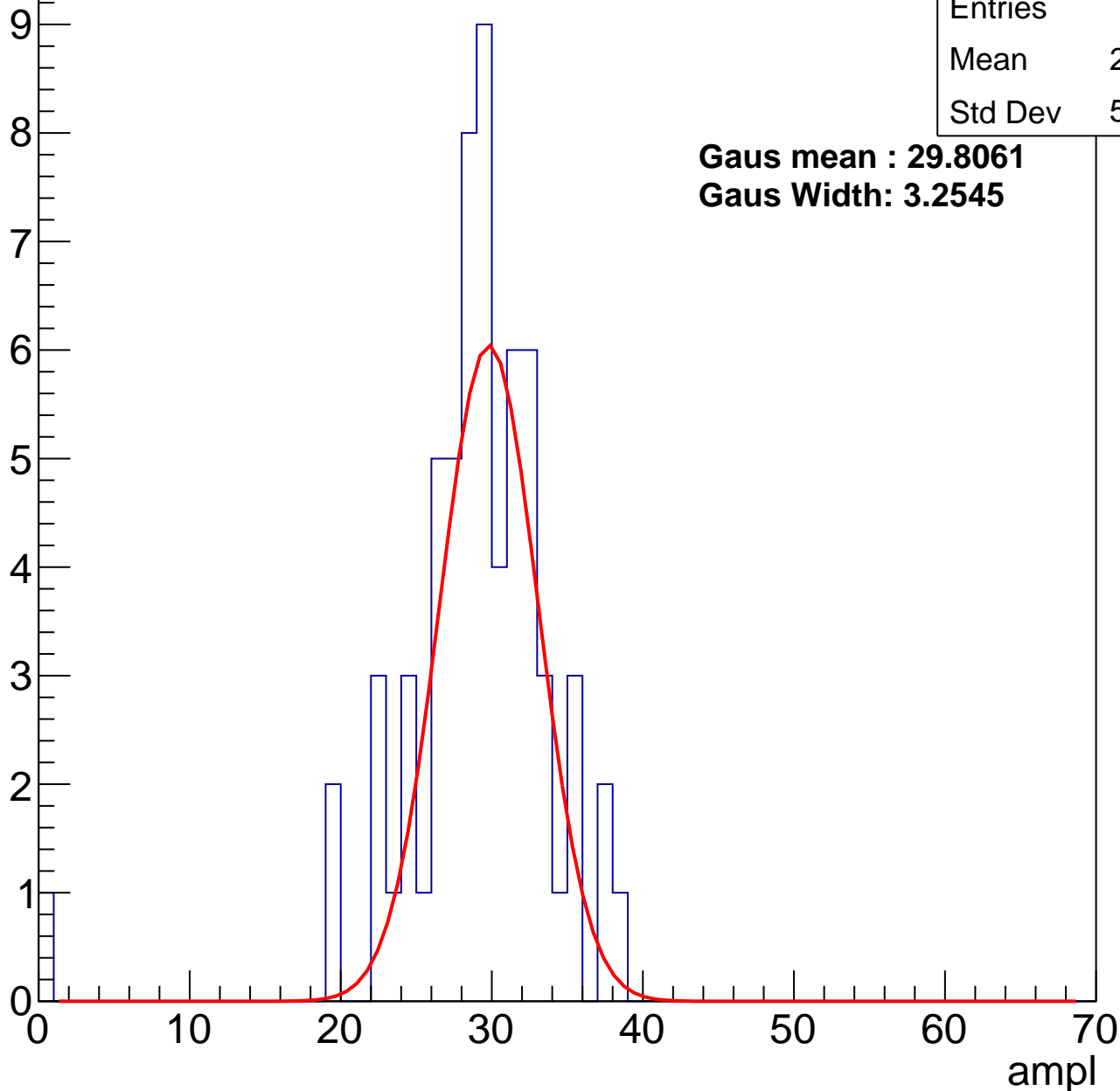
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	28.47
Std Dev	5.365

Gaus mean : 29.8061

Gaus Width: 3.2545



B1L103S, U10-ch98, adc1

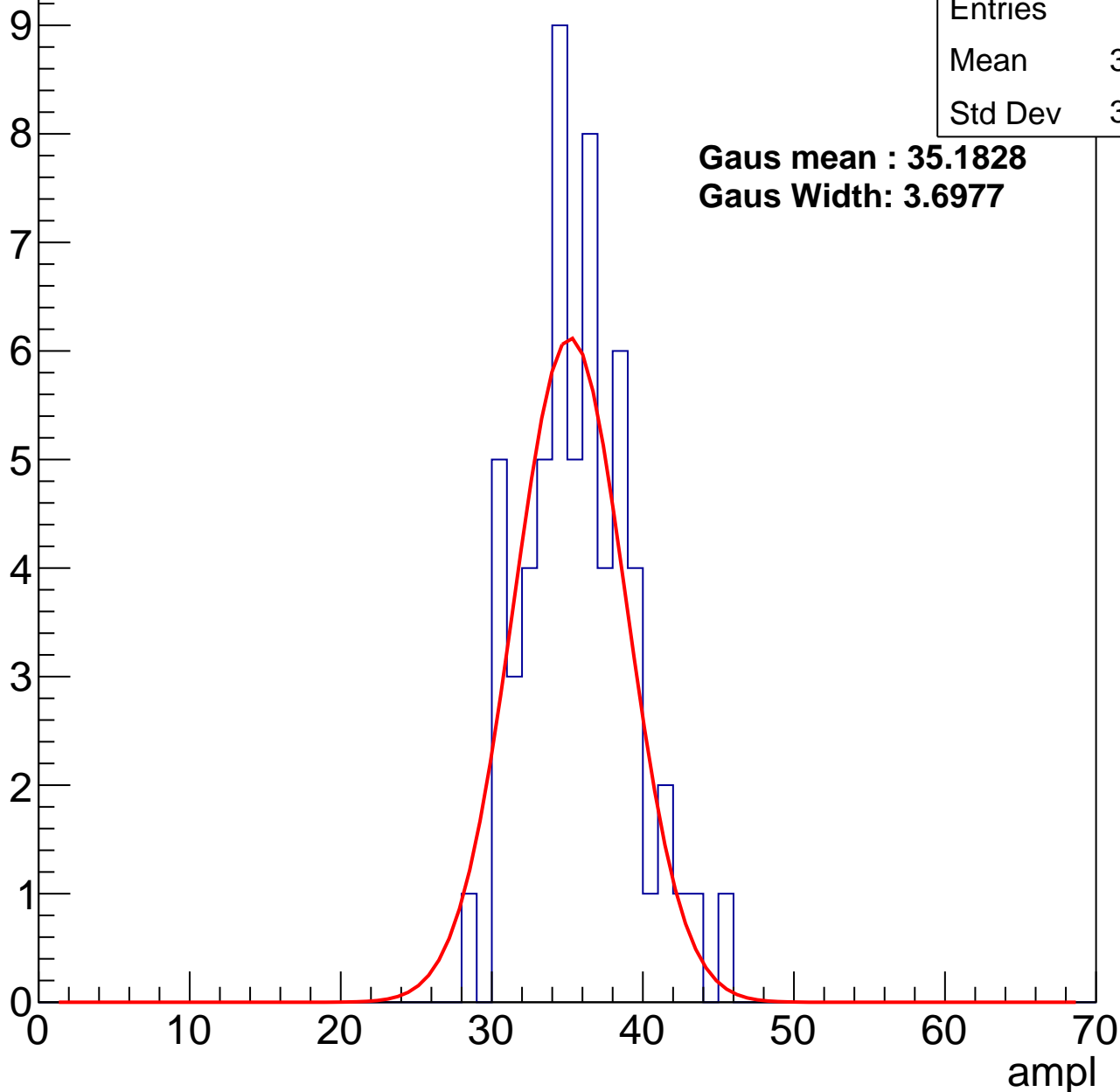
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	35.28
Std Dev	3.498

Gaus mean : 35.1828

Gaus Width: 3.6977



B1L103S, U10-ch98, adc2

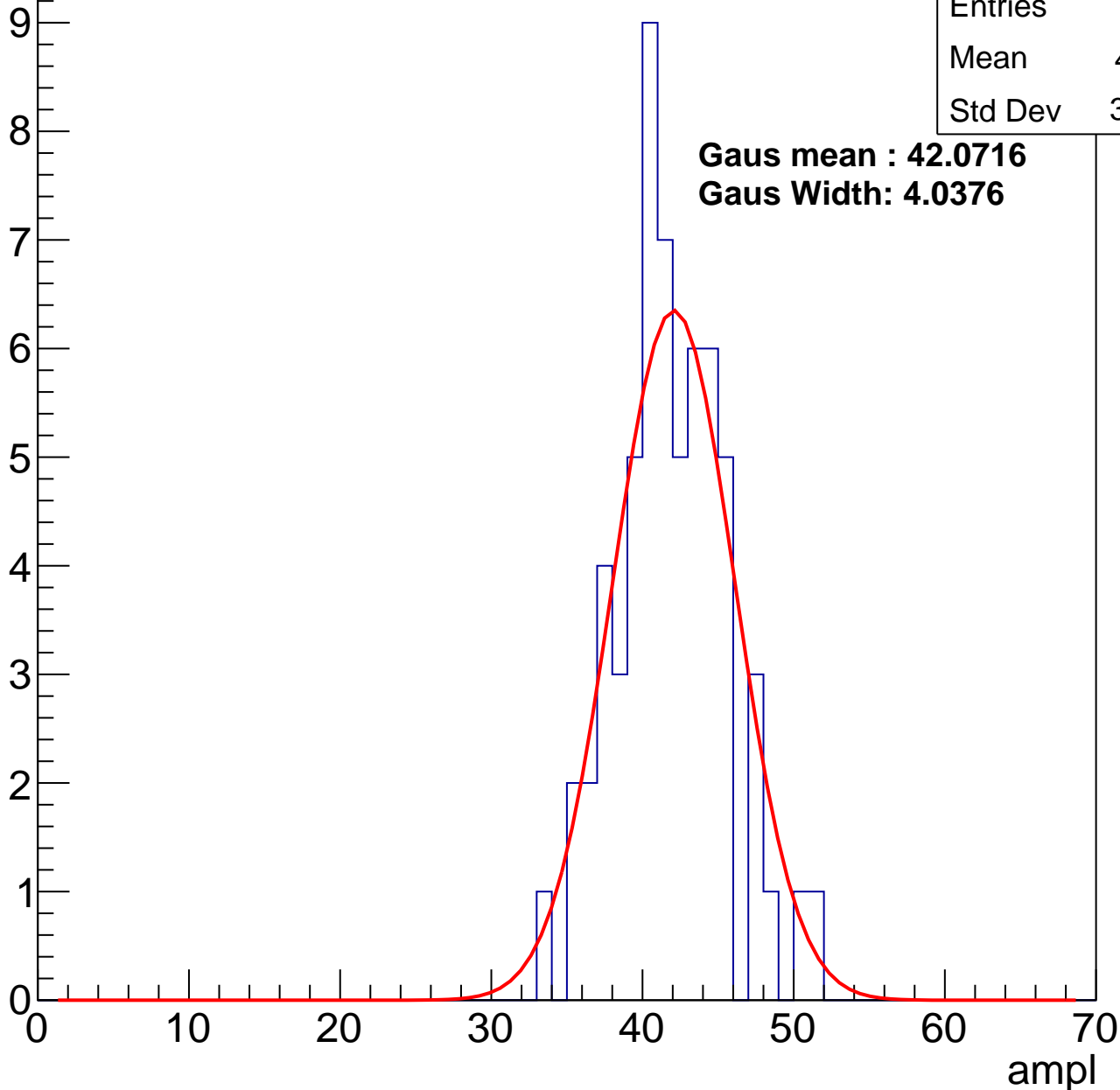
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	41.41
Std Dev	3.614

Gaus mean : 42.0716

Gaus Width: 4.0376

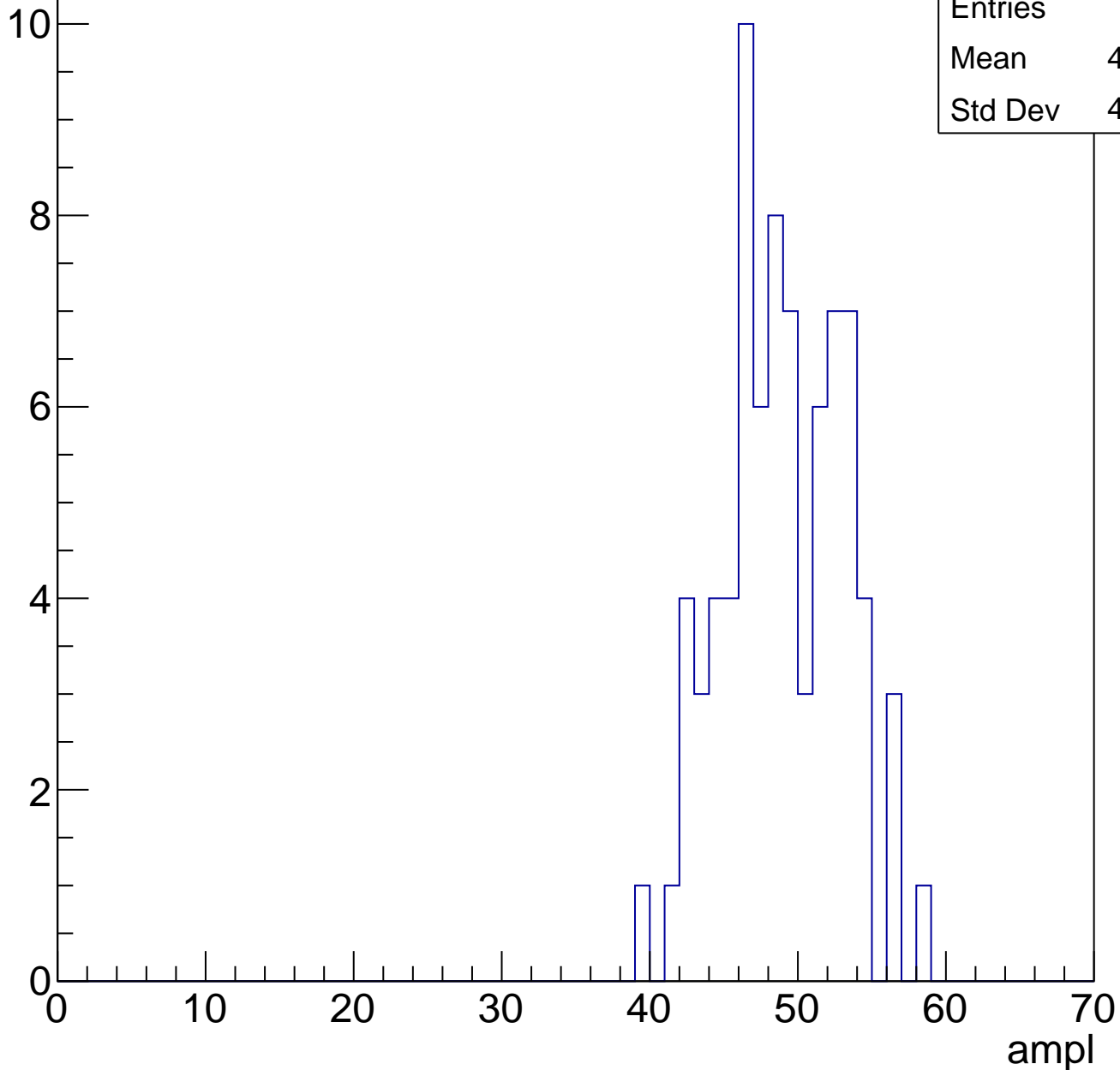


B1L103S, U10-ch98, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	48.54
Std Dev	4.006

Entry

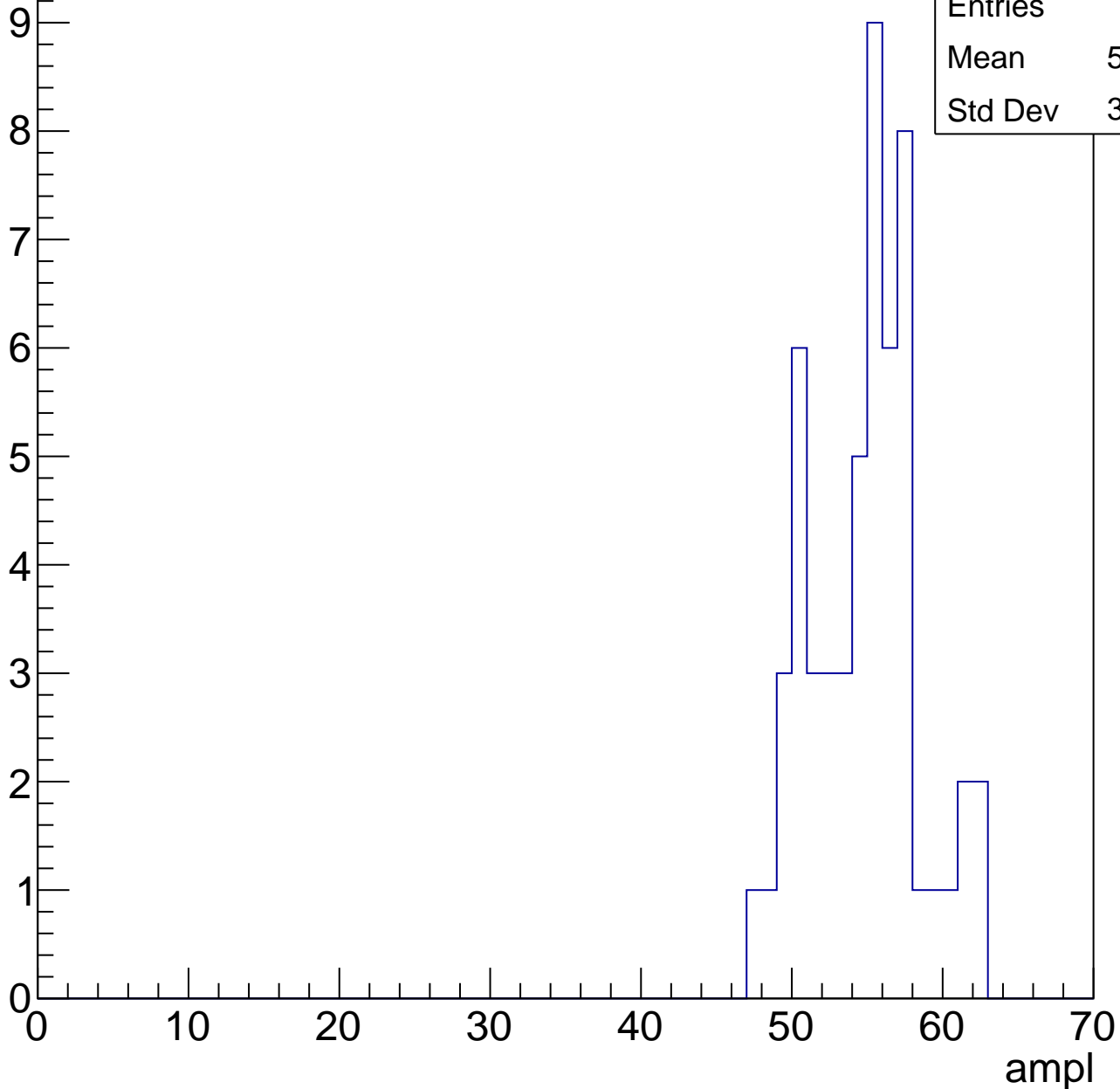


B1L103S, U10-ch98, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

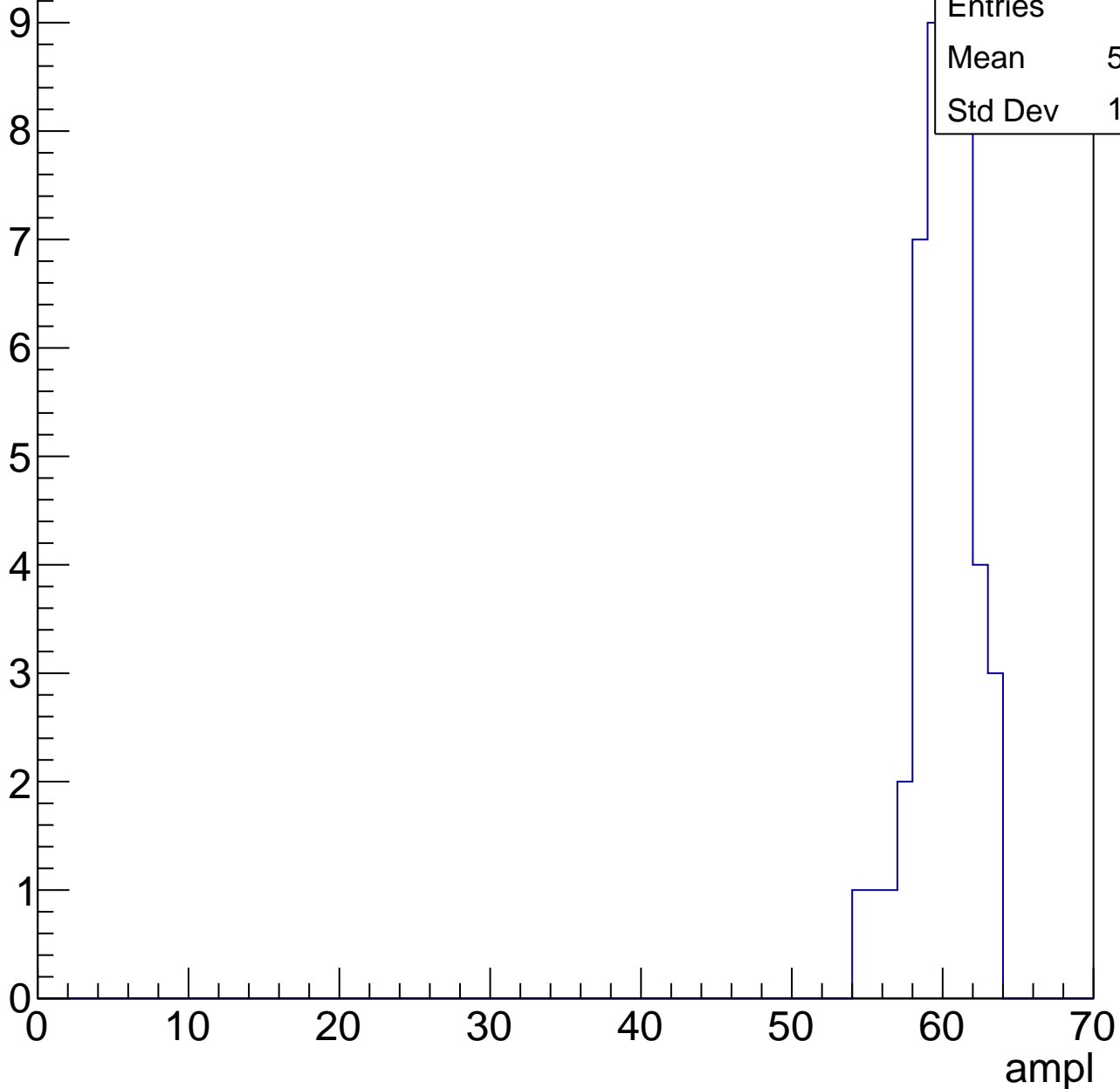
Entries	55
Mean	54.36
Std Dev	3.564



B1L103S, U10-ch98, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



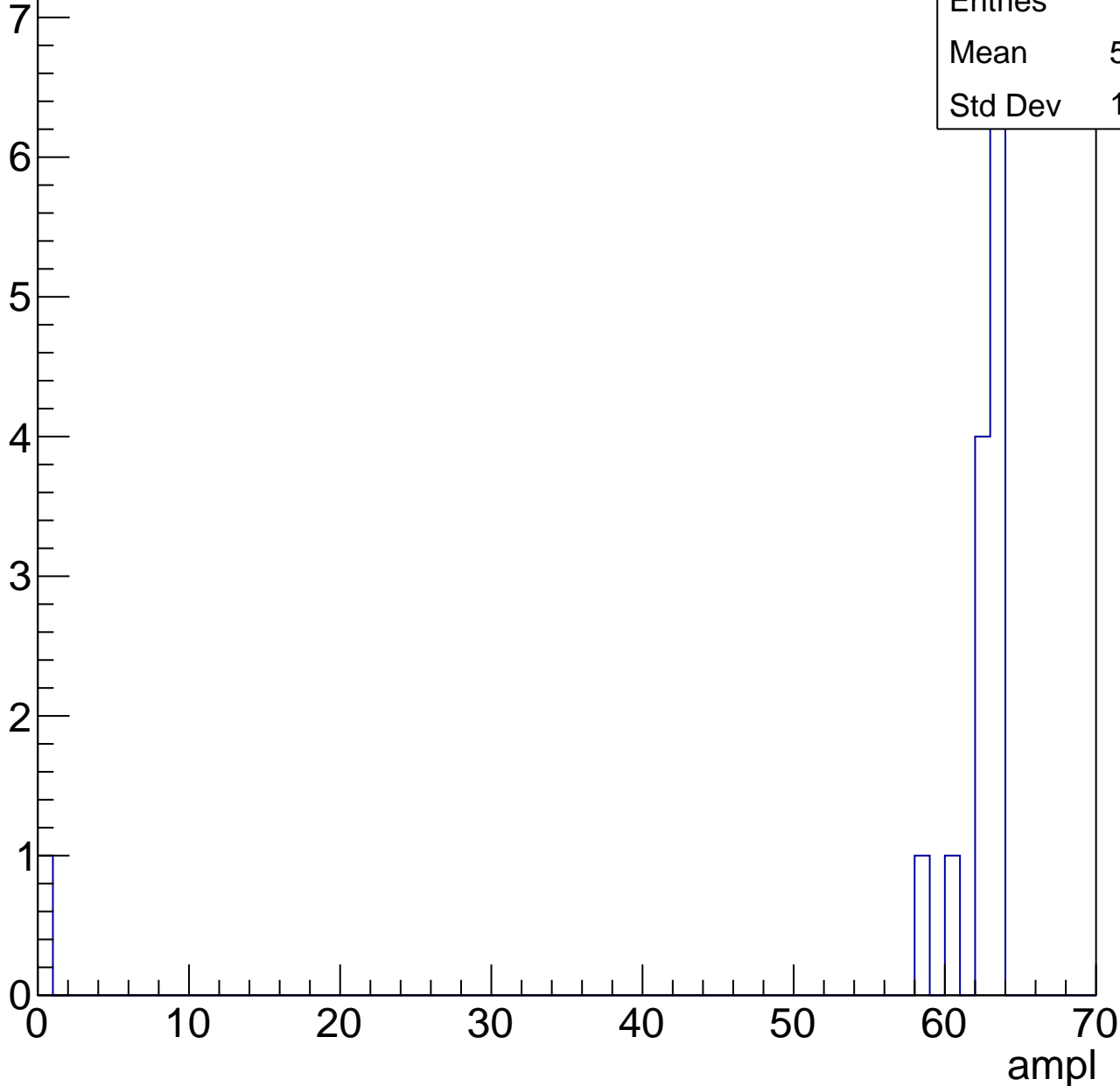
Entries	44
Mean	59.57
Std Dev	1.993

B1L103S, U10-ch98, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	14
Mean	57.64
Std Dev	16.05



B1L103S, U10-ch98, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch99, adc0

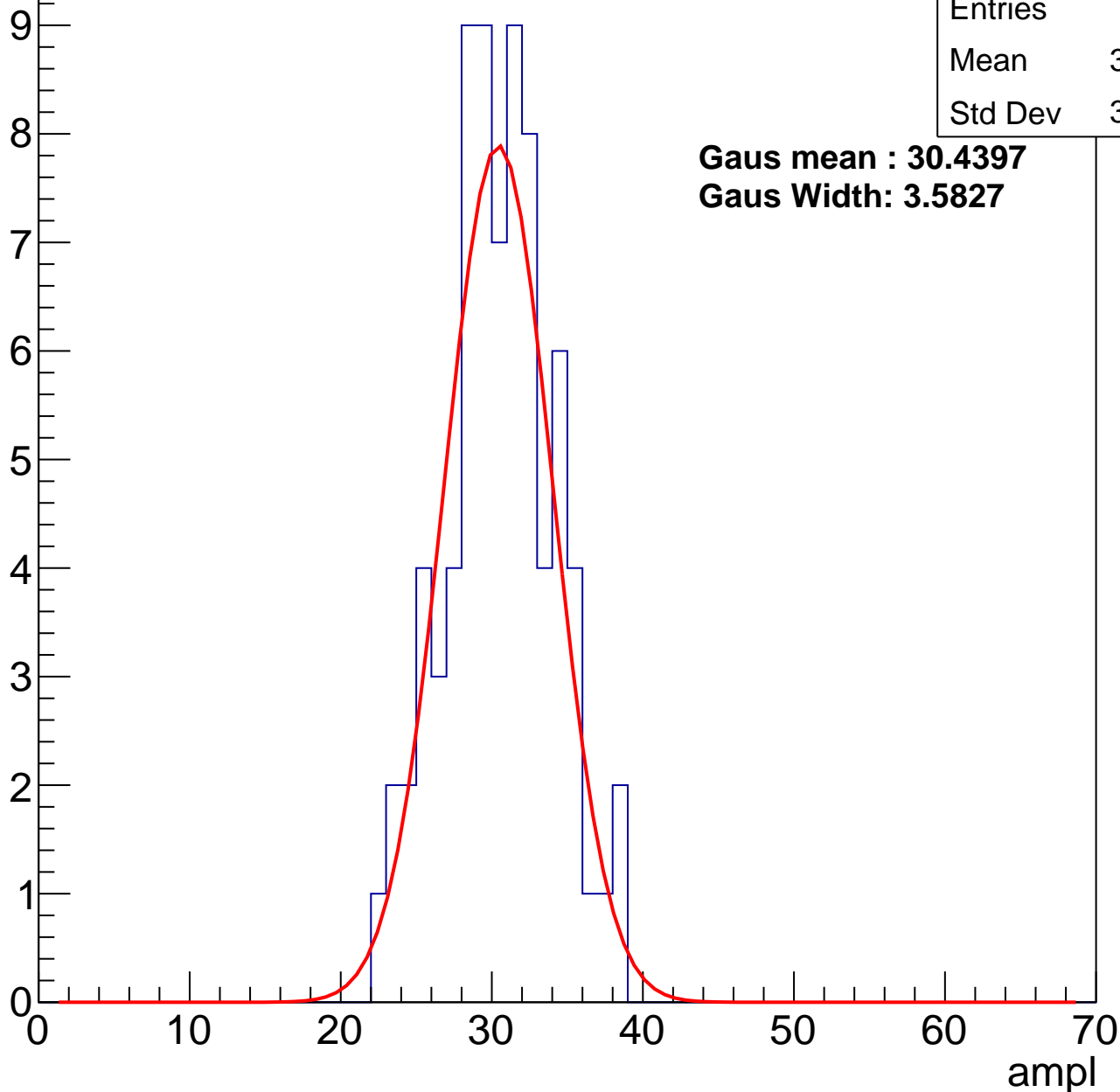
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	30.07
Std Dev	3.533

Gaus mean : 30.4397

Gaus Width: 3.5827



B1L103S, U10-ch99, adc1

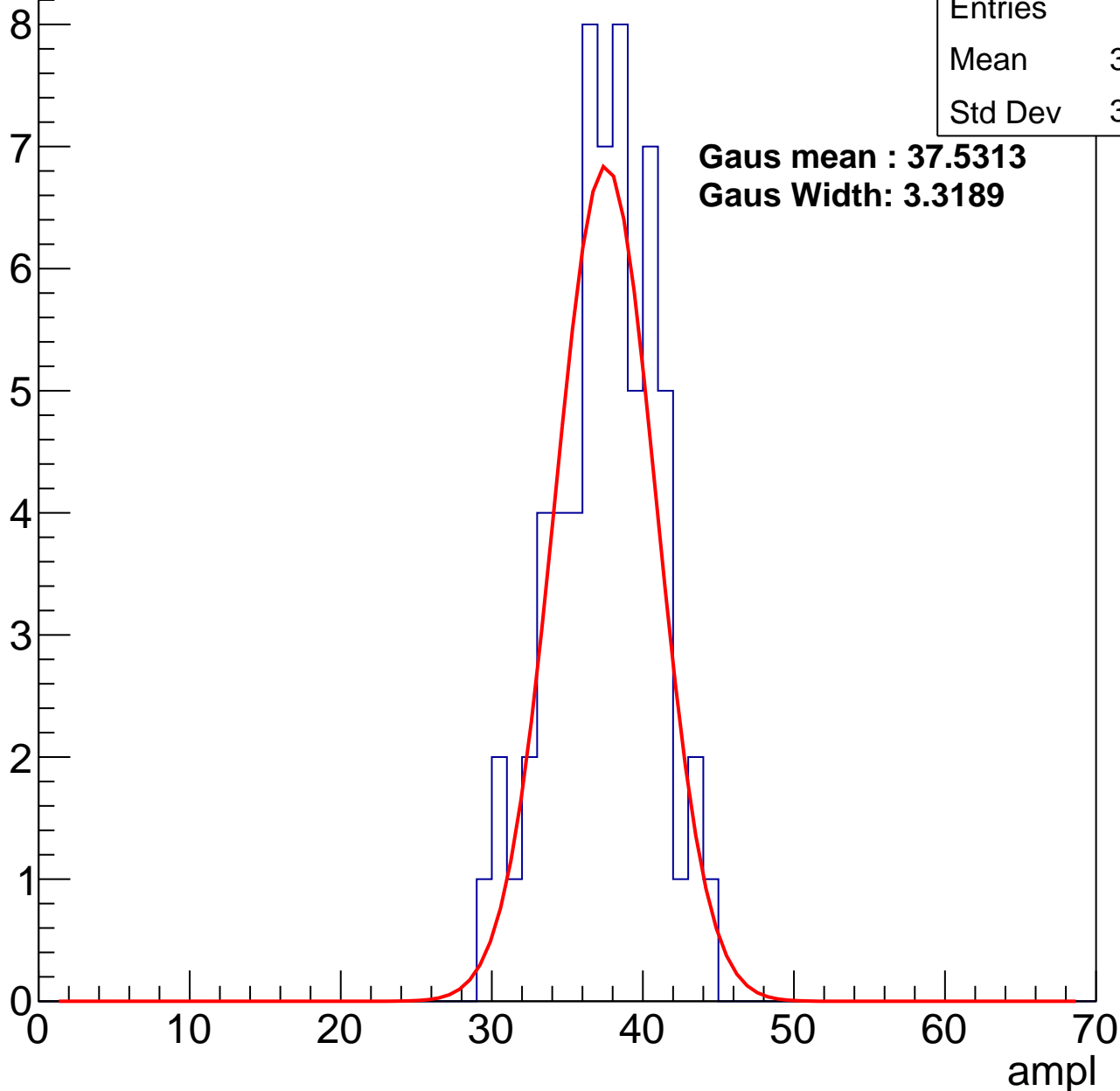
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	37.02
Std Dev	3.334

Gaus mean : 37.5313

Gaus Width: 3.3189



B1L103S, U10-ch99, adc2

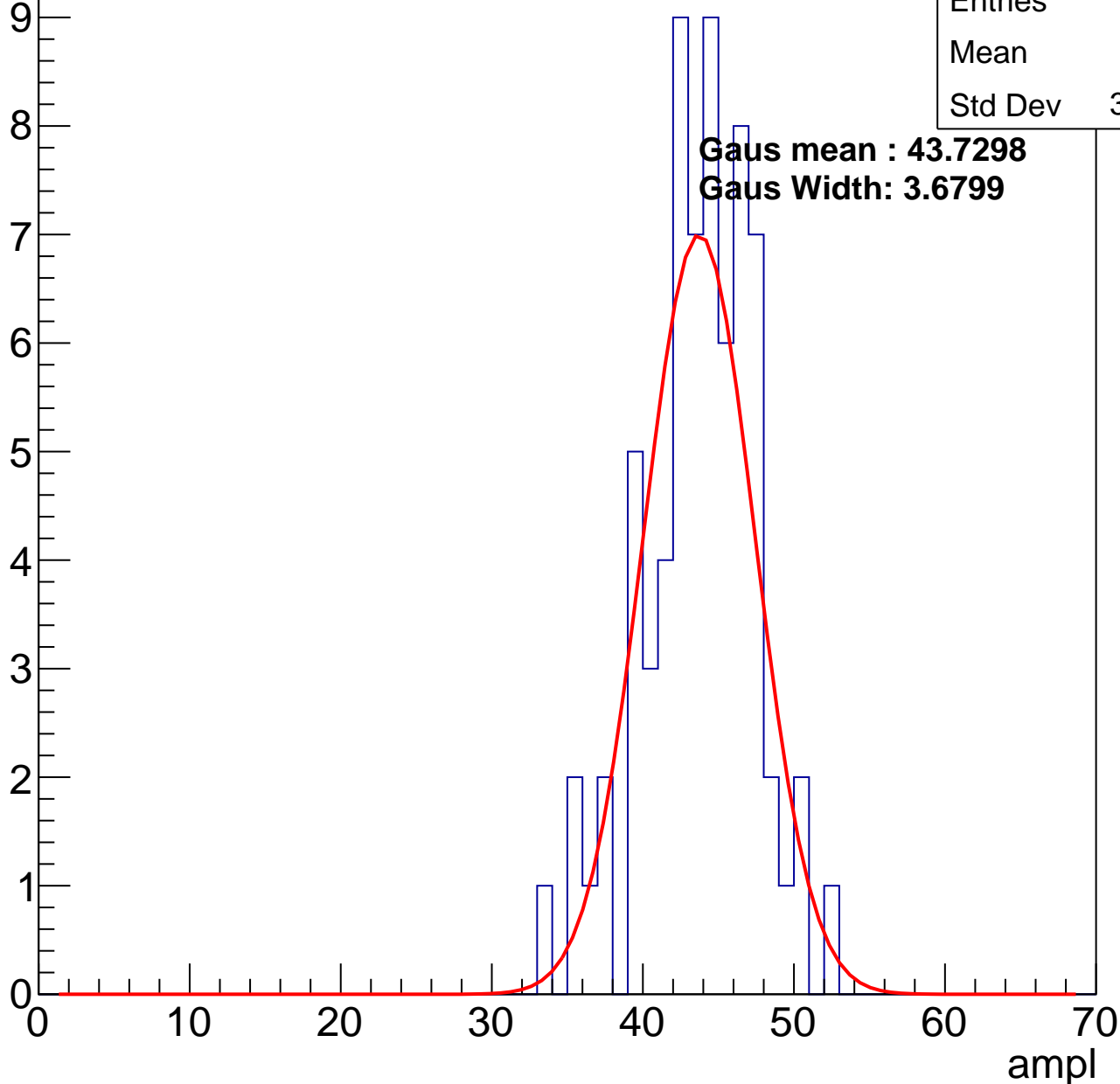
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	43.3
Std Dev	3.697

Gaus mean : 43.7298

Gaus Width: 3.6799

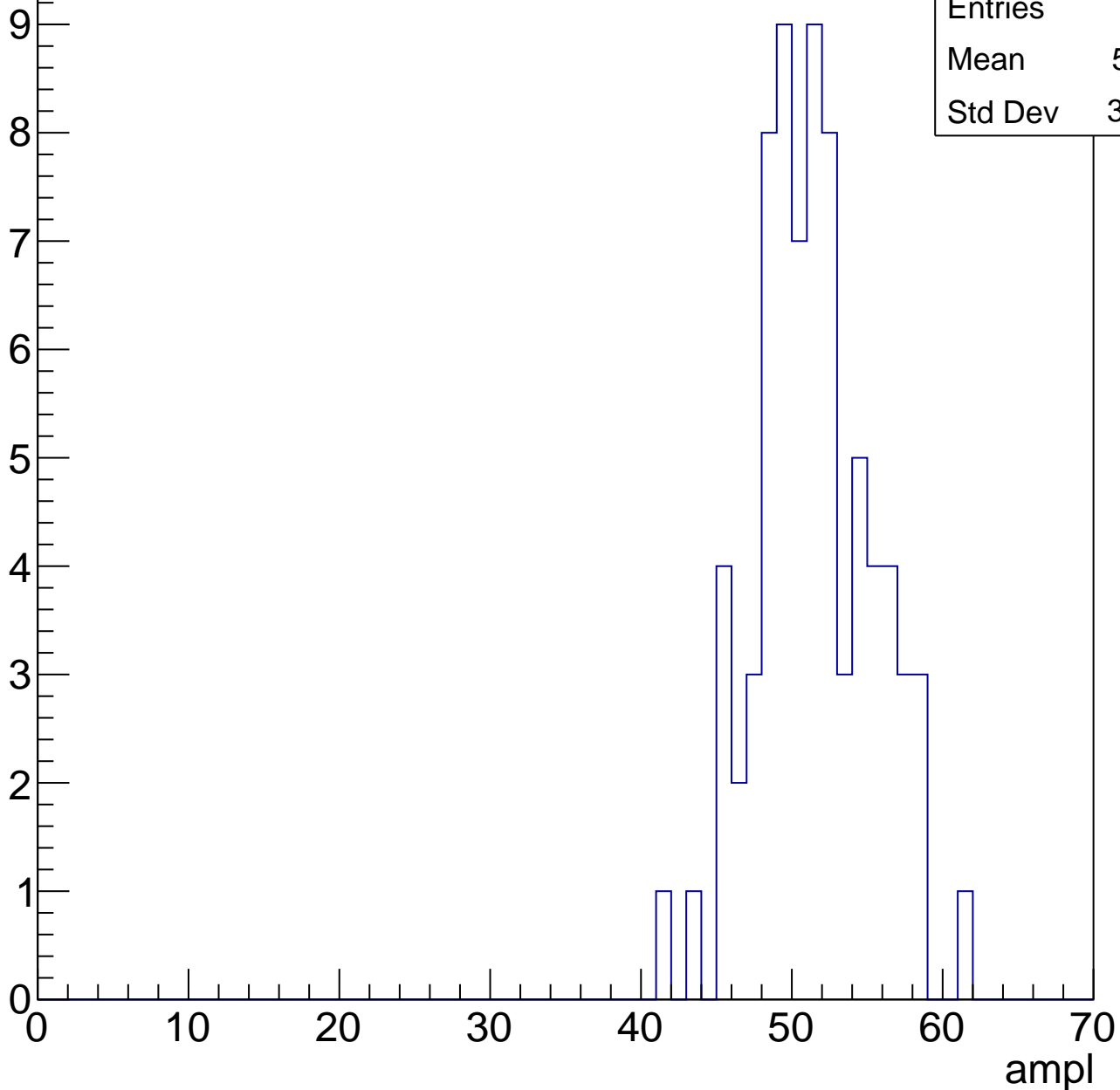


B1L103S, U10-ch99, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

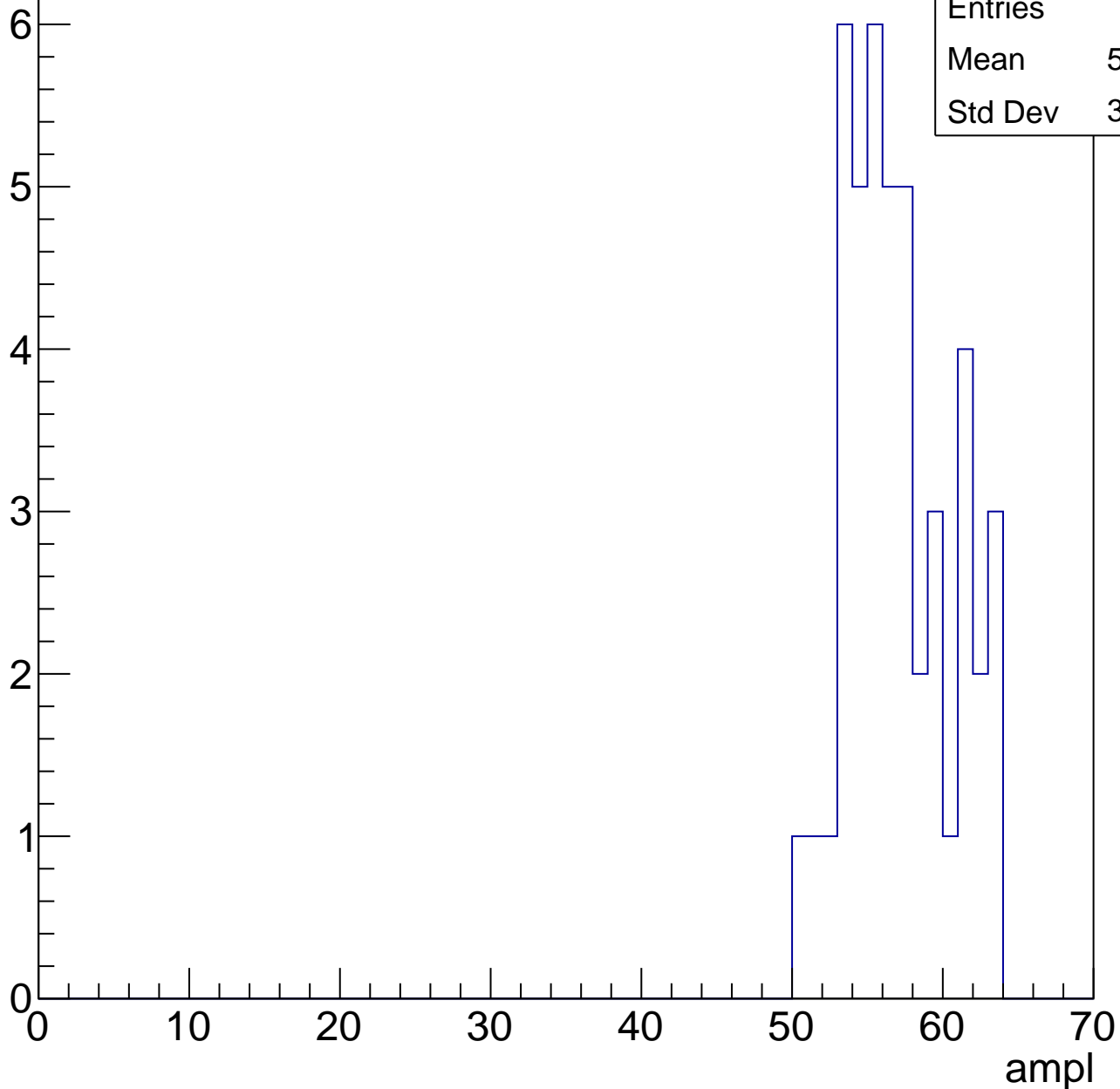
Entries	75
Mean	51.01
Std Dev	3.838



B1L103S, U10-ch99, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry



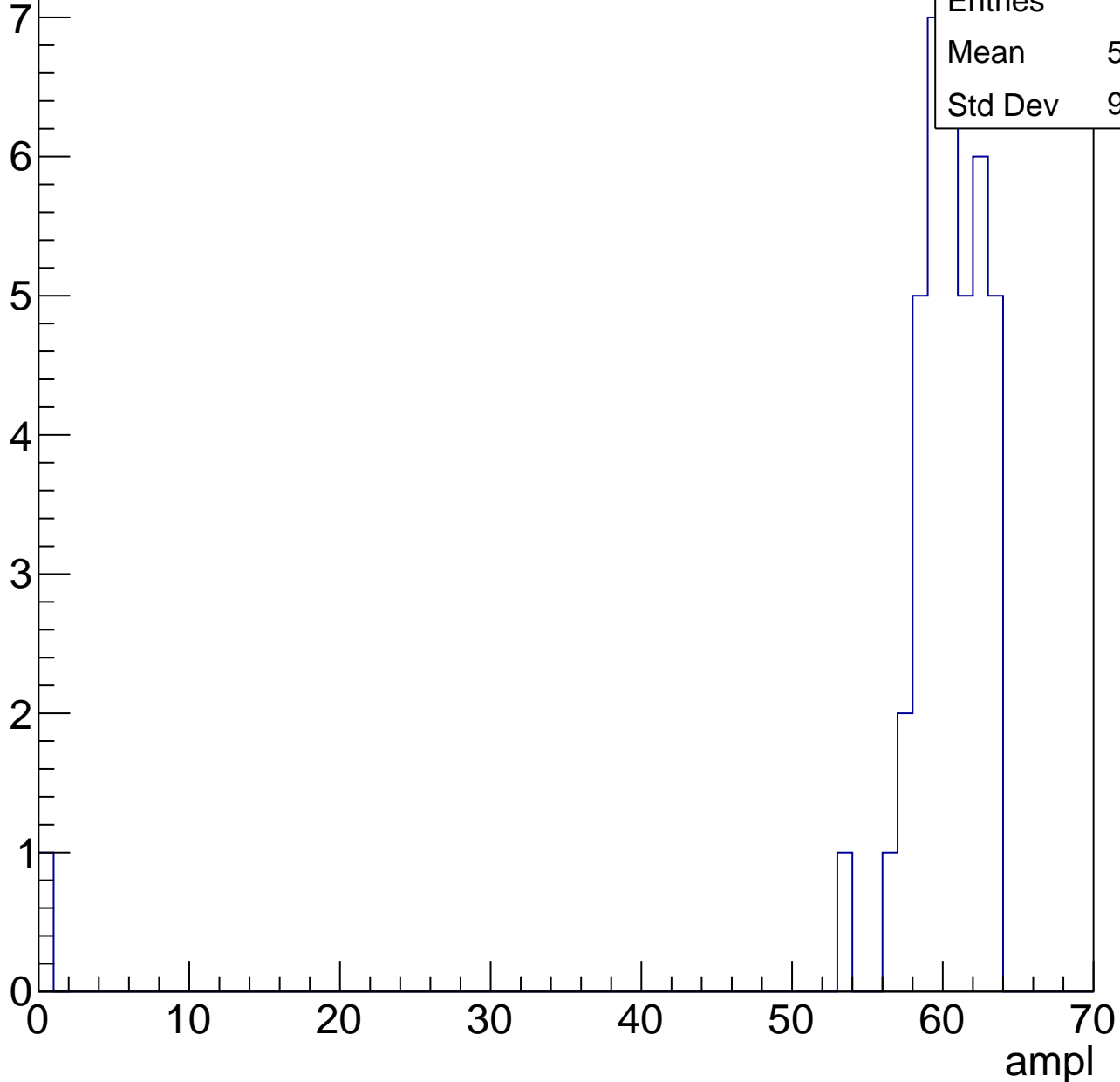
Entries	45
Mean	56.58
Std Dev	3.389

B1L103S, U10-ch99, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	40
Mean	58.45
Std Dev	9.602



B1L103S, U10-ch99, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

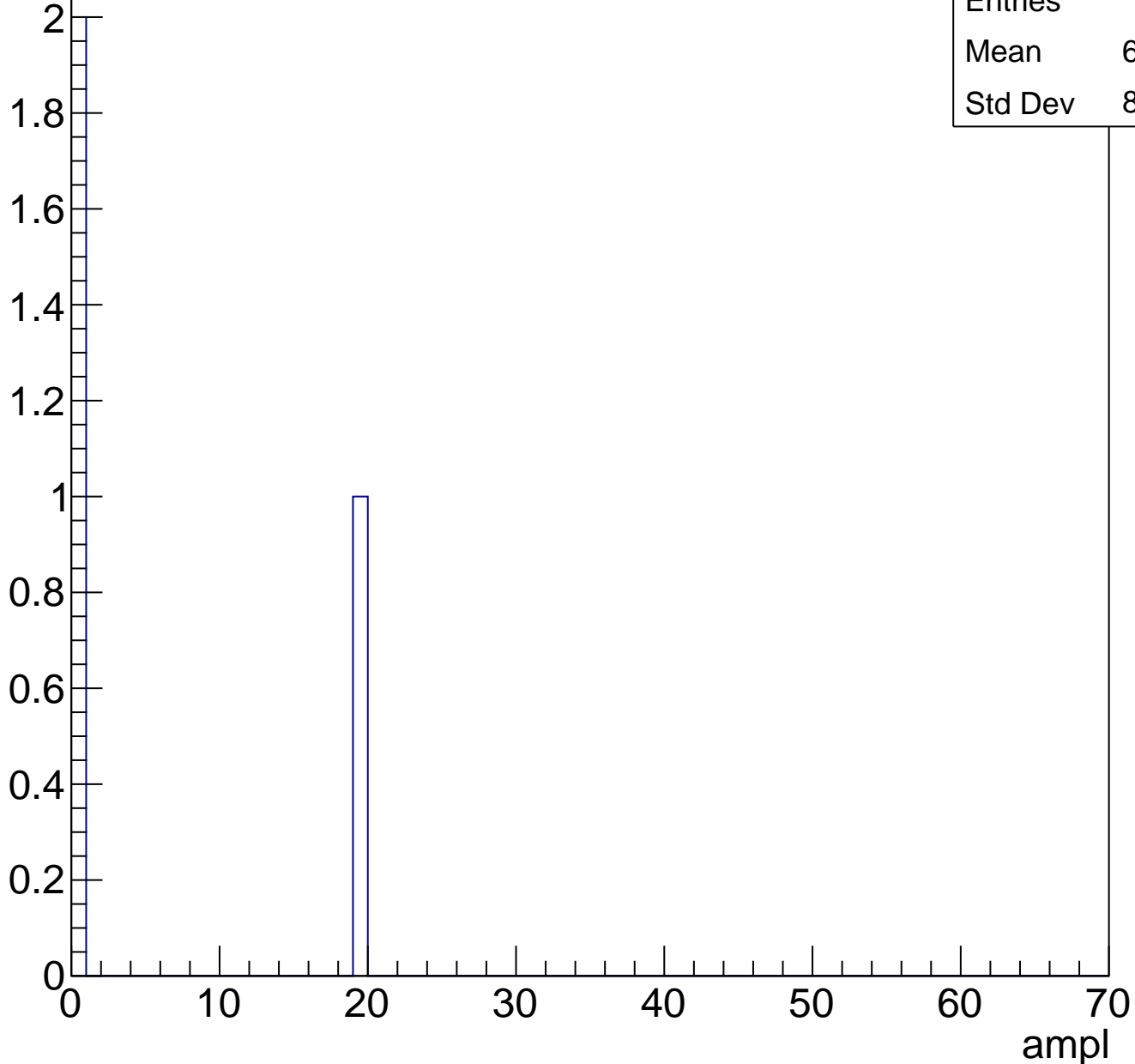
Entries	6
Mean	61.67
Std Dev	1.247

ampl

B1L103S, U10-ch99, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B1L103S, U10-ch100, adc0

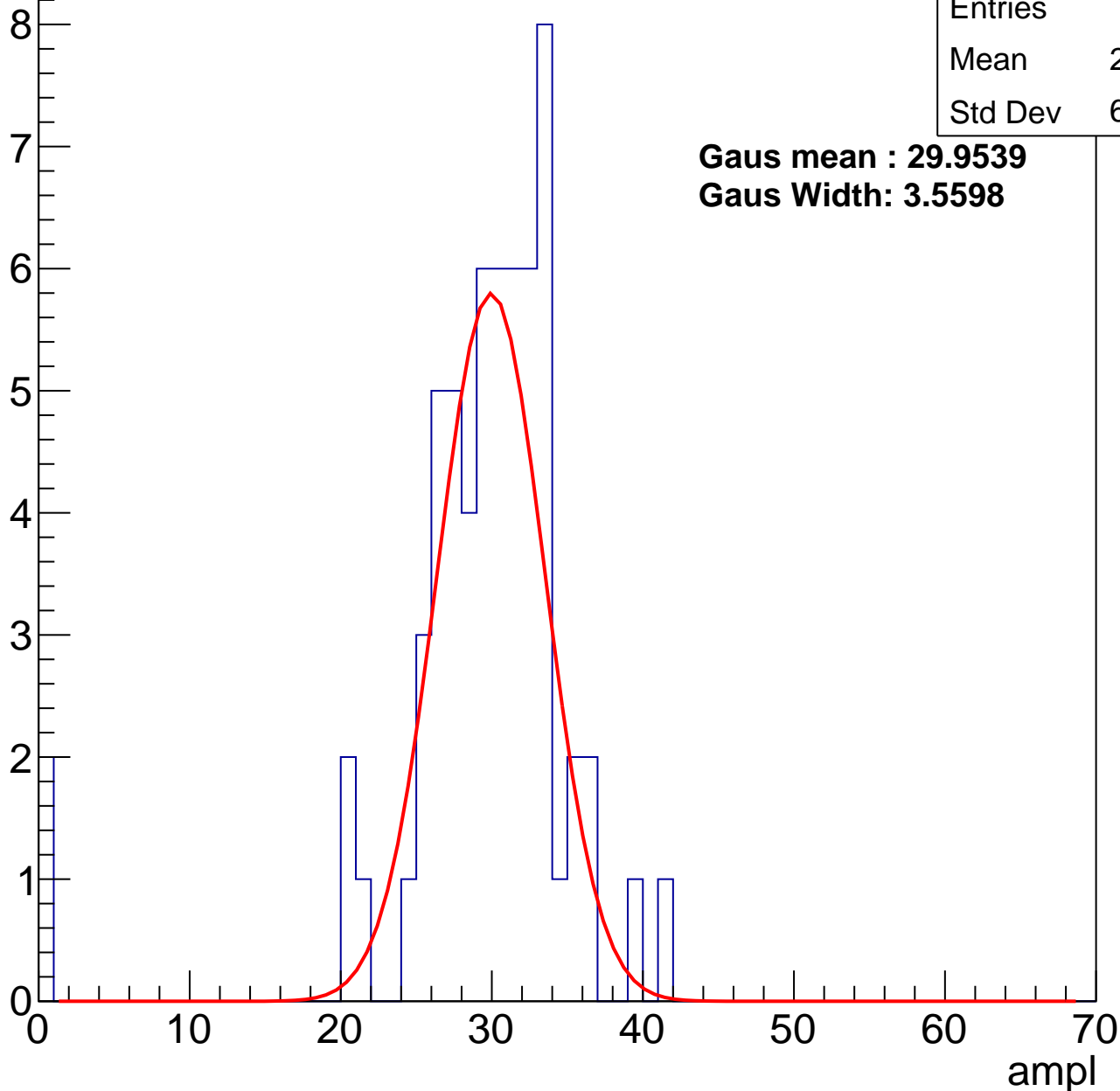
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	28.85
Std Dev	6.599

Gaus mean : 29.9539

Gaus Width: 3.5598



B1L103S, U10-ch100, adc1

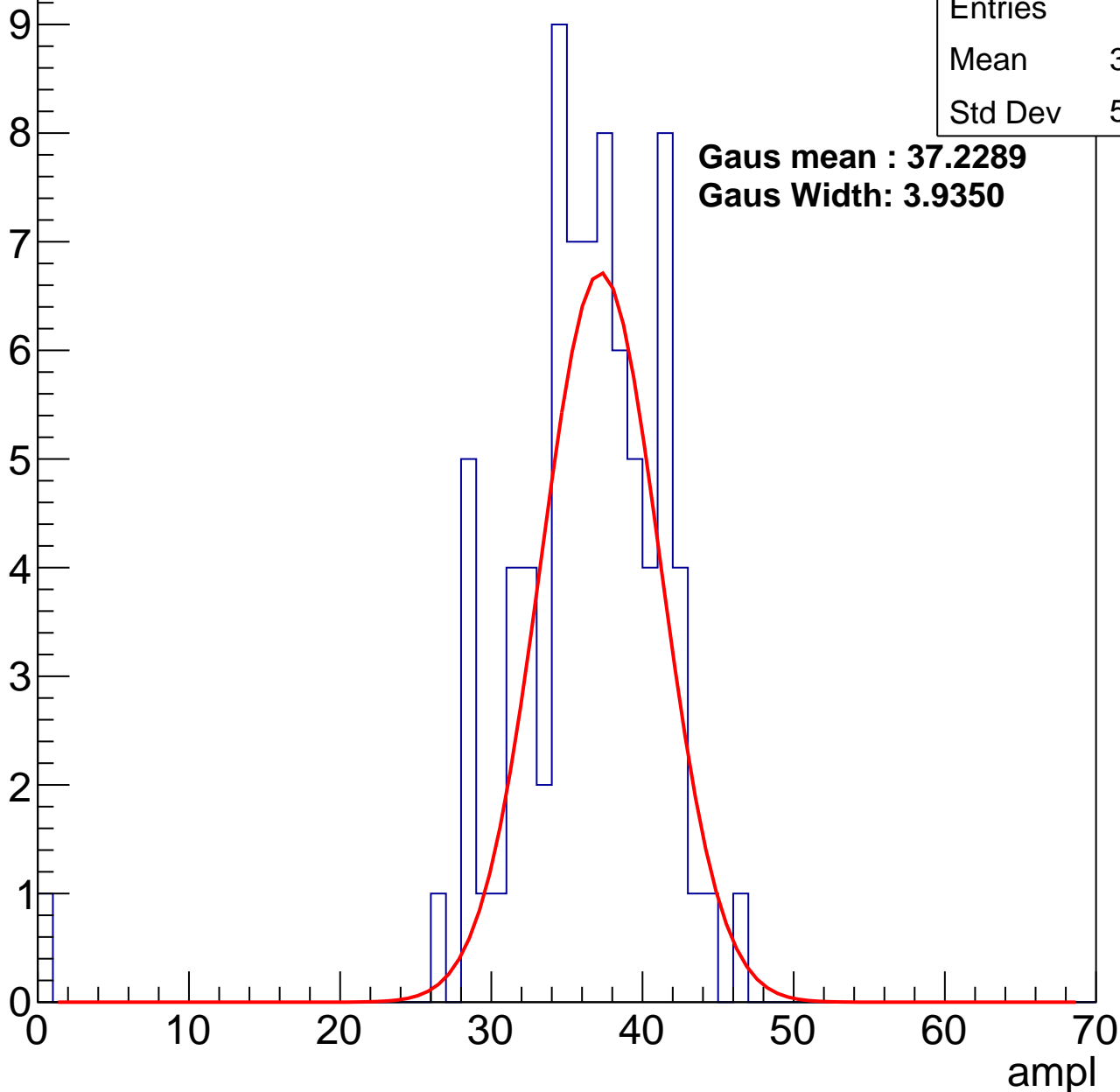
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	35.67
Std Dev	5.826

Gaus mean : 37.2289

Gaus Width: 3.9350



B1L103S, U10-ch100, adc2

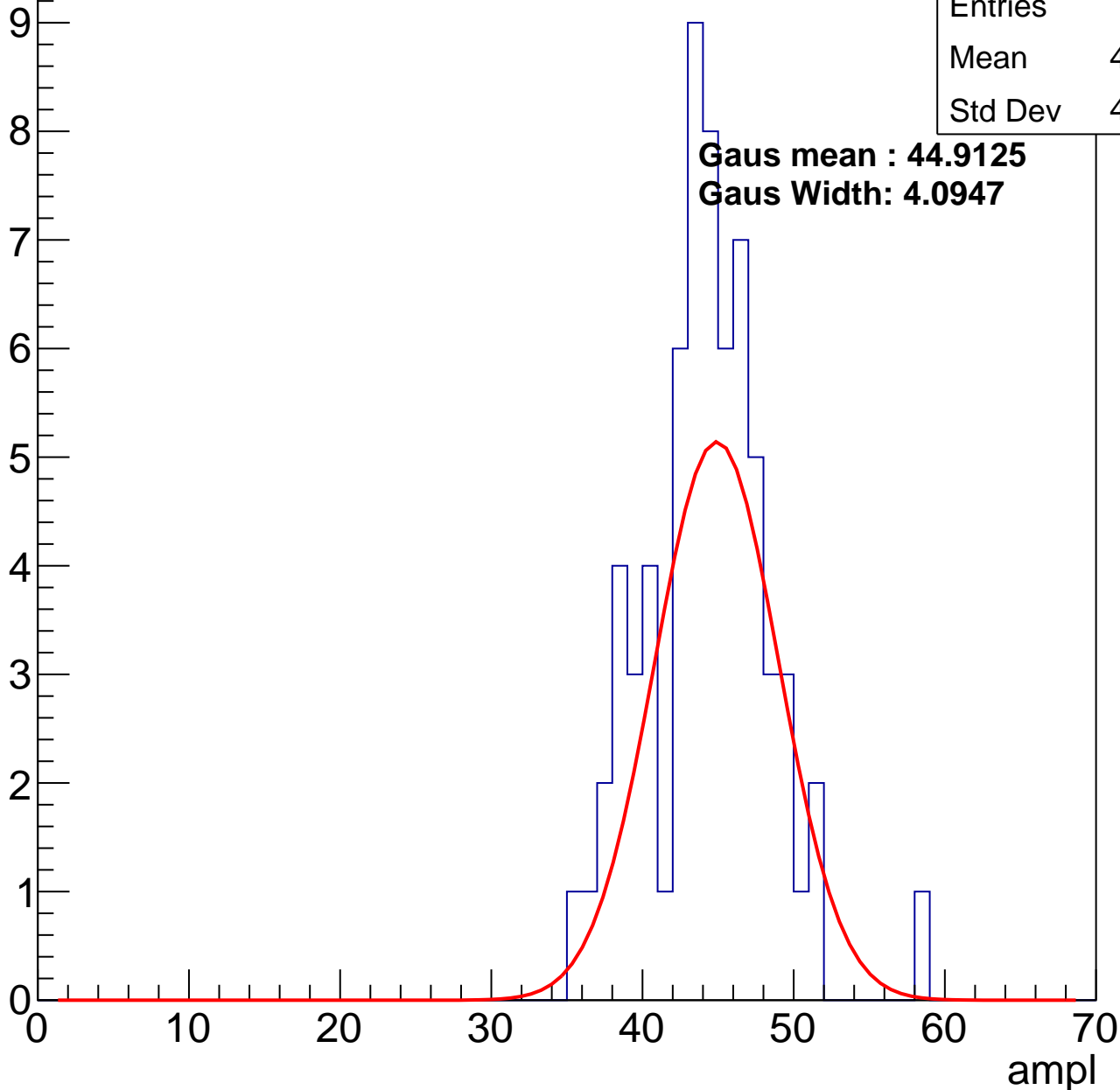
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	43.79
Std Dev	4.047

Gaus mean : 44.9125

Gaus Width: 4.0947

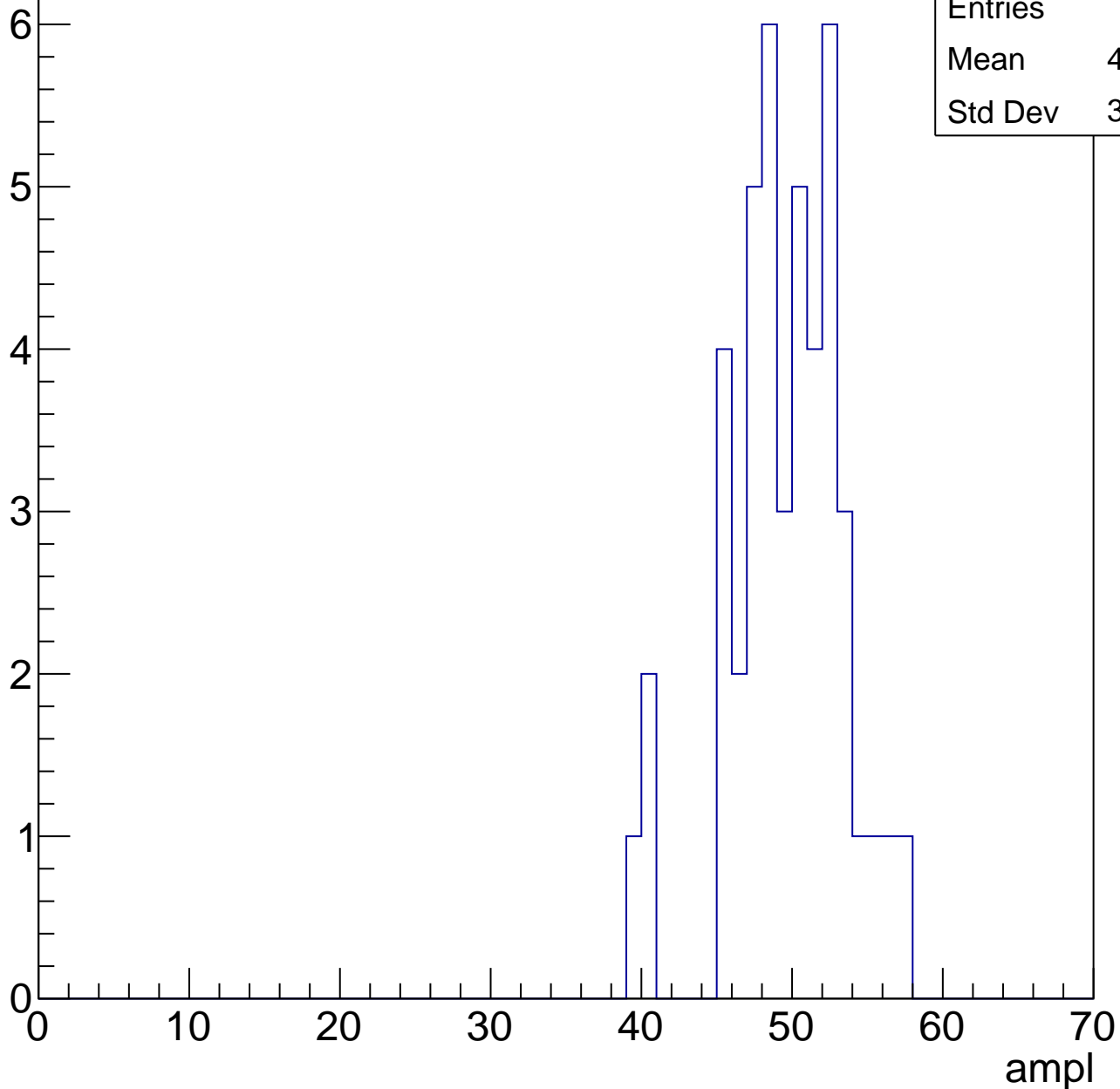


B1L103S, U10-ch100, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	49.07
Std Dev	3.849

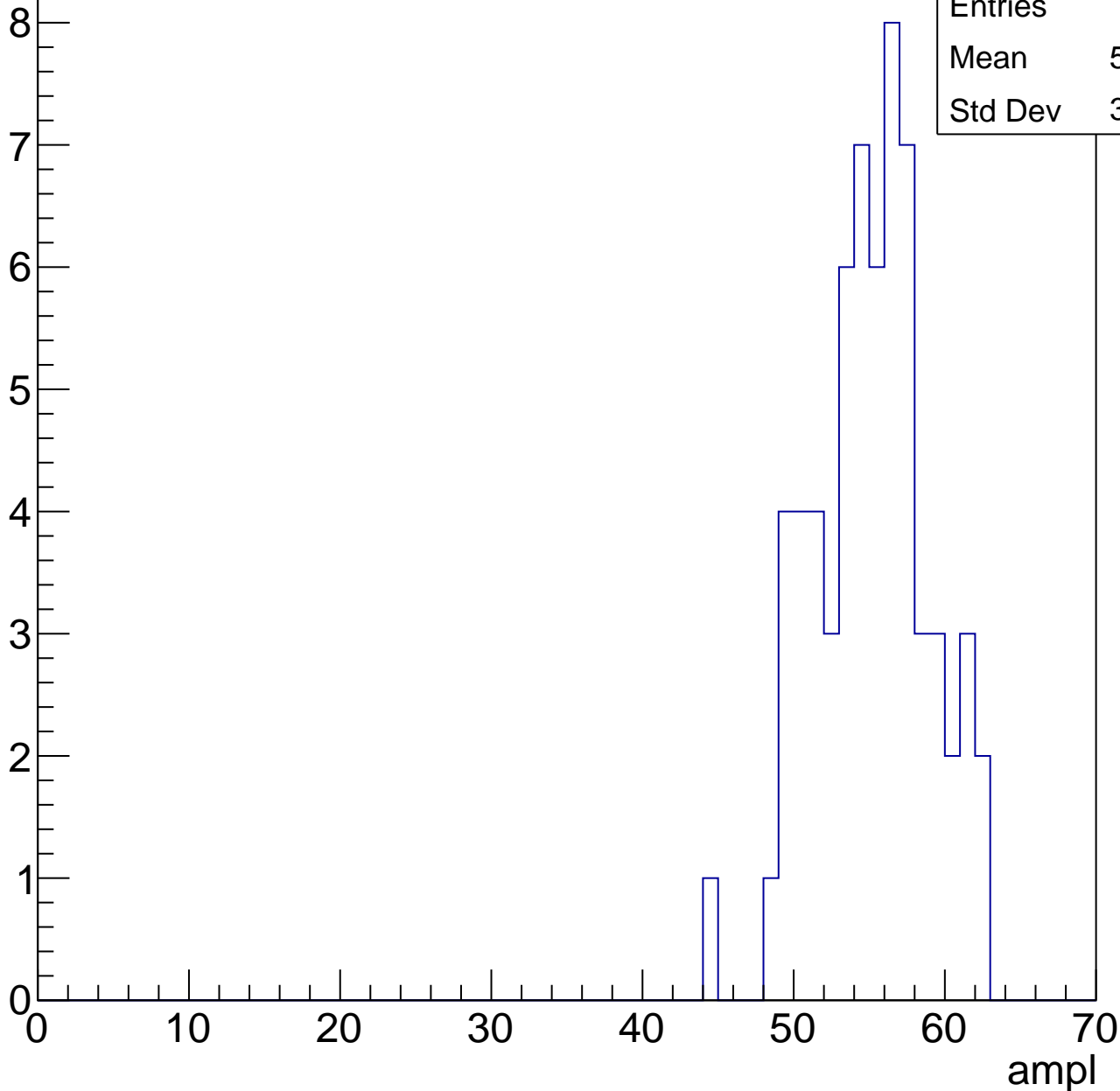


B1L103S, U10-ch100, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	54.67
Std Dev	3.742

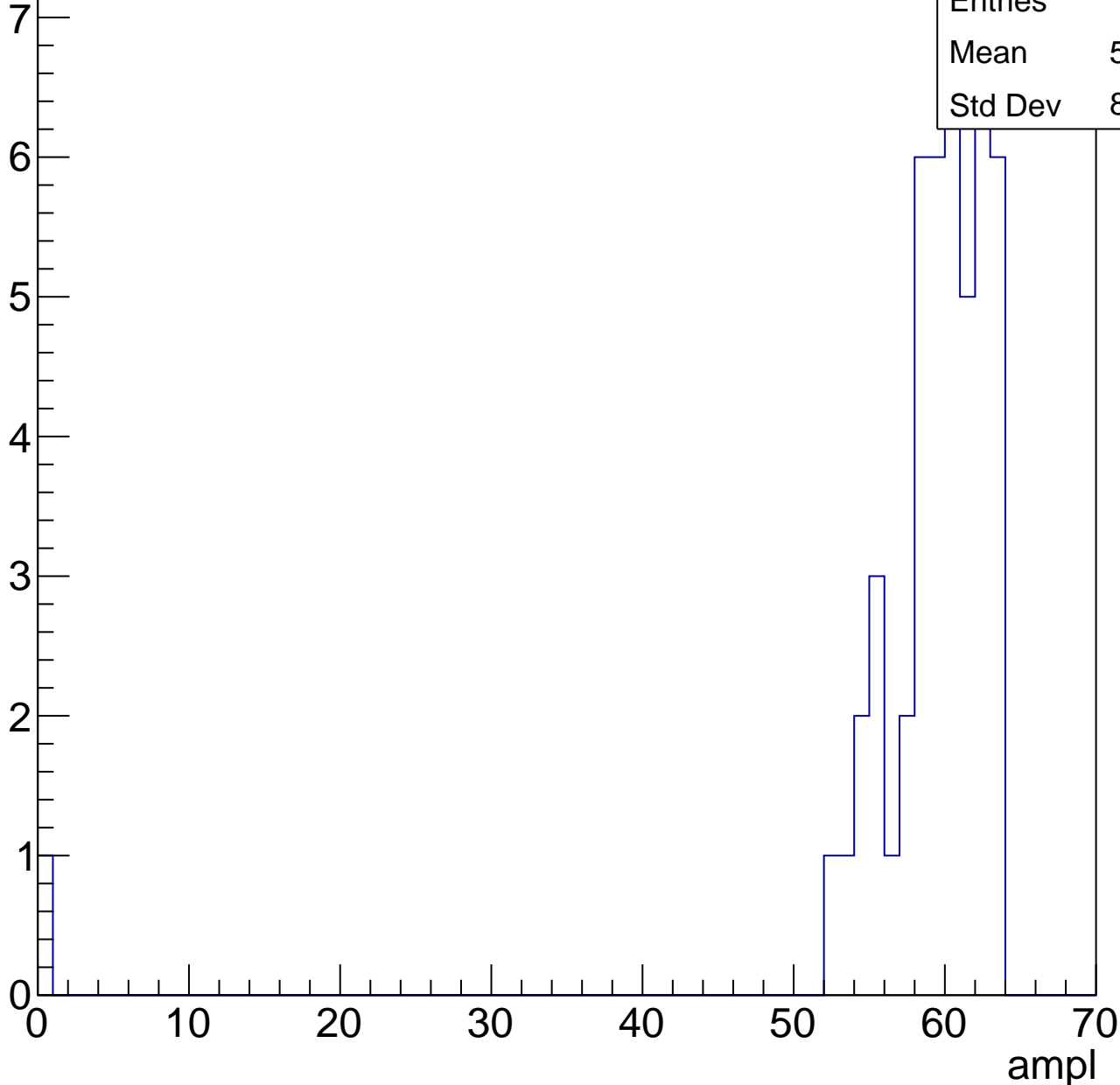


B1L103S, U10-ch100, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	58.06
Std Dev	8.934



B1L103S, U10-ch100, adc6

calib_packv5_042523_0143.root, FC#7, port C2

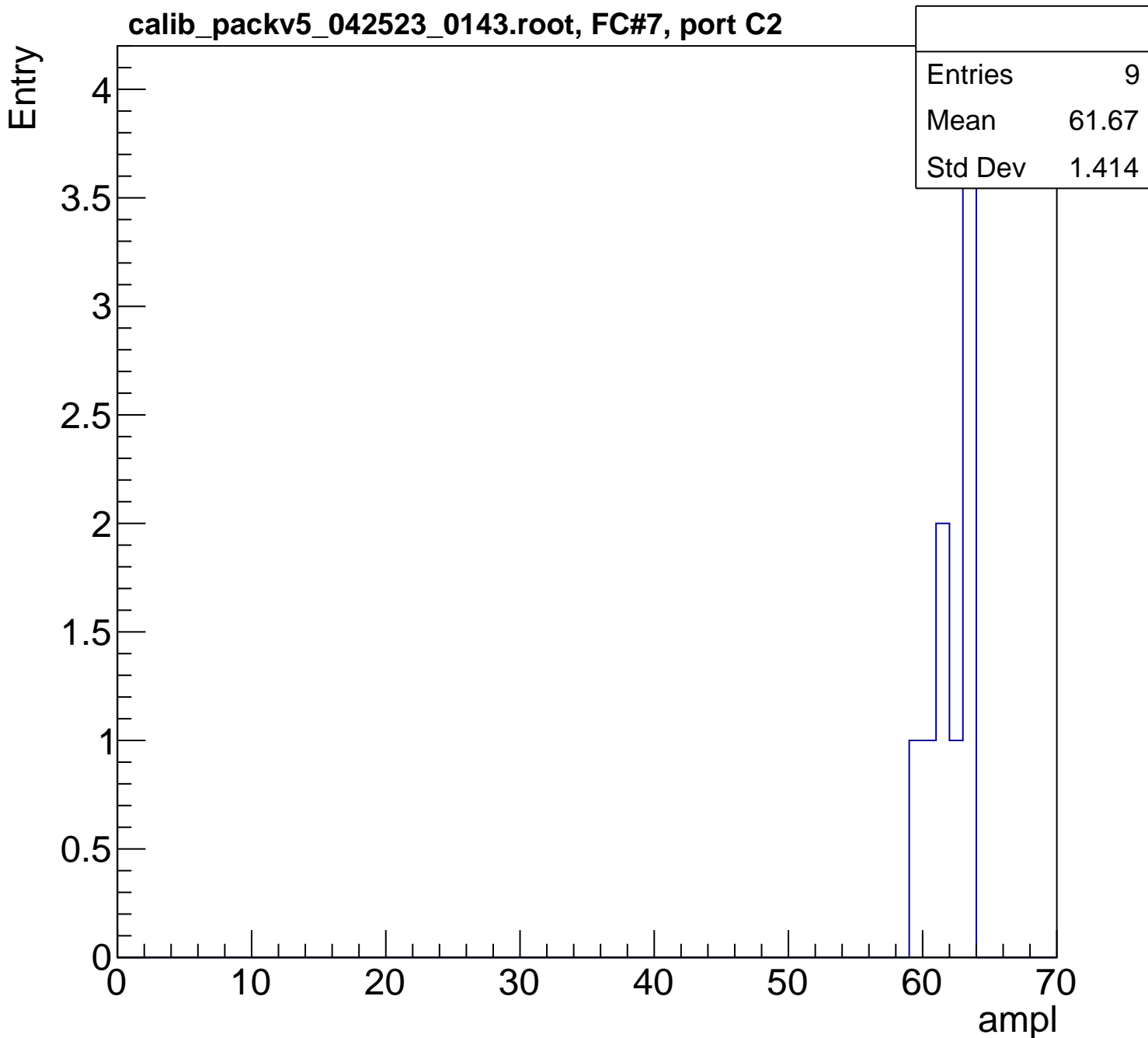
Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	9
Mean	61.67
Std Dev	1.414

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch100, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch101, adc0

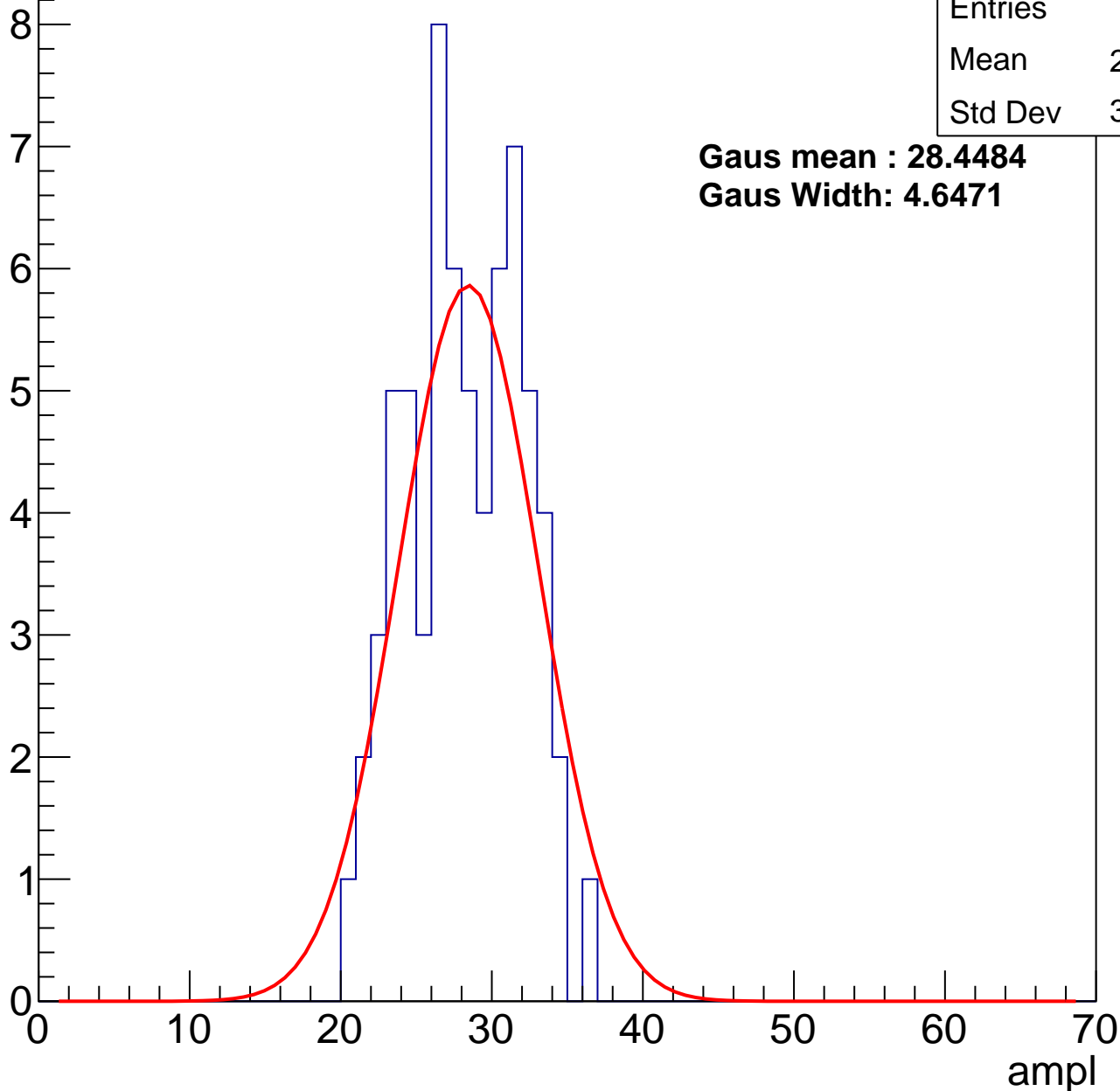
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	27.72
Std Dev	3.749

Gaus mean : 28.4484

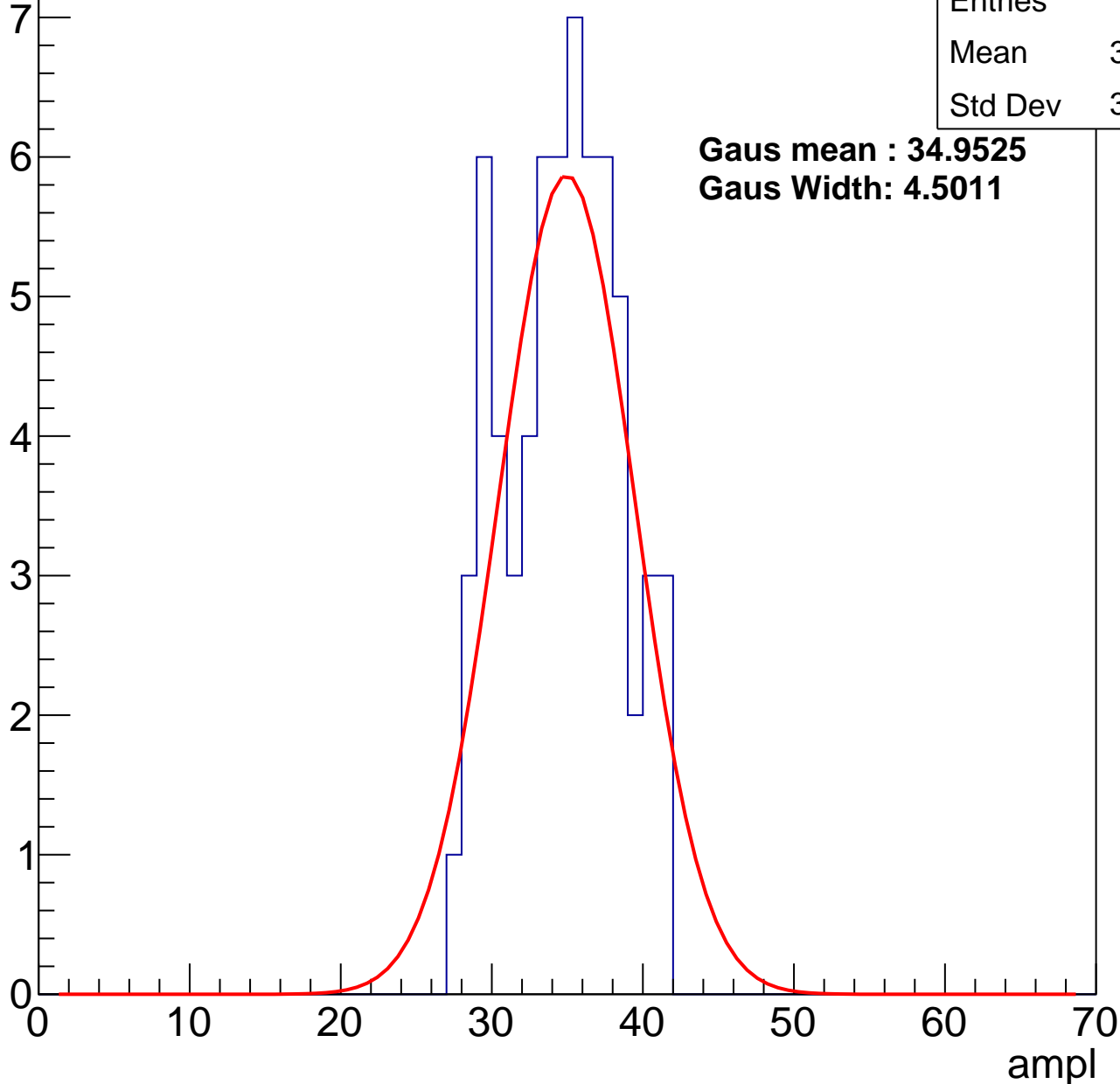
Gaus Width: 4.6471



B1L103S, U10-ch101, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch101, adc2

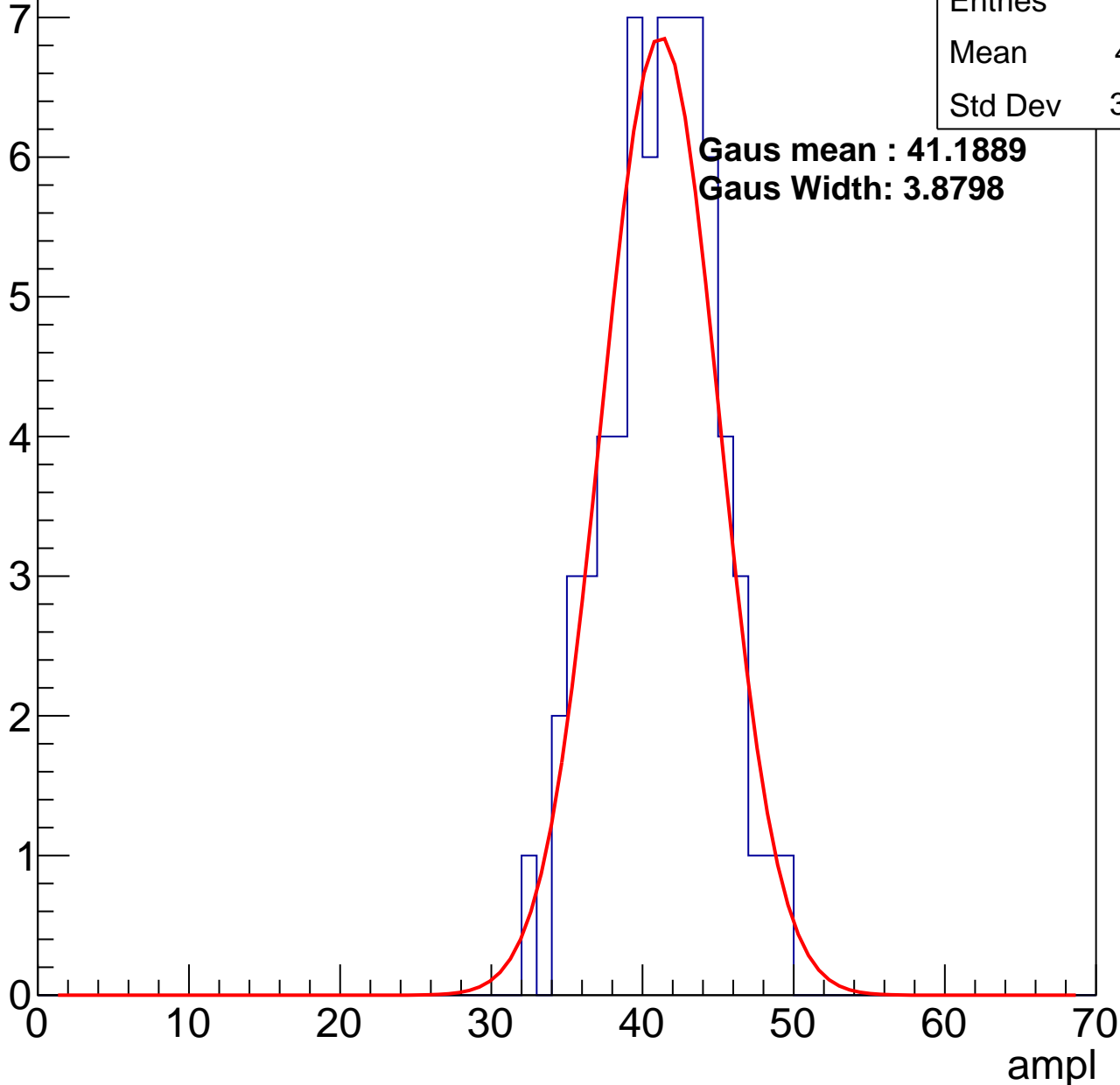
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	40.81
Std Dev	3.604

Gaus mean : 41.1889

Gaus Width: 3.8798

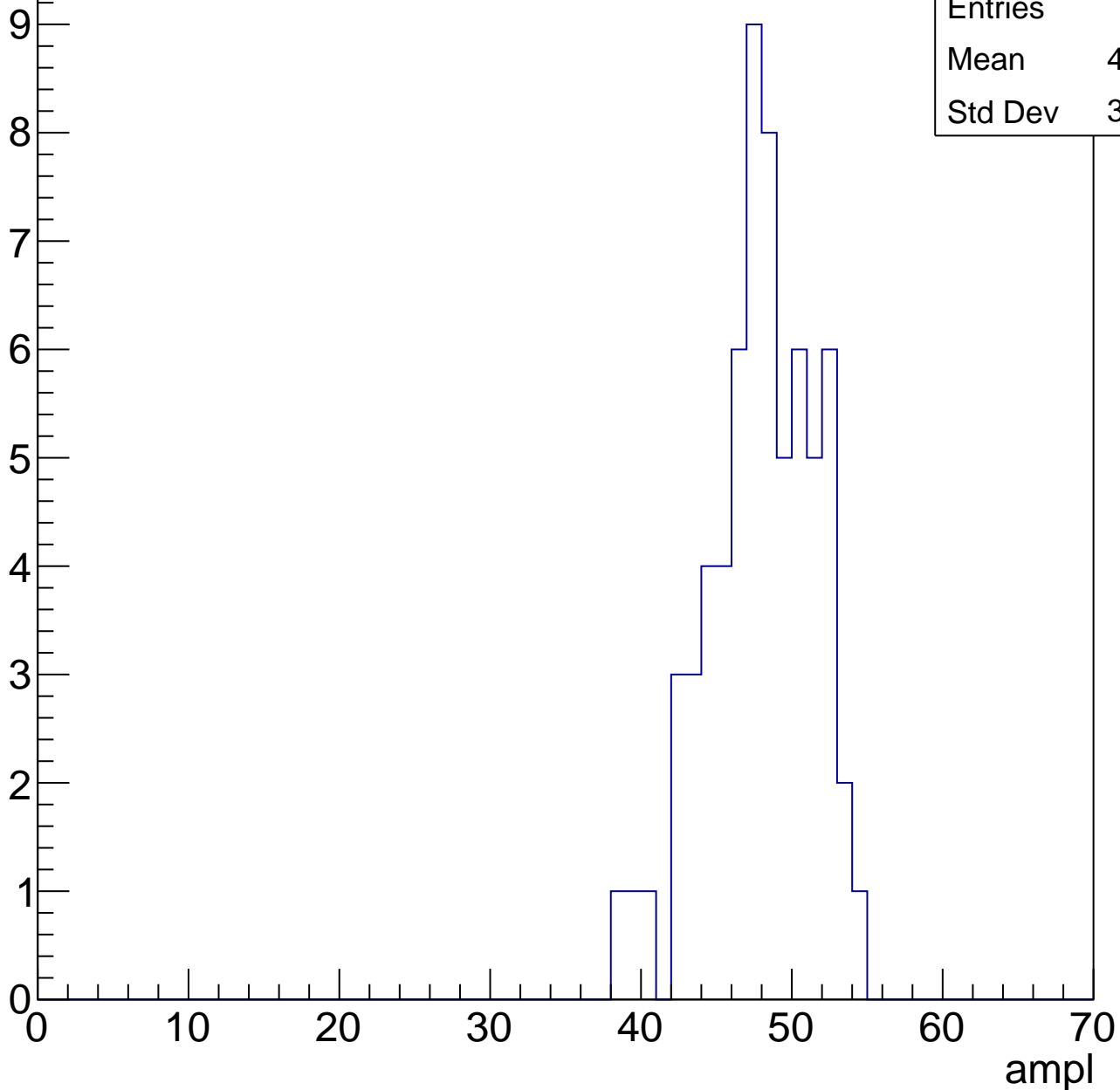


B1L103S, U10-ch101, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	47.43
Std Dev	3.499

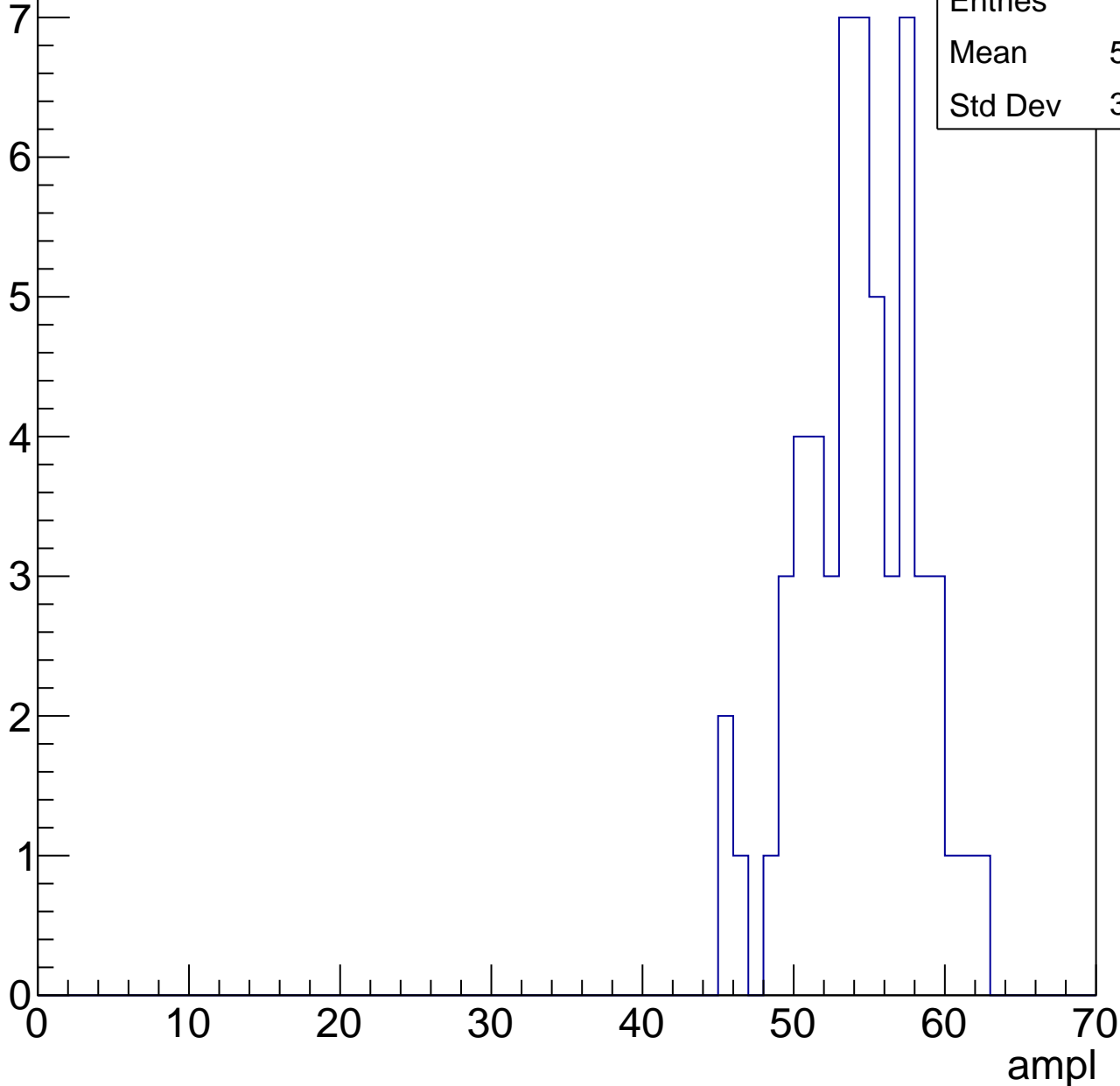


B1L103S, U10-ch101, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	53.86
Std Dev	3.796

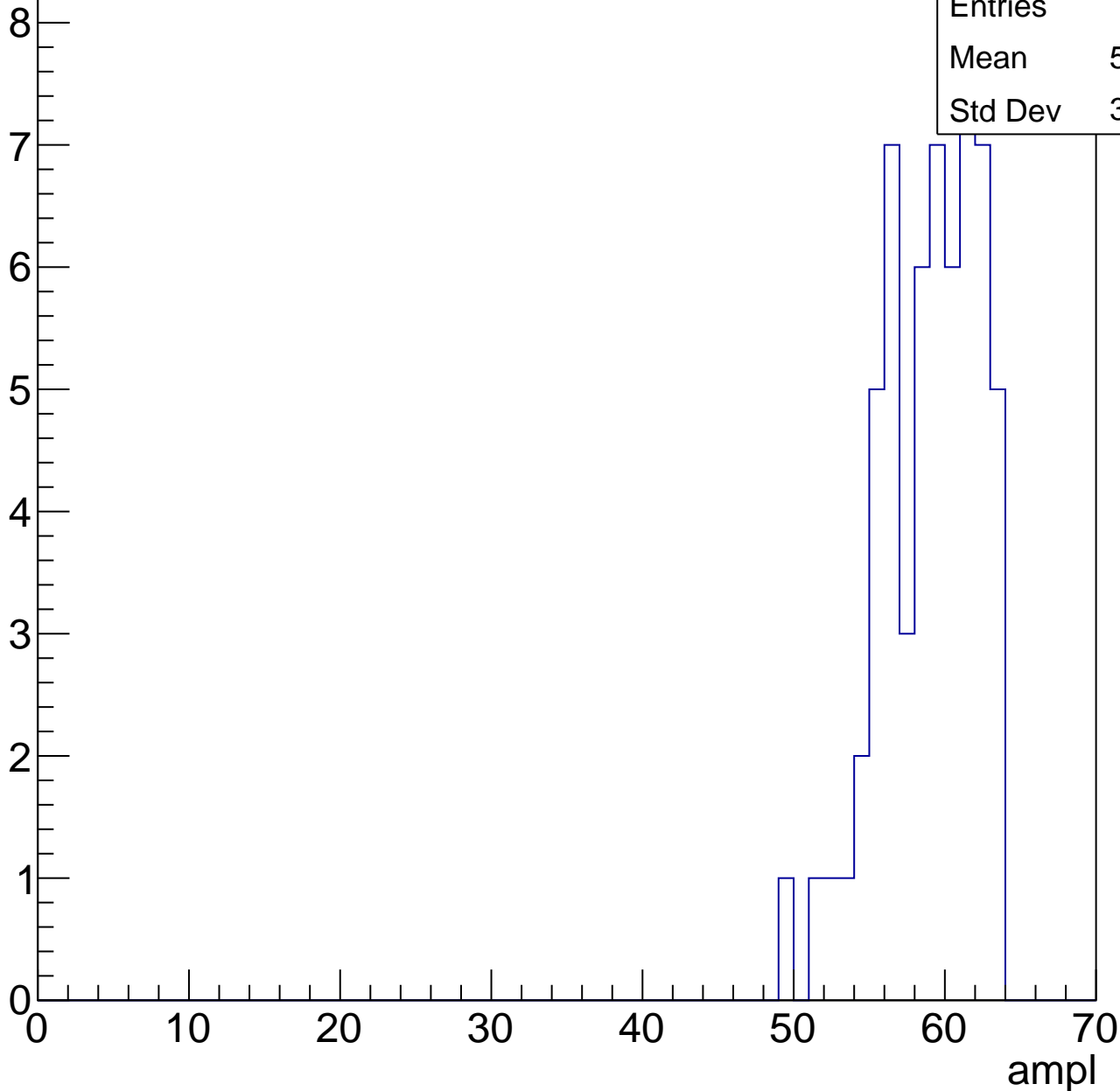


B1L103S, U10-ch101, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	58.48
Std Dev	3.227

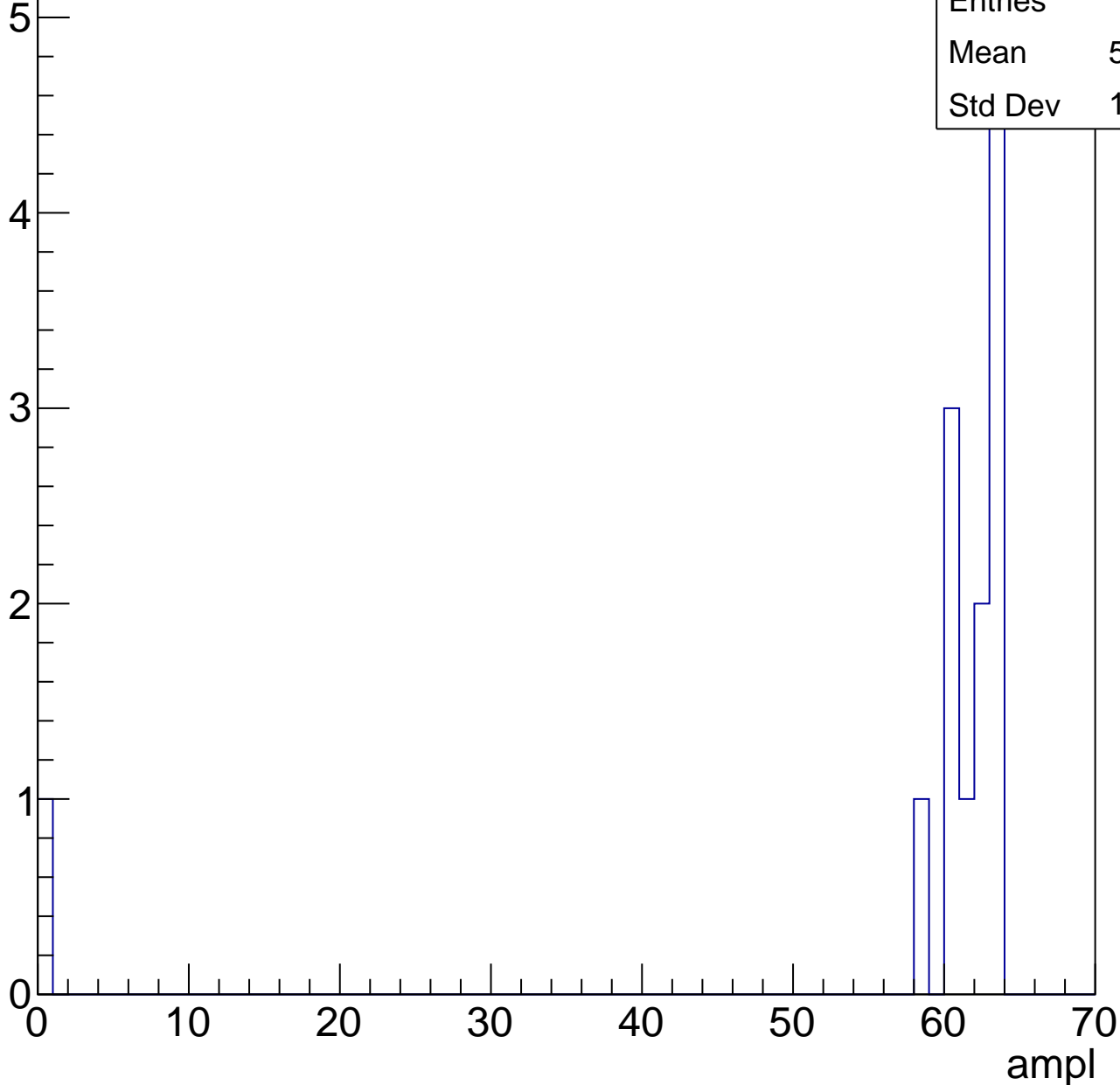


B1L103S, U10-ch101, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

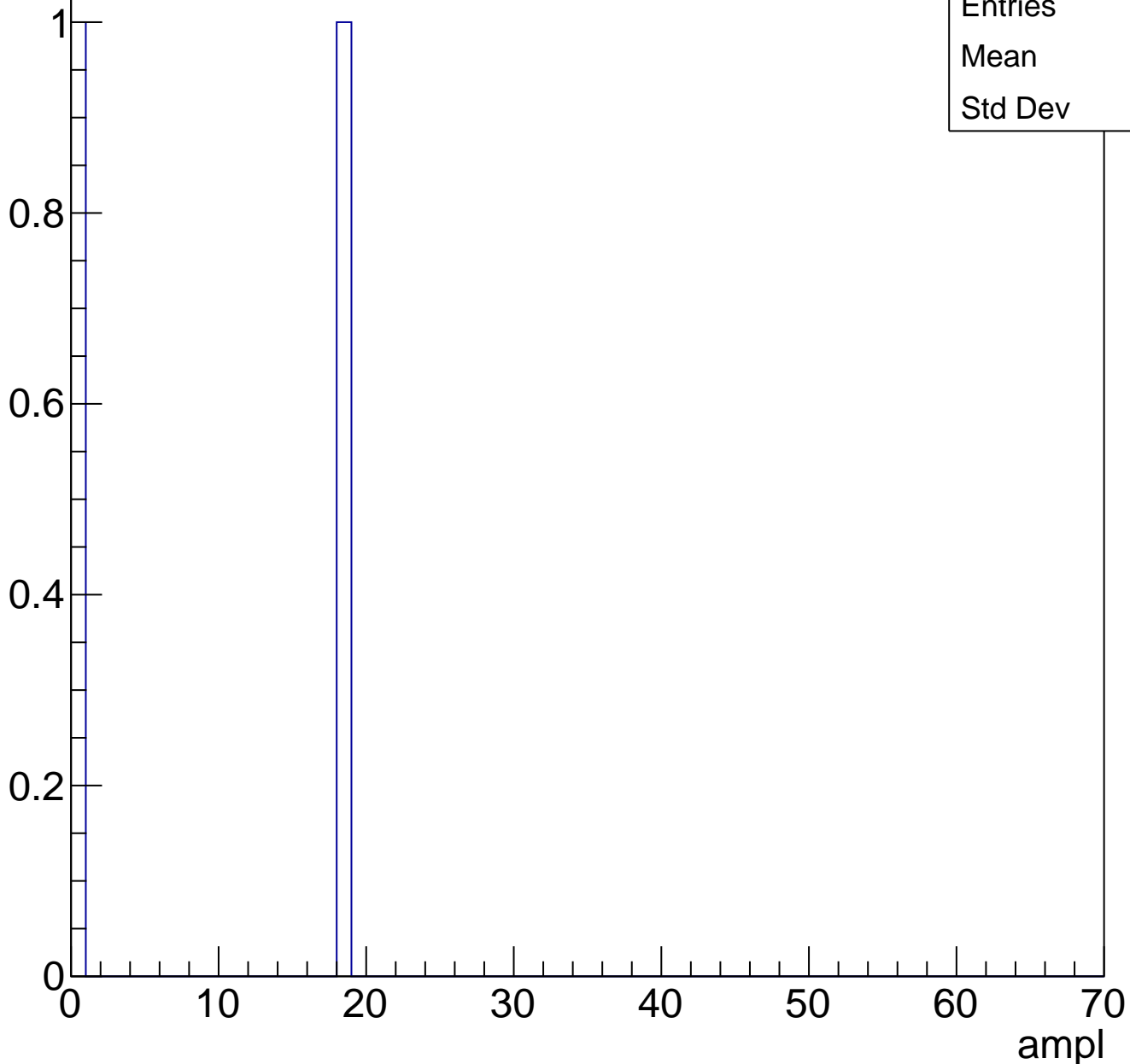
Entries	13
Mean	56.77
Std Dev	16.46



B1L103S, U10-ch101, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch102, adc0

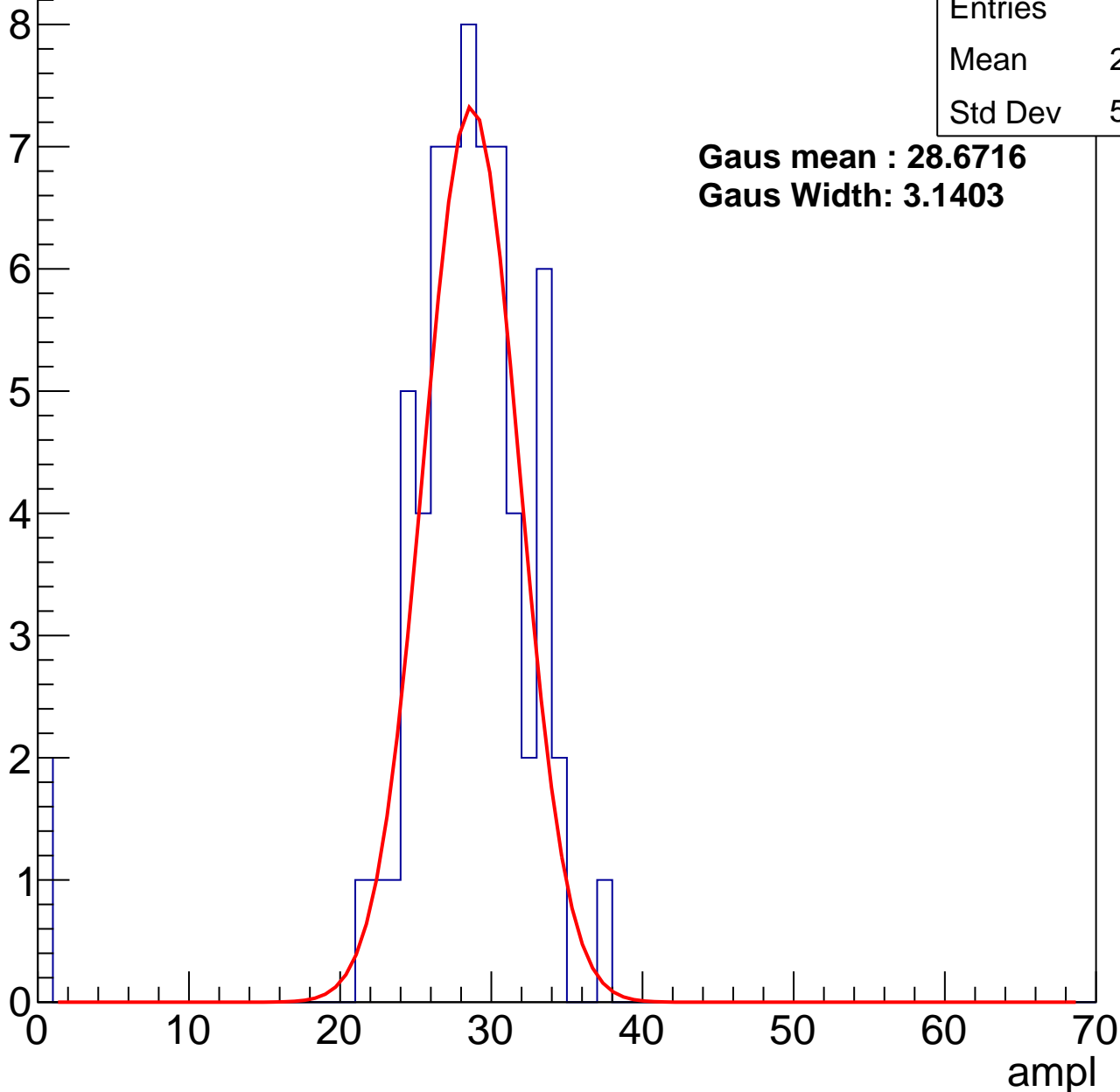
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	27.46
Std Dev	5.839

Gaus mean : 28.6716

Gaus Width: 3.1403



B1L103S, U10-ch102, adc1

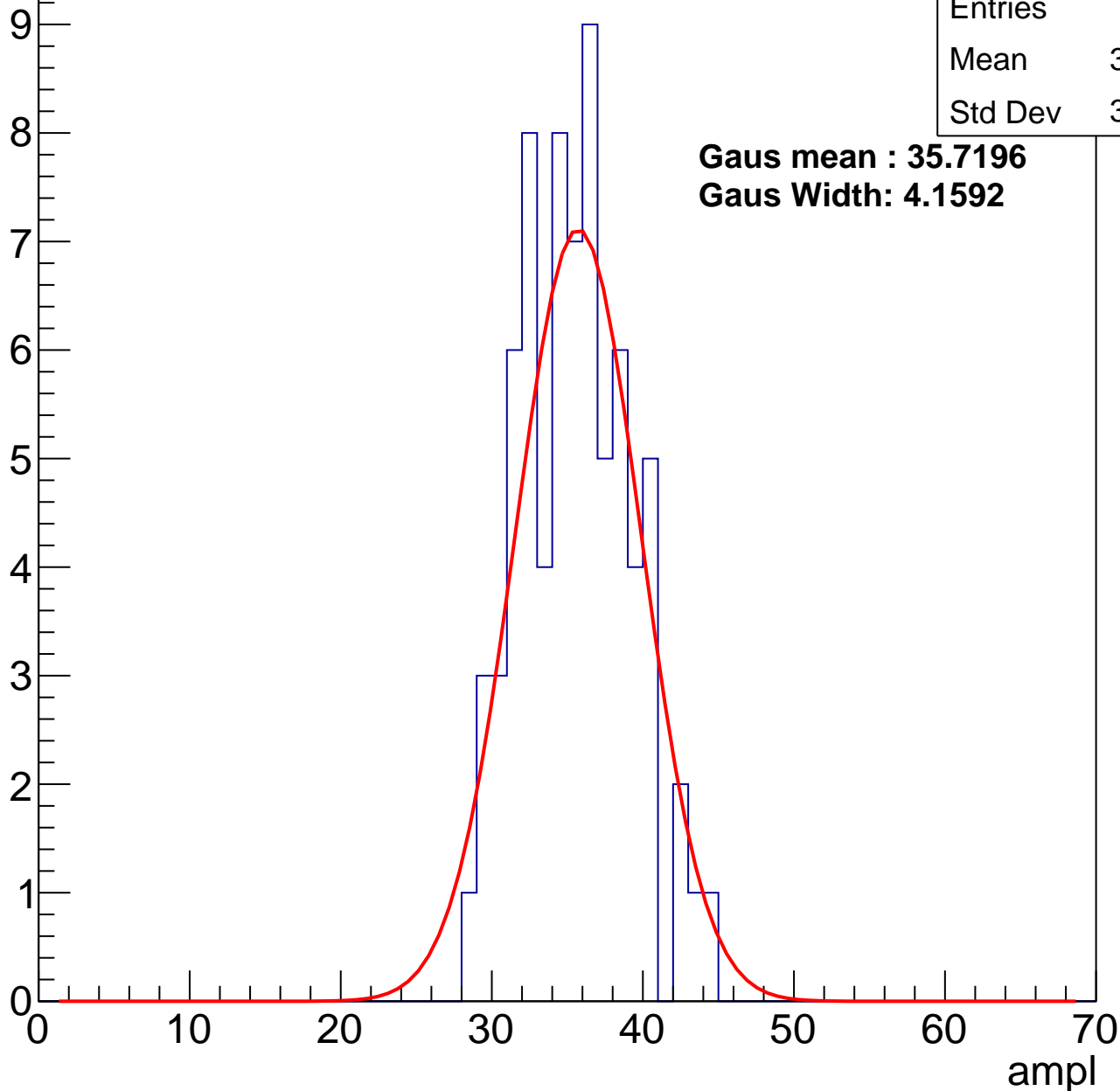
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	35.07
Std Dev	3.586

Gaus mean : 35.7196

Gaus Width: 4.1592



B1L103S, U10-ch102, adc2

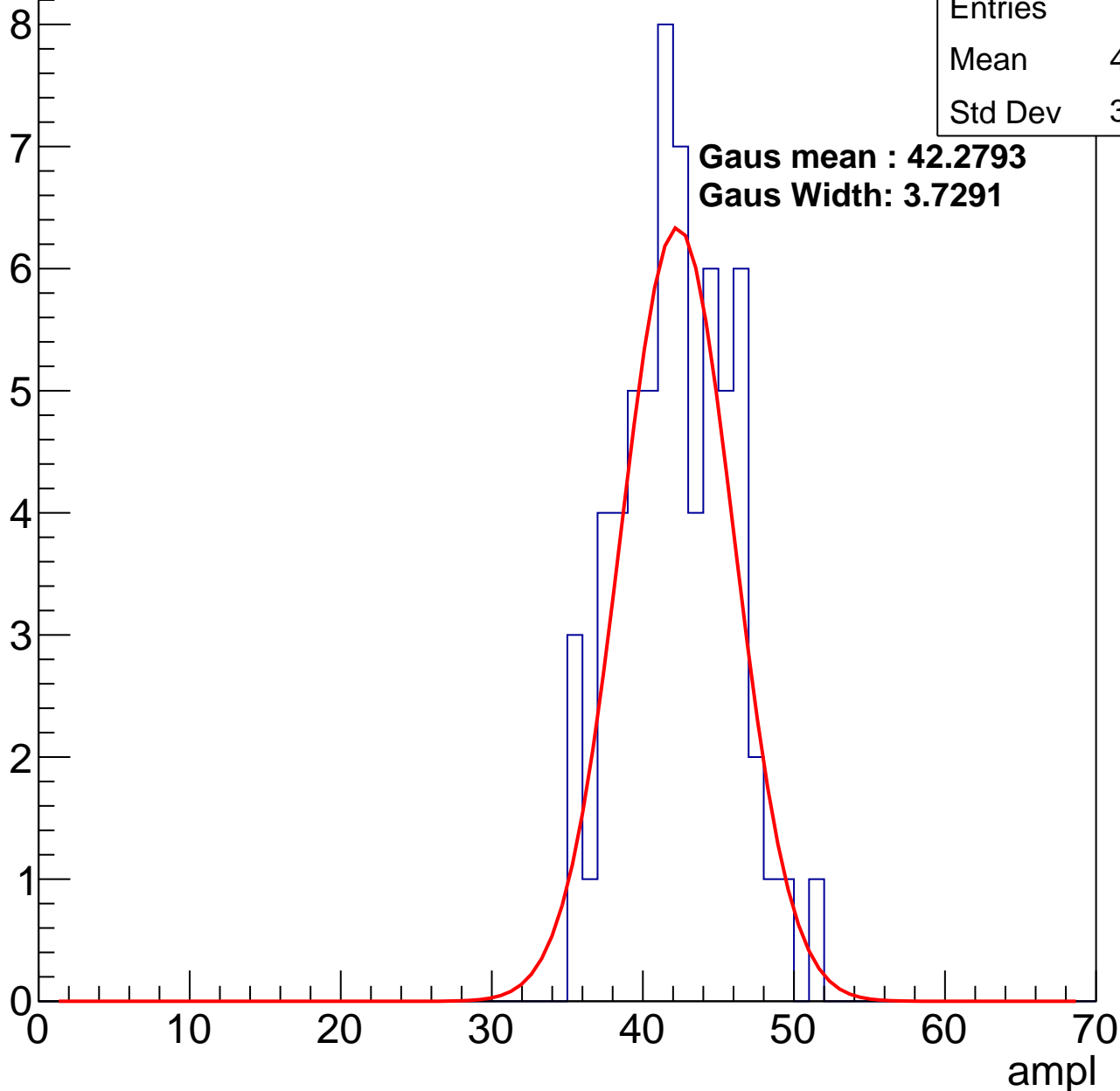
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	41.86
Std Dev	3.572

Gaus mean : 42.2793

Gaus Width: 3.7291

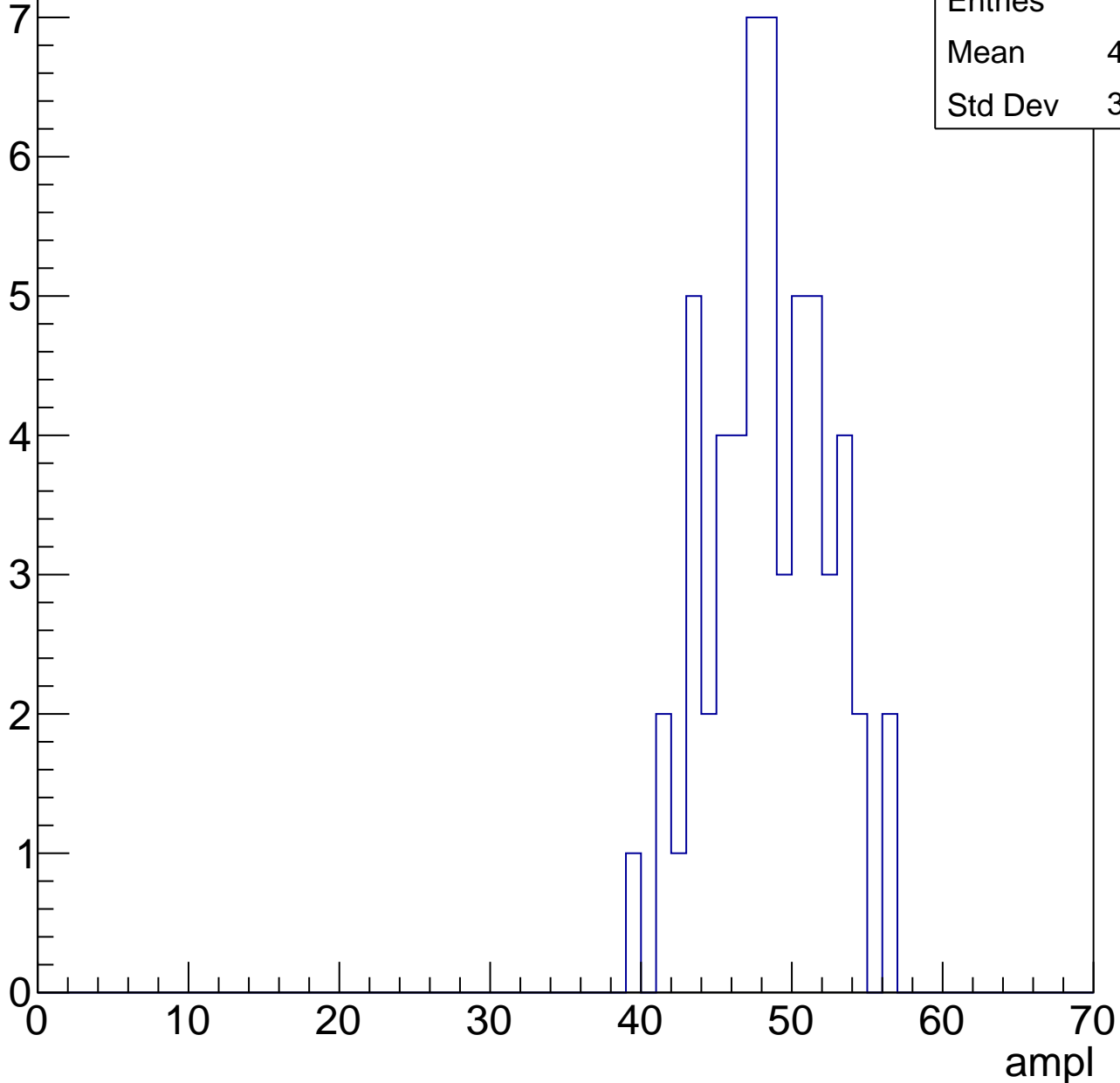


B1L103S, U10-ch102, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	47.98
Std Dev	3.855

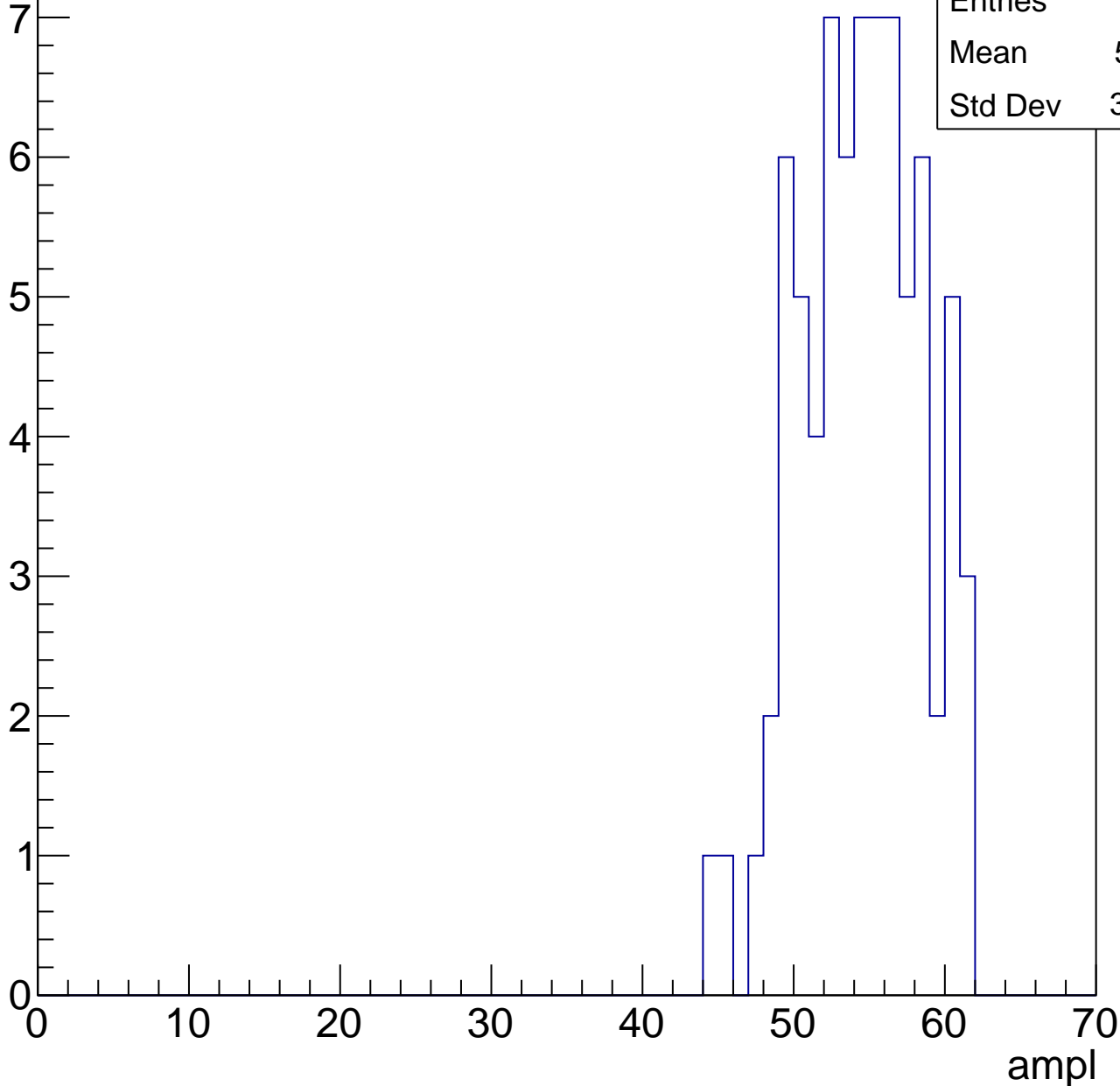


B1L103S, U10-ch102, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	54.01
Std Dev	3.924

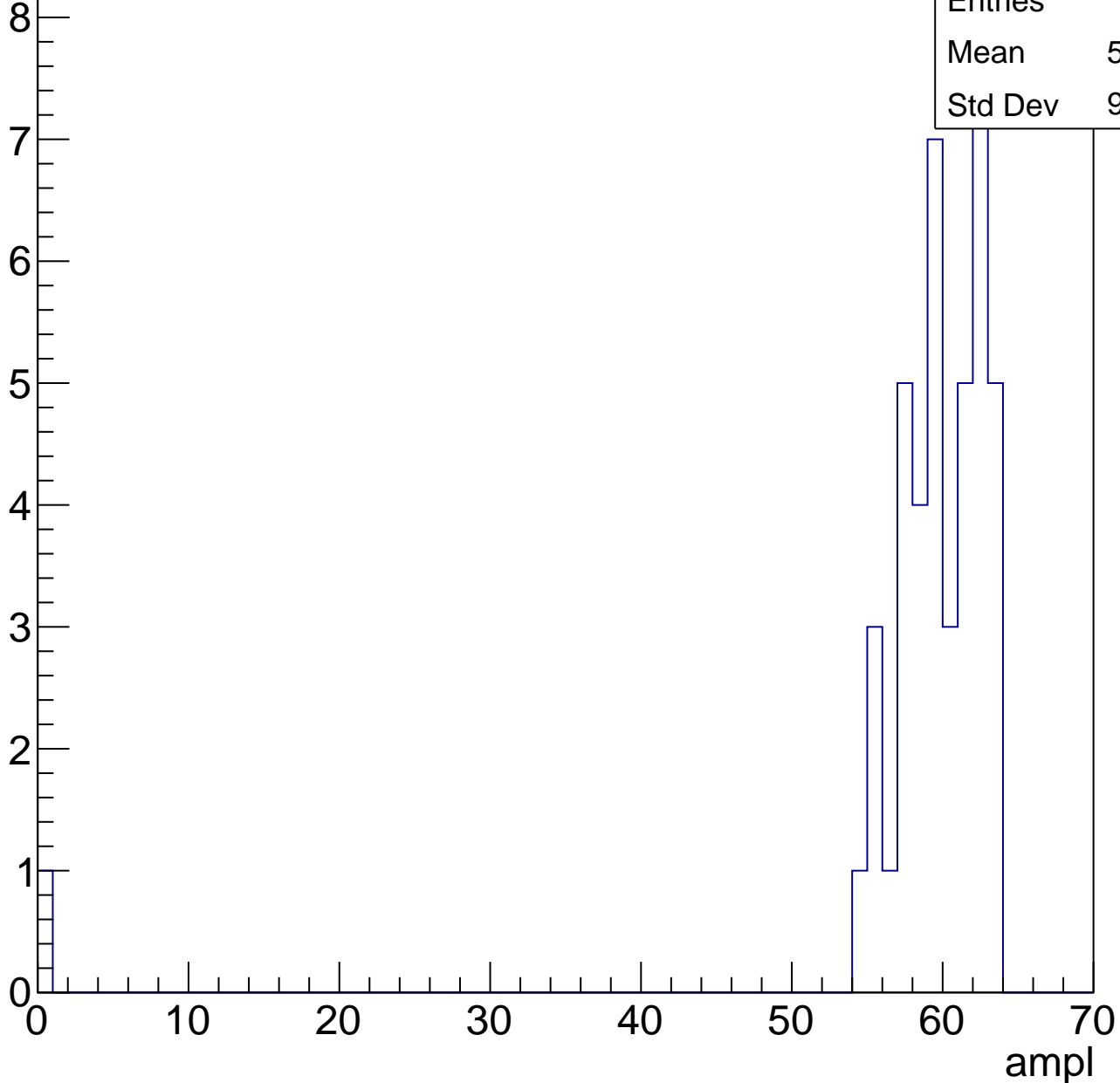


B1L103S, U10-ch102, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

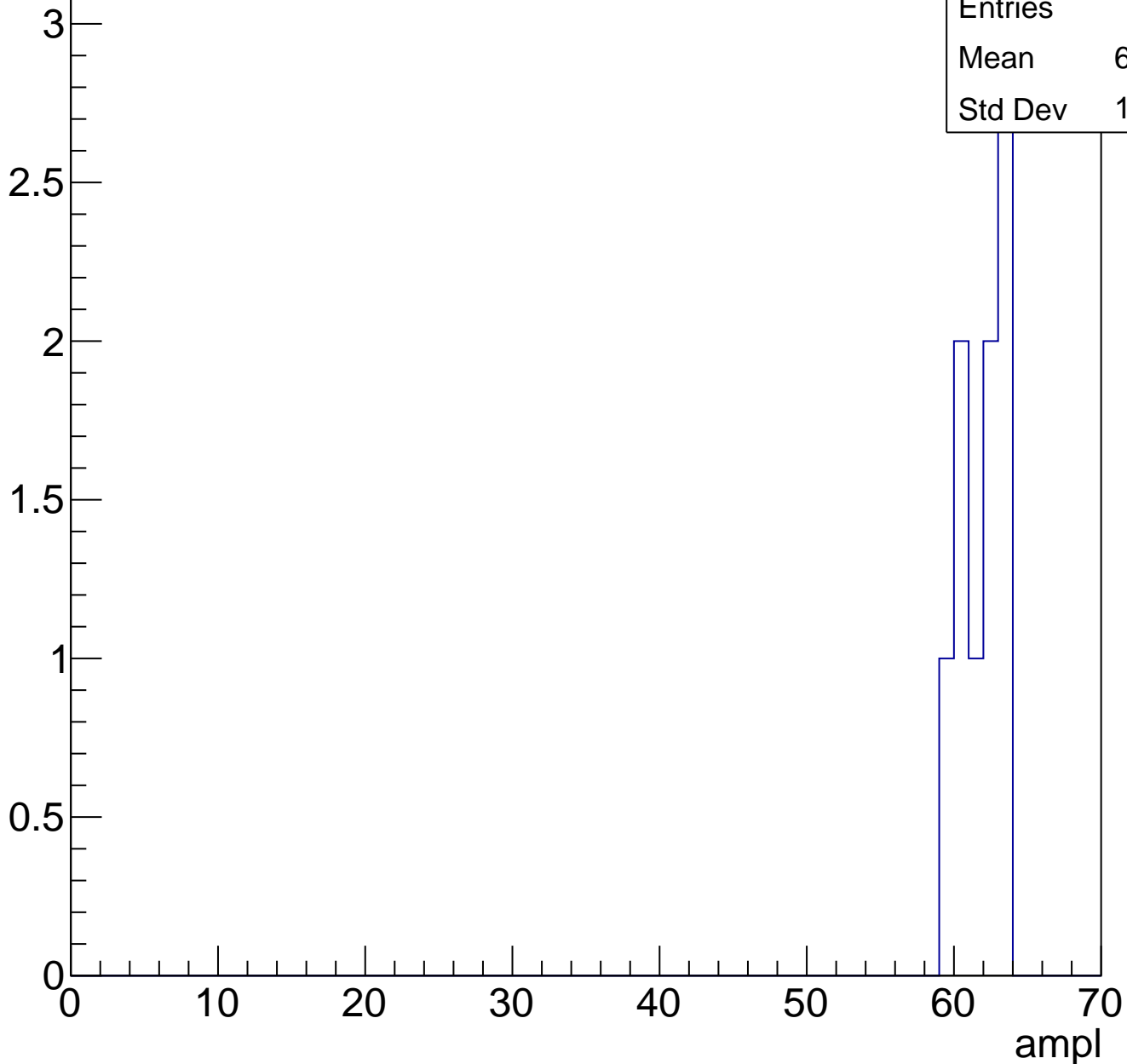
Entries	43
Mean	58.16
Std Dev	9.316



B1L103S, U10-ch102, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch102, adc7

calib_packv5_042523_0143.root, FC#7, port C2

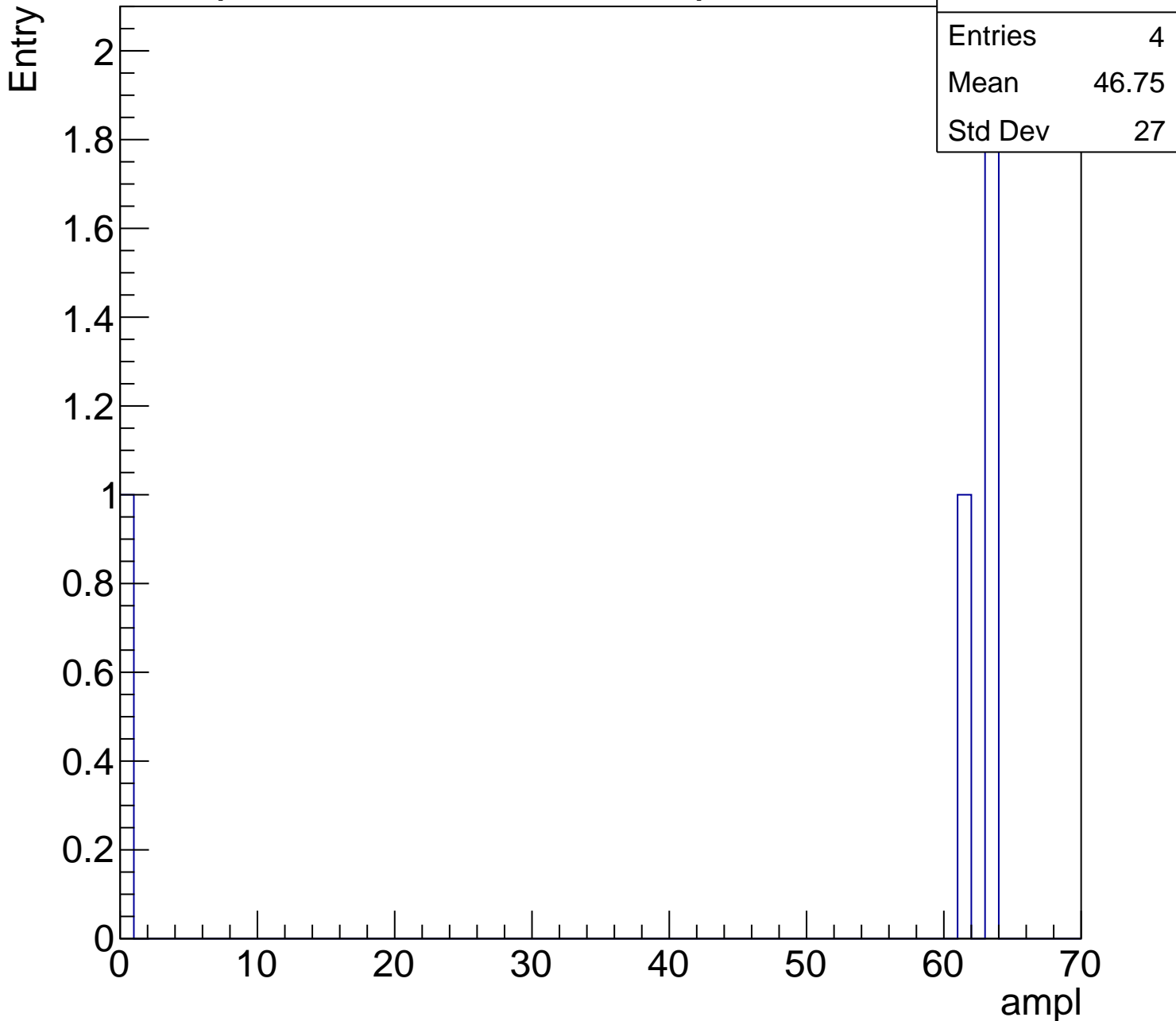
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	46.75
Std Dev	27

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch103, adc0

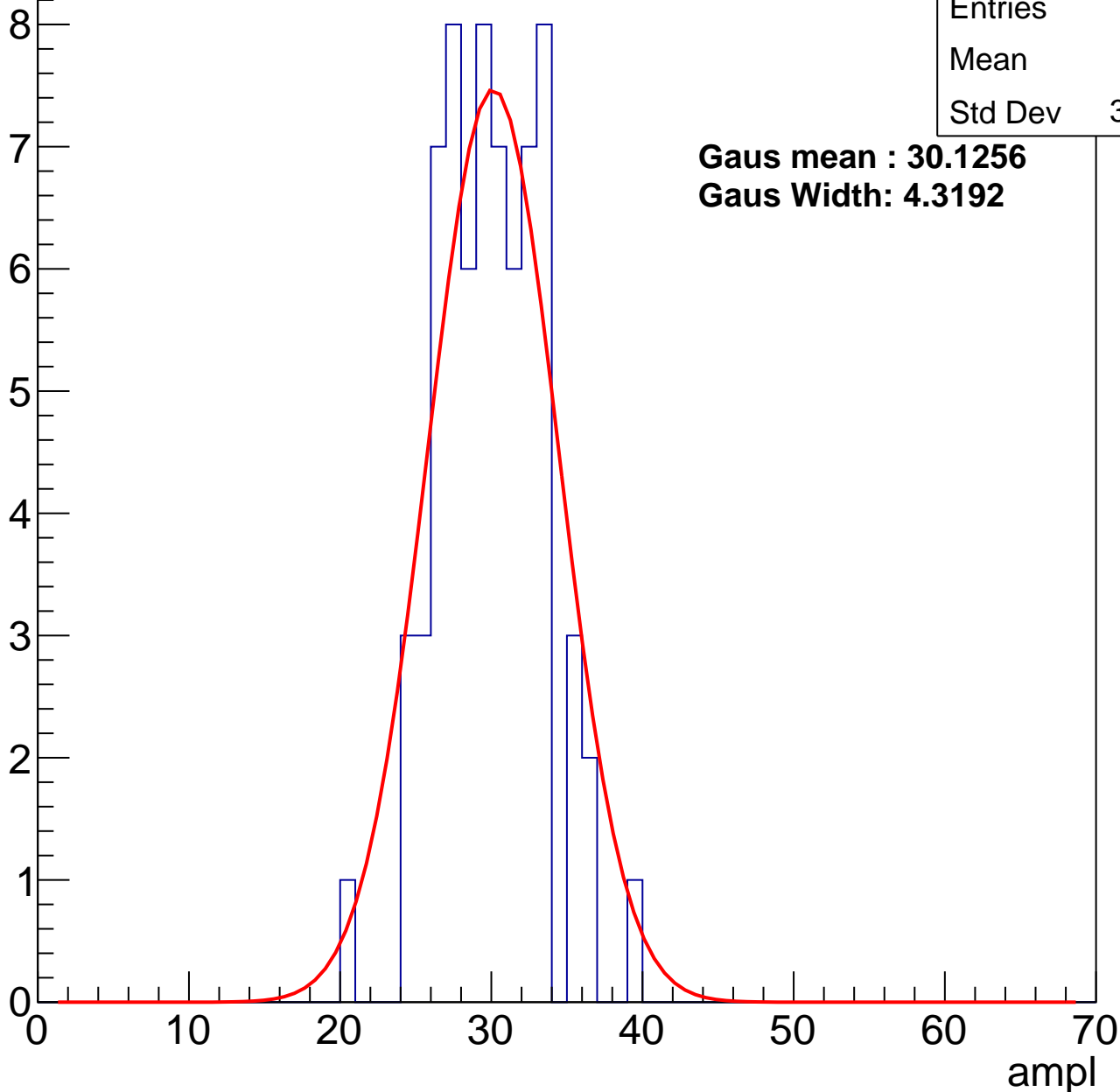
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.5
Std Dev	3.413

Gaus mean : 30.1256

Gaus Width: 4.3192



B1L103S, U10-ch103, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	79
Mean	37.14
Std Dev	3.945

Gaus mean : 37.9080

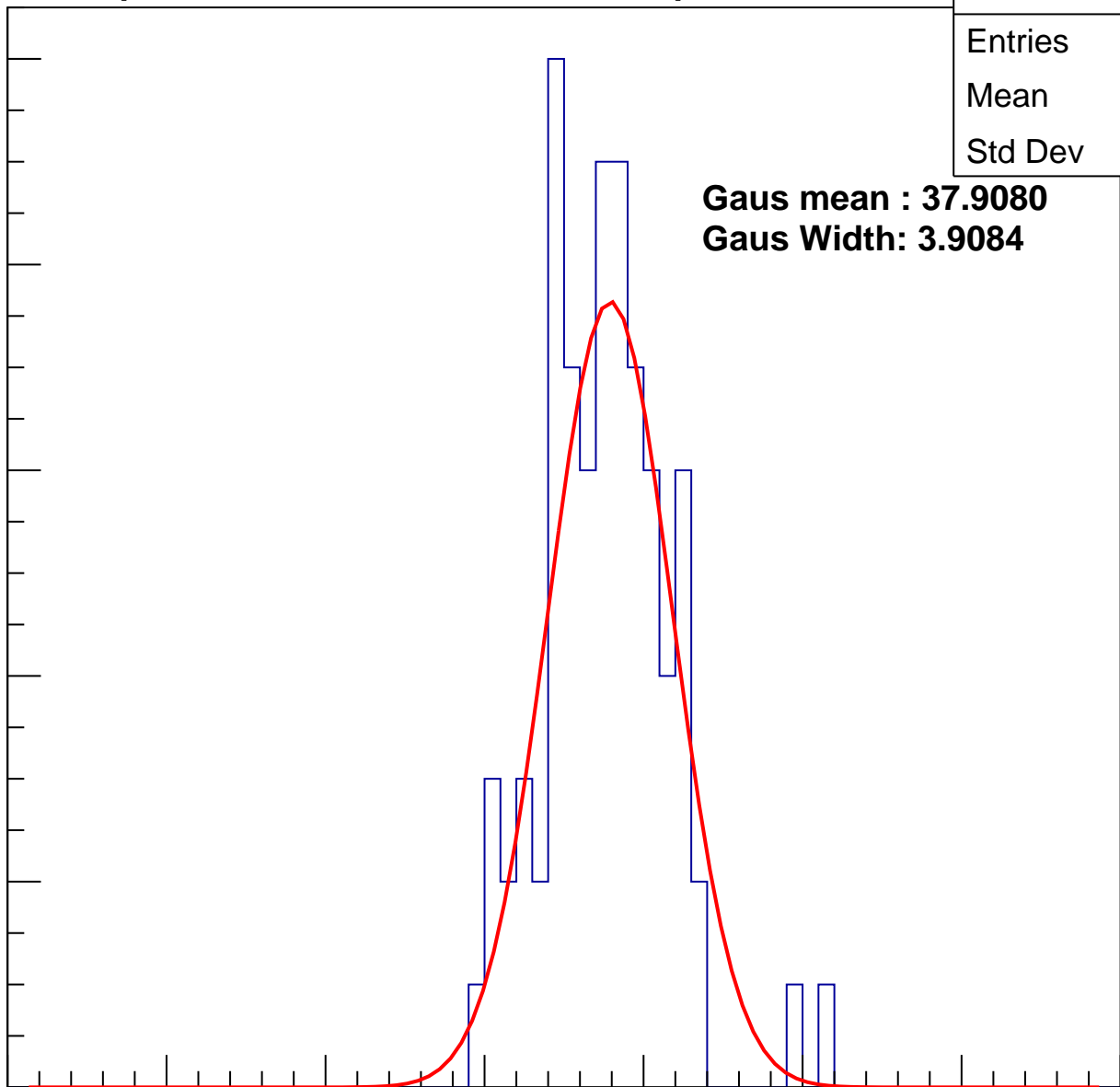
Gaus Width: 3.9084

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch103, adc2

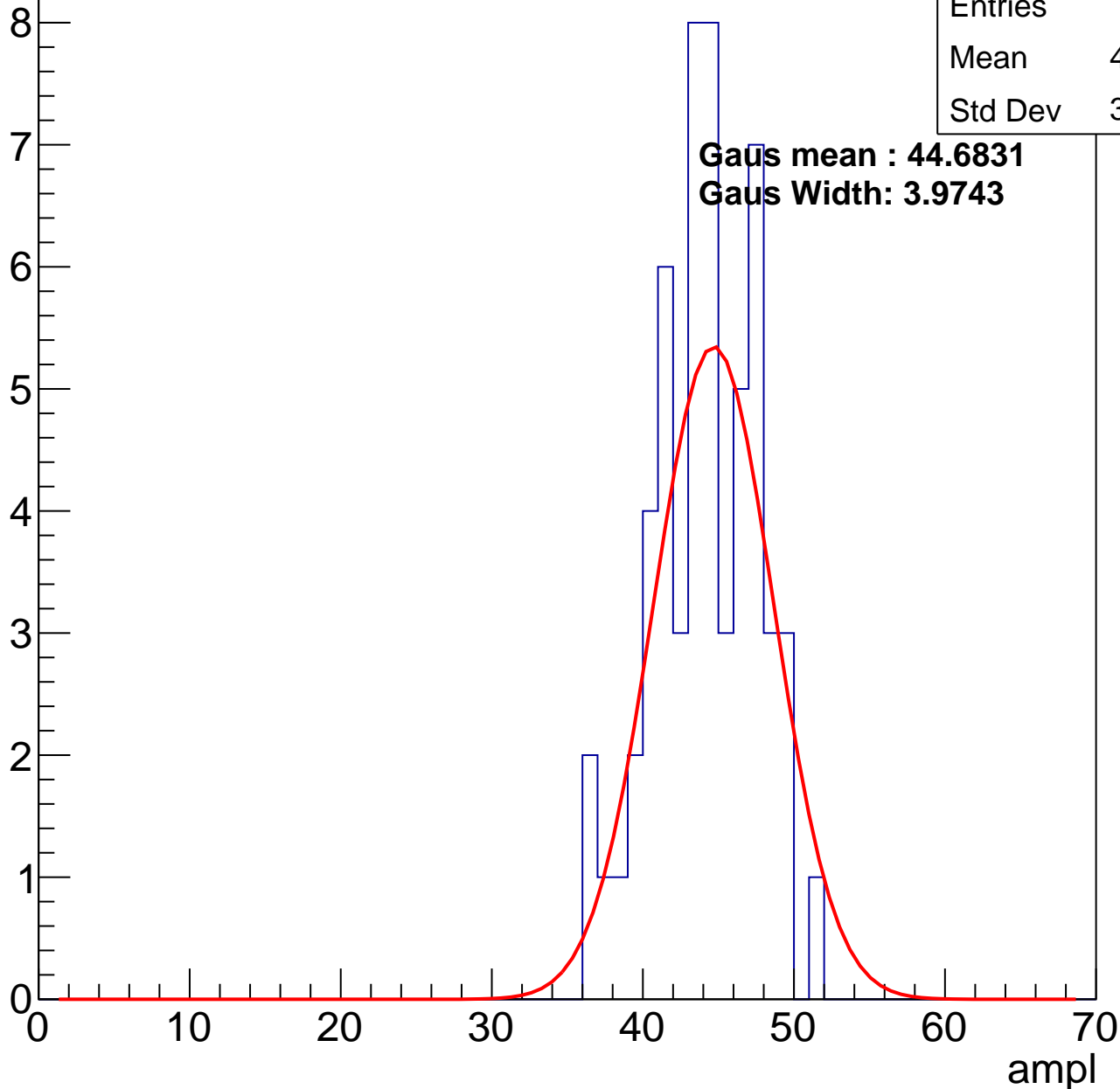
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	43.67
Std Dev	3.384

Gaus mean : 44.6831

Gaus Width: 3.9743



B1L103S, U10-ch103, adc3

calib_packv5_042523_0143.root, FC#7, port C2

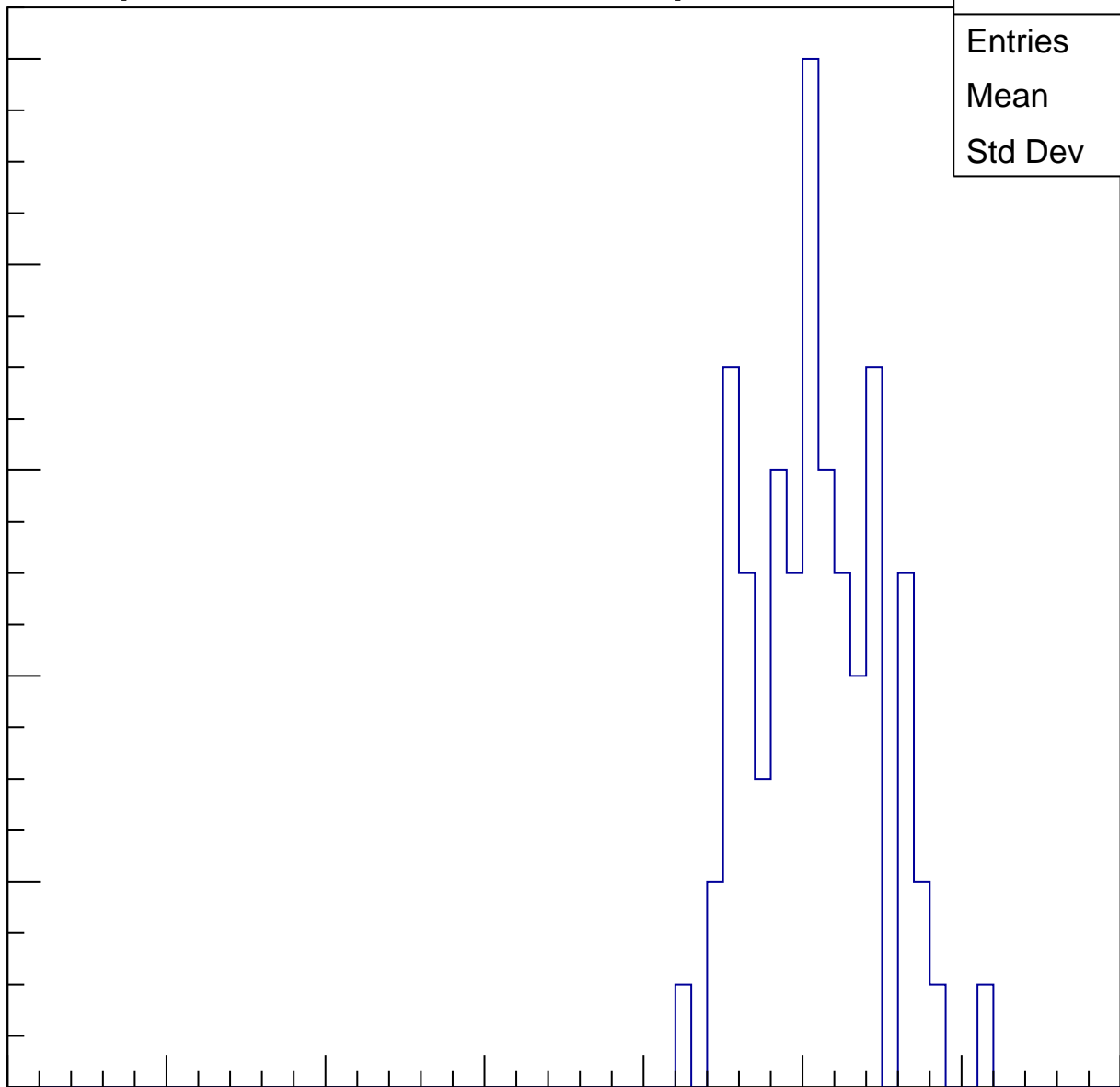
Entries	70
Mean	50.26
Std Dev	3.927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

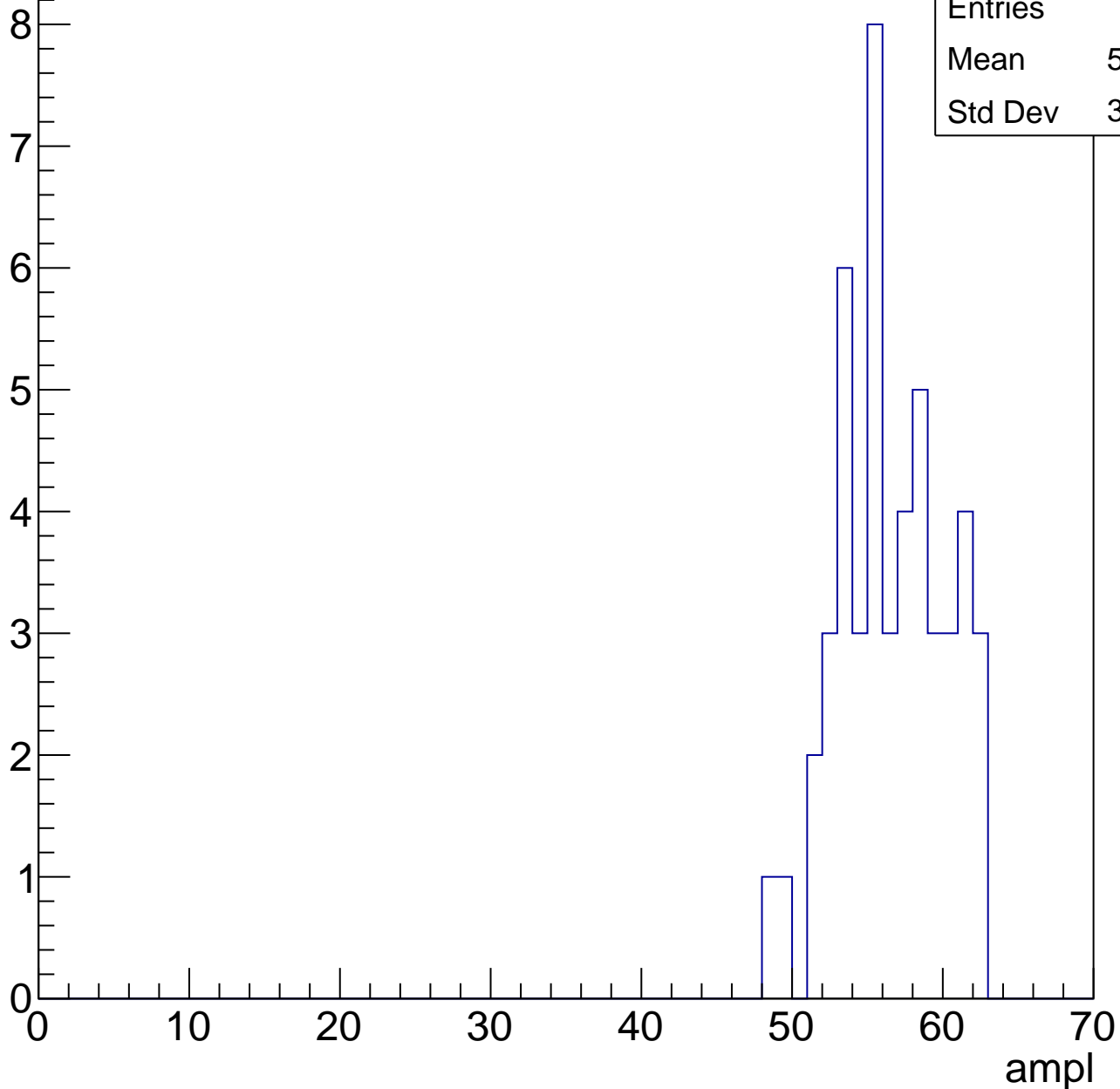


B1L103S, U10-ch103, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	56.08
Std Dev	3.475

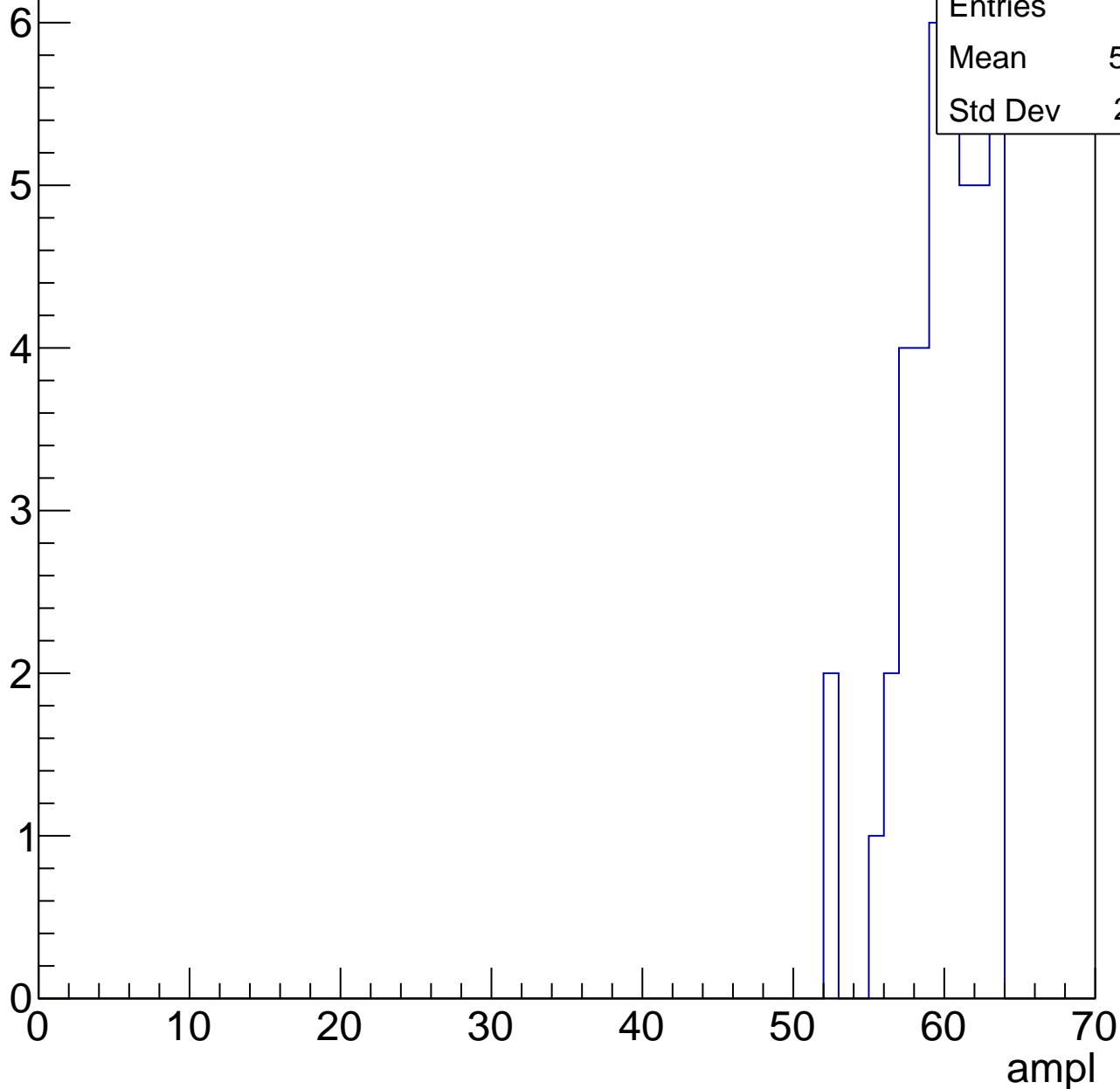


B1L103S, U10-ch103, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

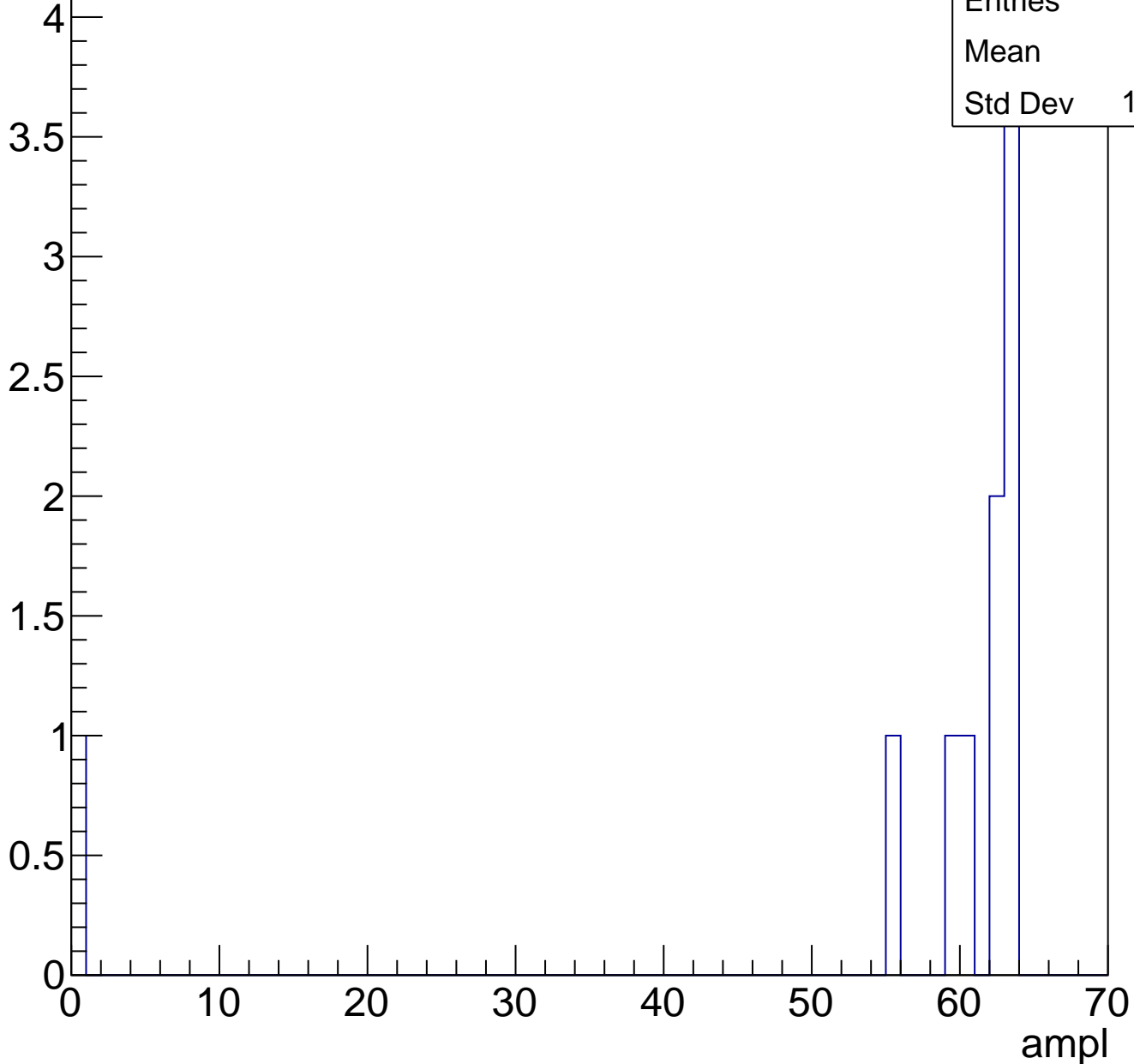
Entries	41
Mean	59.46
Std Dev	2.751



B1L103S, U10-ch103, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch103, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch104, adc0

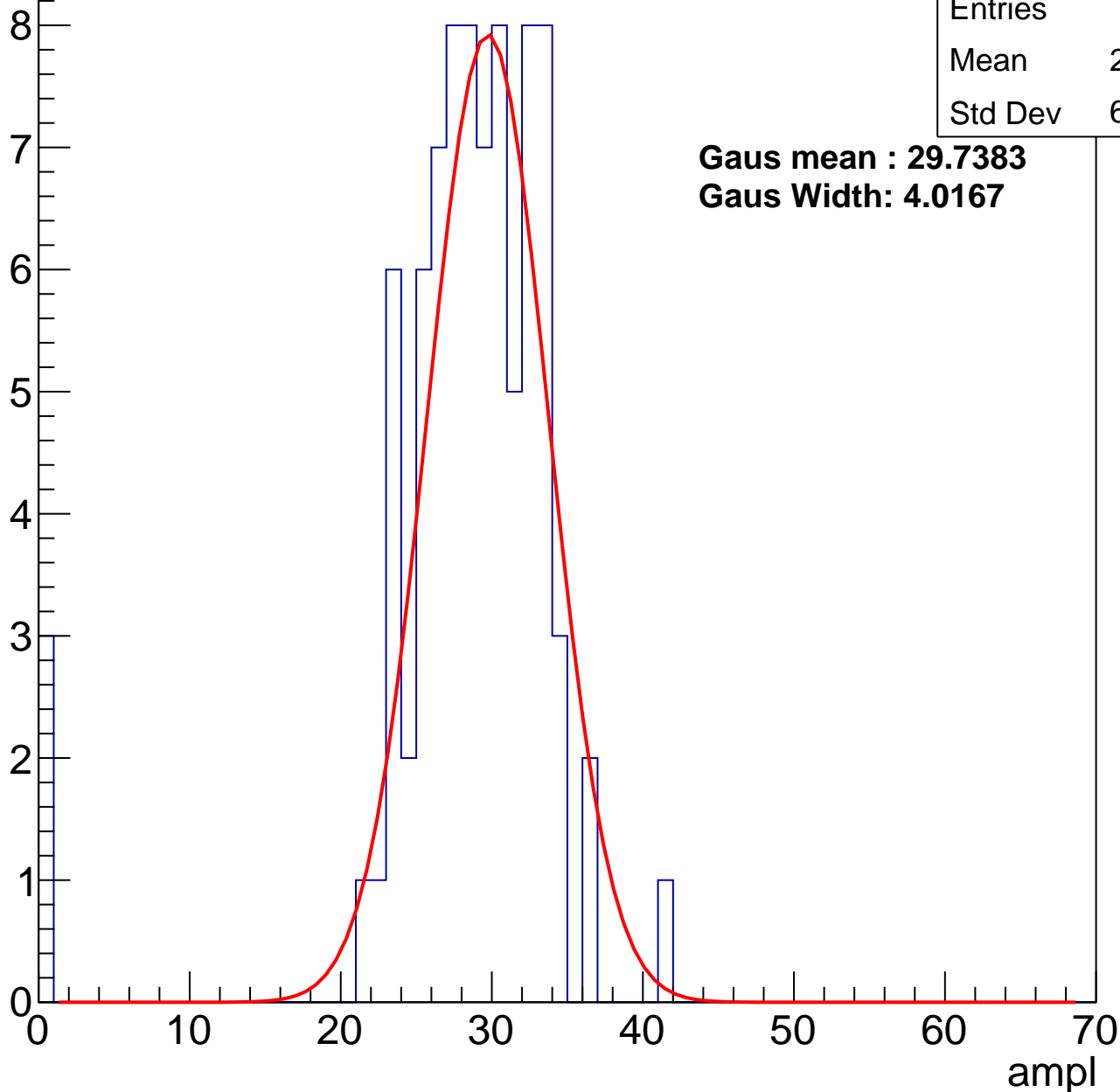
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	27.79
Std Dev	6.475

Gaus mean : 29.7383

Gaus Width: 4.0167



B1L103S, U10-ch104, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	66
Mean	35.92
Std Dev	3.932

Gaus mean : 36.6044

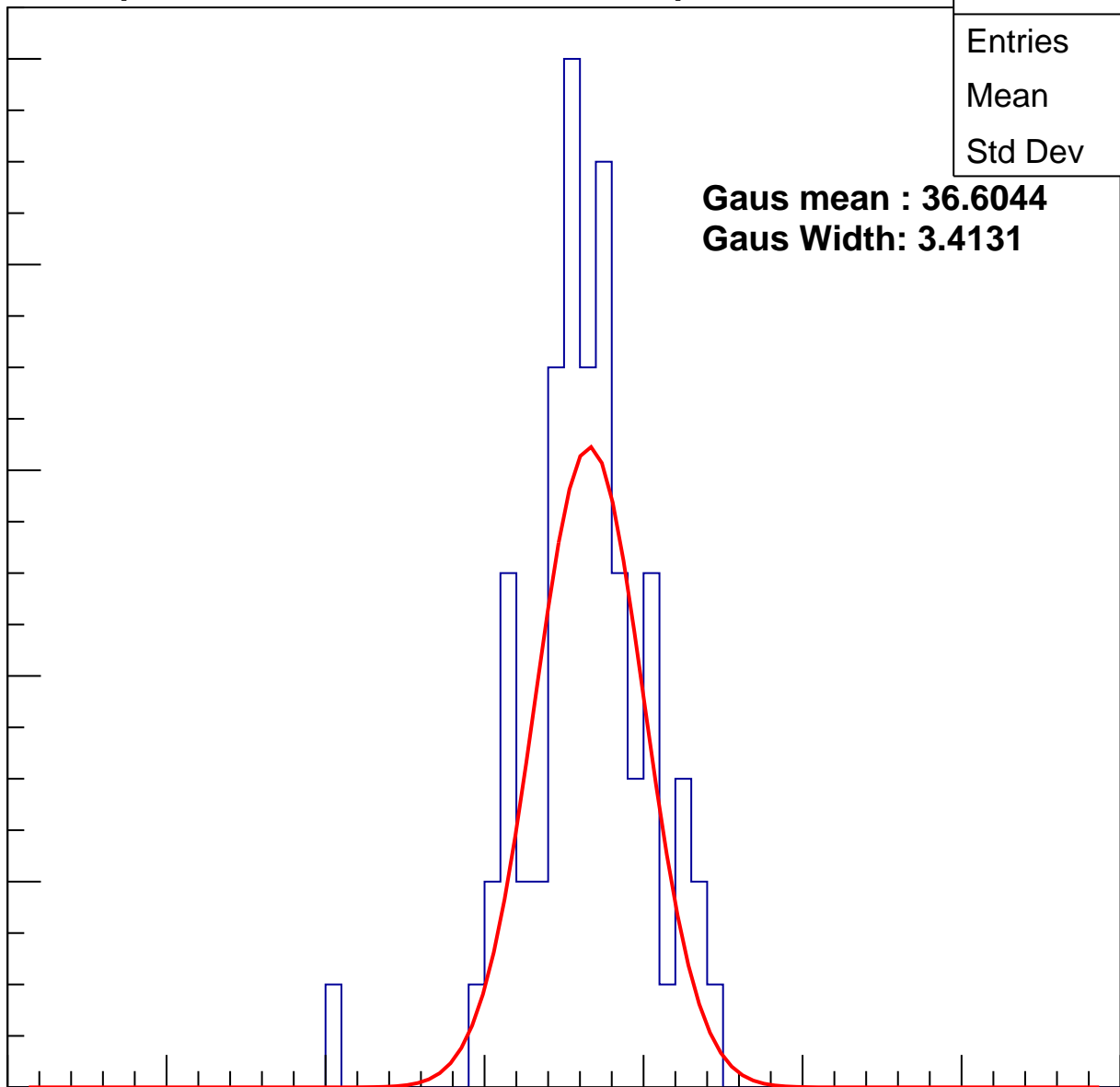
Gaus Width: 3.4131

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch104, adc2

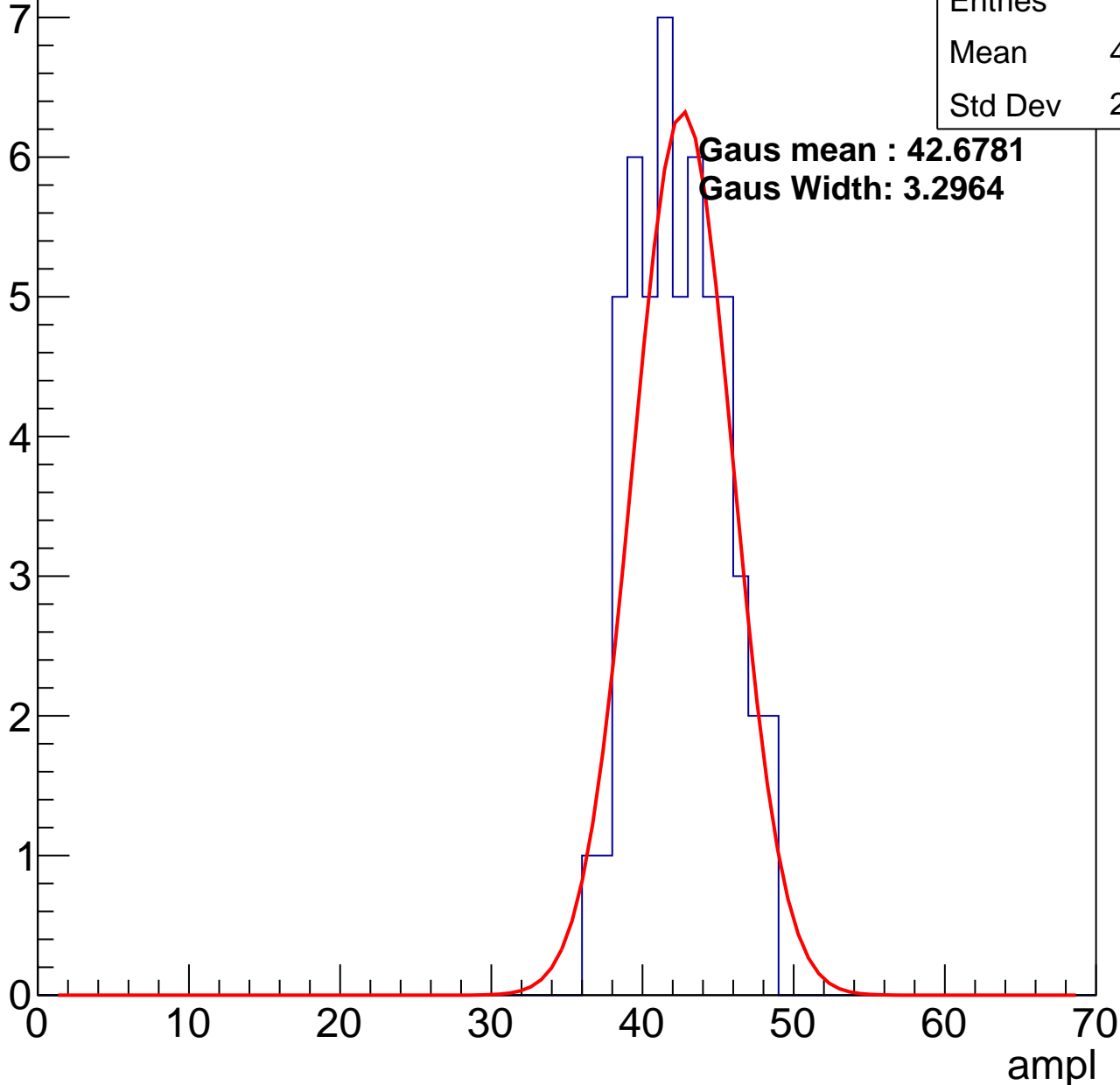
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	41.98
Std Dev	2.956

Gaus mean : 42.6781

Gaus Width: 3.2964

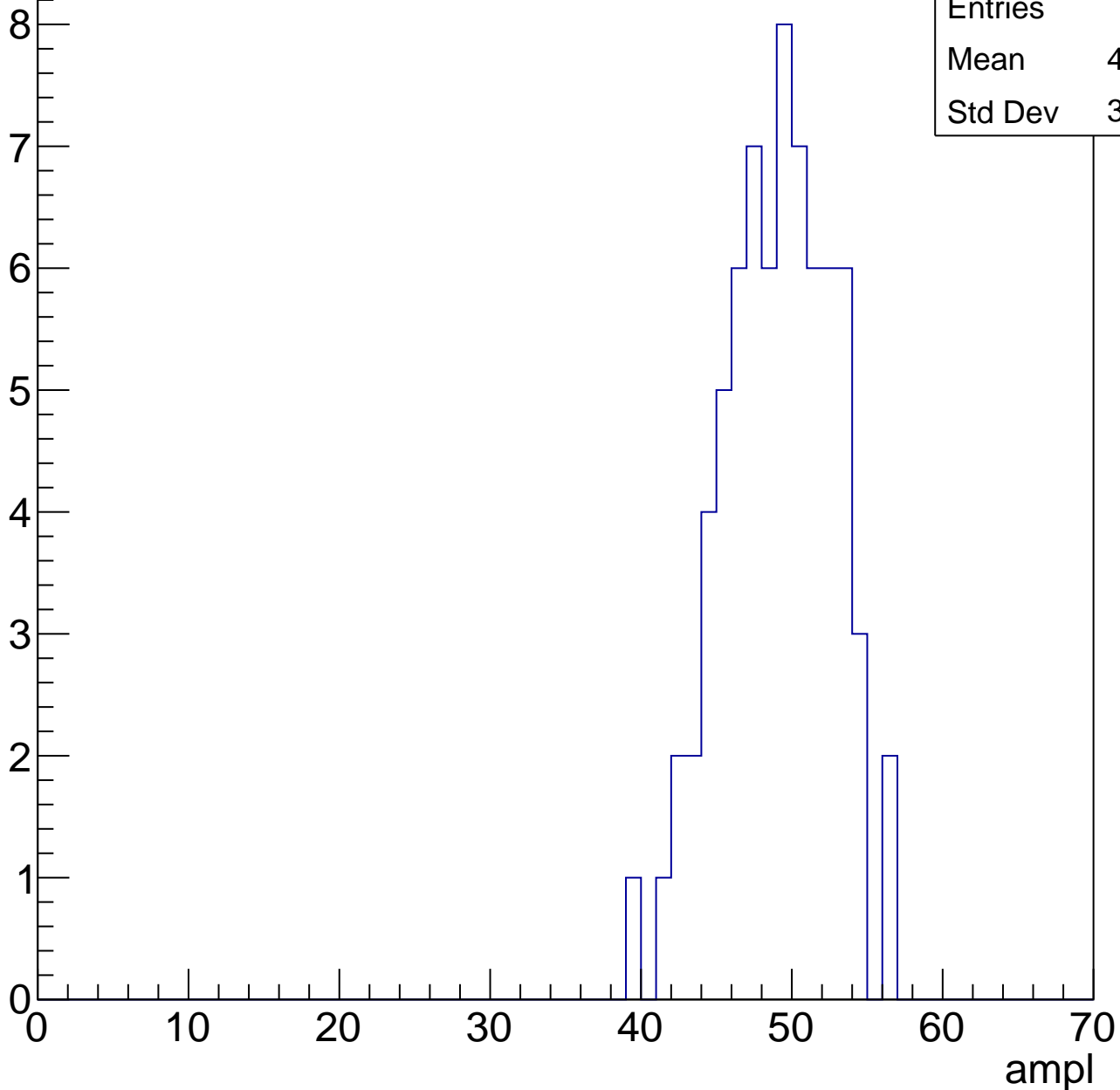


B1L103S, U10-ch104, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	48.56
Std Dev	3.617

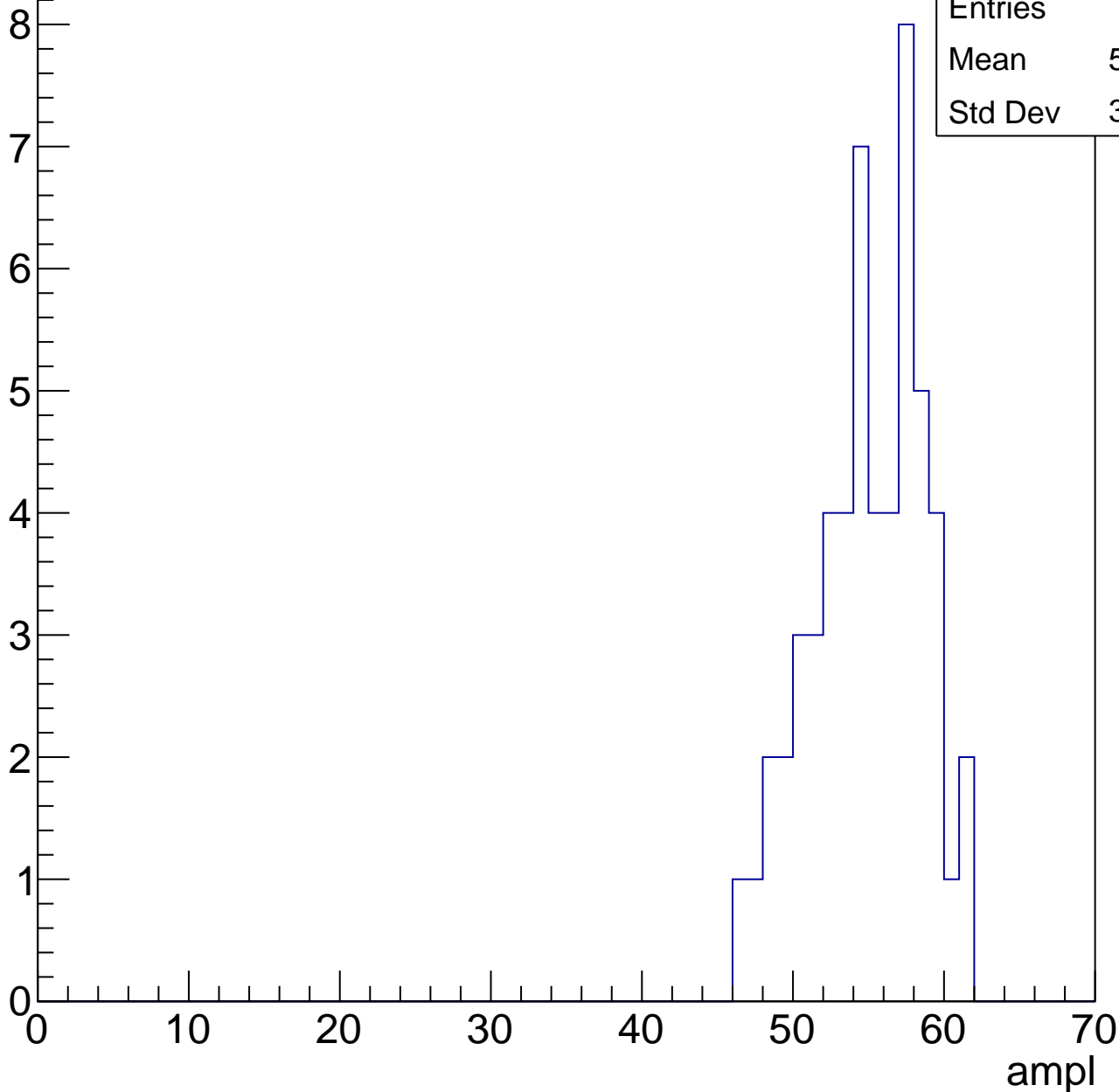


B1L103S, U10-ch104, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	54.47
Std Dev	3.627

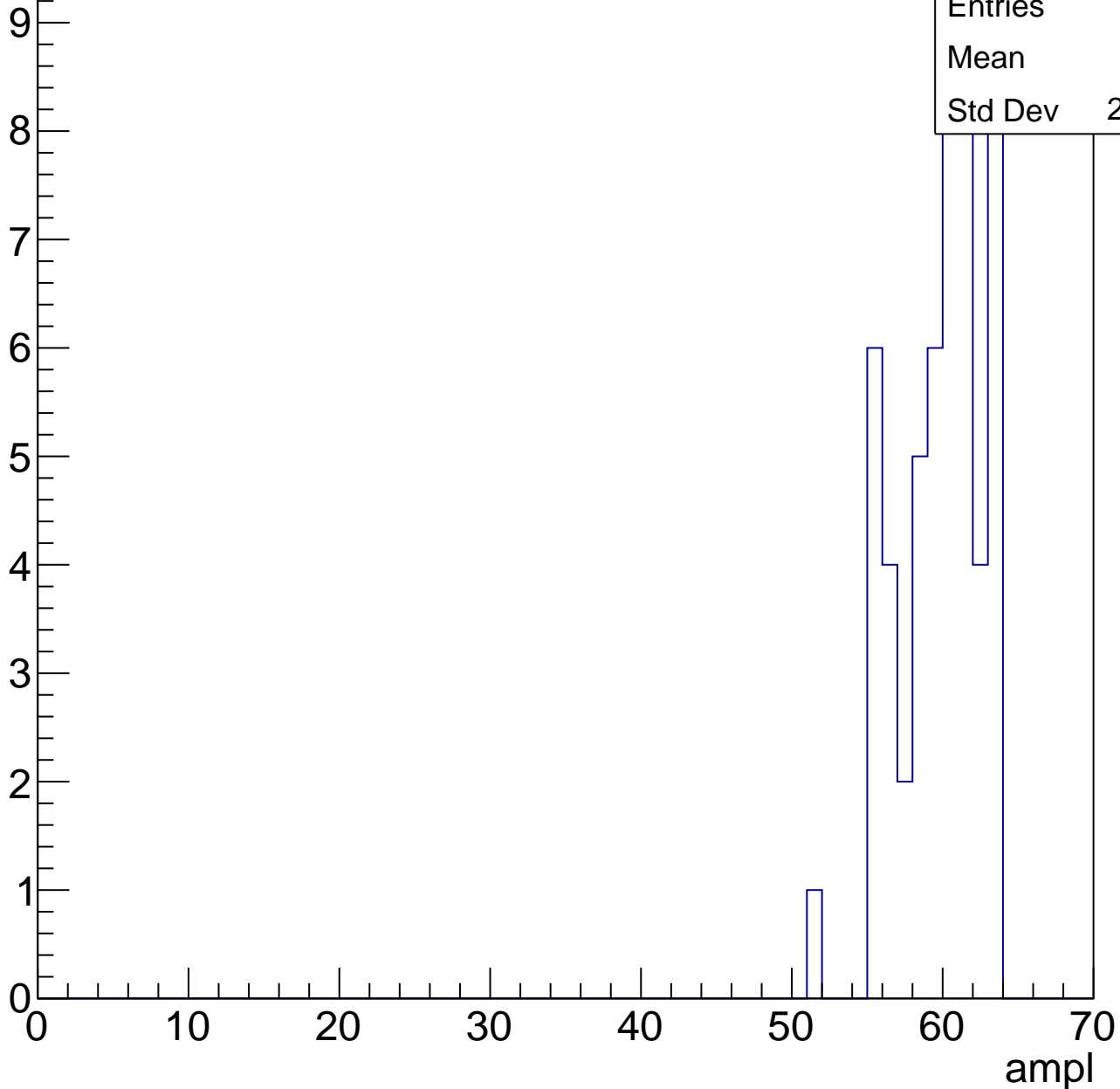


B1L103S, U10-ch104, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	59.3
Std Dev	2.779

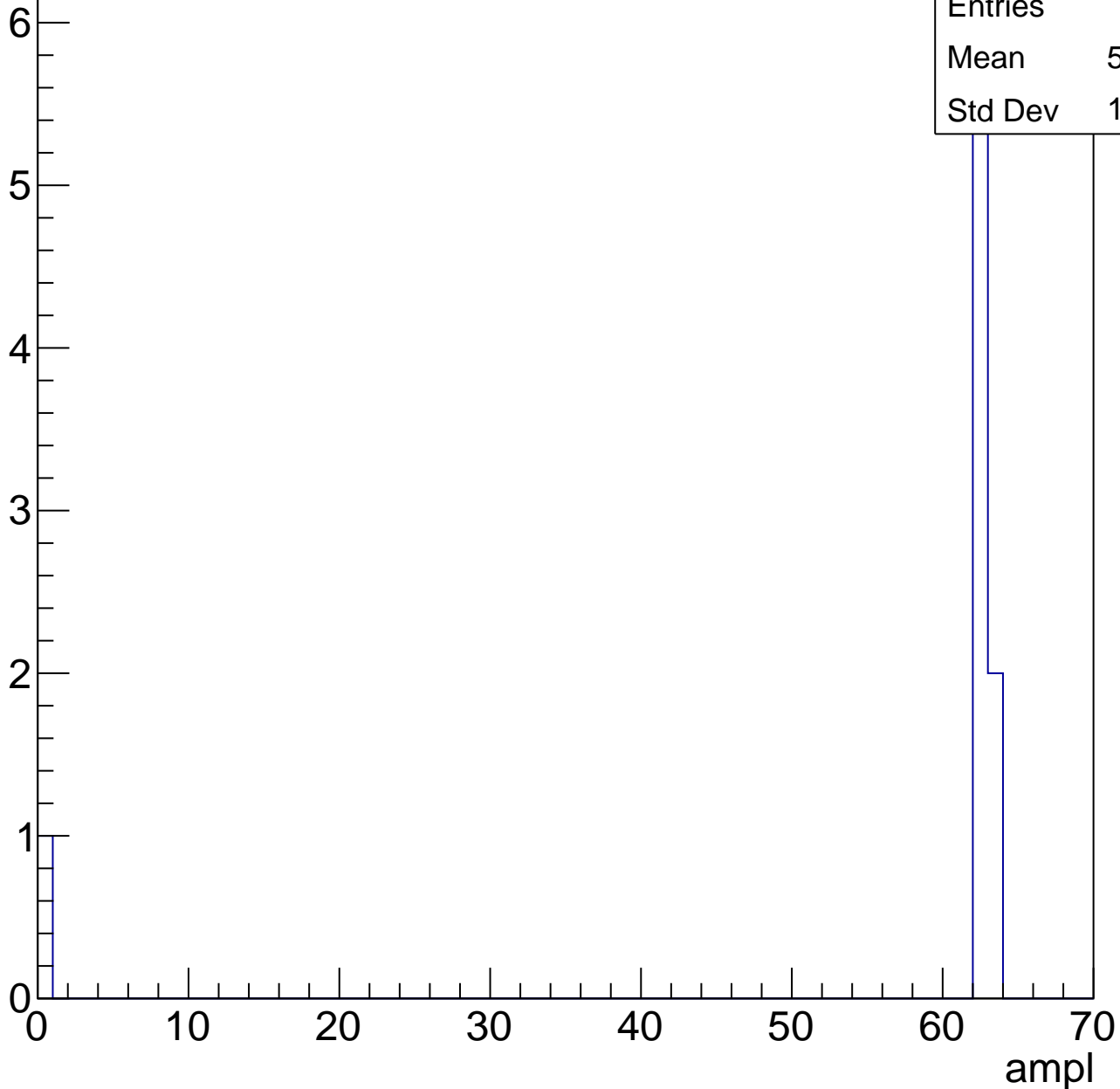


B1L103S, U10-ch104, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	9
Mean	55.33
Std Dev	19.57



B1L103S, U10-ch104, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch105, adc0

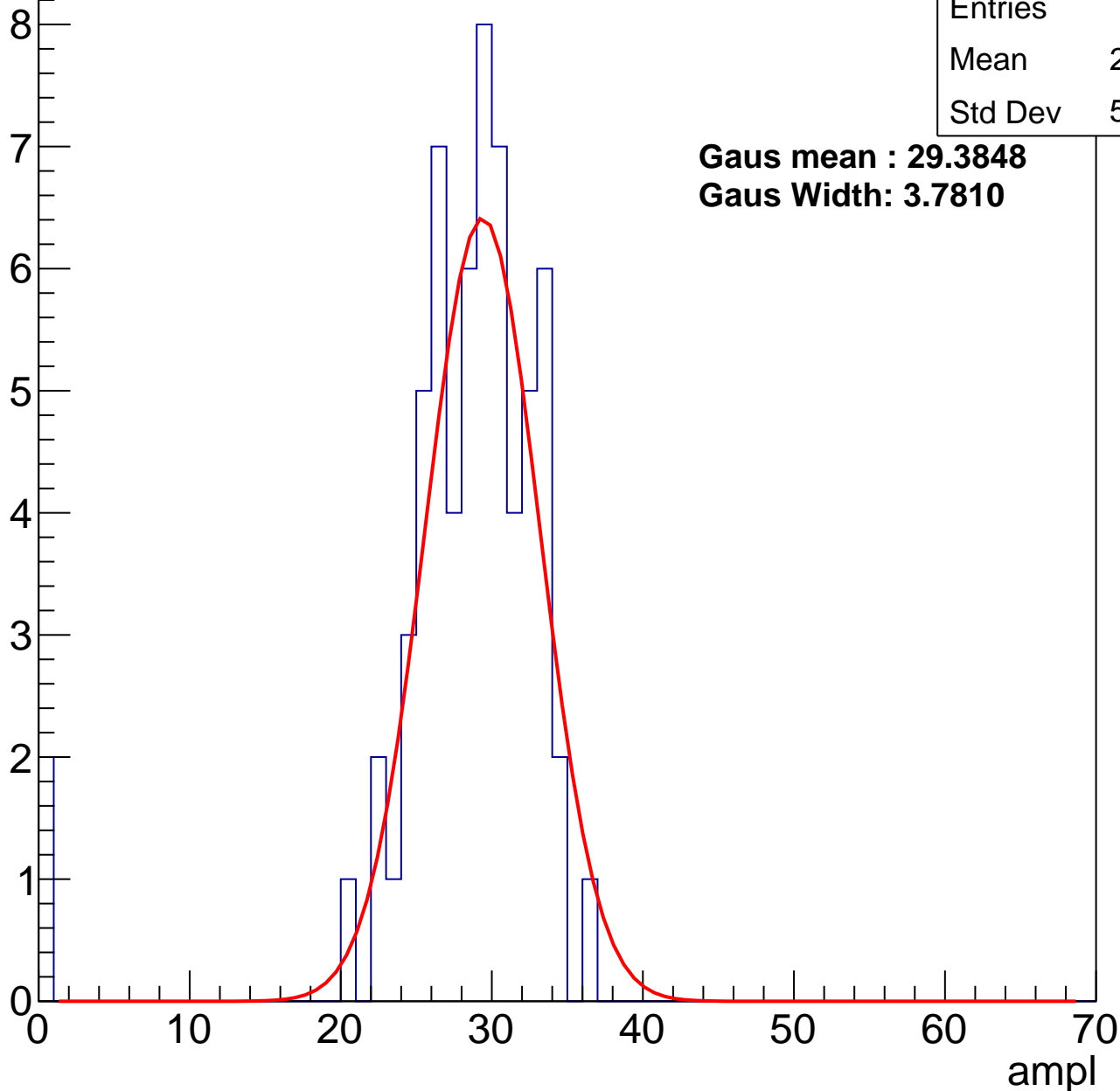
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	27.66
Std Dev	5.974

Gaus mean : 29.3848

Gaus Width: 3.7810



B1L103S, U10-ch105, adc1

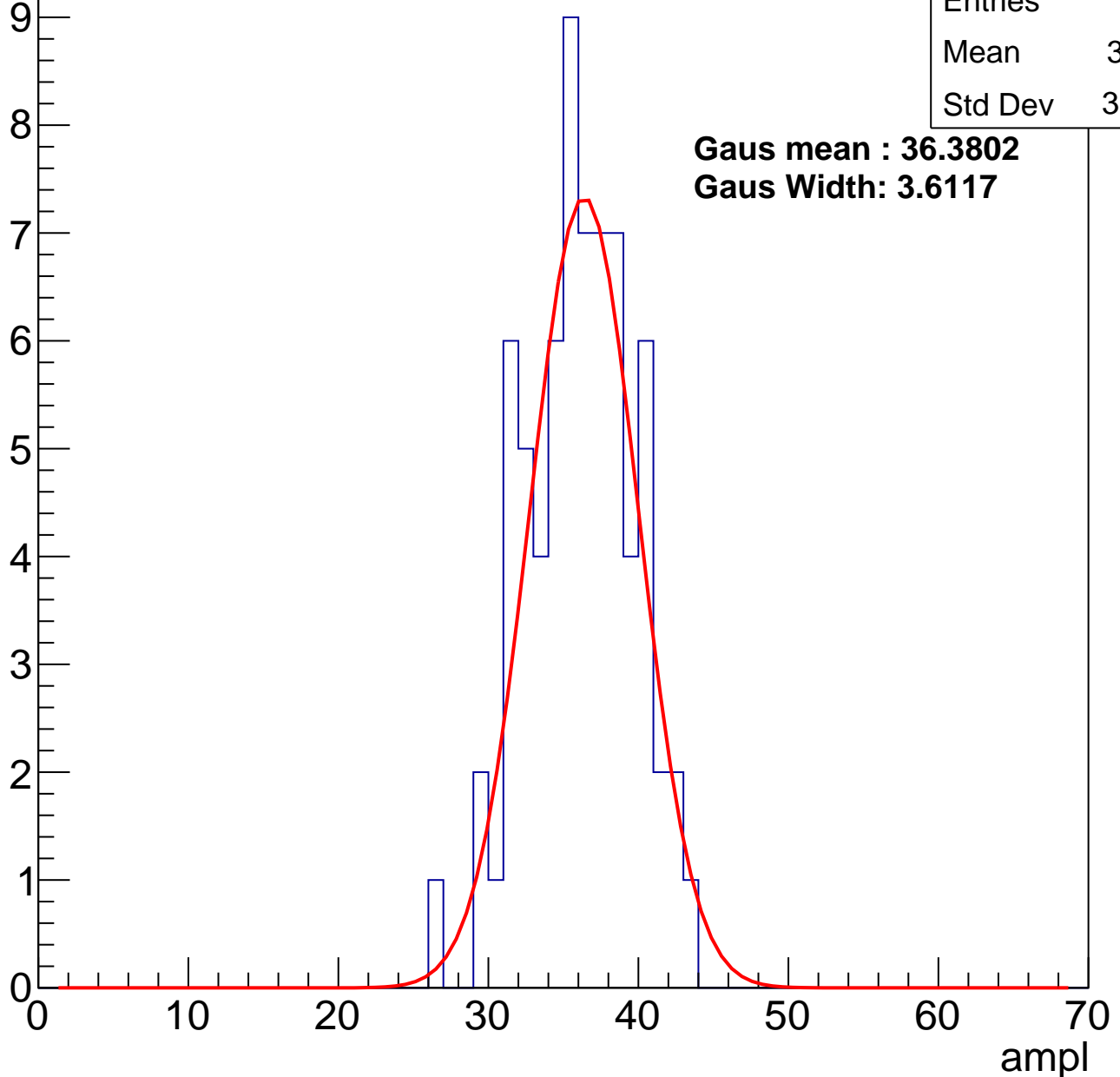
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	35.61
Std Dev	3.506

Gaus mean : 36.3802

Gaus Width: 3.6117



B1L103S, U10-ch105, adc2

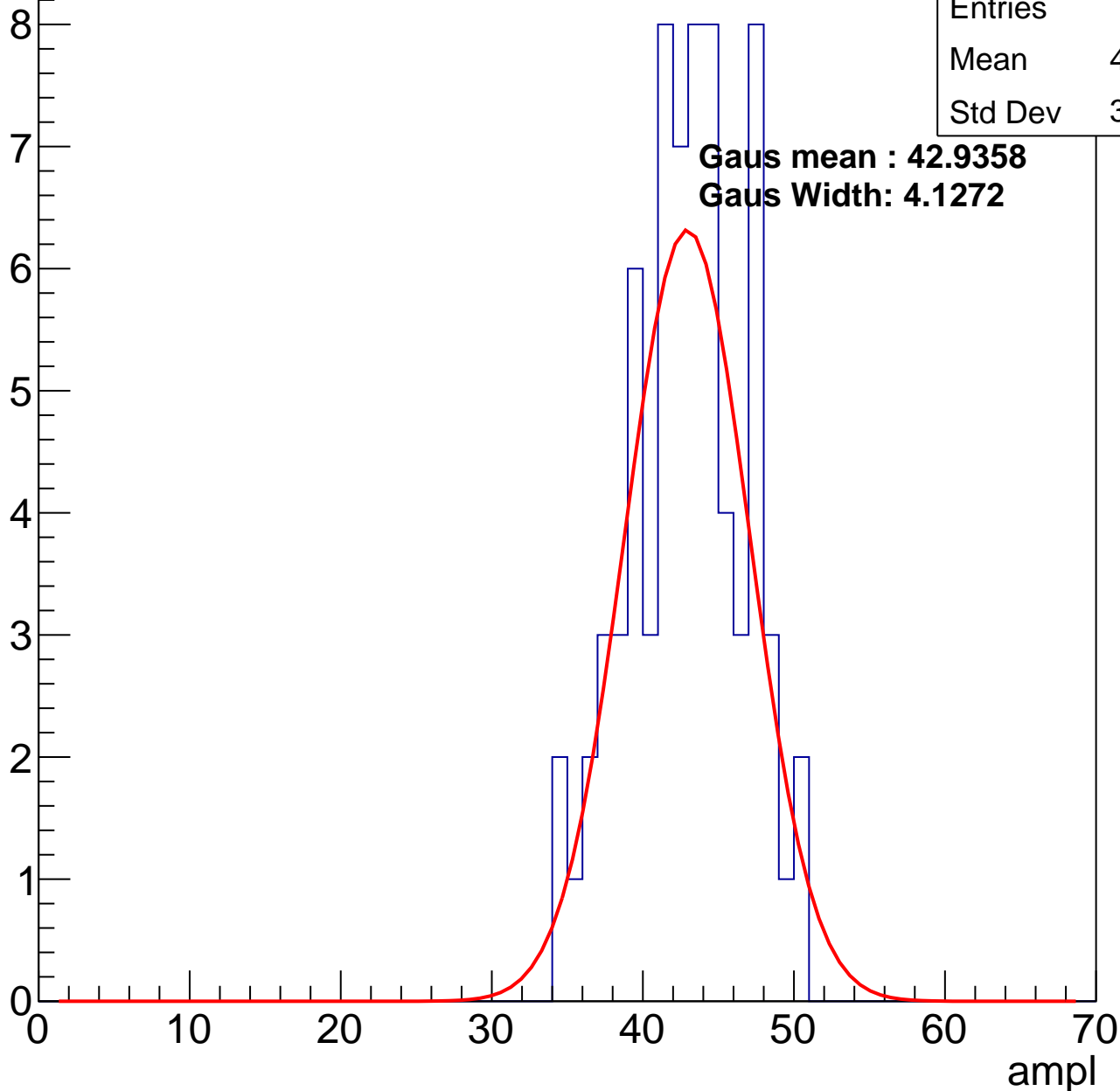
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	42.49
Std Dev	3.797

Gaus mean : 42.9358

Gaus Width: 4.1272

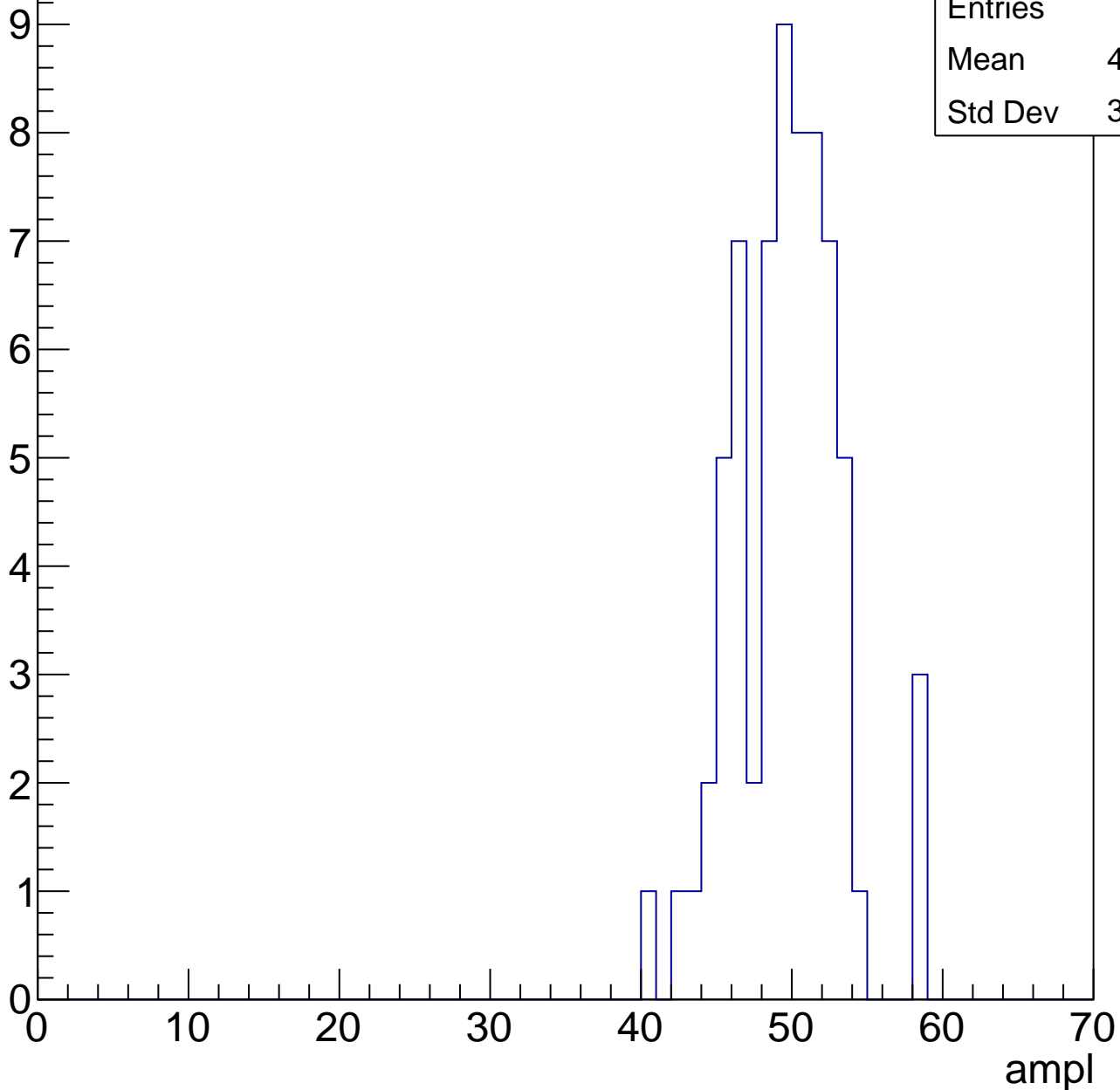


B1L103S, U10-ch105, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	49.19
Std Dev	3.504

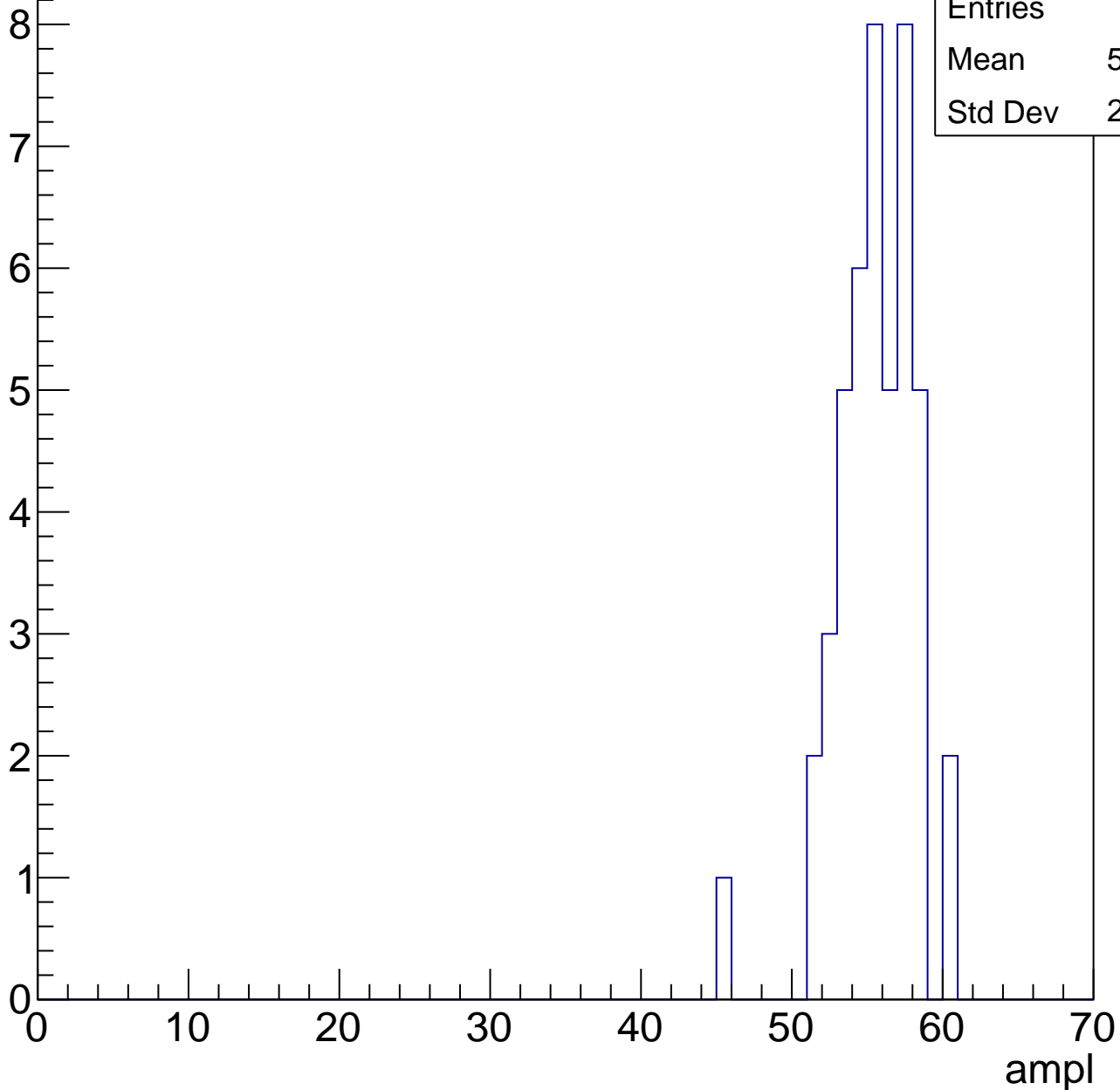


B1L103S, U10-ch105, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

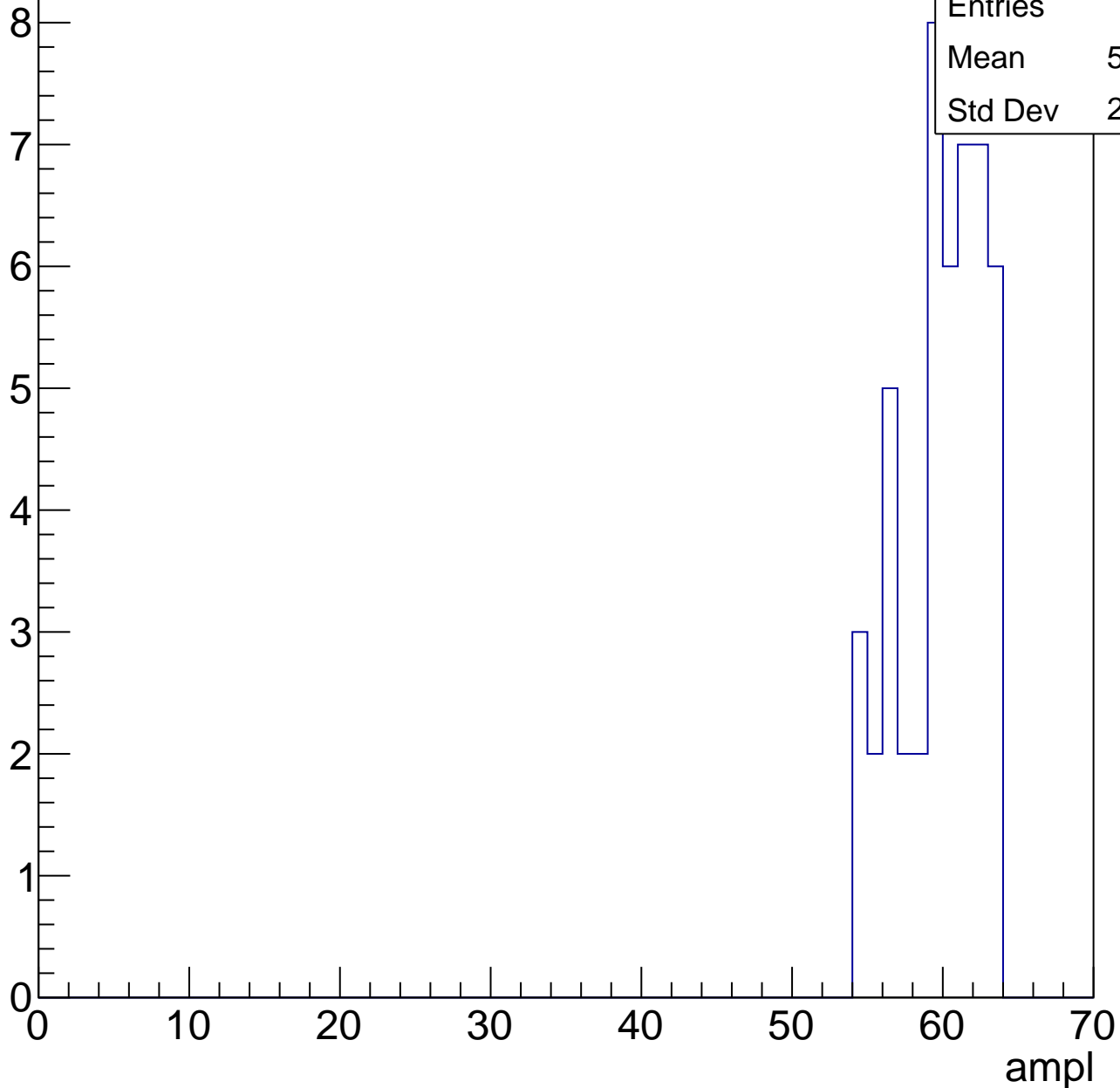
Entries	45
Mean	55.07
Std Dev	2.653



B1L103S, U10-ch105, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

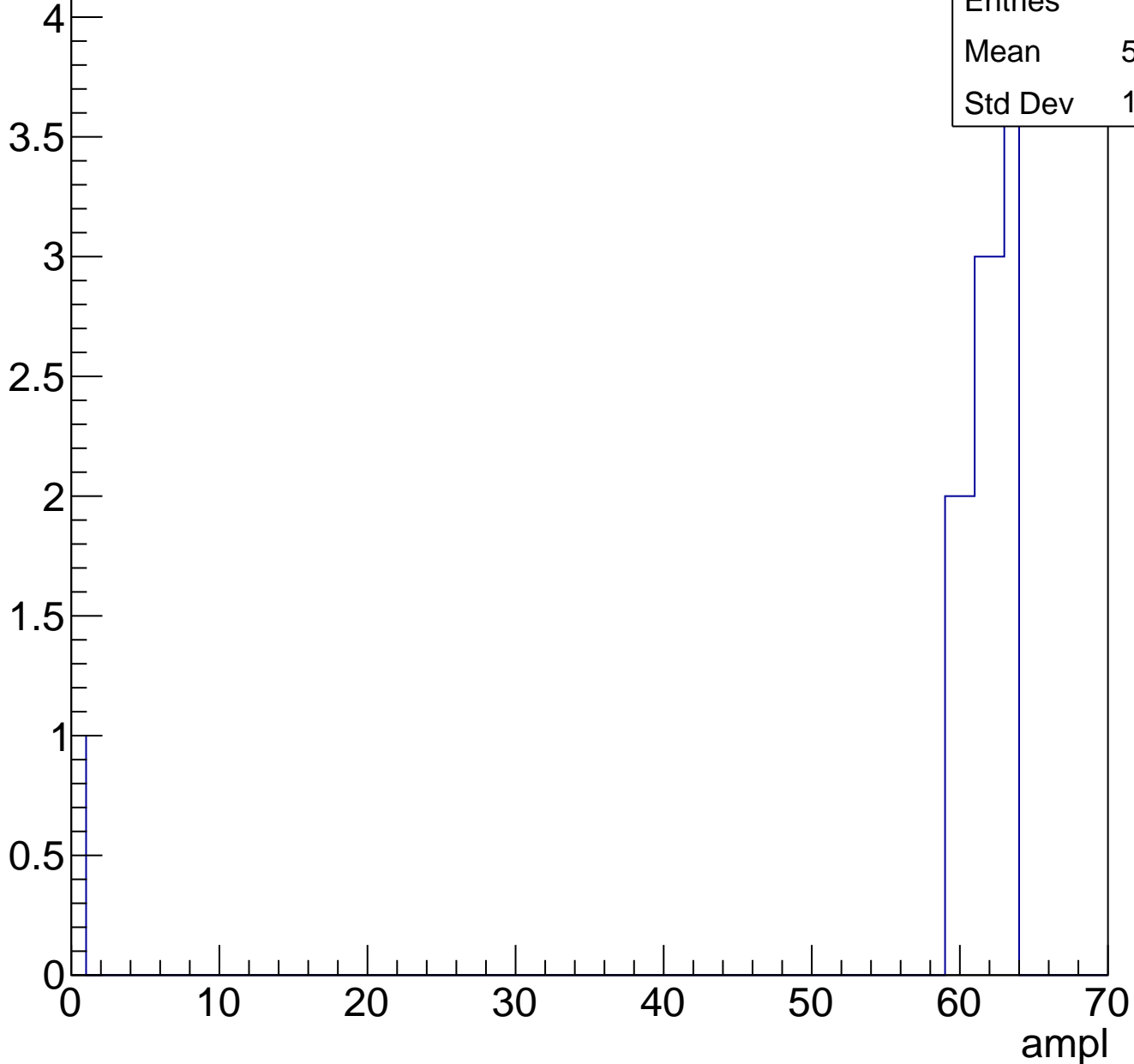


Entries	48
Mean	59.44
Std Dev	2.684

B1L103S, U10-ch105, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch105, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch106, adc0

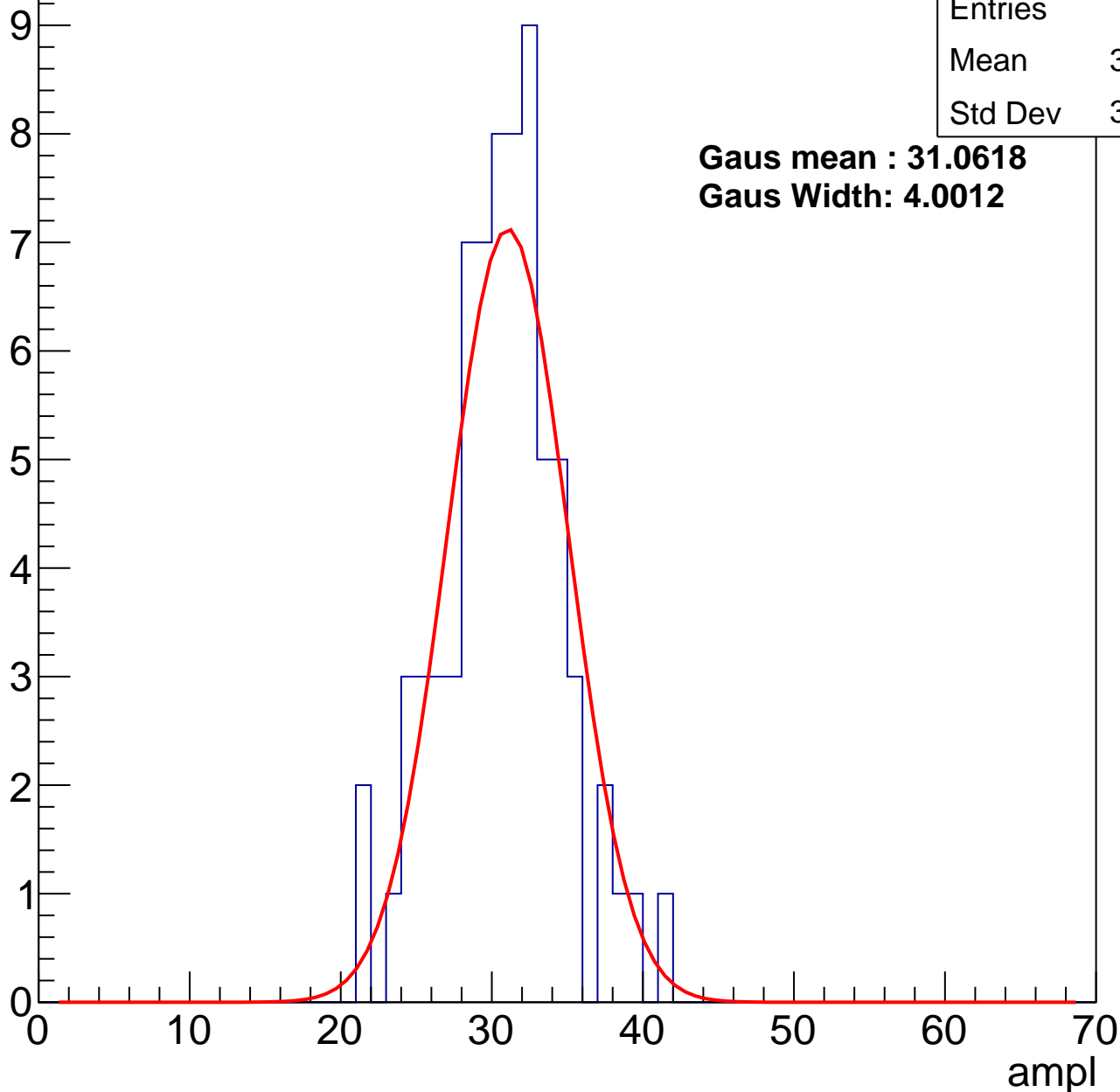
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	30.25
Std Dev	3.922

Gaus mean : 31.0618

Gaus Width: 4.0012



B1L103S, U10-ch106, adc1

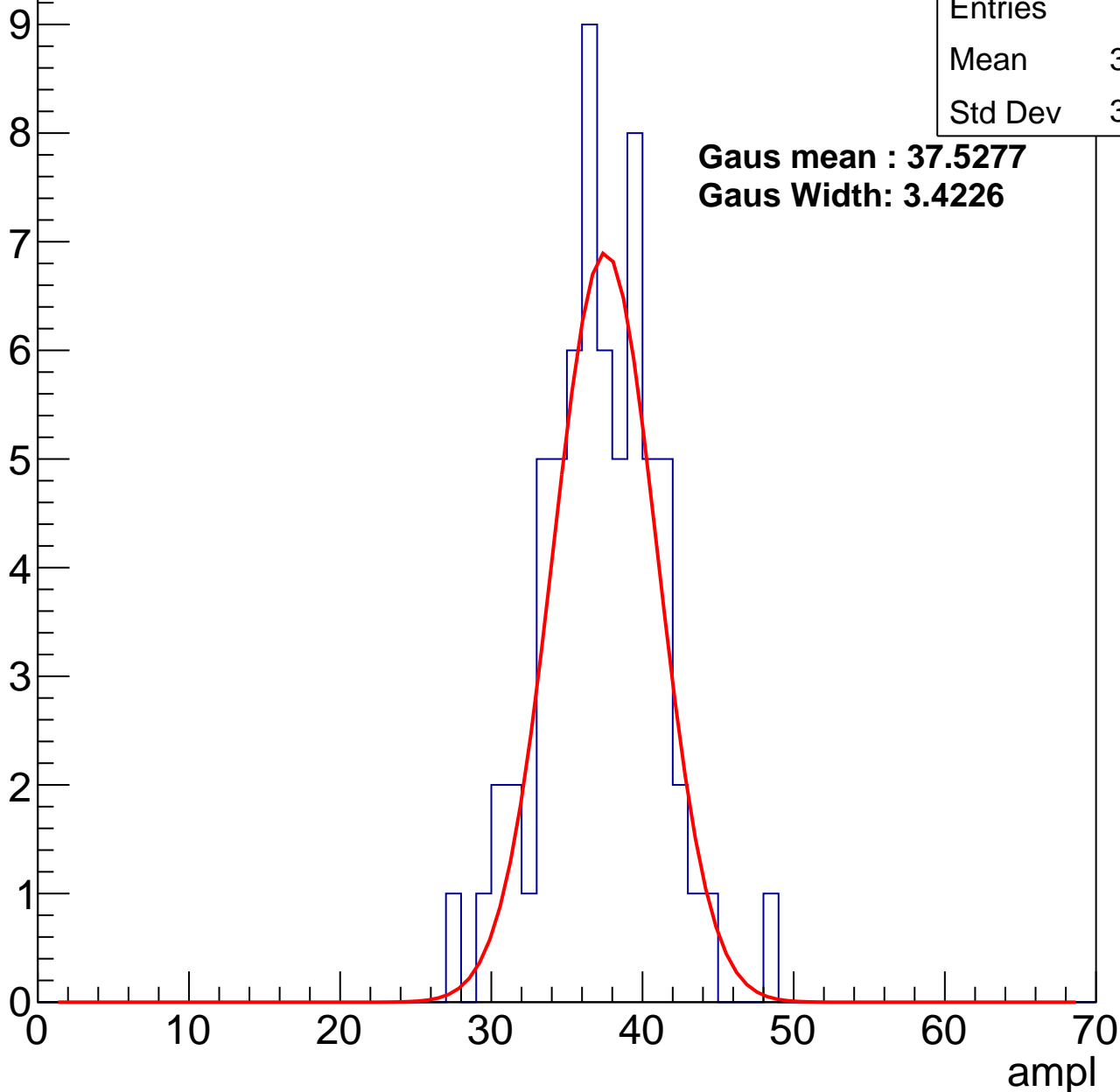
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.77
Std Dev	3.757

Gaus mean : 37.5277

Gaus Width: 3.4226



B1L103S, U10-ch106, adc2

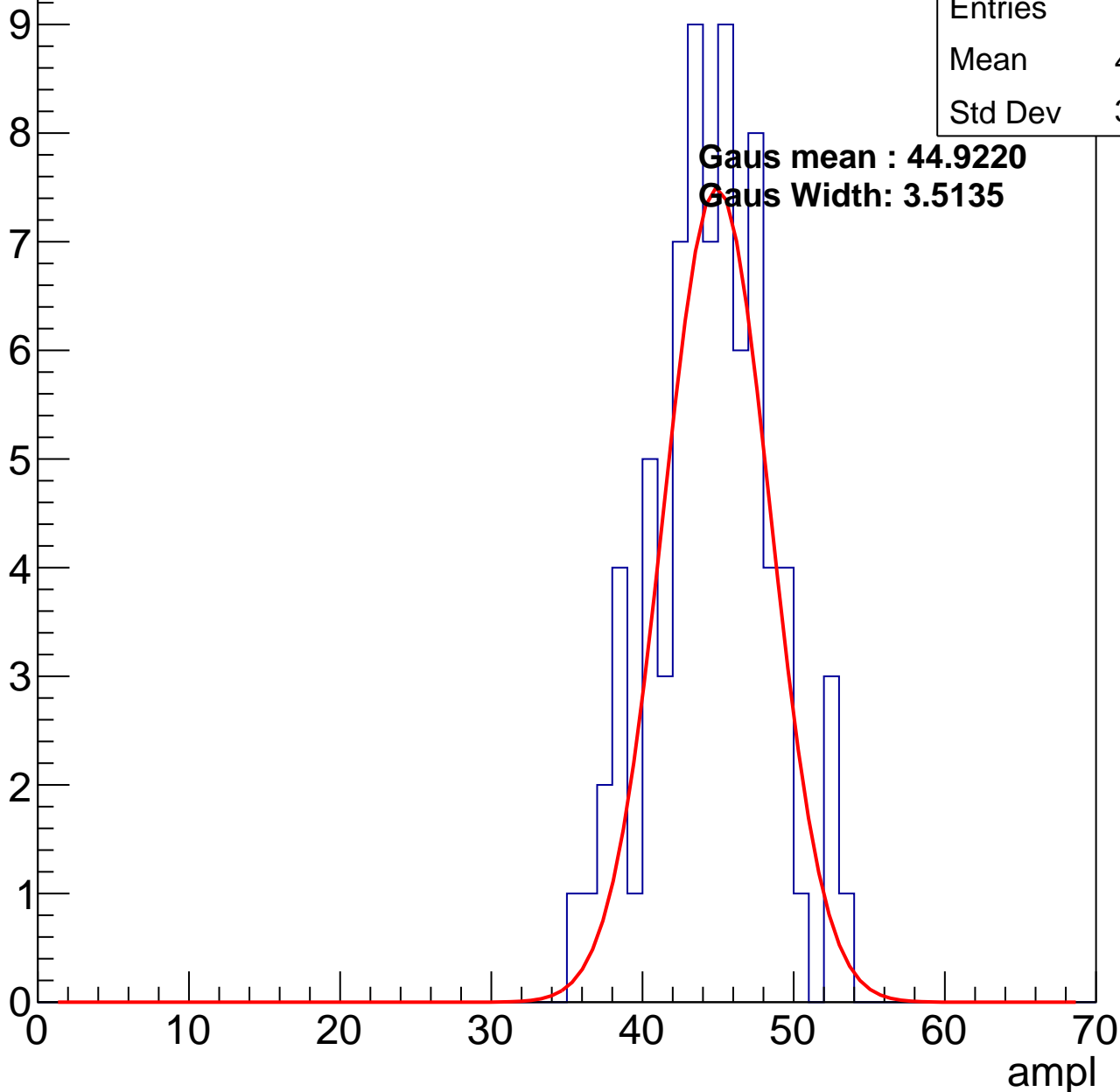
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	44.11
Std Dev	3.861

Gaus mean : 44.9220

Gaus Width: 3.5135

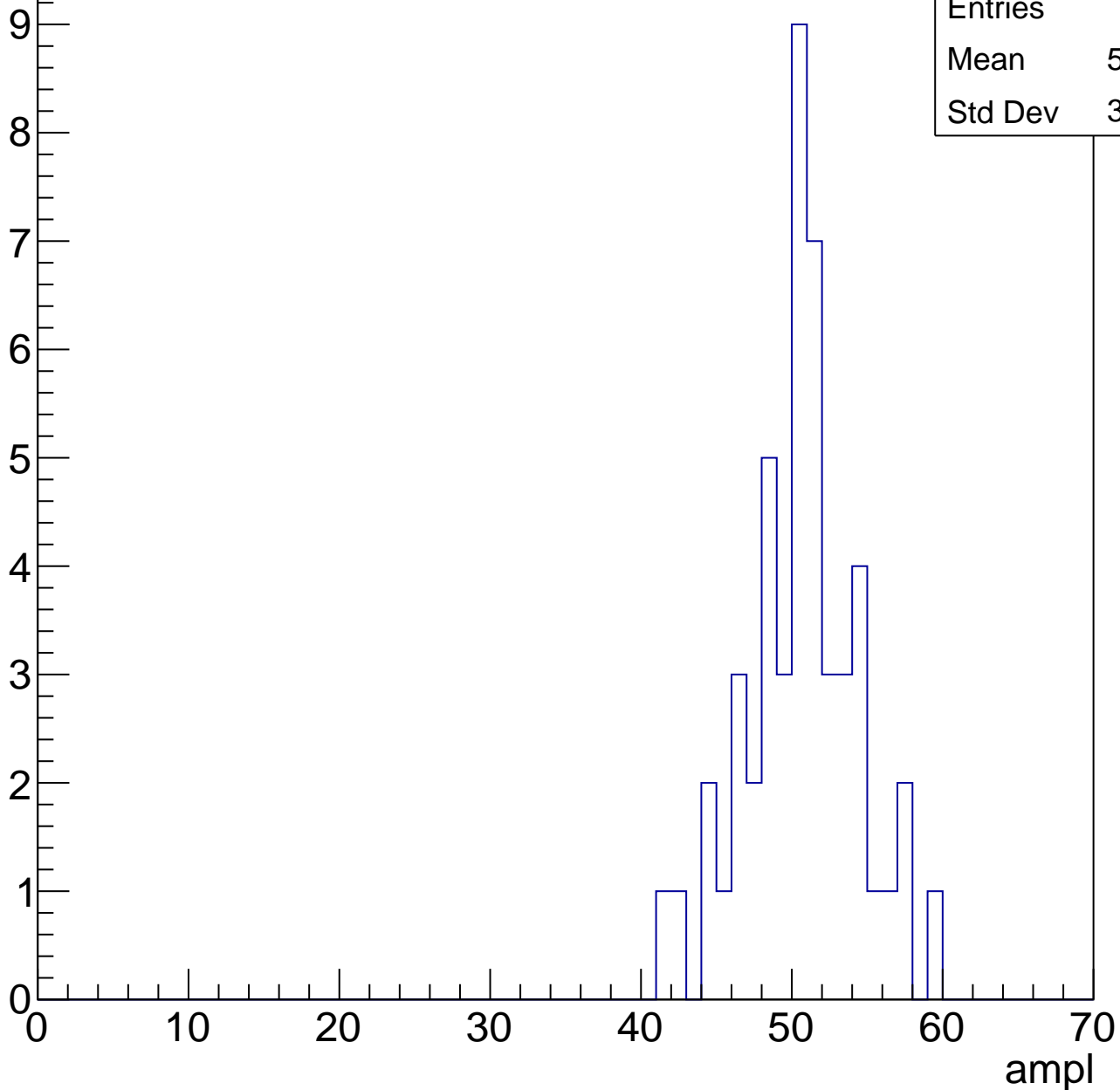


B1L103S, U10-ch106, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	50.14
Std Dev	3.725

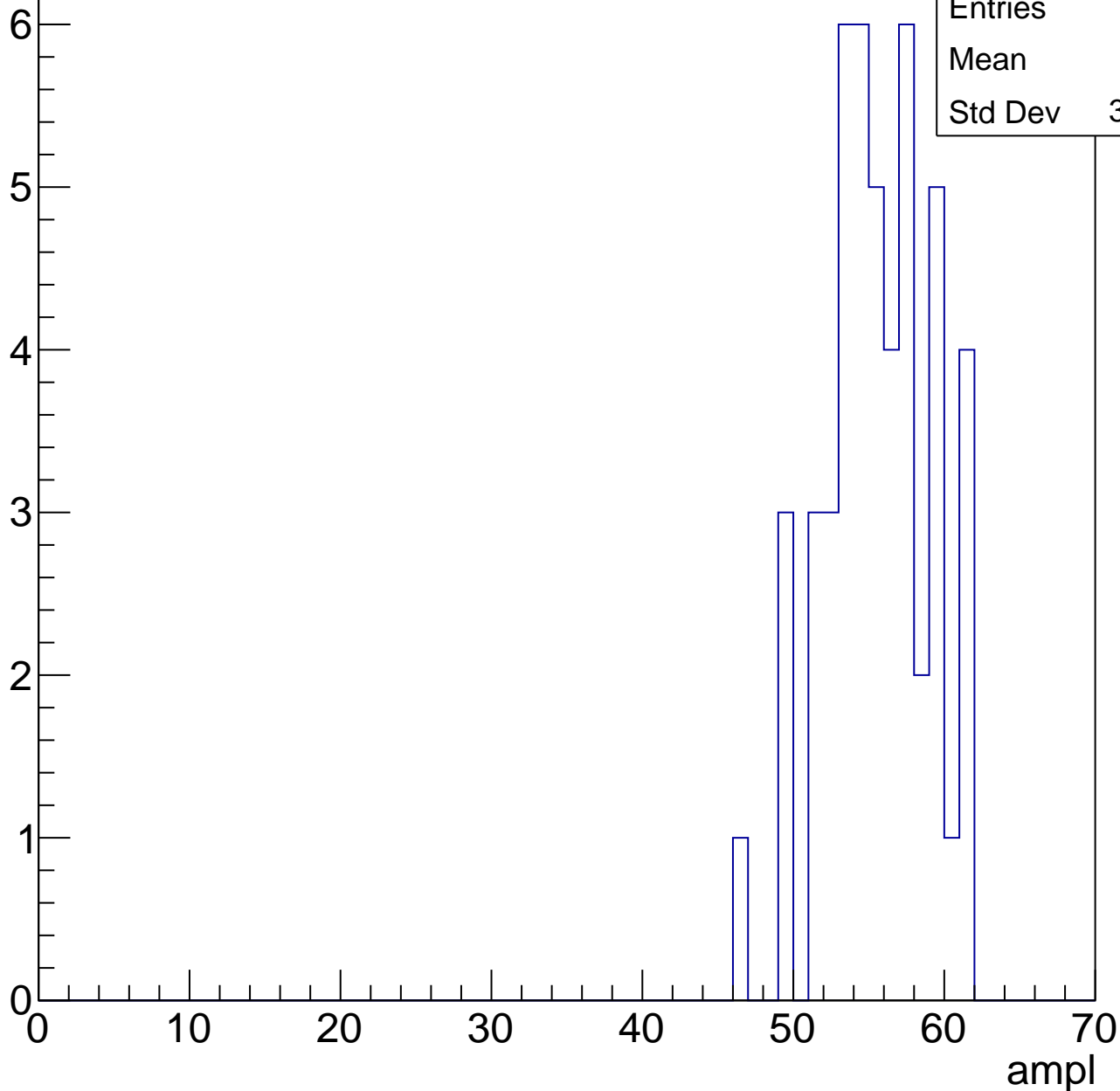


B1L103S, U10-ch106, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	55.1
Std Dev	3.466

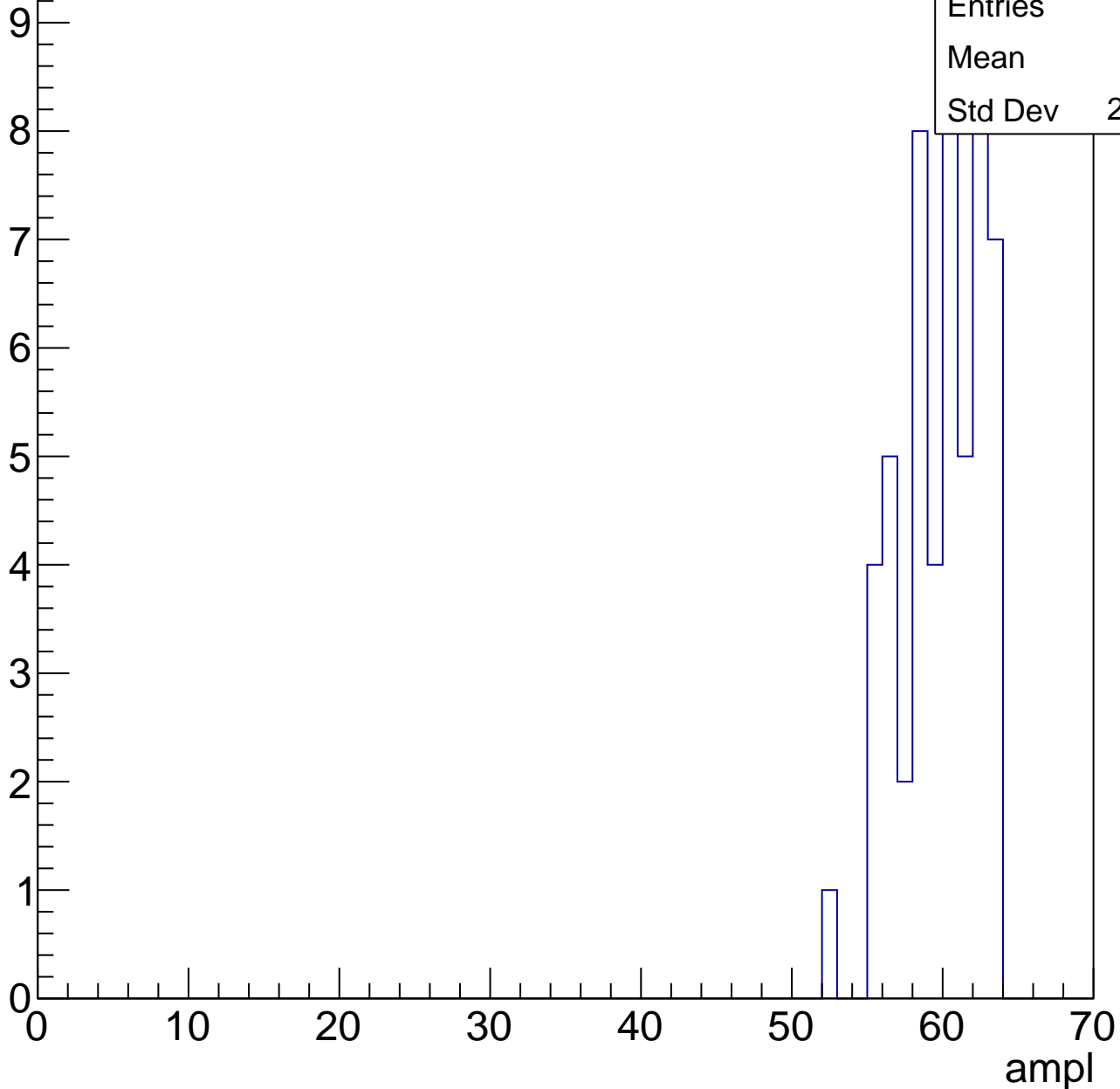


B1L103S, U10-ch106, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	59.4
Std Dev	2.673



B1L103S, U10-ch106, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch106, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch107, adc0

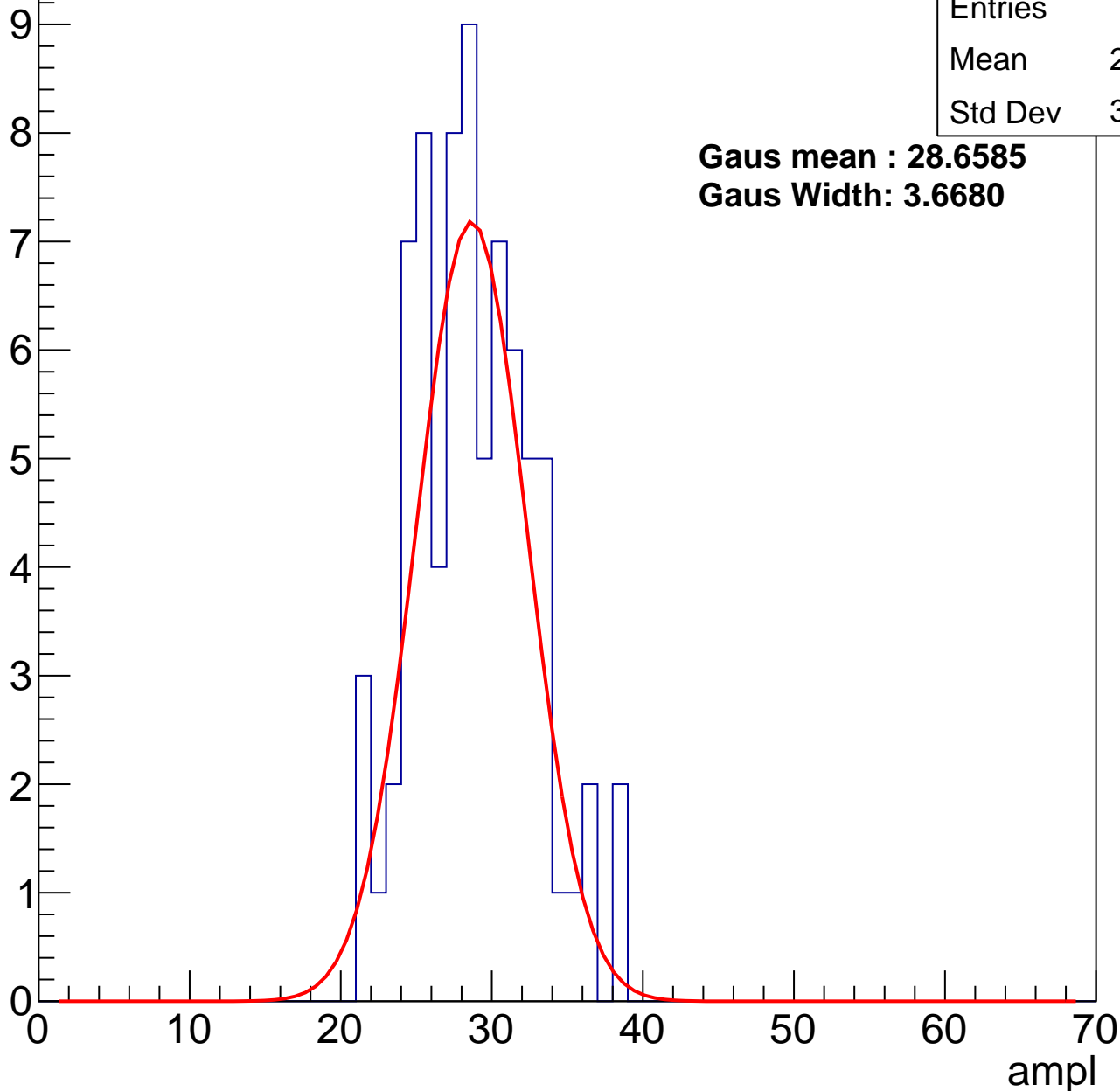
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	28.34
Std Dev	3.868

Gaus mean : 28.6585

Gaus Width: 3.6680



B1L103S, U10-ch107, adc1

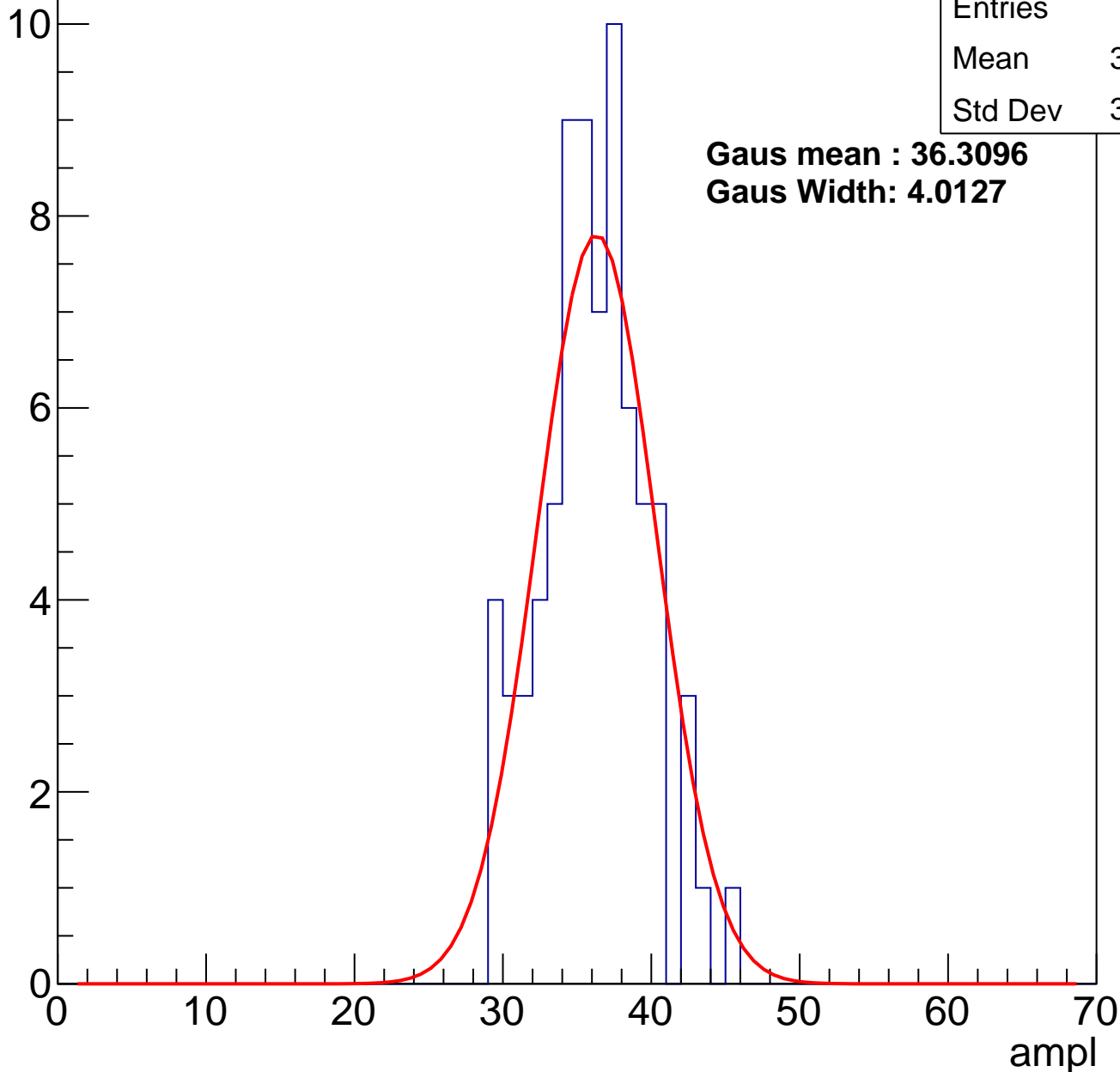
calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	35.63
Std Dev	3.502

Gaus mean : 36.3096

Gaus Width: 4.0127

Entry



B1L103S, U10-ch107, adc2

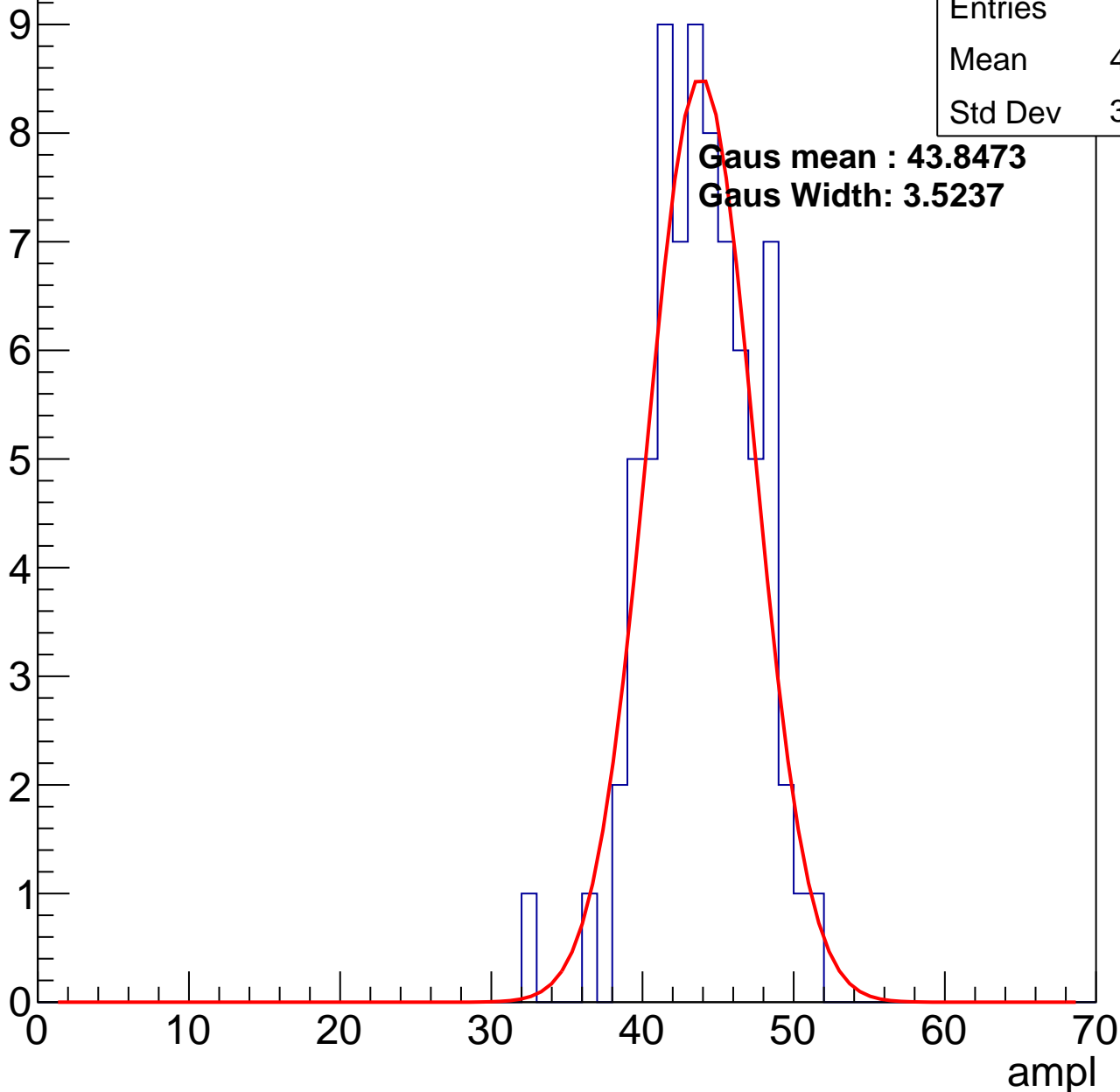
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	43.45
Std Dev	3.447

Gaus mean : 43.8473

Gaus Width: 3.5237

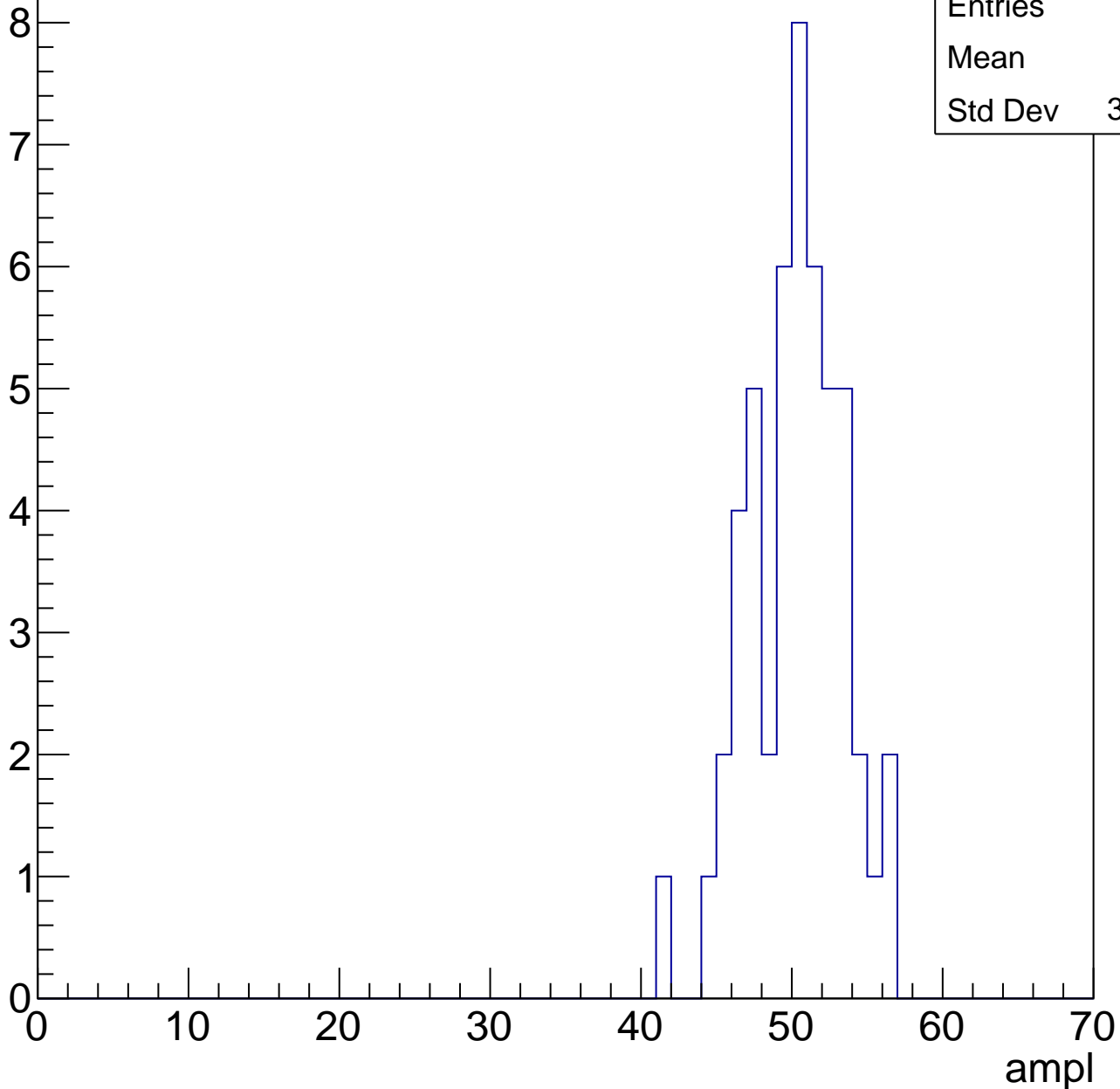


B1L103S, U10-ch107, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	49.8
Std Dev	3.124

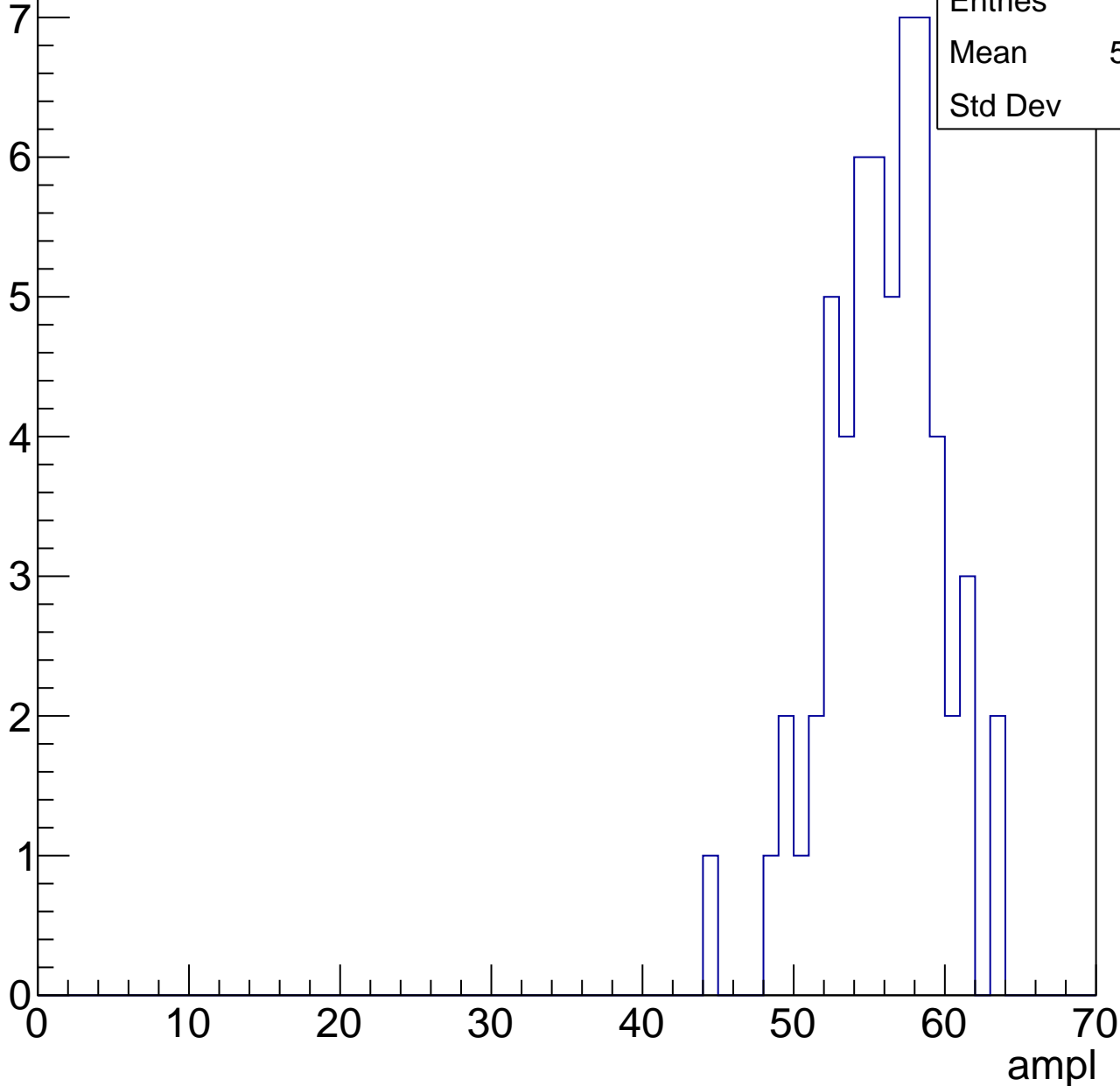


B1L103S, U10-ch107, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	55.48
Std Dev	3.71

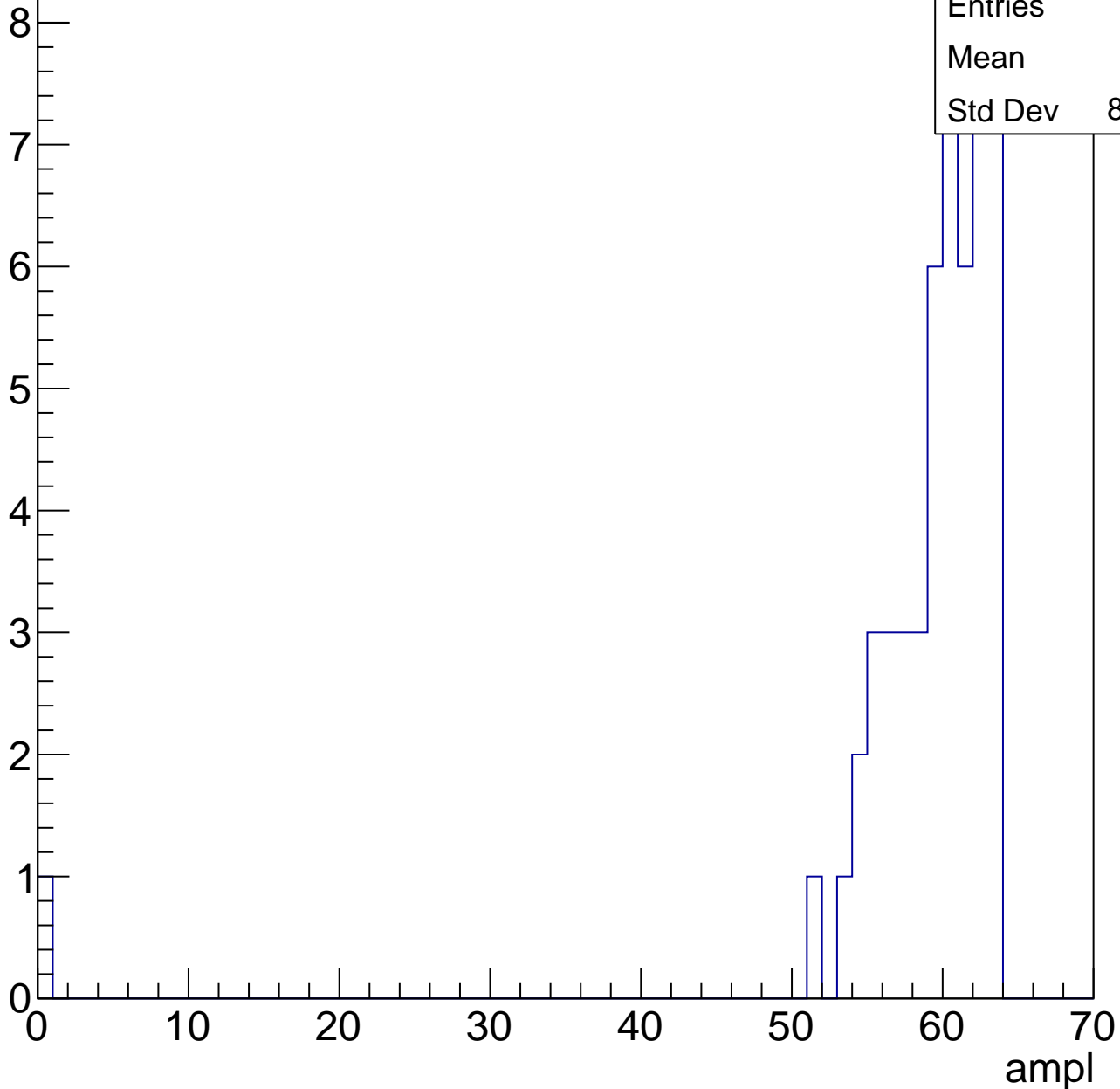


B1L103S, U10-ch107, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	58.3
Std Dev	8.608



B1L103S, U10-ch107, adc6

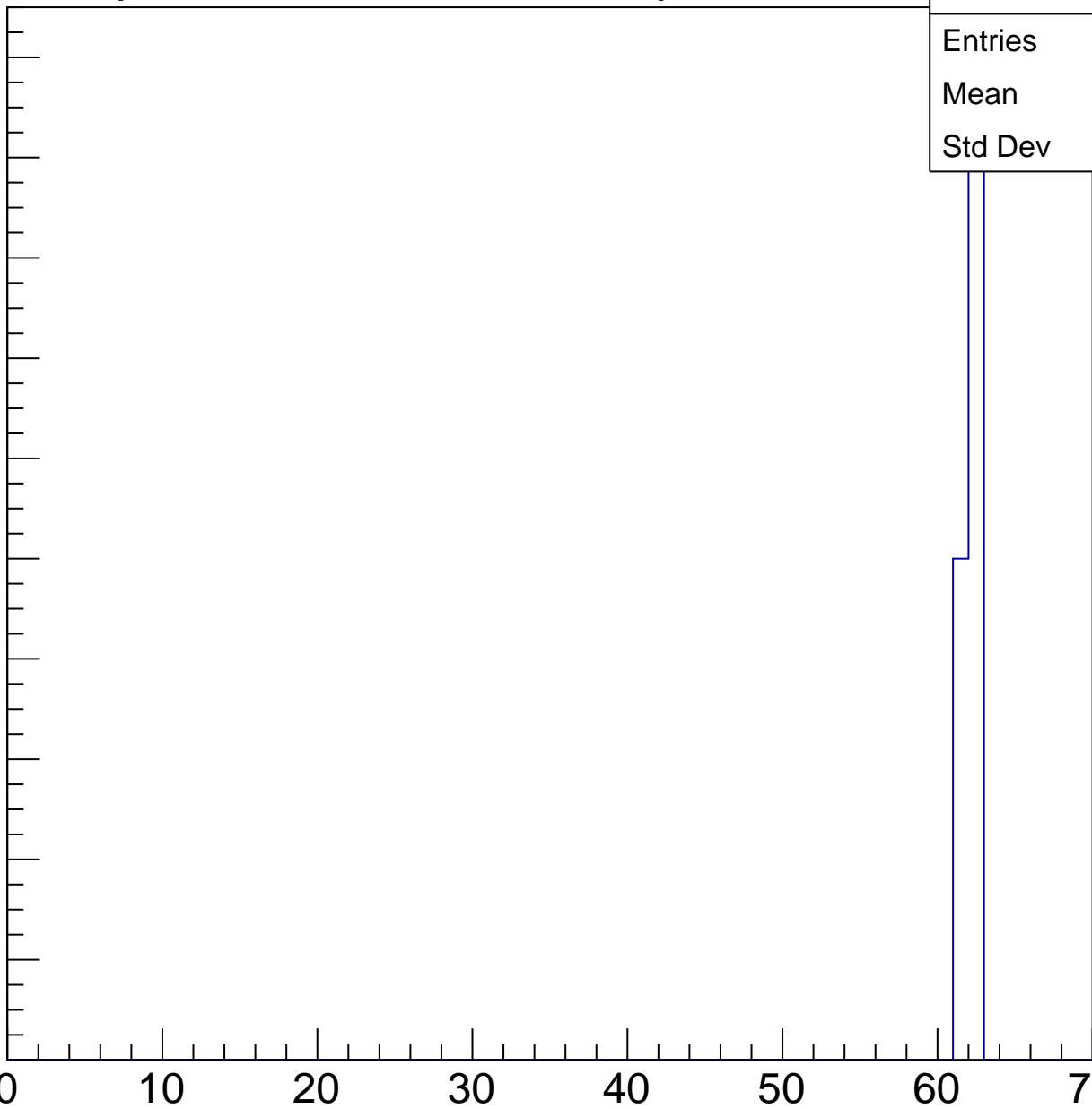
calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl



B1L103S, U10-ch107, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch108, adc0

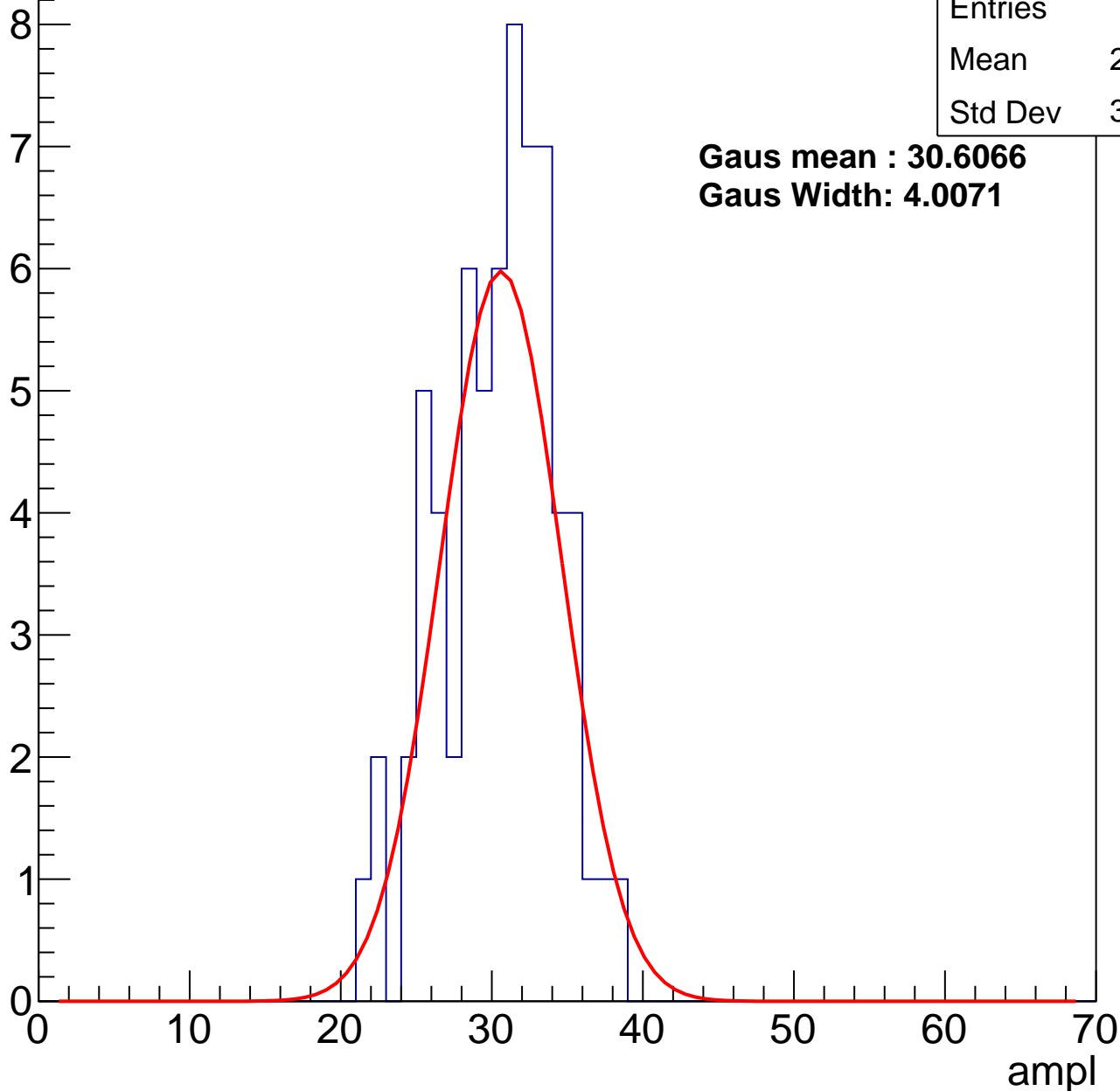
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	29.98
Std Dev	3.752

Gaus mean : 30.6066

Gaus Width: 4.0071



B1L103S, U10-ch108, adc1

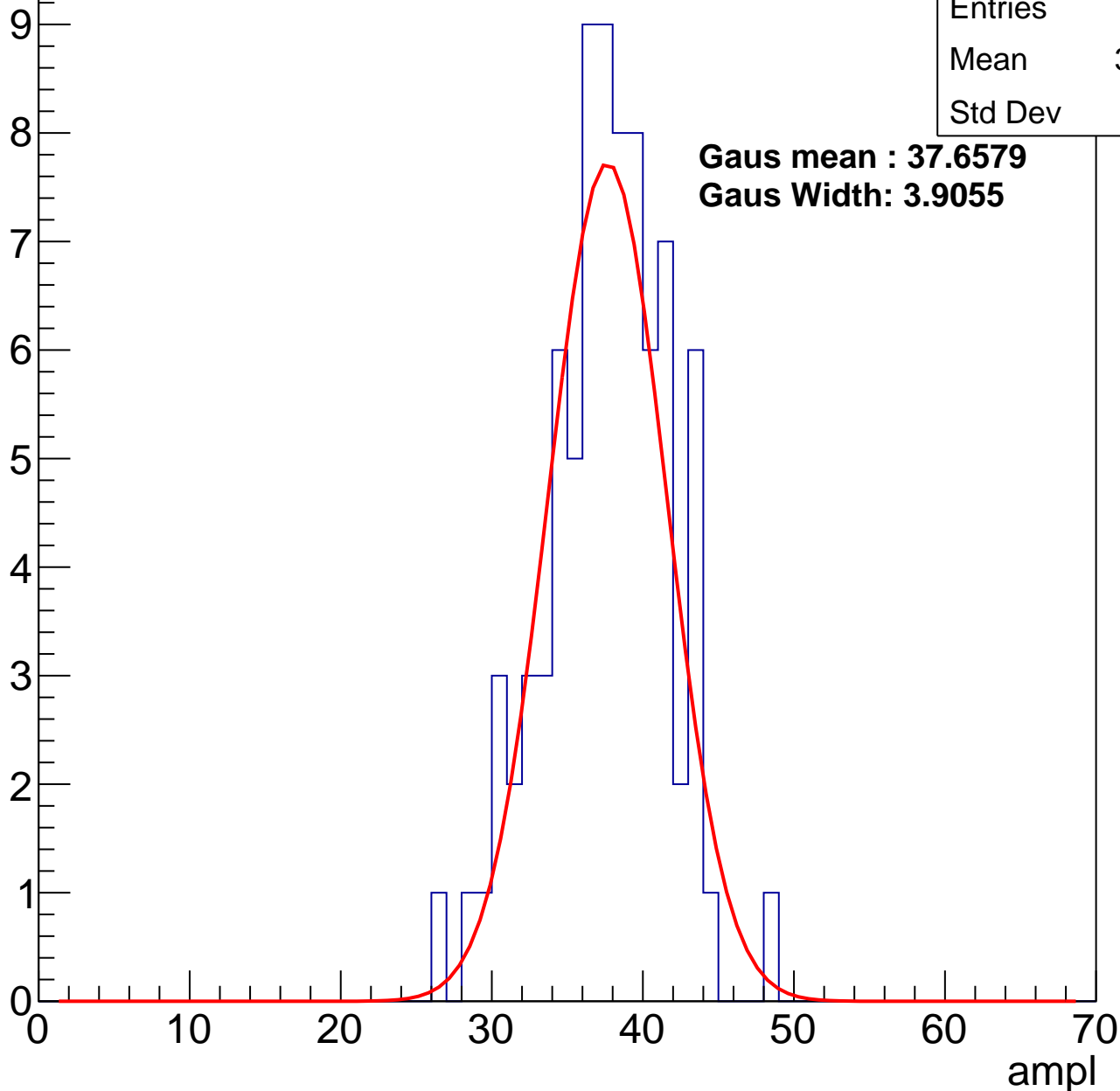
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	37.11
Std Dev	4.03

Gaus mean : 37.6579

Gaus Width: 3.9055



B1L103S, U10-ch108, adc2

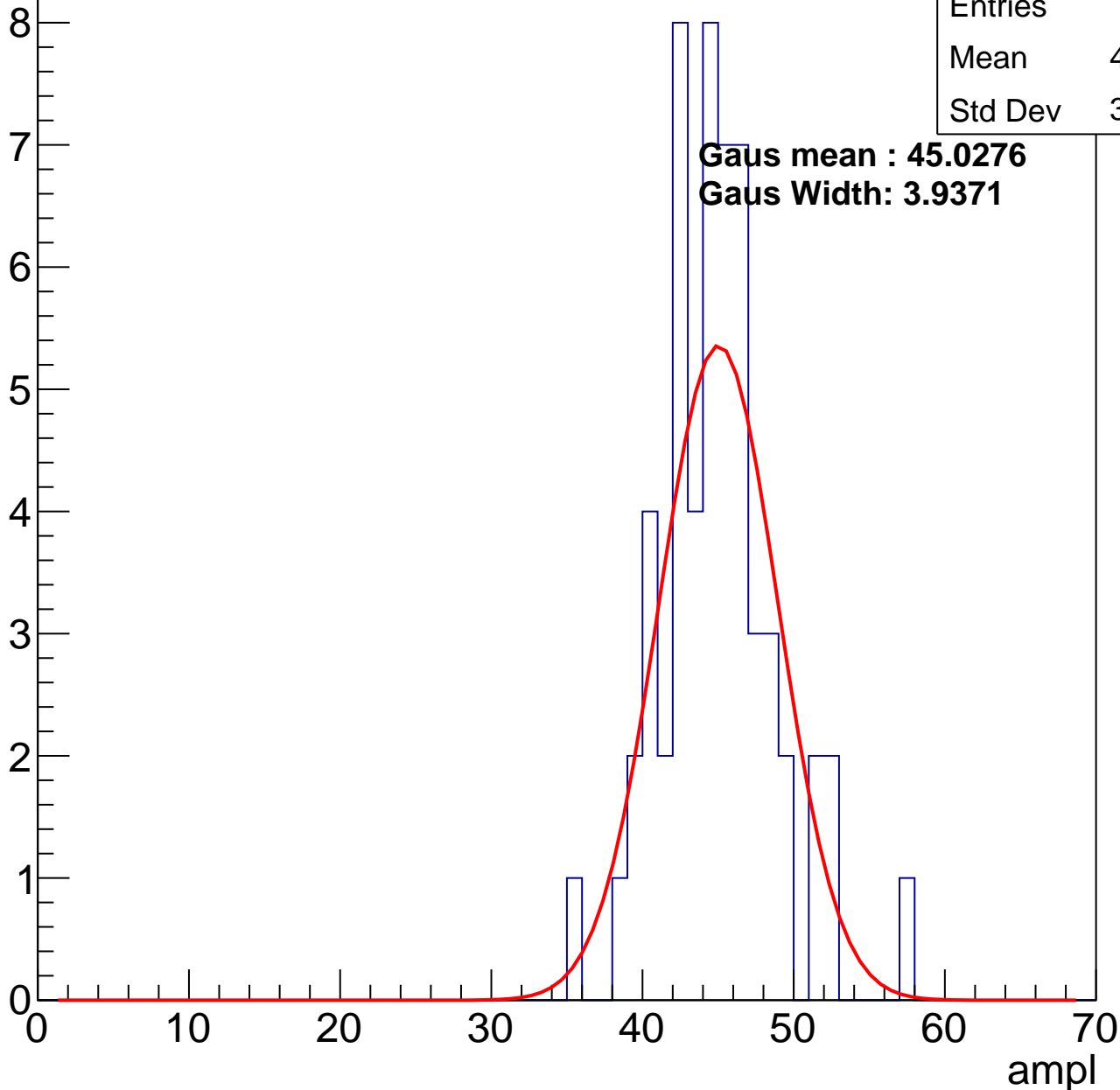
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	57
Mean	44.49
Std Dev	3.807

Gaus mean : 45.0276

Gaus Width: 3.9371

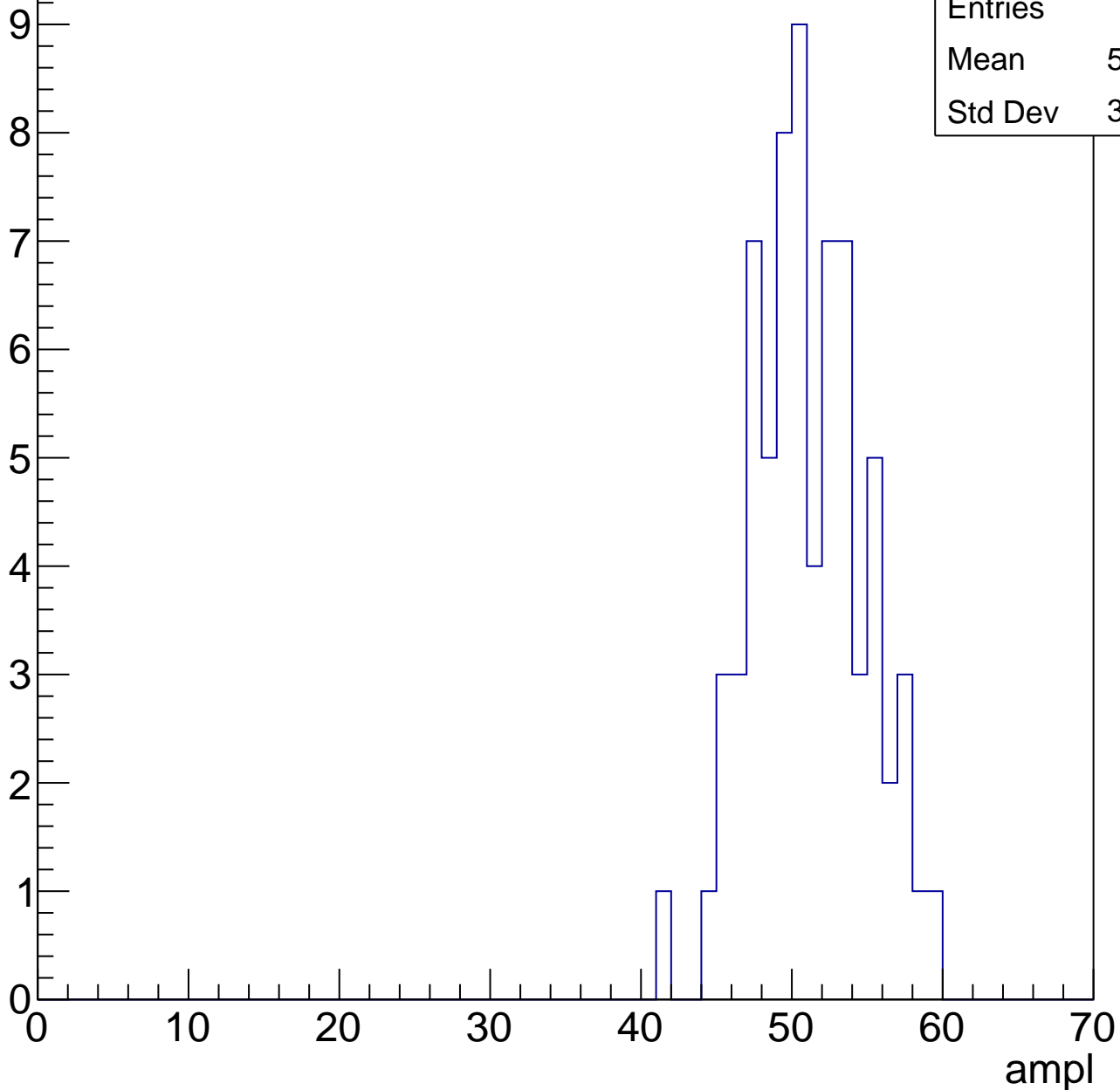


B1L103S, U10-ch108, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	50.64
Std Dev	3.653

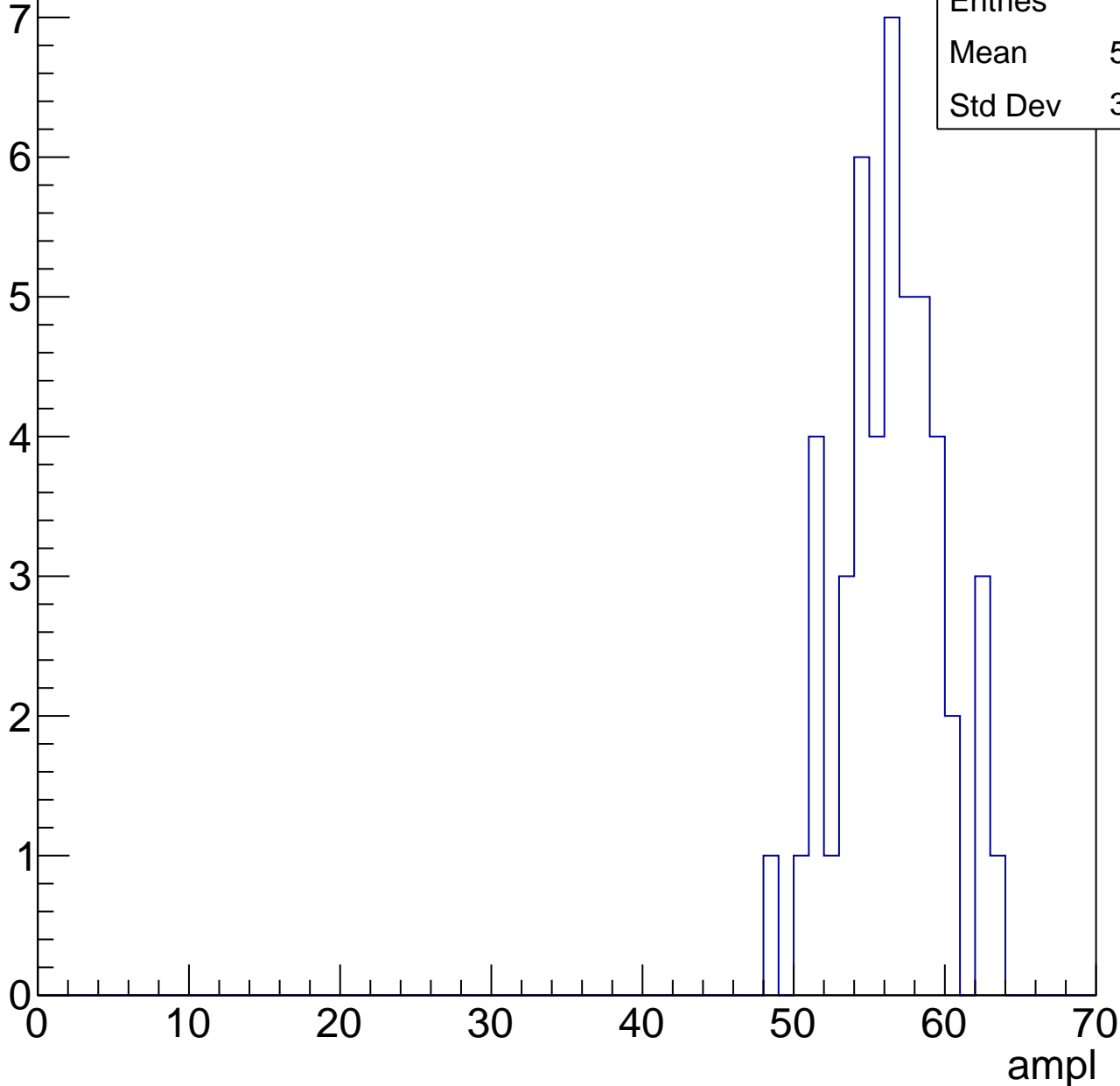


B1L103S, U10-ch108, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

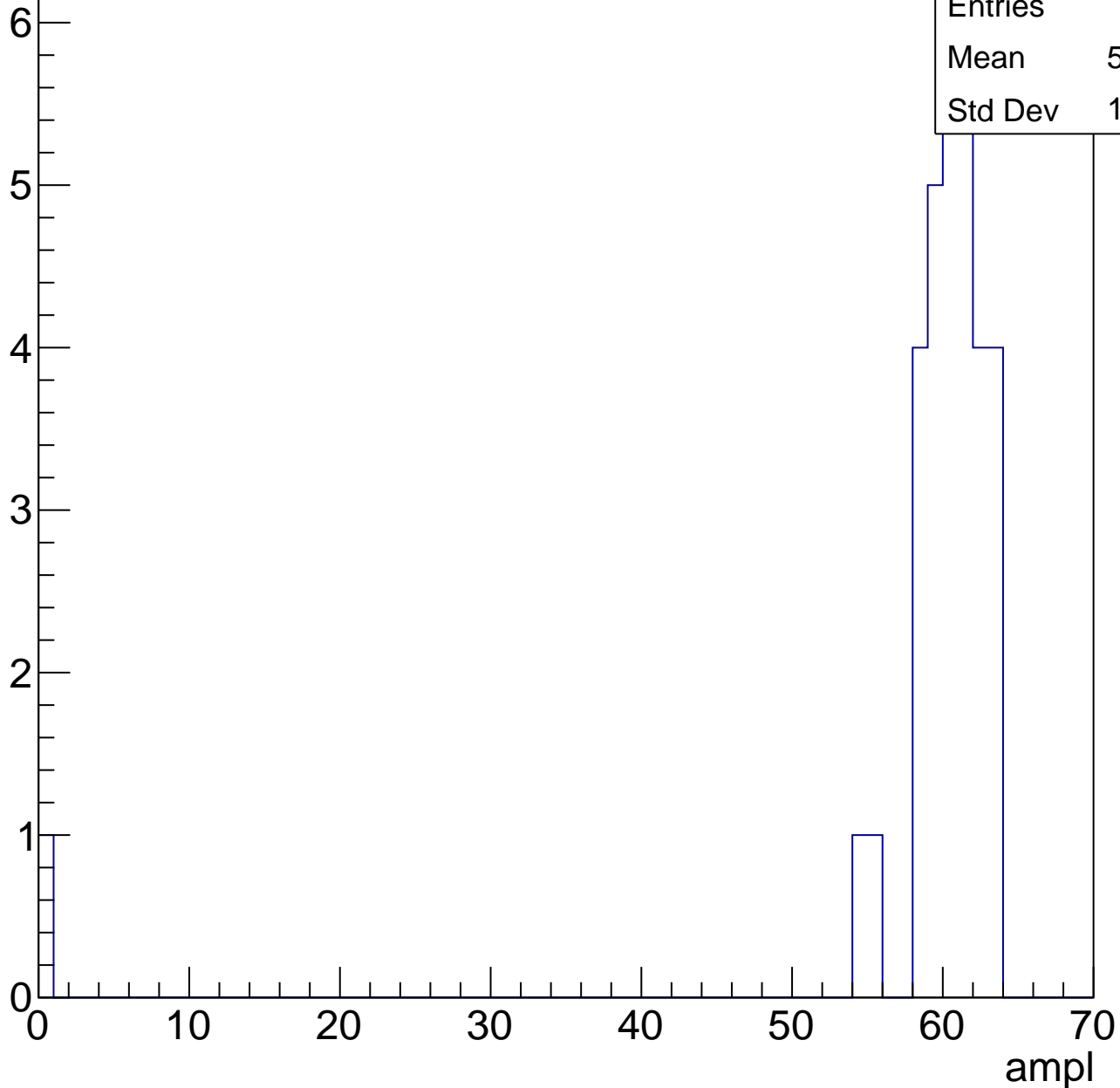
Entries	47
Mean	55.94
Std Dev	3.329



B1L103S, U10-ch108, adc5

calib_packv5_042523_0143.root, FC#7, port C2

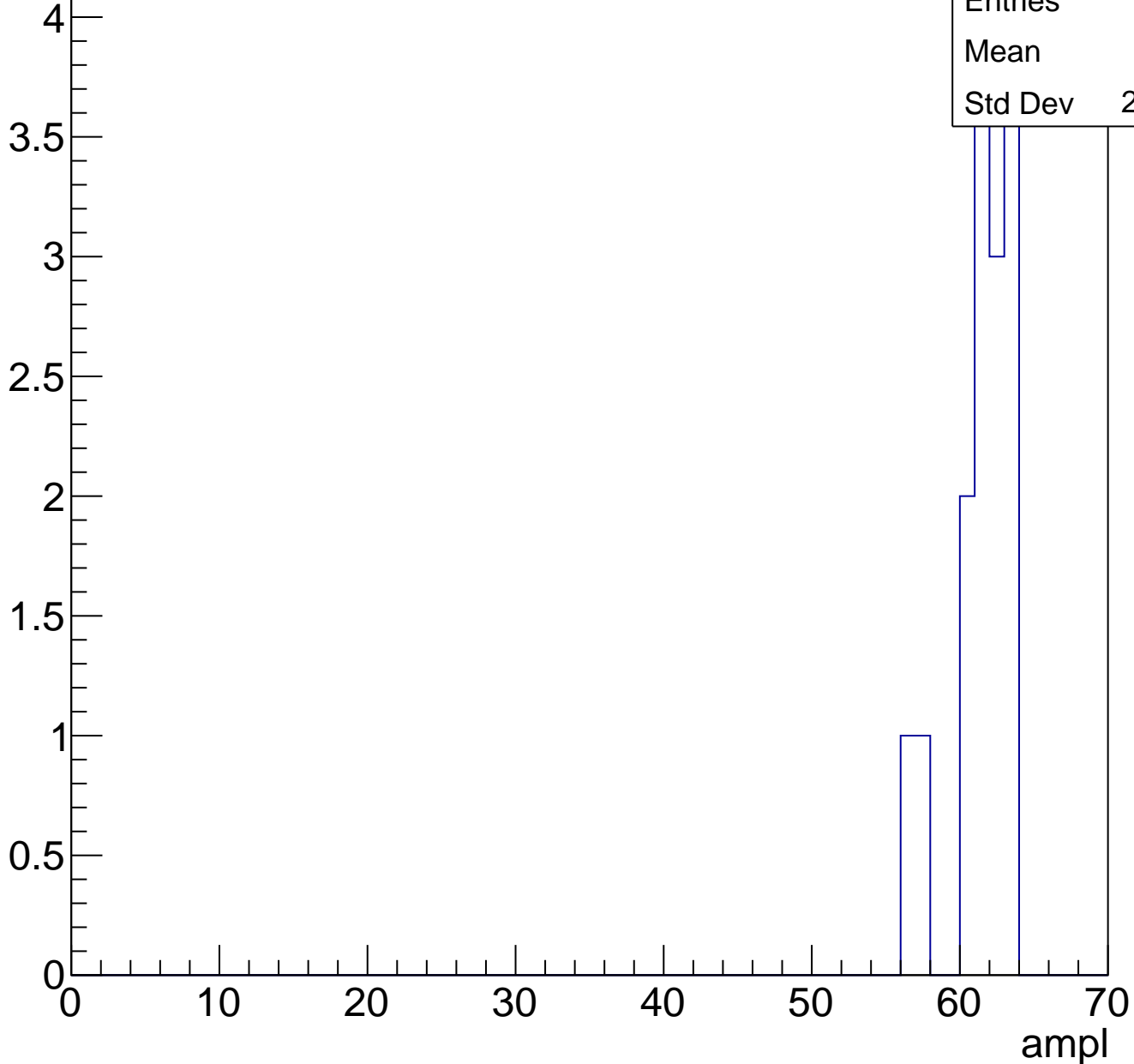
Entry



B1L103S, U10-ch108, adc6

calib_packv5_042523_0143.root, FC#7, port C2

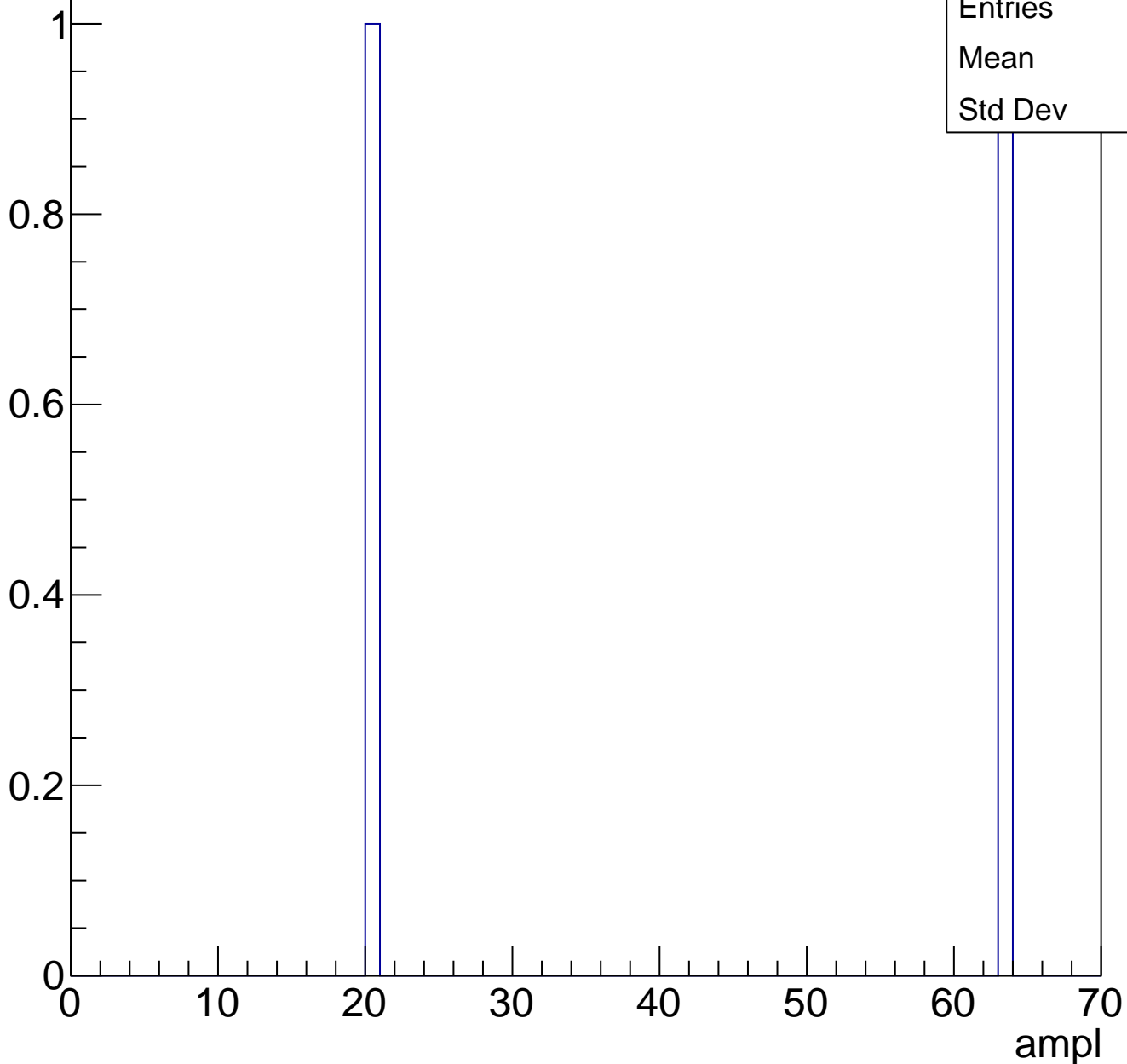
Entry



B1L103S, U10-ch108, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	41.5
Std Dev	21.5

B1L103S, U10-ch109, adc0

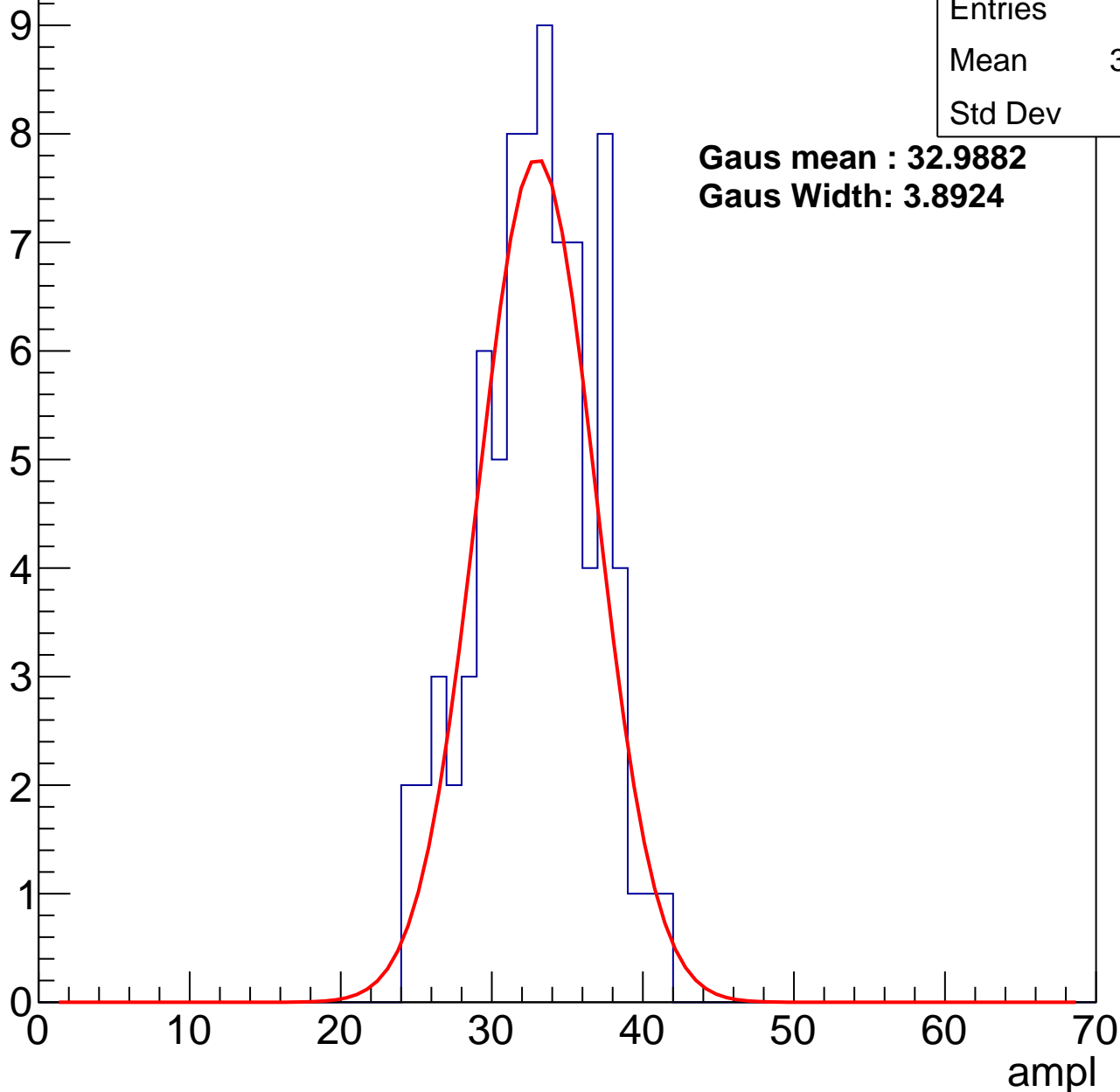
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	81
Mean	32.52
Std Dev	3.84

Gaus mean : 32.9882

Gaus Width: 3.8924



B1L103S, U10-ch109, adc1

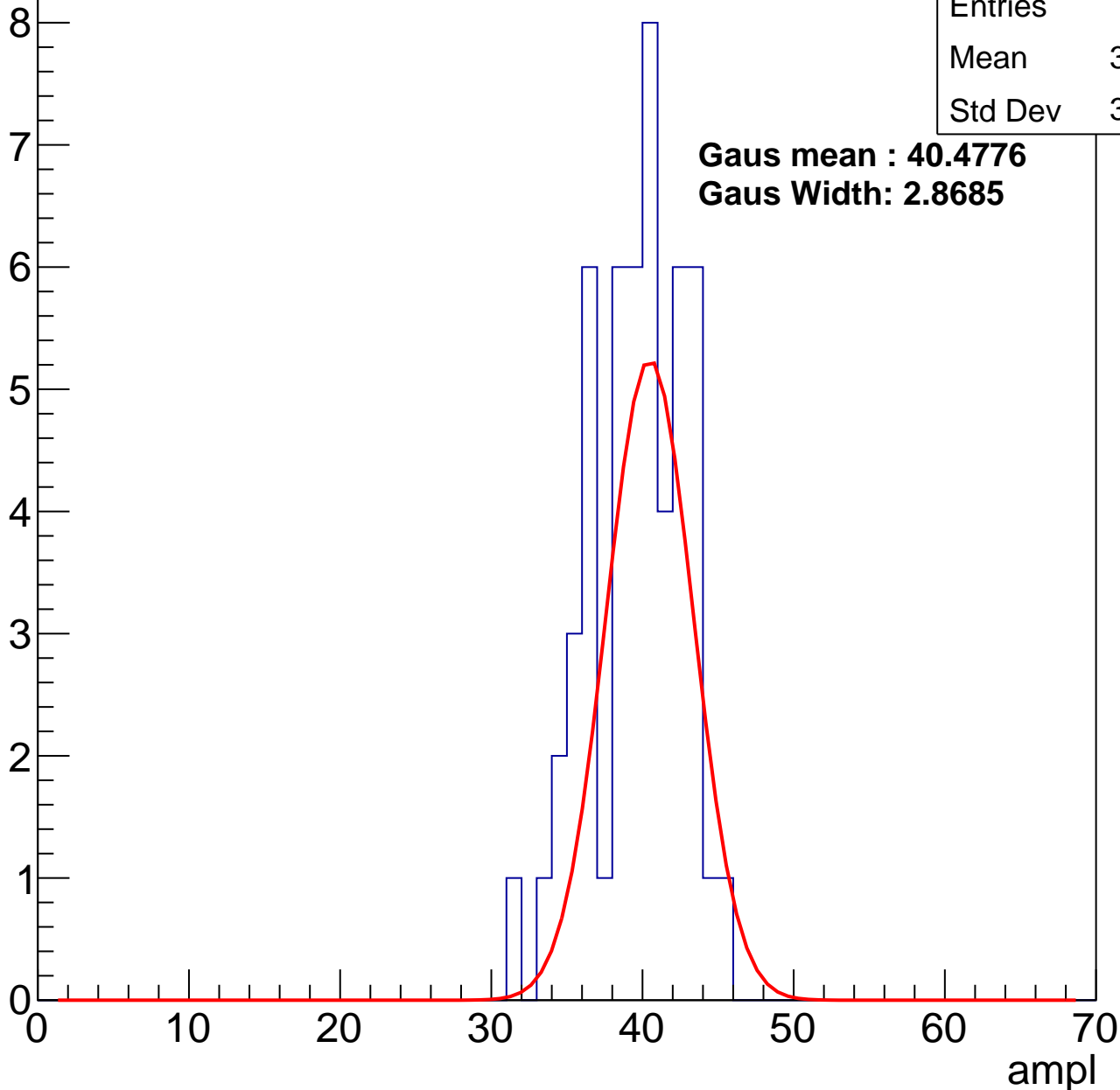
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	39.13
Std Dev	3.089

Gaus mean : 40.4776

Gaus Width: 2.8685



B1L103S, U10-ch109, adc2

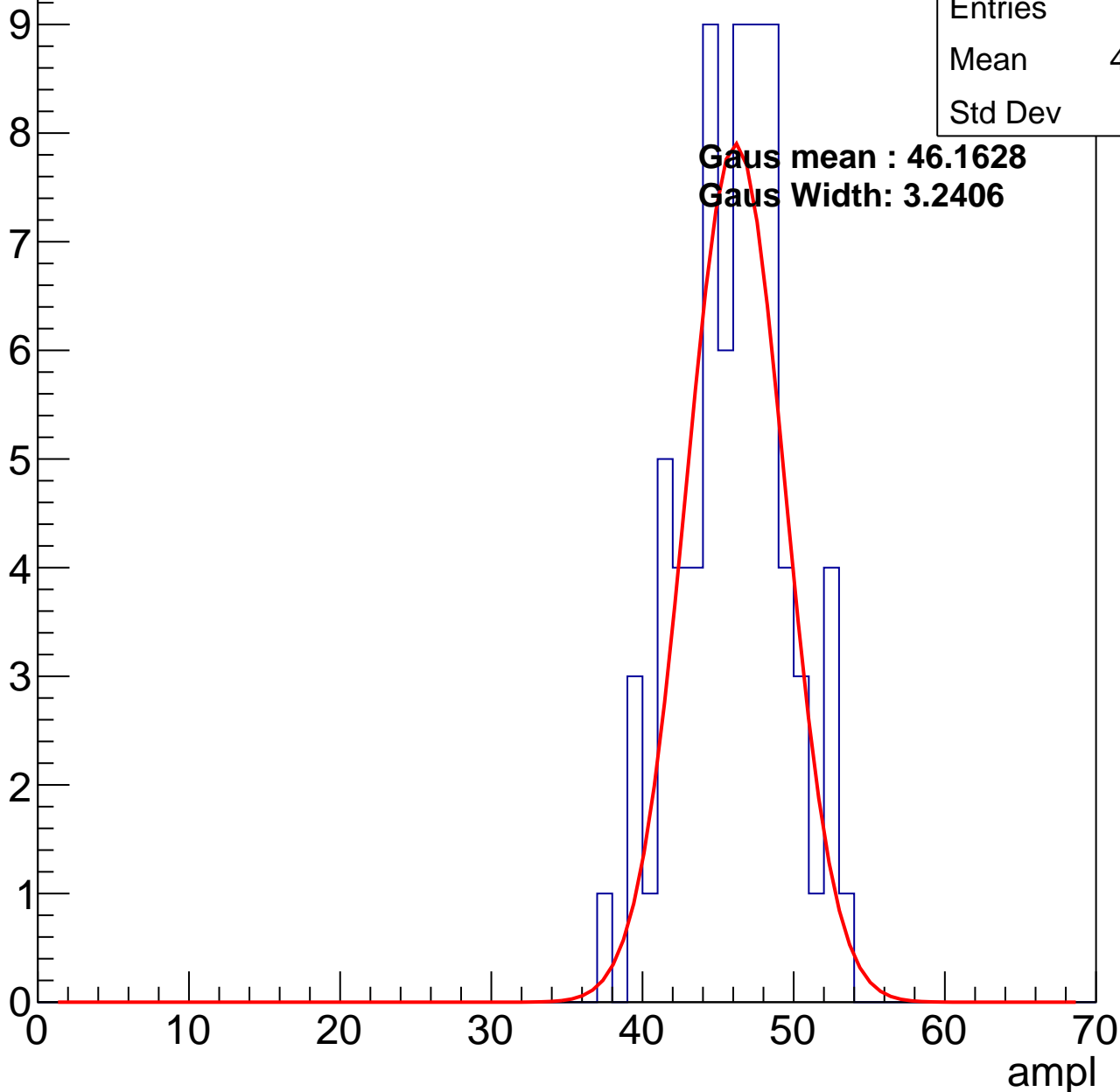
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	73
Mean	45.64
Std Dev	3.45

Gaus mean : 46.1628

Gaus Width: 3.2406

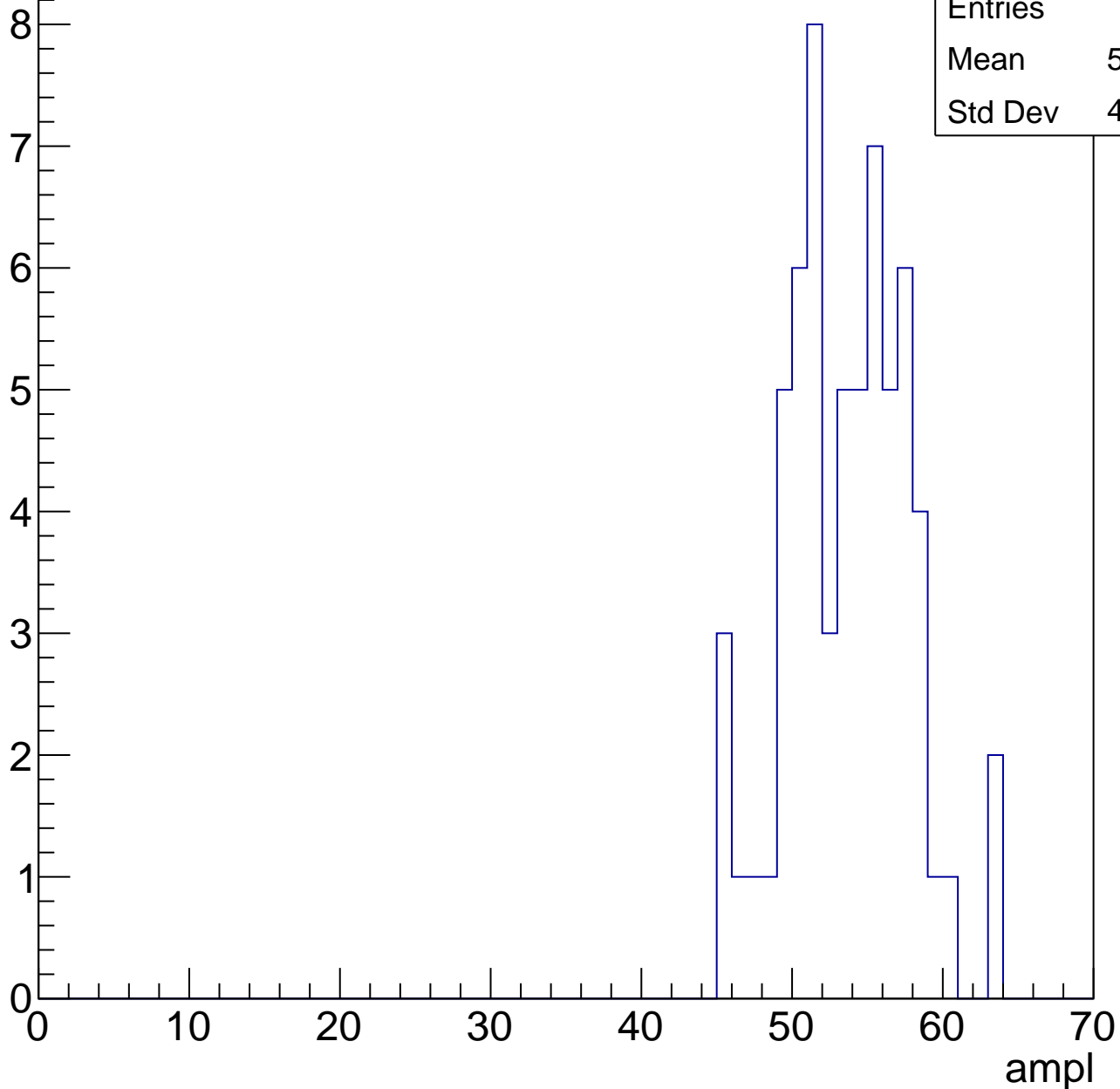


B1L103S, U10-ch109, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	53.19
Std Dev	4.007

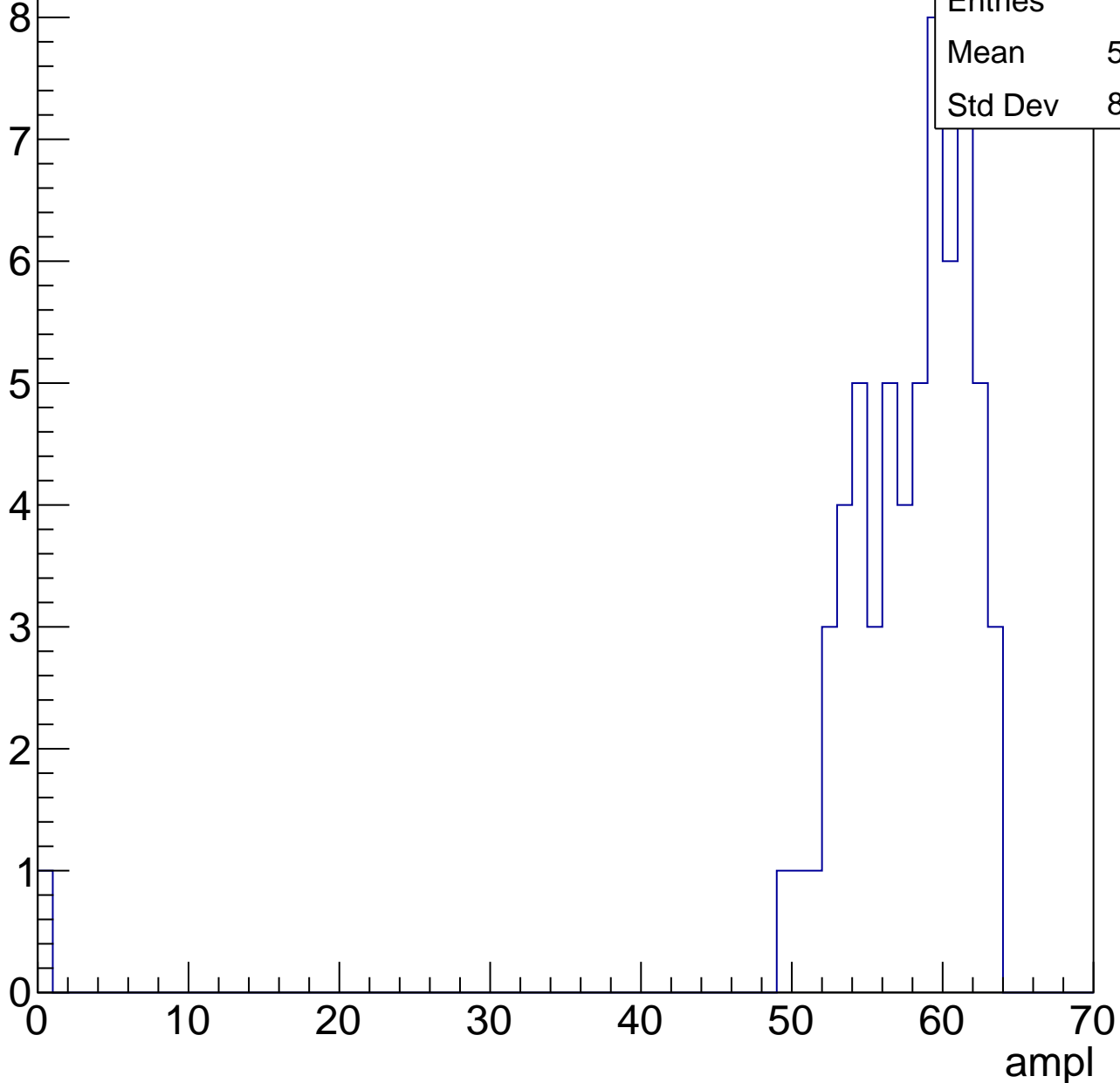


B1L103S, U10-ch109, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	56.67
Std Dev	8.012

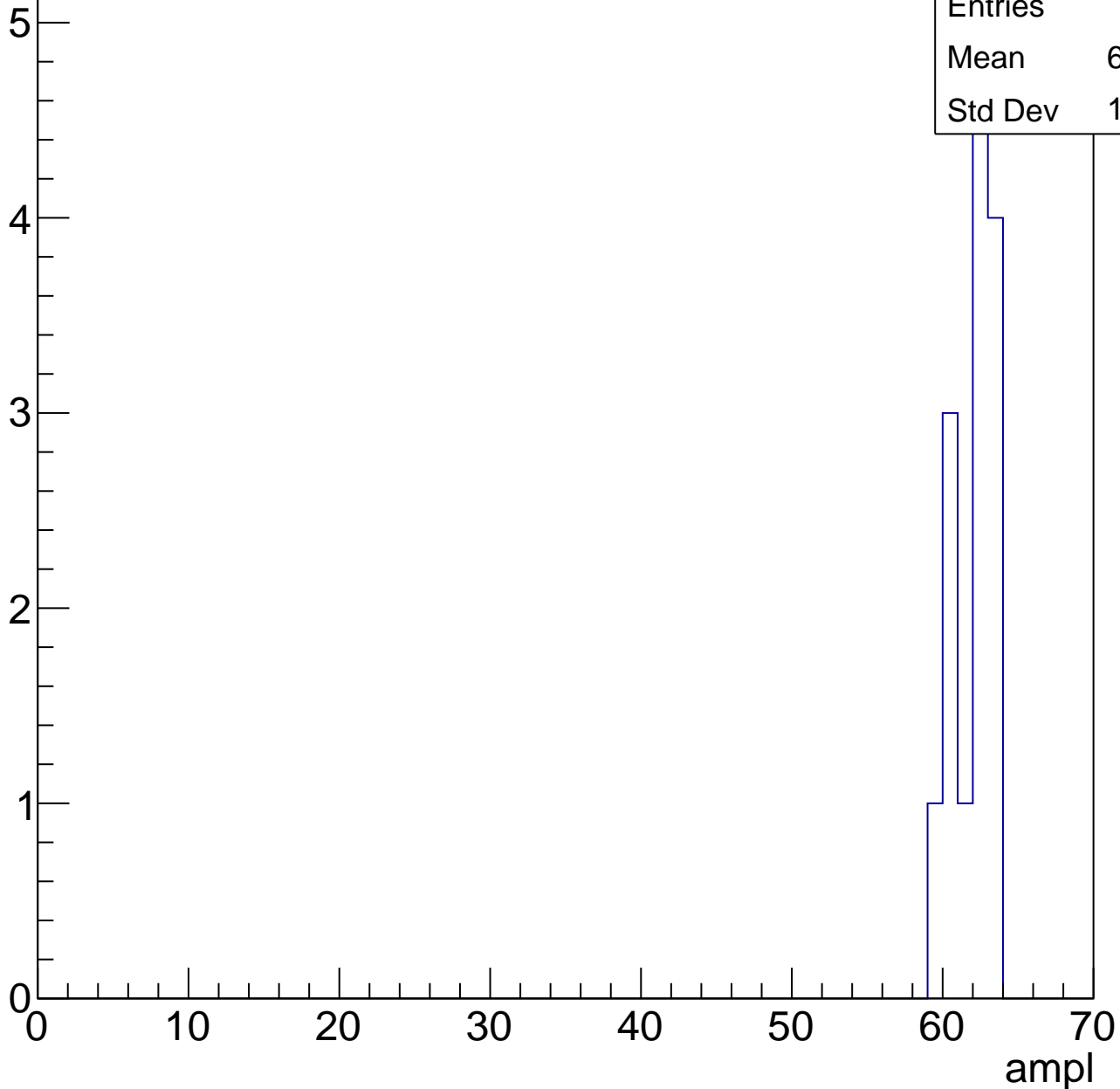


B1L103S, U10-ch109, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

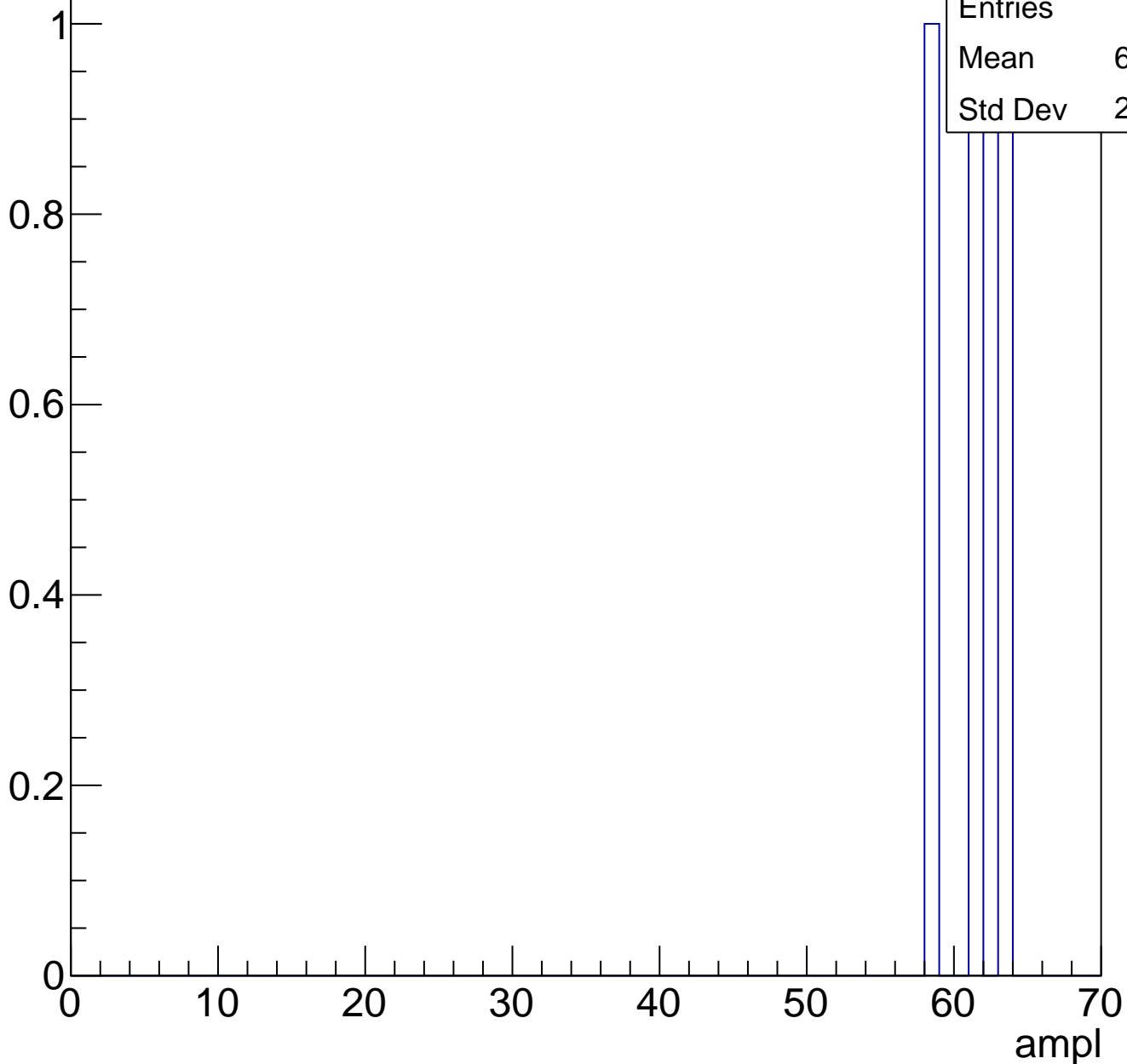
Entries	14
Mean	61.57
Std Dev	1.294



B1L103S, U10-ch109, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch109, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	11
Std Dev	11

B1L103S, U10-ch110, adc0

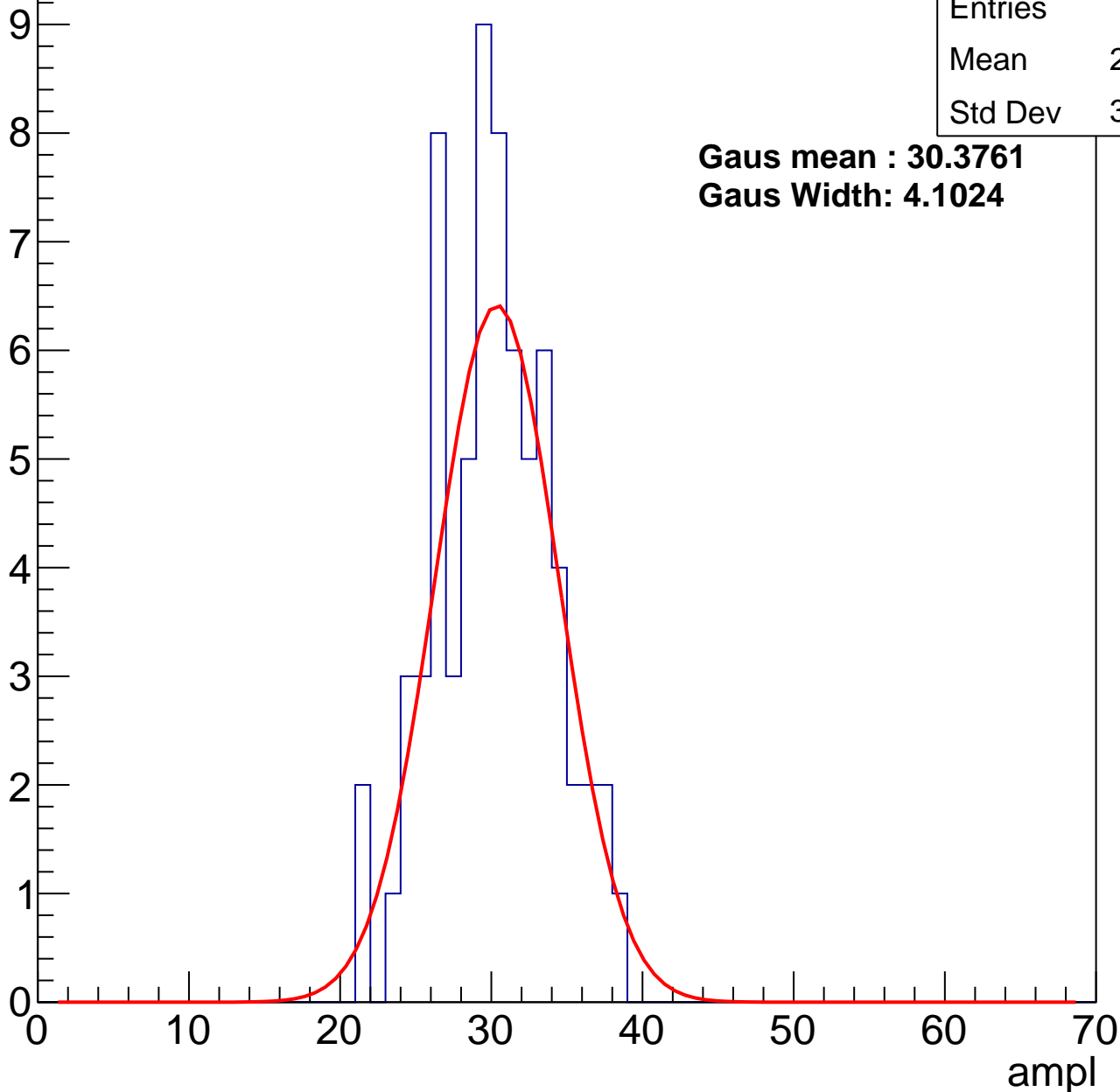
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	29.66
Std Dev	3.775

Gaus mean : 30.3761

Gaus Width: 4.1024



B1L103S, U10-ch110, adc1

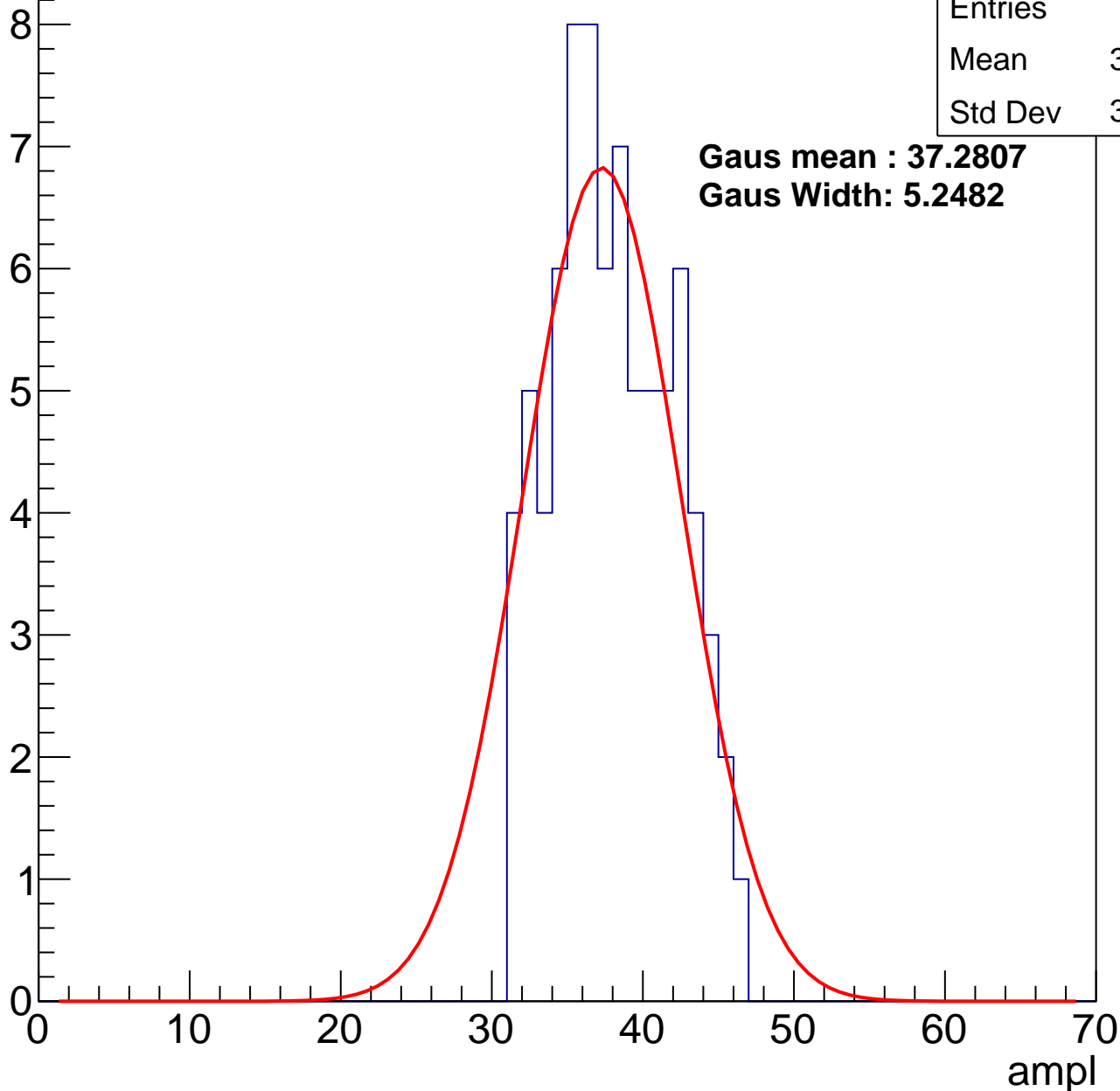
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	79
Mean	37.57
Std Dev	3.903

Gaus mean : 37.2807

Gaus Width: 5.2482



B1L103S, U10-ch110, adc2

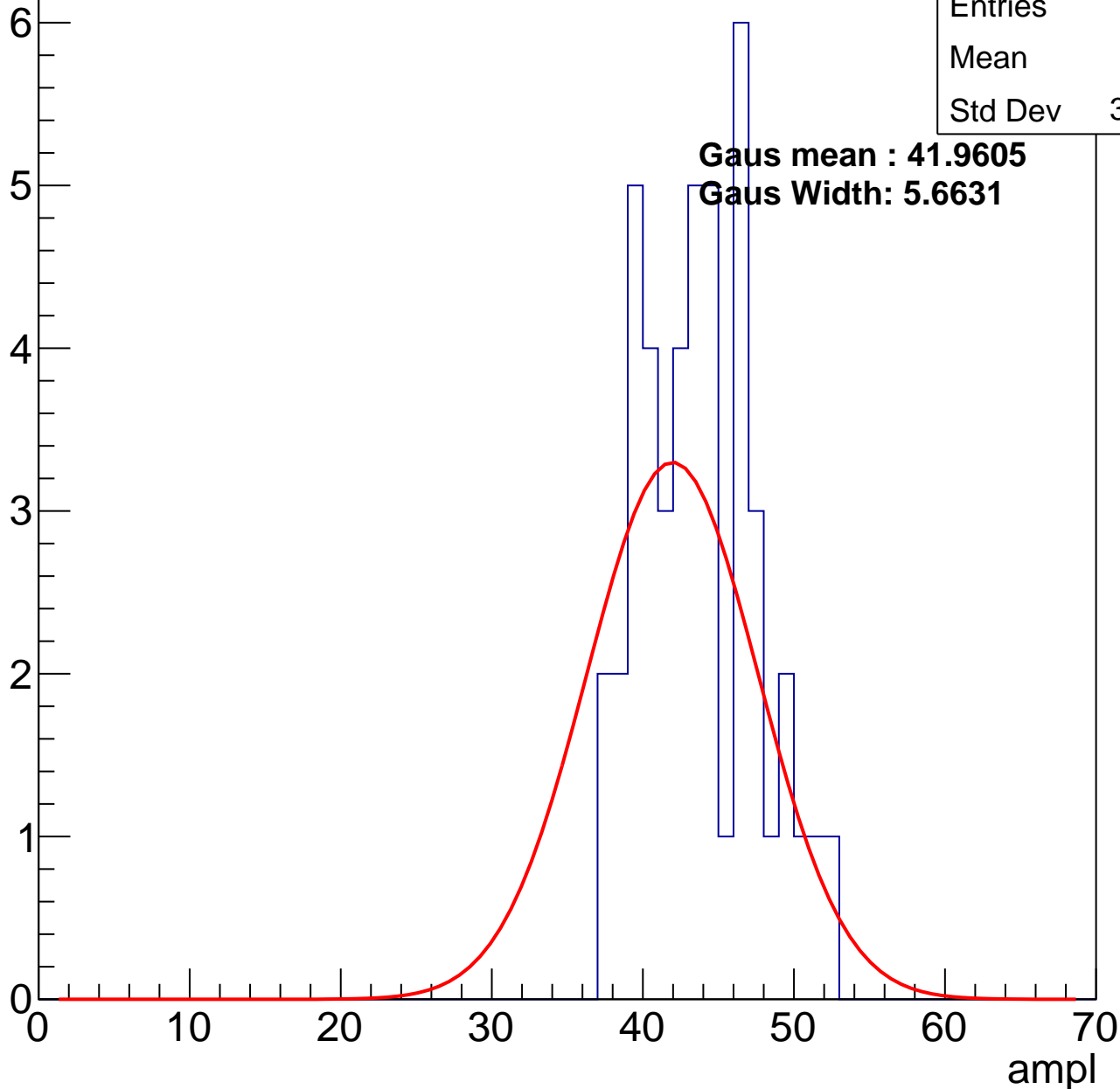
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	46
Mean	43.3
Std Dev	3.764

Gaus mean : 41.9605

Gaus Width: 5.6631

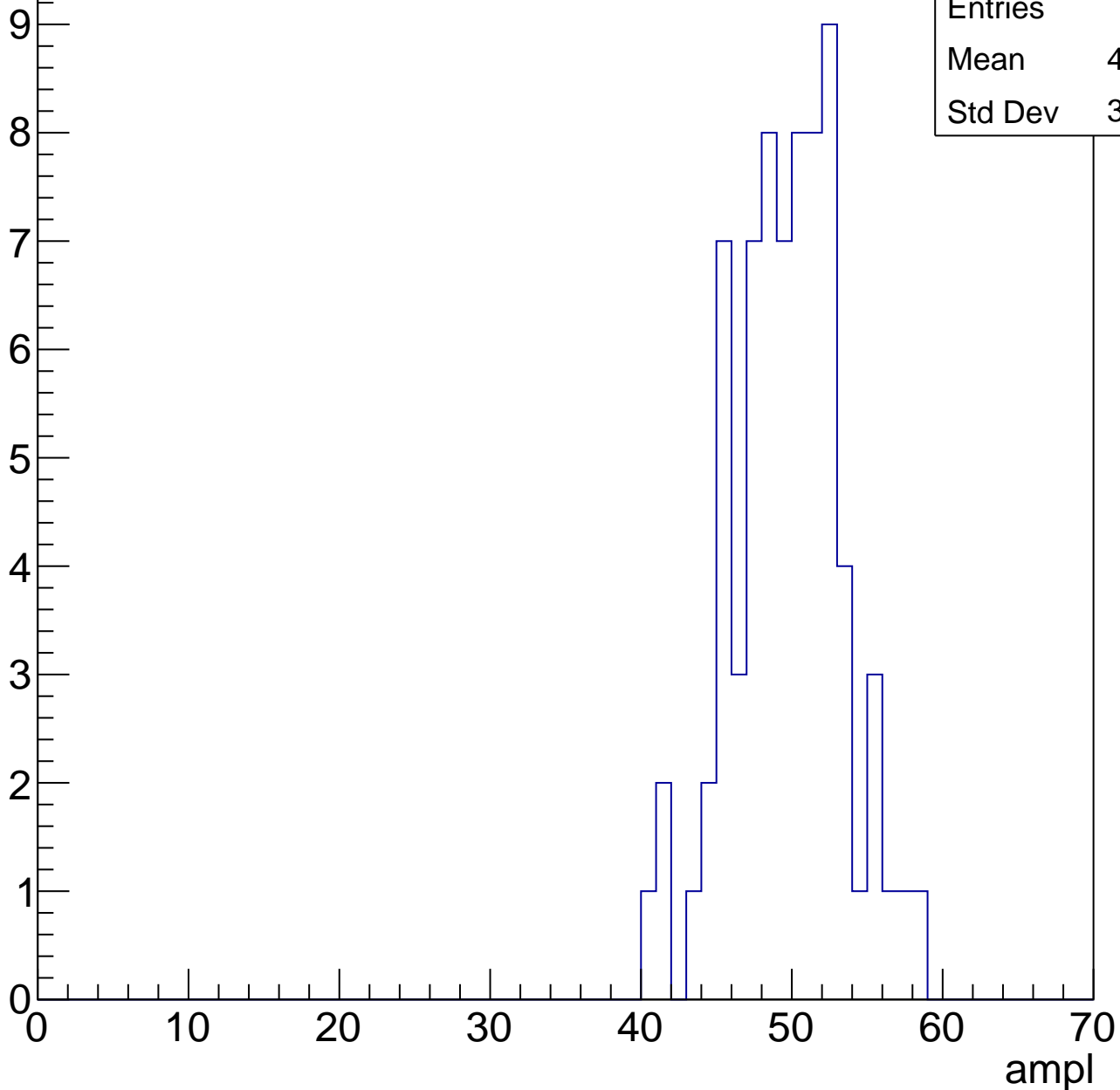


B1L103S, U10-ch110, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	49.19
Std Dev	3.638

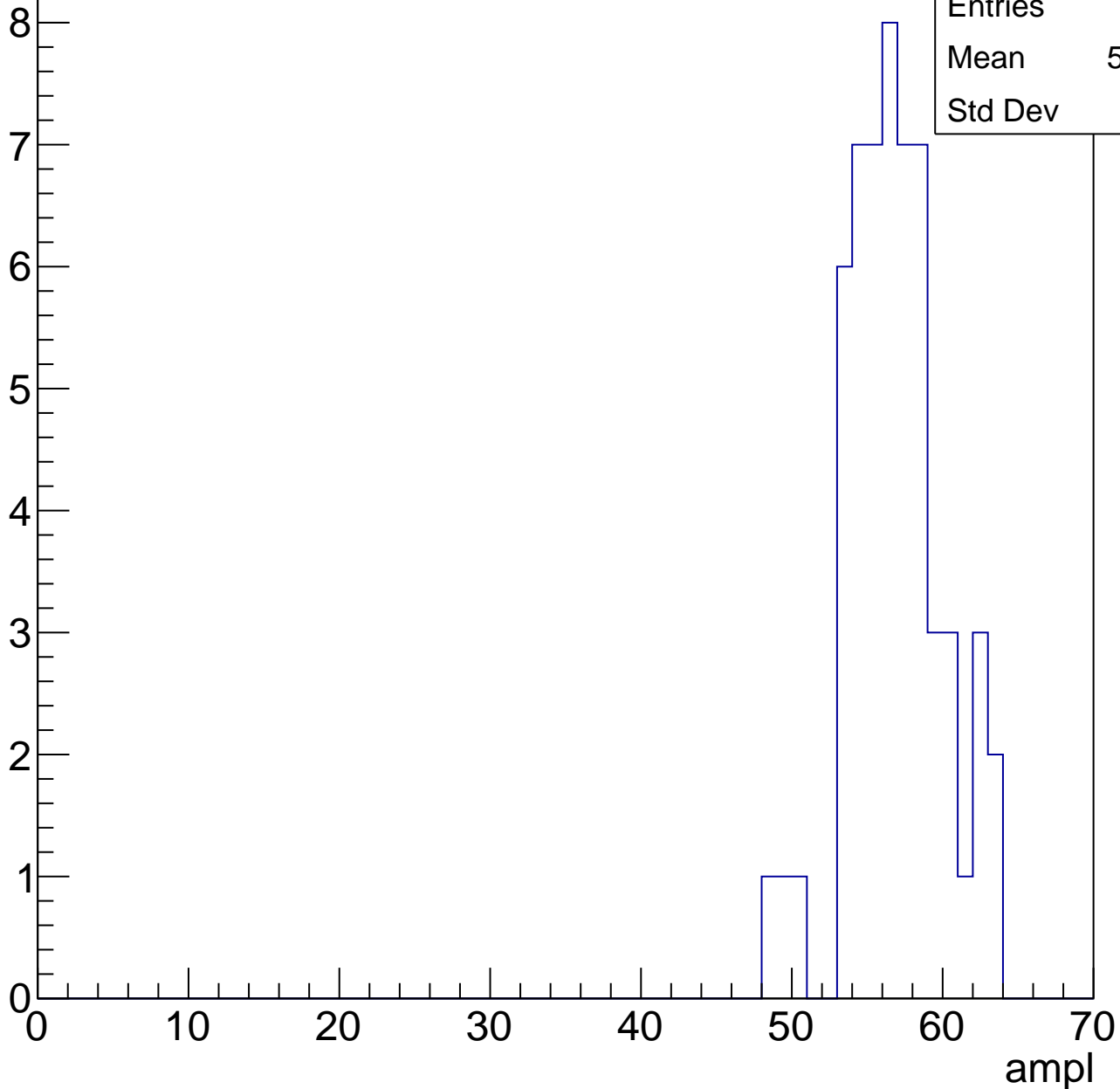


B1L103S, U10-ch110, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

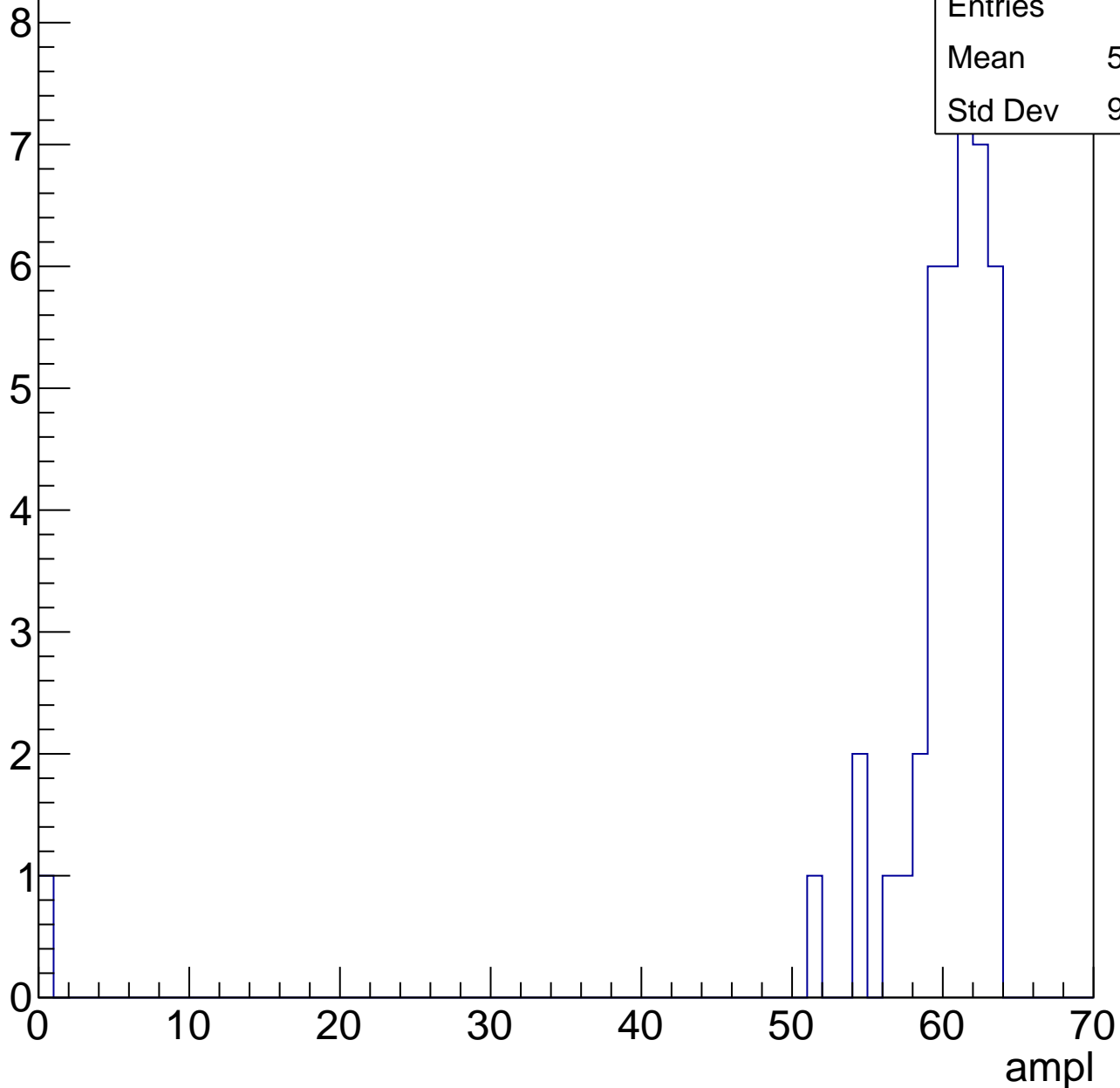
Entries	57
Mean	56.33
Std Dev	3.17



B1L103S, U10-ch110, adc5

calib_packv5_042523_0143.root, FC#7, port C2

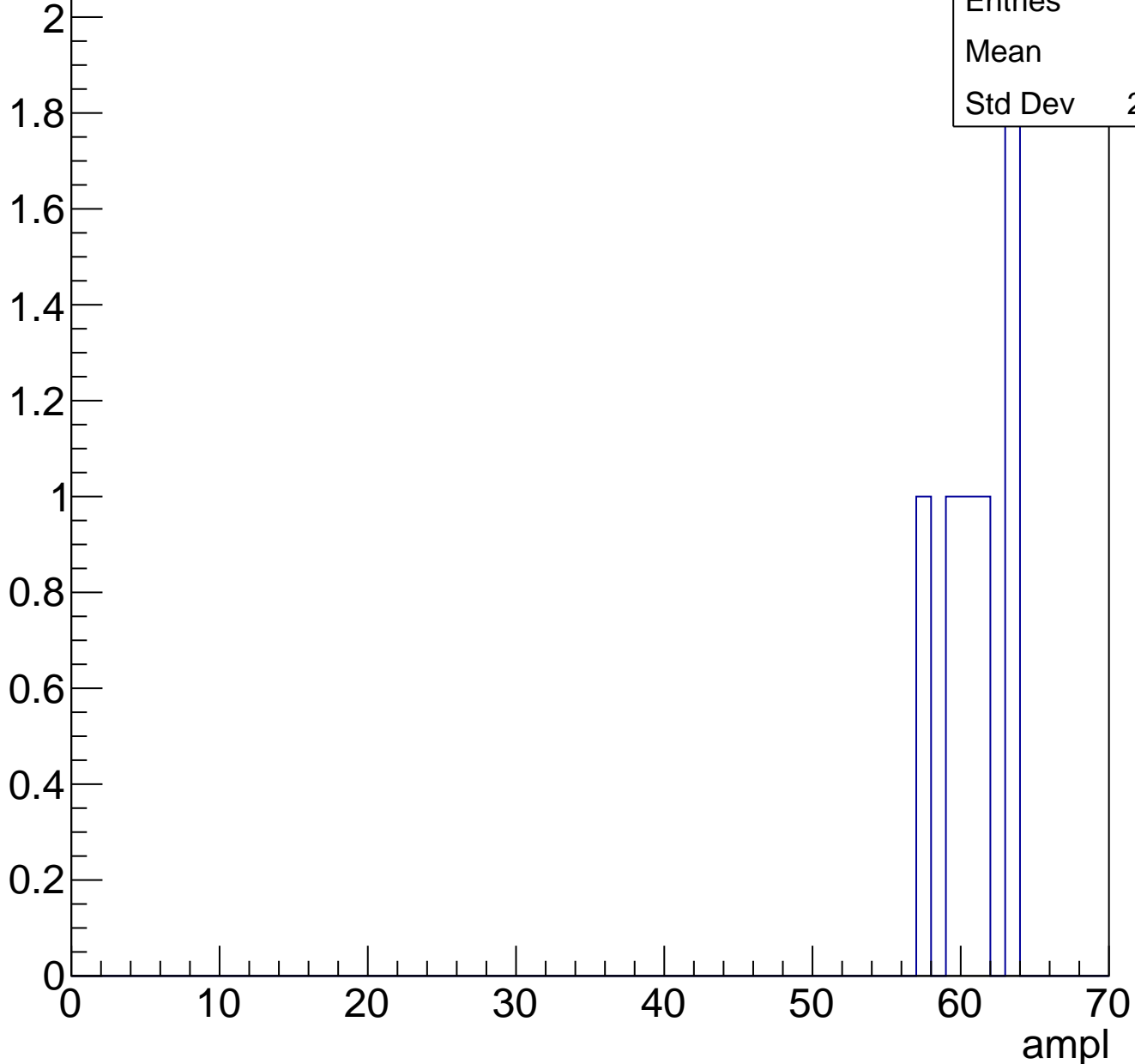
Entry



B1L103S, U10-ch110, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	6
Mean	60.5
Std Dev	2.141

B1L103S, U10-ch110, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch111, adc0

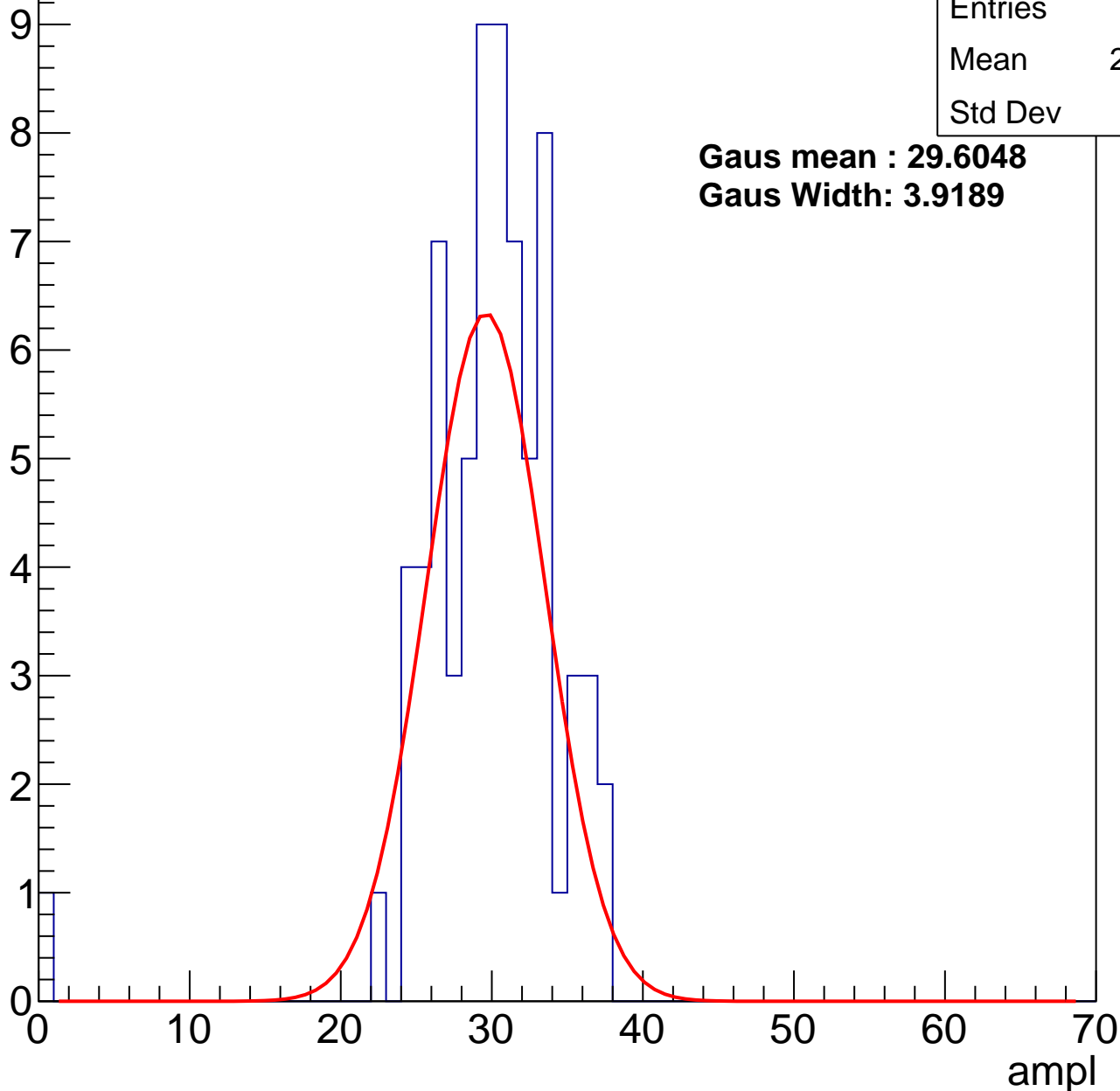
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	29.36
Std Dev	4.92

Gaus mean : 29.6048

Gaus Width: 3.9189



B1L103S, U10-ch111, adc1

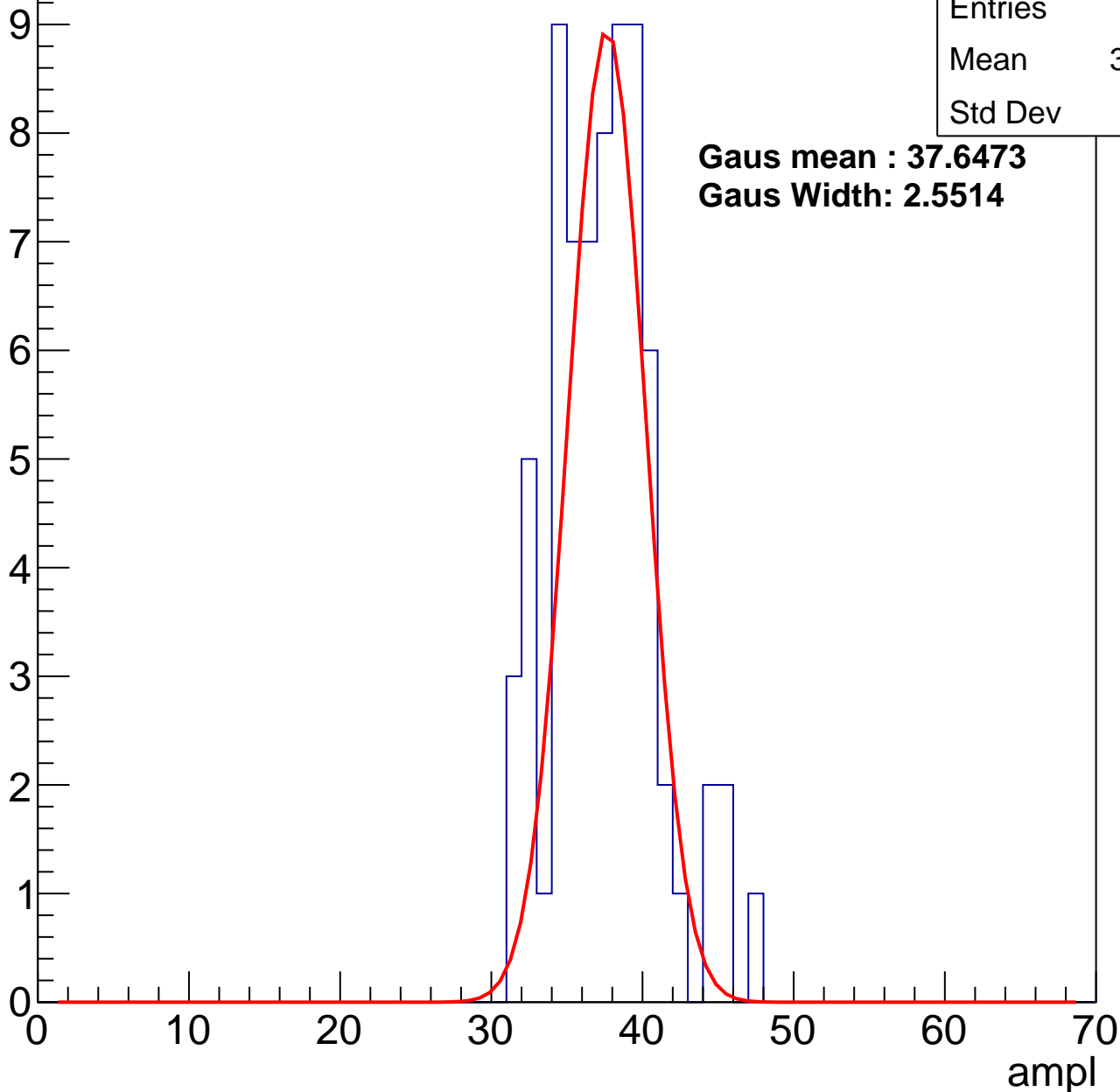
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	37.04
Std Dev	3.43

Gaus mean : 37.6473

Gaus Width: 2.5514



B1L103S, U10-ch111, adc2

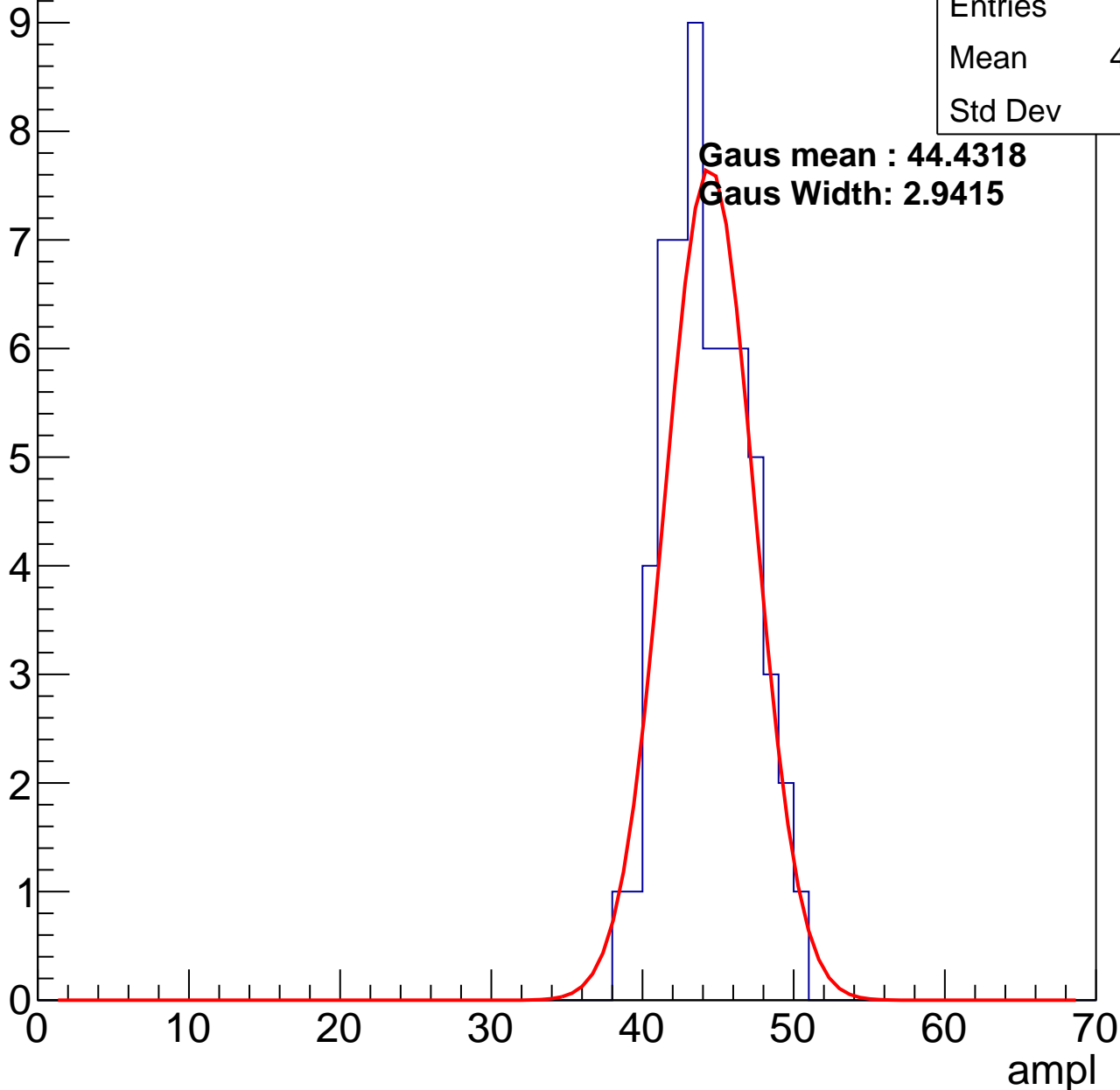
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	43.83
Std Dev	2.73

Gaus mean : 44.4318

Gaus Width: 2.9415

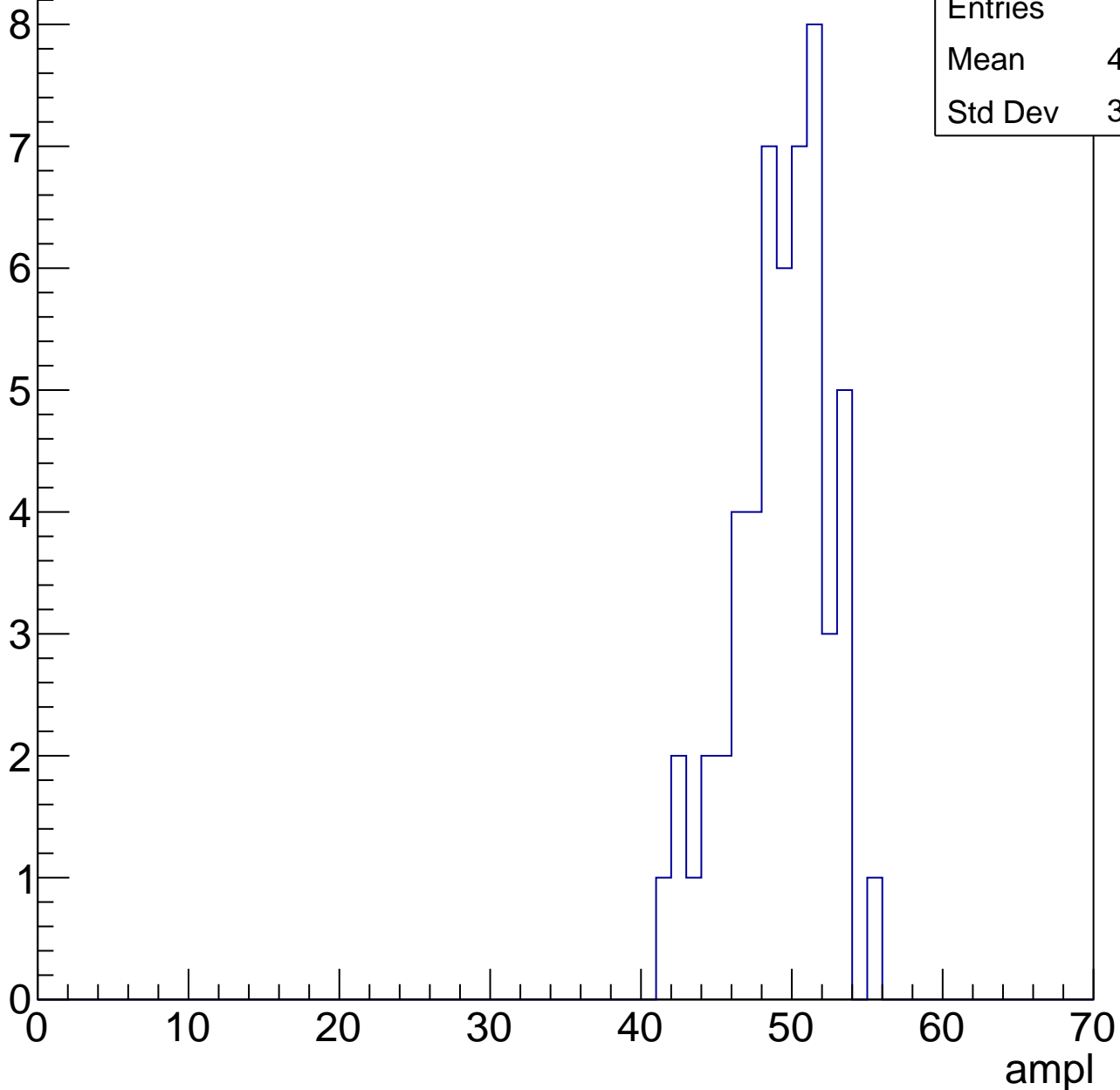


B1L103S, U10-ch111, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	48.72
Std Dev	3.123

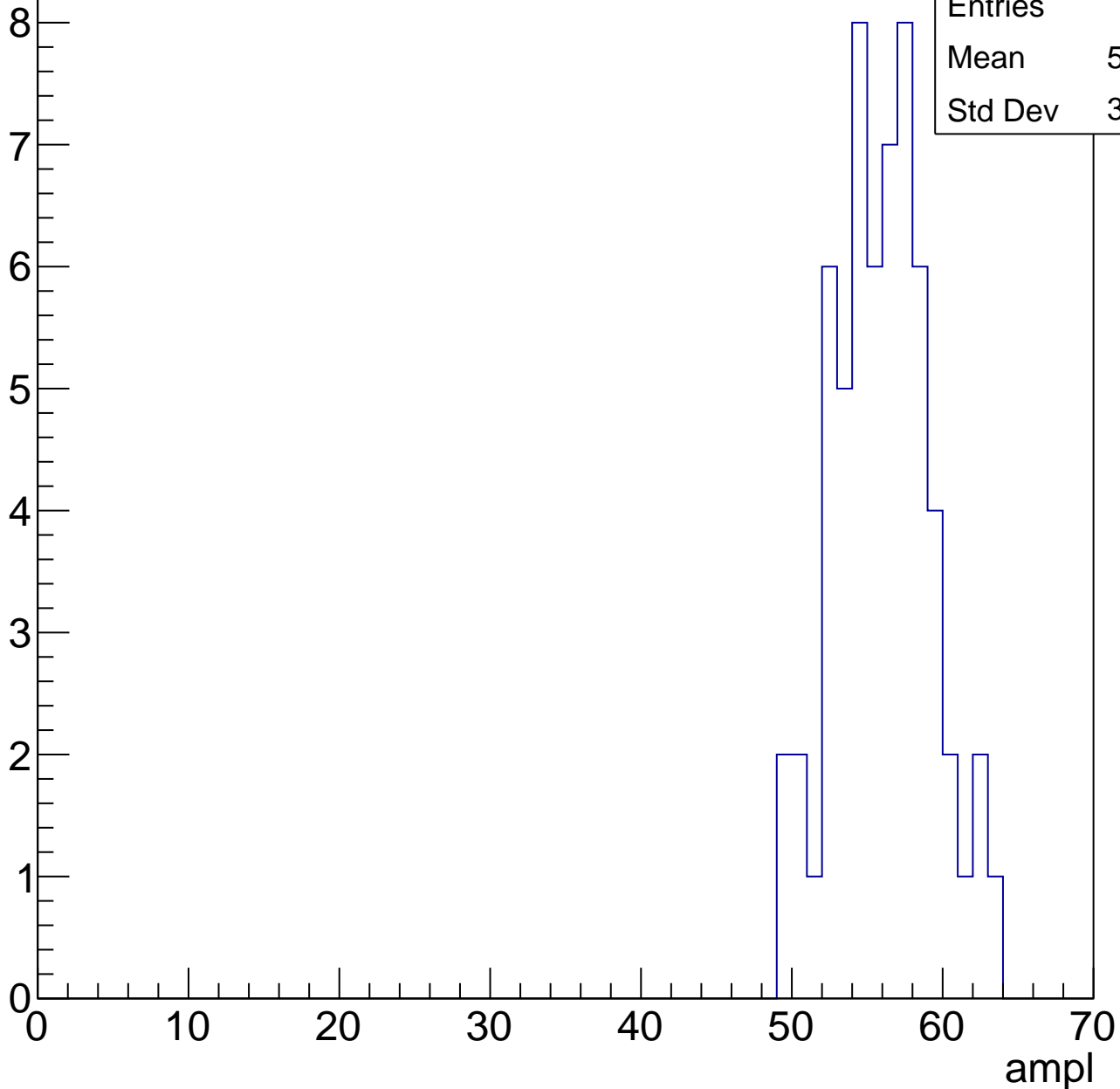


B1L103S, U10-ch111, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	55.54
Std Dev	3.155

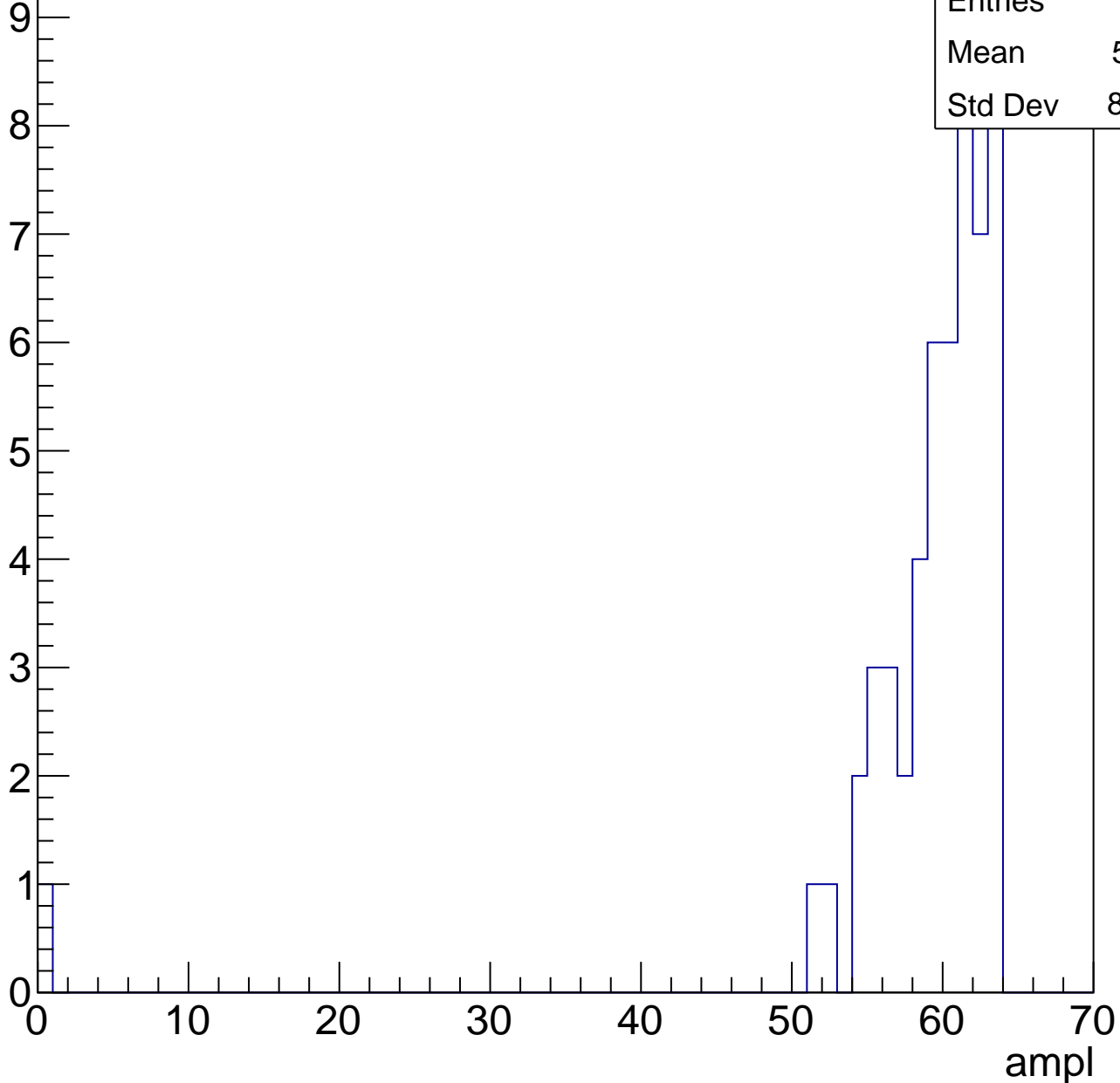


B1L103S, U10-ch111, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	58.41
Std Dev	8.565



B1L103S, U10-ch111, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	60.67
Std Dev	0.9428

ampl

0 10 20 30 40 50 60 70

B1L103S, U10-ch111, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	2
Mean	0
Std Dev	0

B1L103S, U10-ch112, adc0

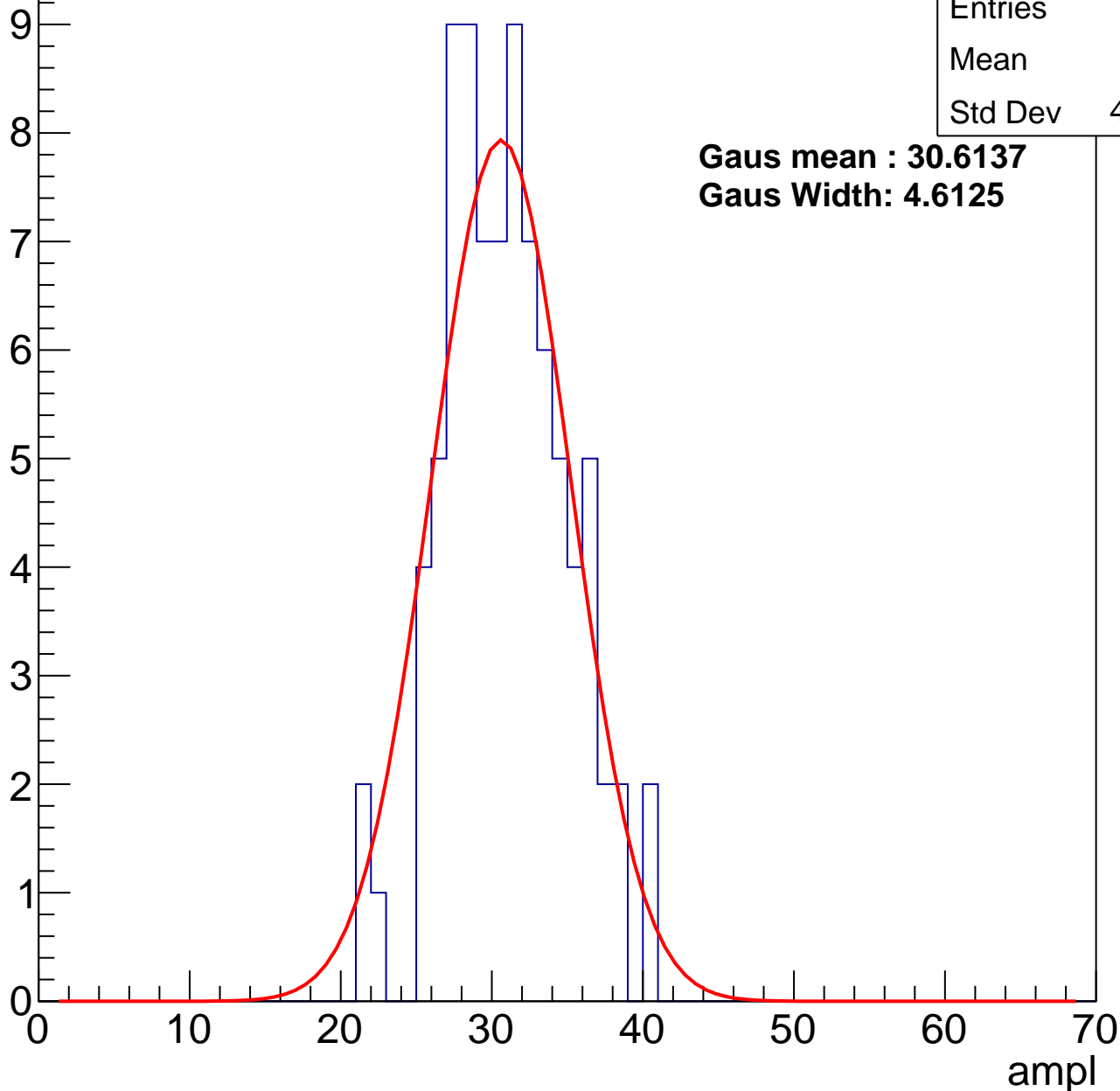
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	86
Mean	30.5
Std Dev	4.014

Gaus mean : 30.6137

Gaus Width: 4.6125



B1L103S, U10-ch112, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	38.03
Std Dev	3.973

Gaus mean : 38.3398

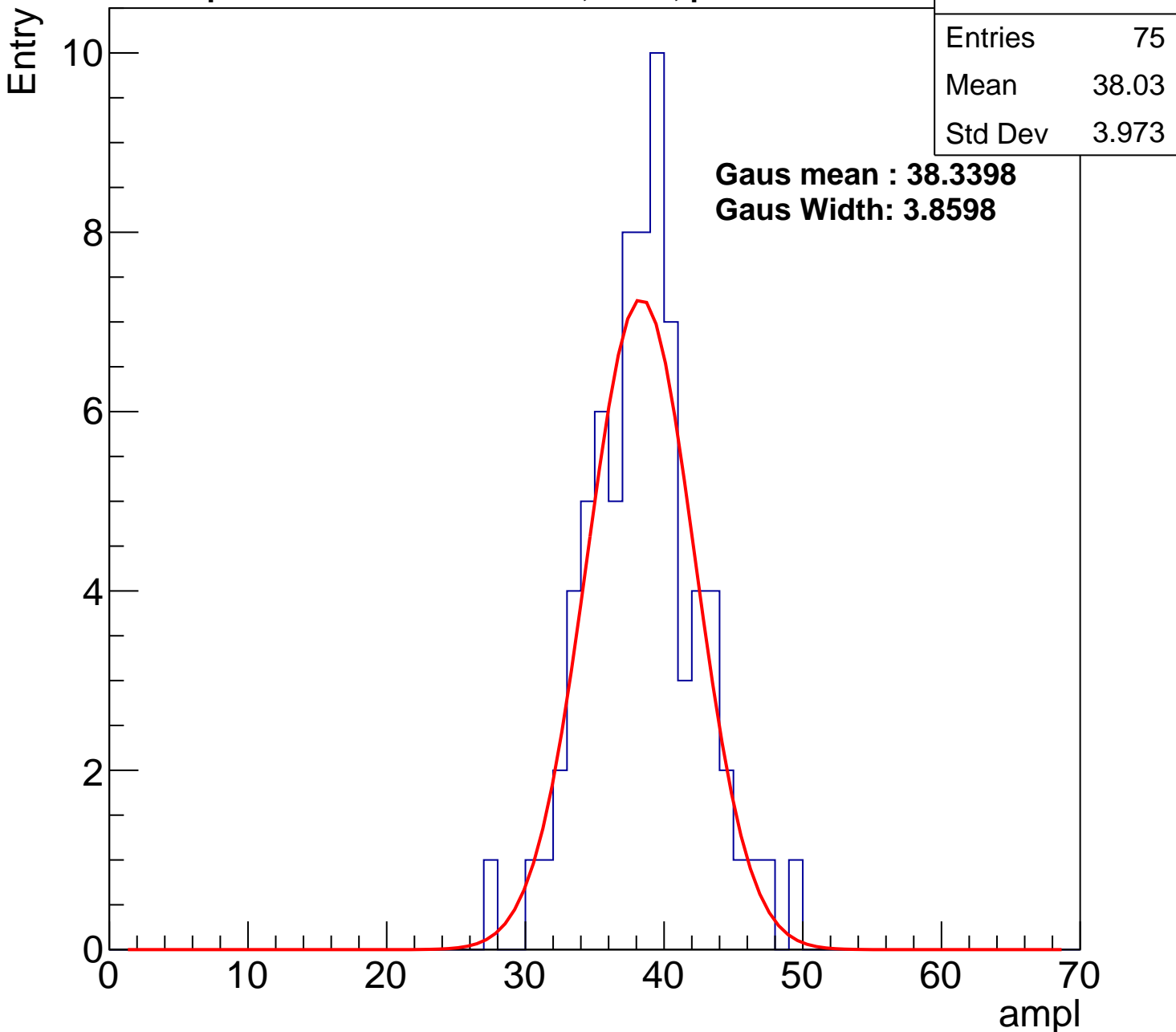
Gaus Width: 3.8598

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L103S, U10-ch112, adc2

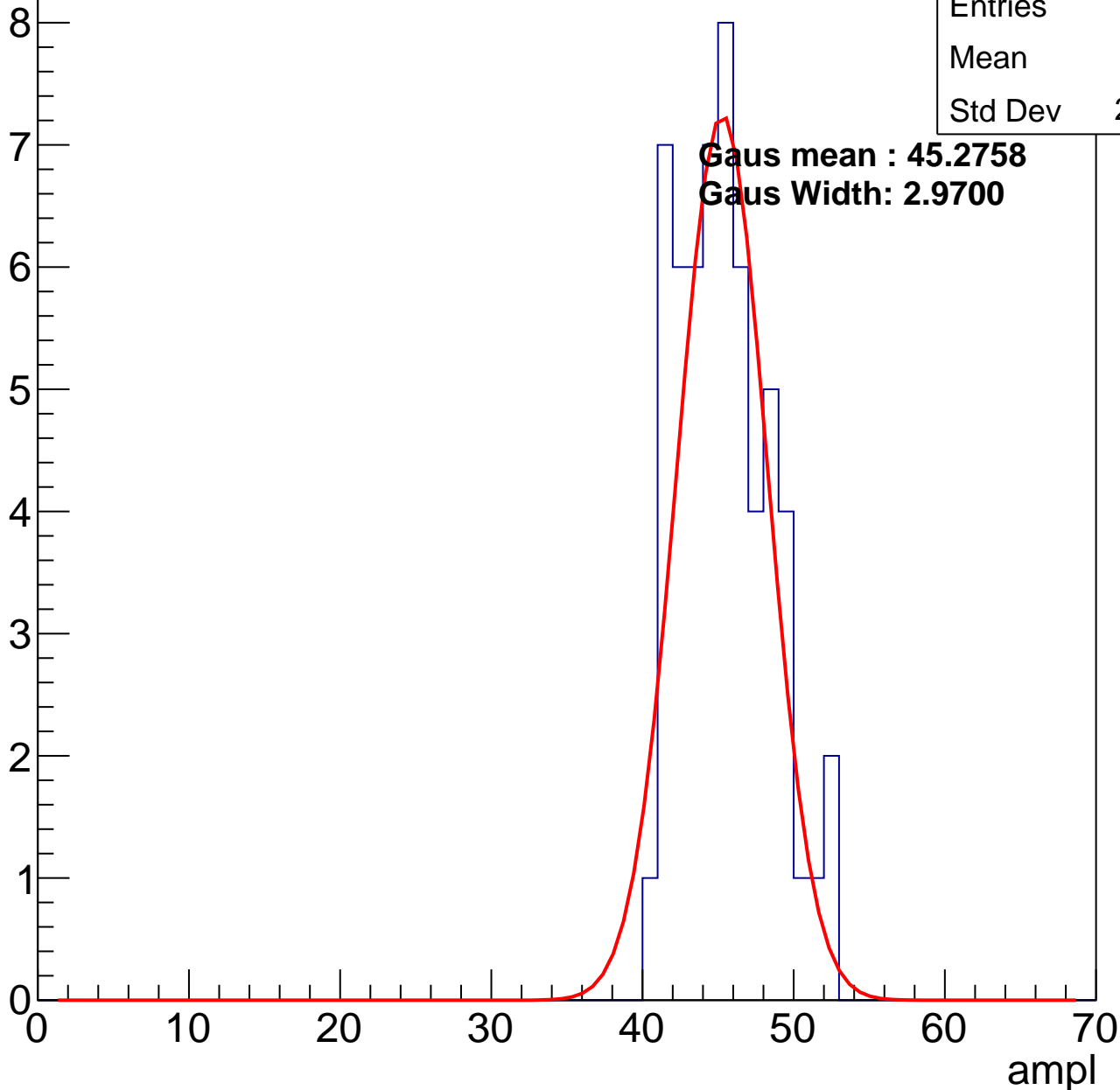
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	45
Std Dev	2.971

Gaus mean : 45.2758

Gaus Width: 2.9700

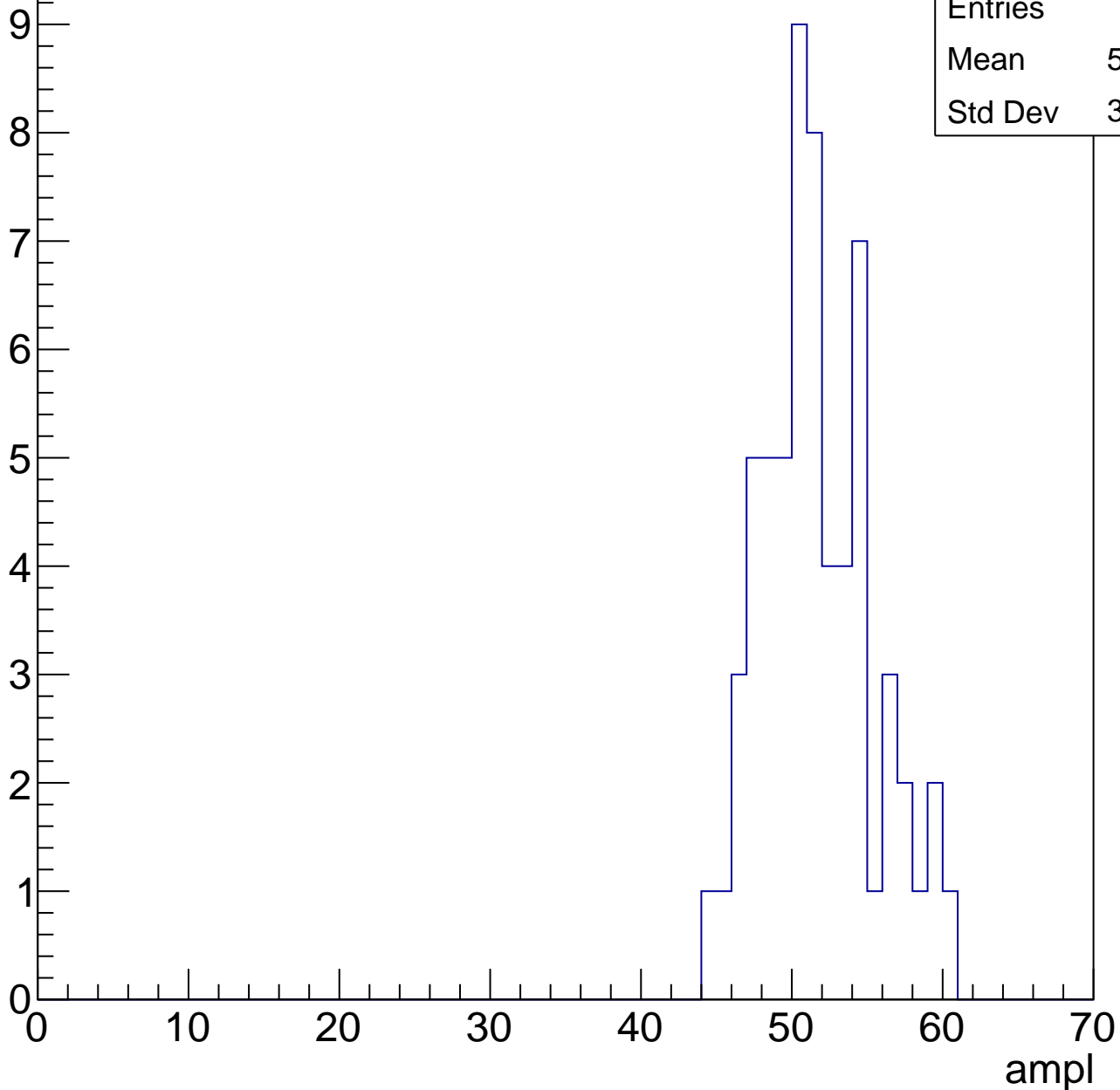


B1L103S, U10-ch112, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	51.23
Std Dev	3.625

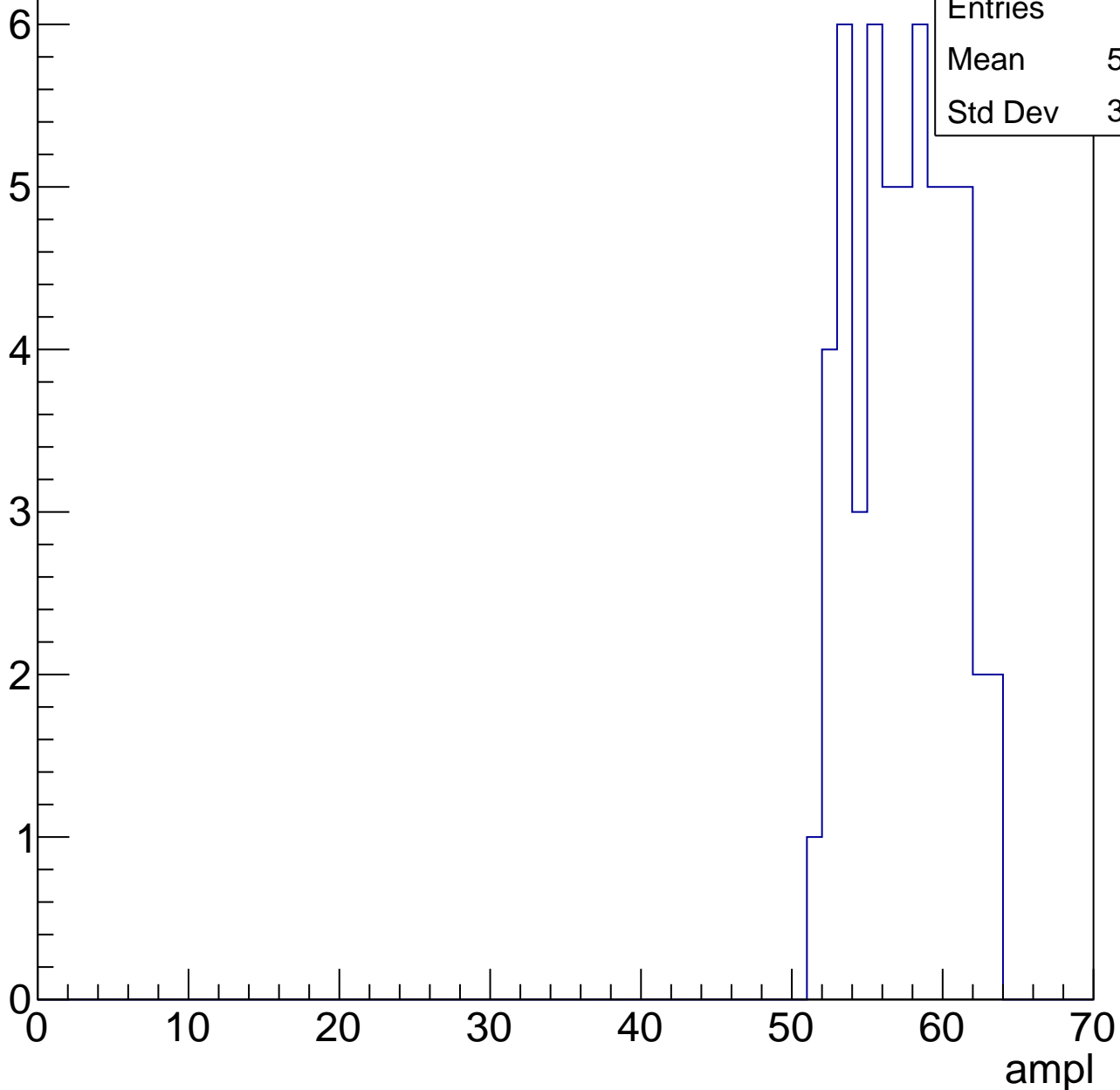


B1L103S, U10-ch112, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	56.95
Std Dev	3.193

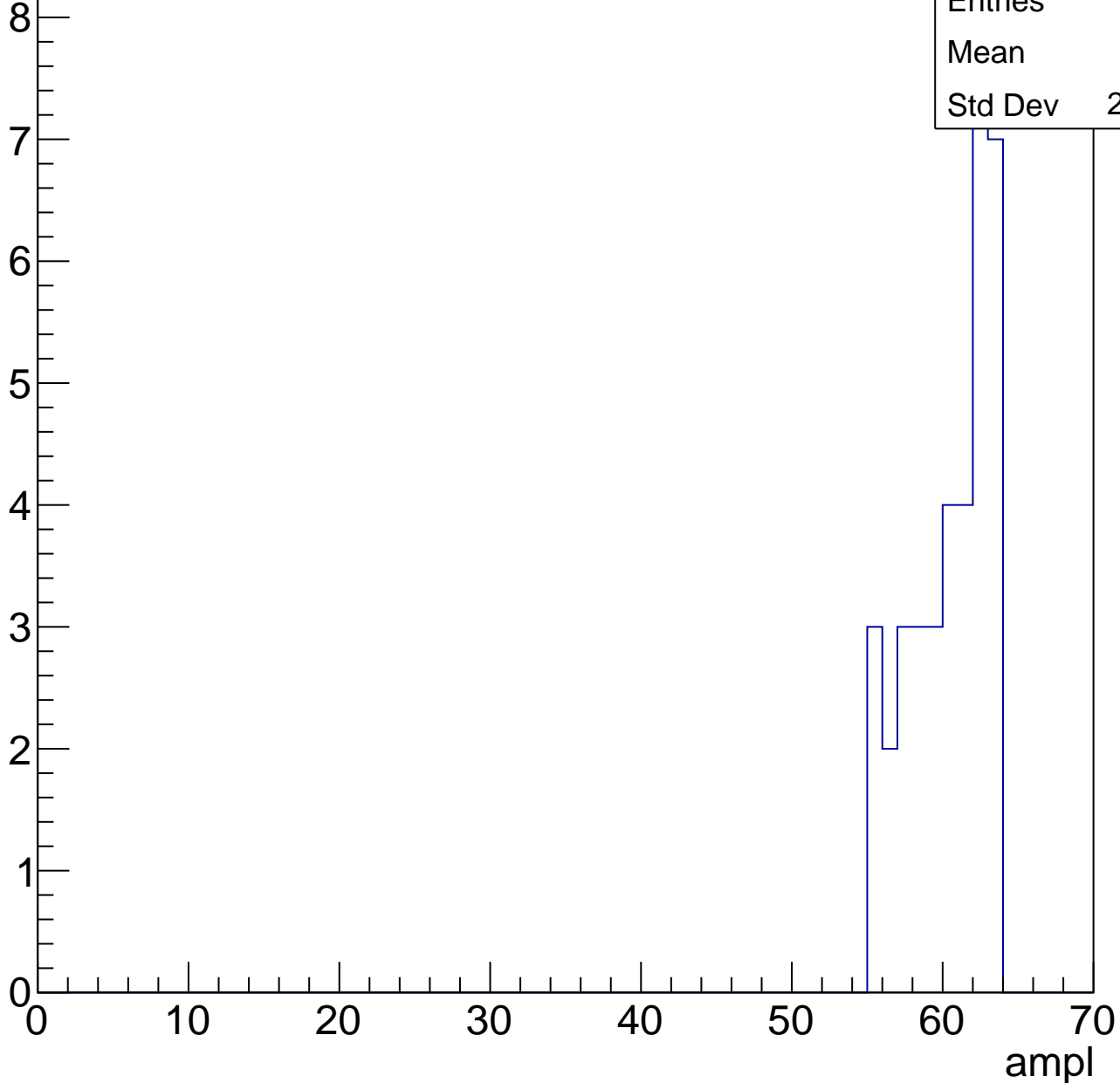


B1L103S, U10-ch112, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	37
Mean	60
Std Dev	2.589



B1L103S, U10-ch112, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch112, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch113, adc0

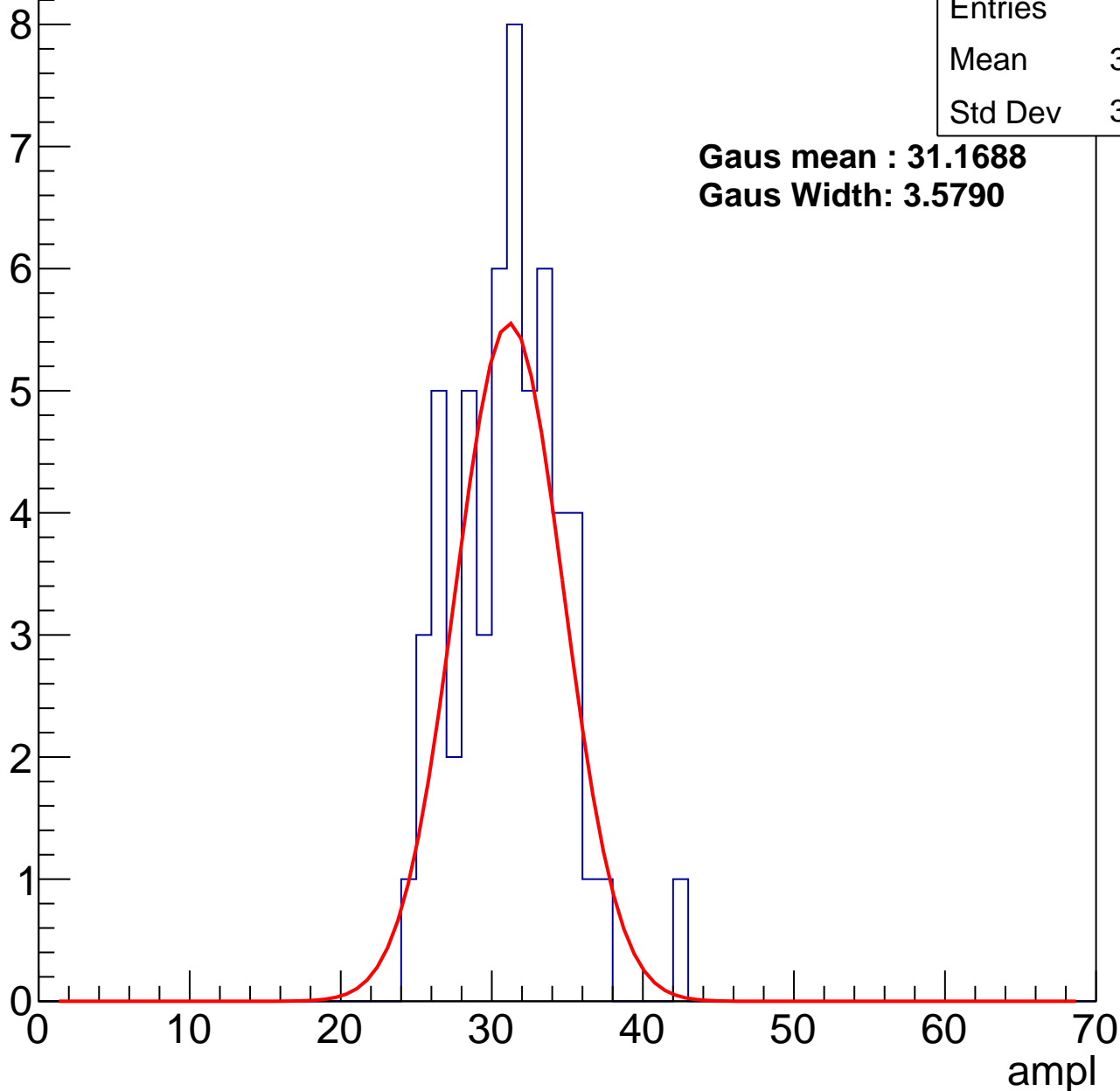
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	55
Mean	30.67
Std Dev	3.522

Gaus mean : 31.1688

Gaus Width: 3.5790



B1L103S, U10-ch113, adc1

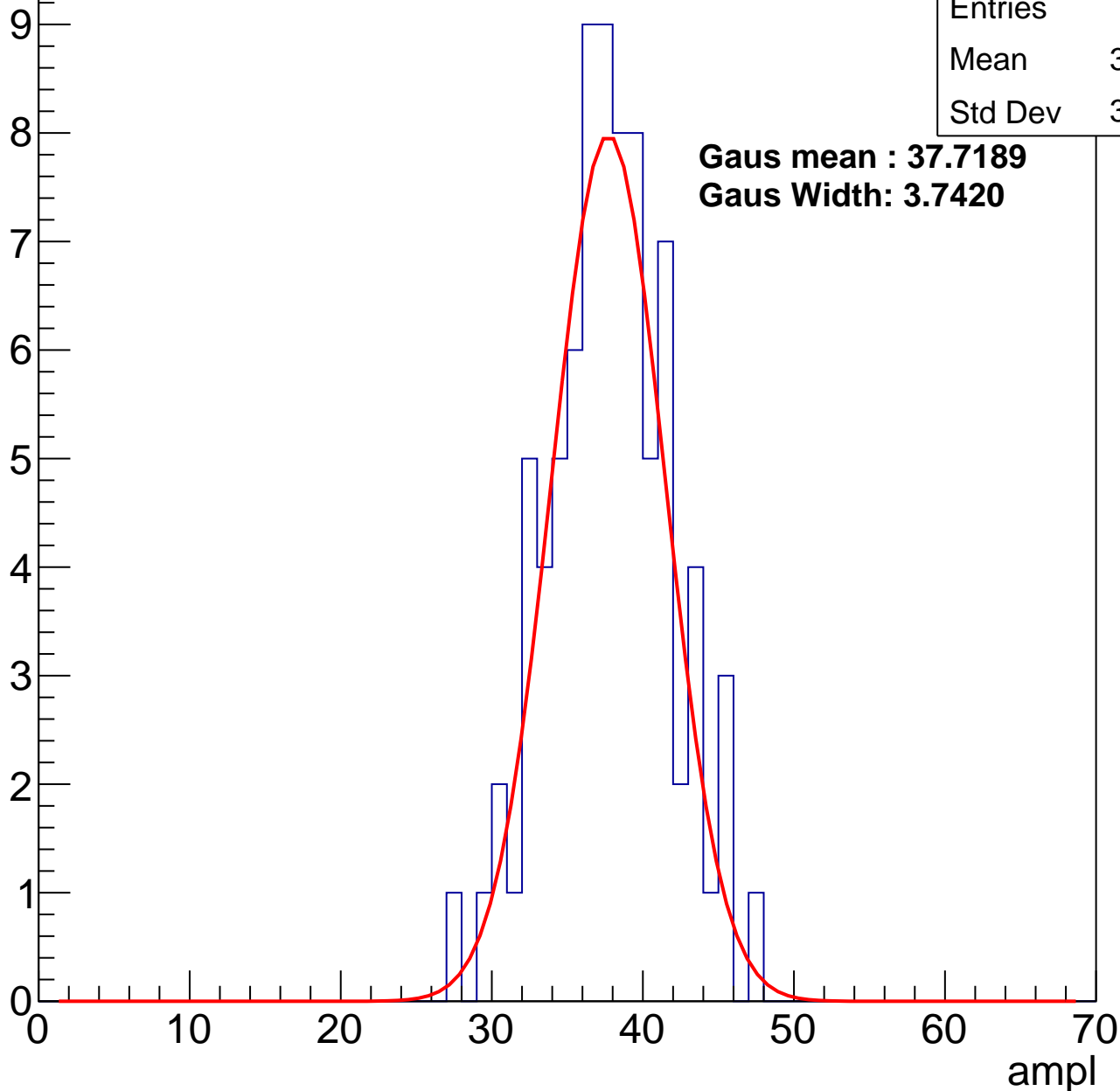
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	37.33
Std Dev	3.963

Gaus mean : 37.7189

Gaus Width: 3.7420



B1L103S, U10-ch113, adc2

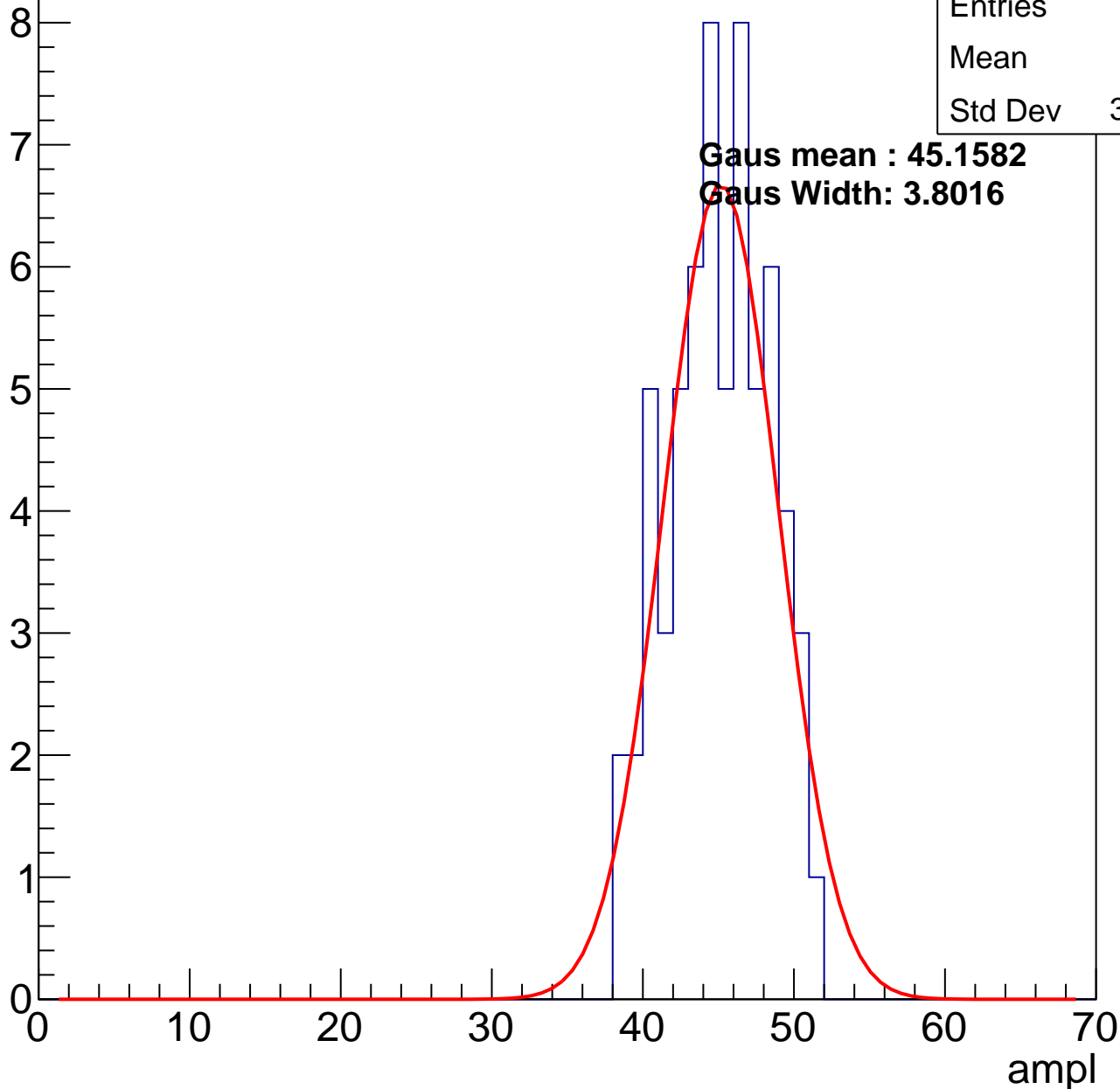
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	44.6
Std Dev	3.254

Gaus mean : 45.1582

Gaus Width: 3.8016

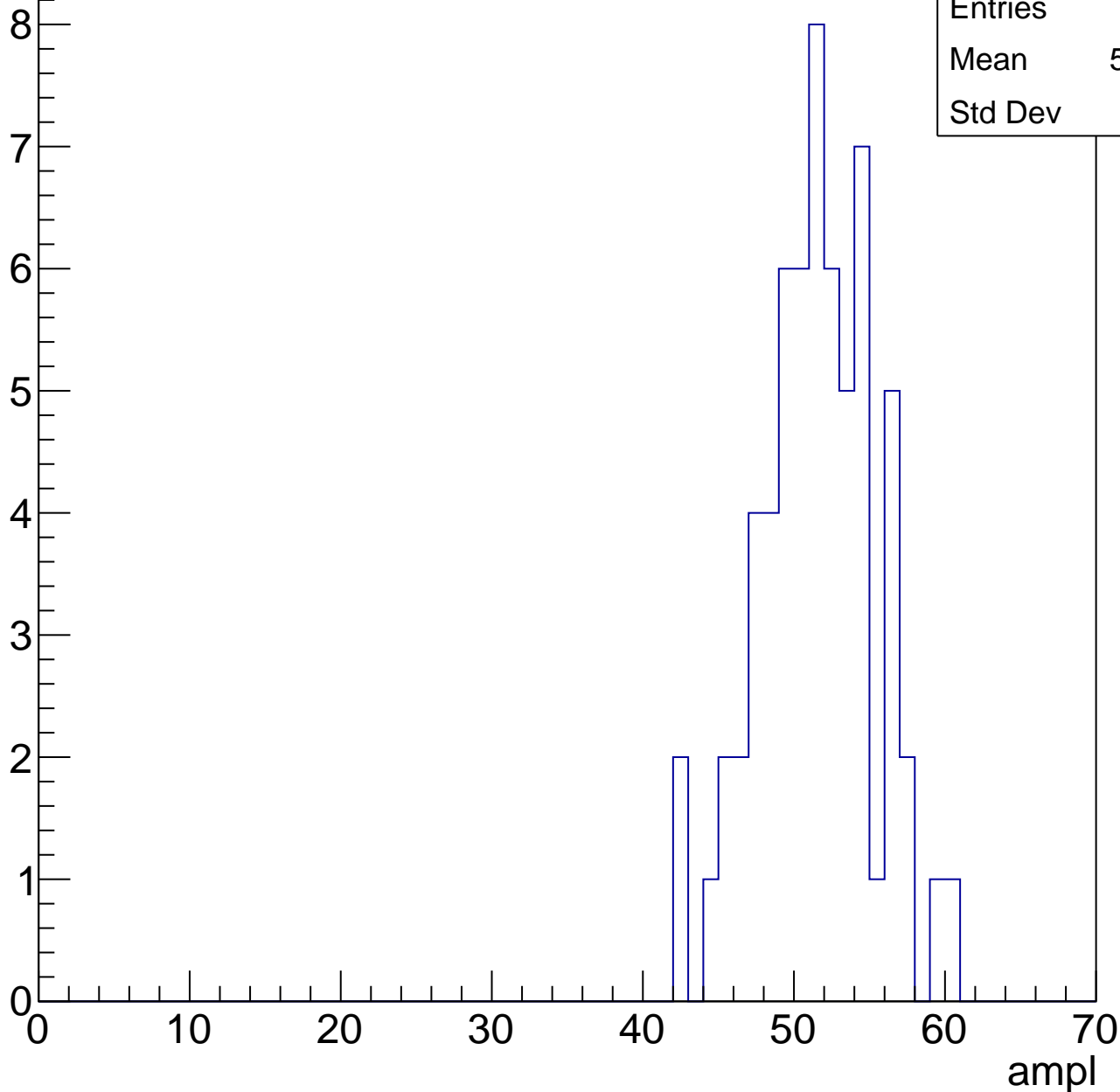


B1L103S, U10-ch113, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	51.03
Std Dev	3.8

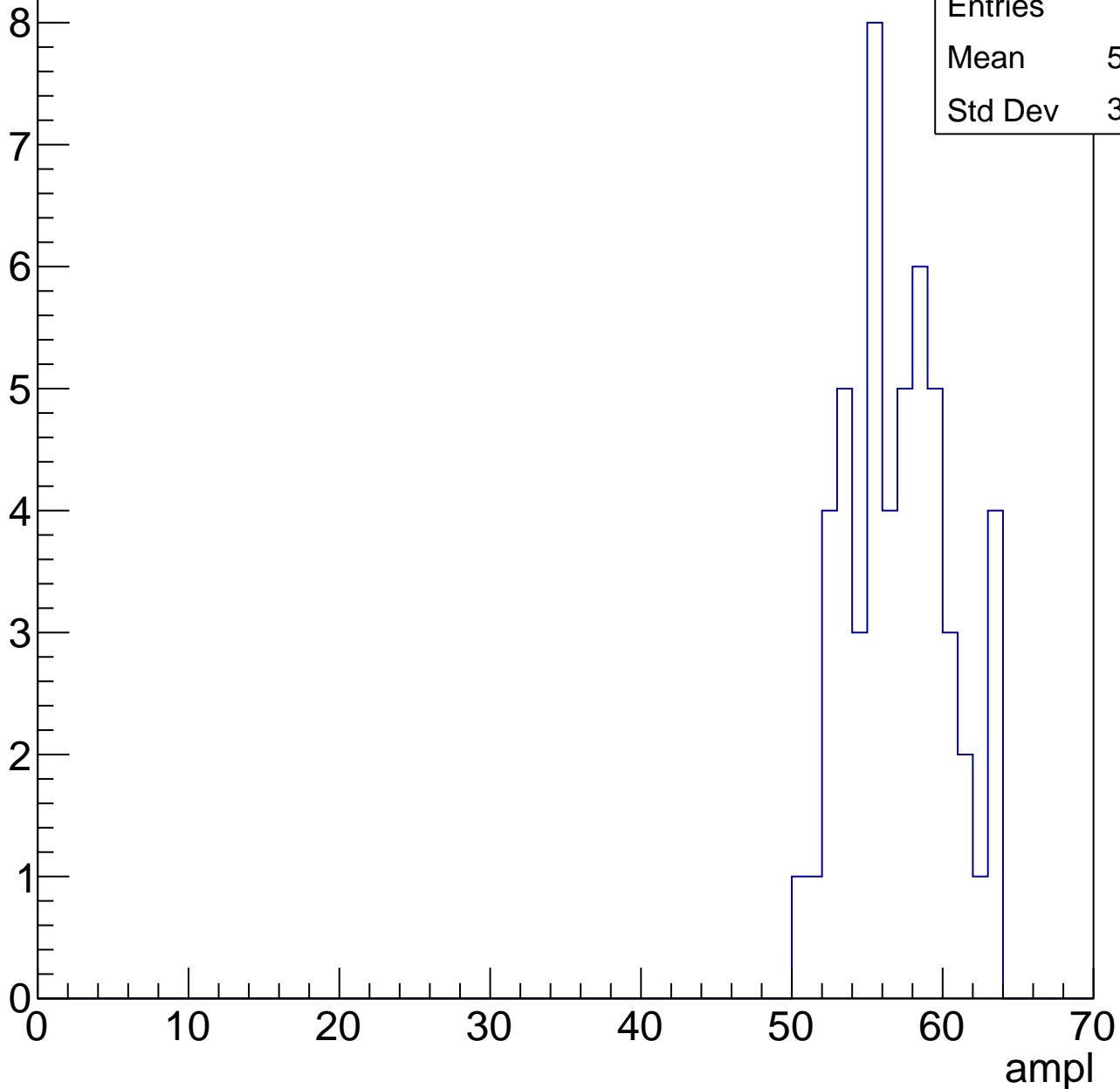


B1L103S, U10-ch113, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	56.62
Std Dev	3.323

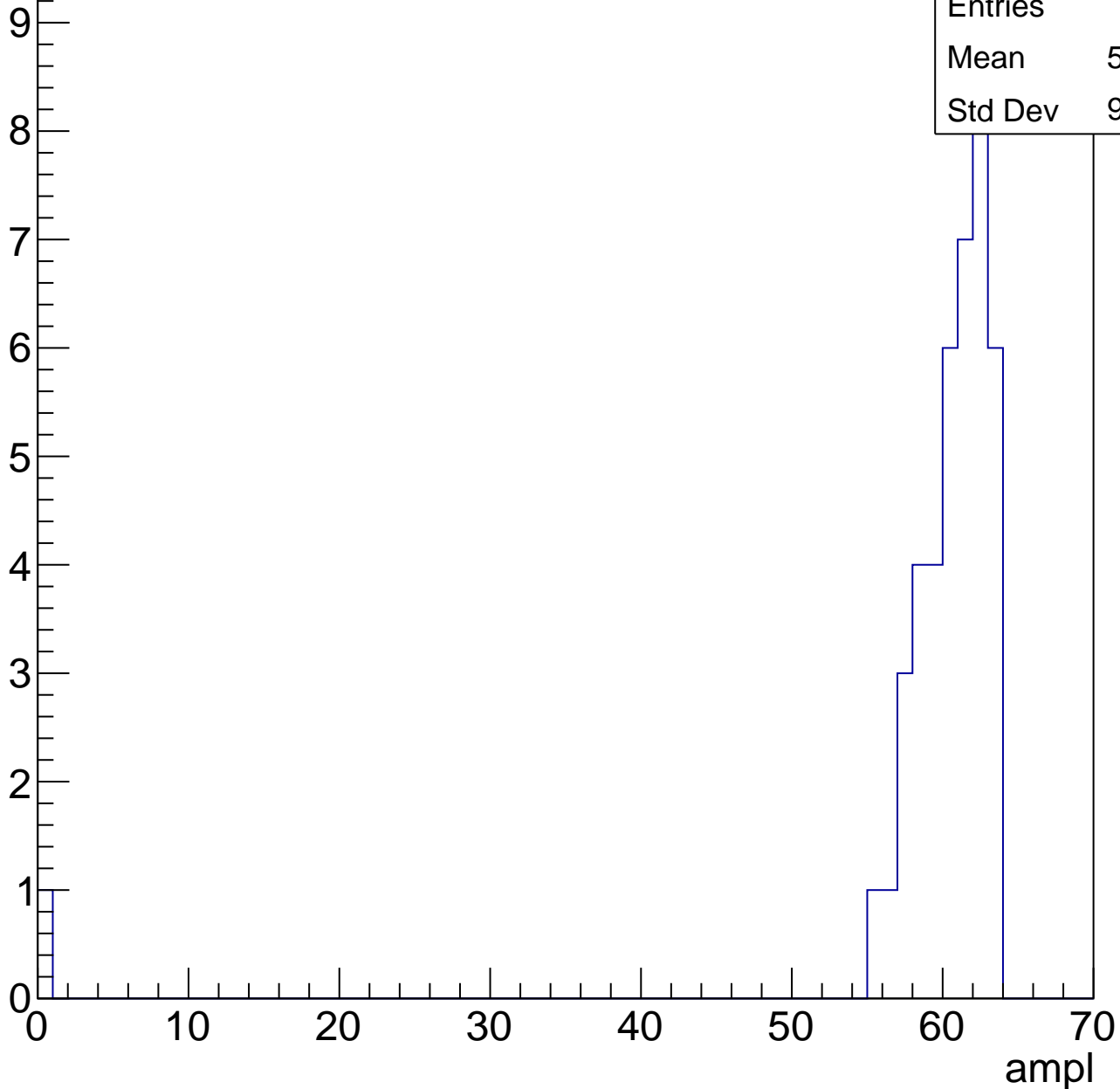


B1L103S, U10-ch113, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	58.88
Std Dev	9.427



B1L103S, U10-ch113, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch113, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch114, adc0

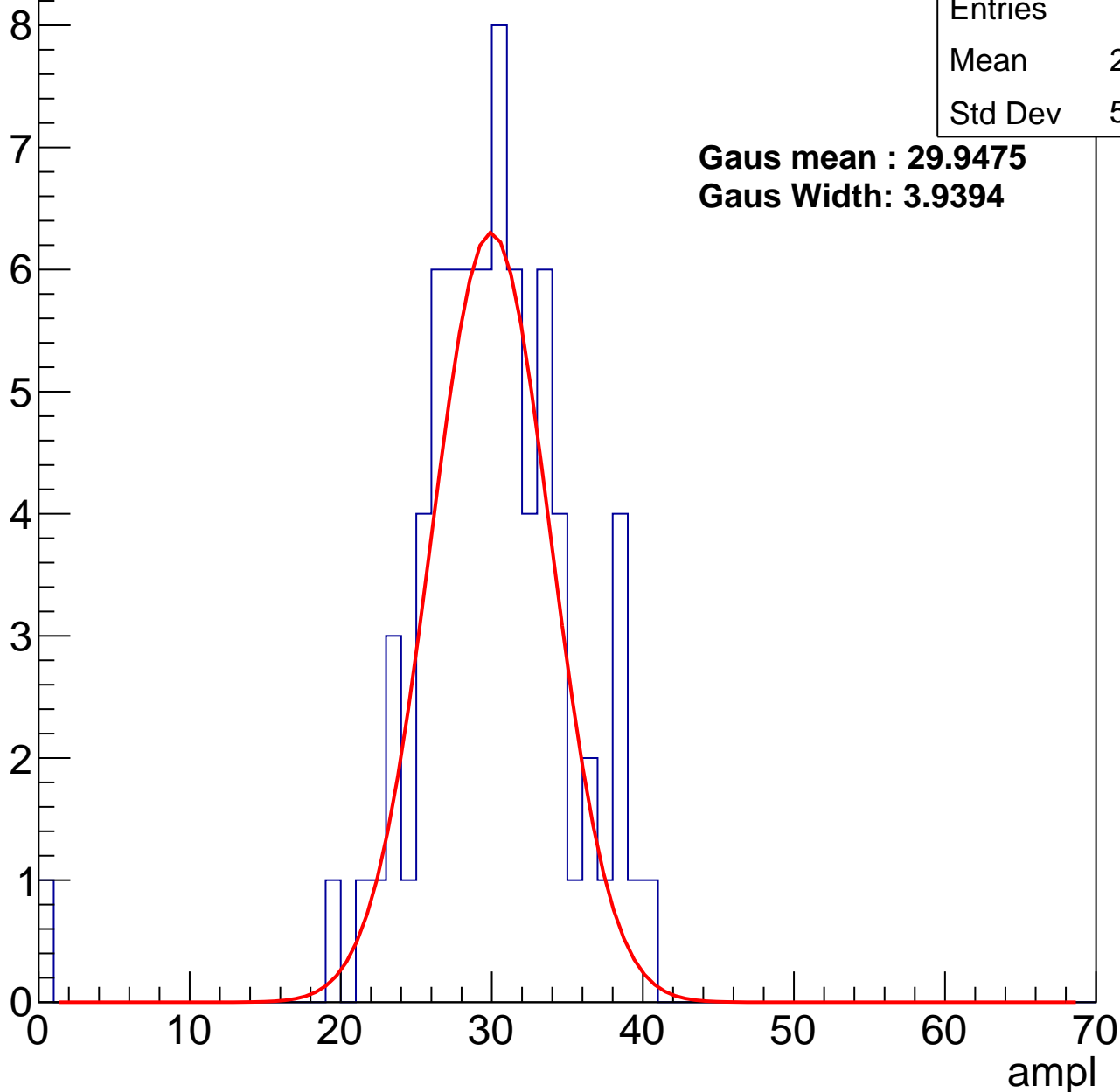
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	29.43
Std Dev	5.609

Gaus mean : 29.9475

Gaus Width: 3.9394



B1L103S, U10-ch114, adc1

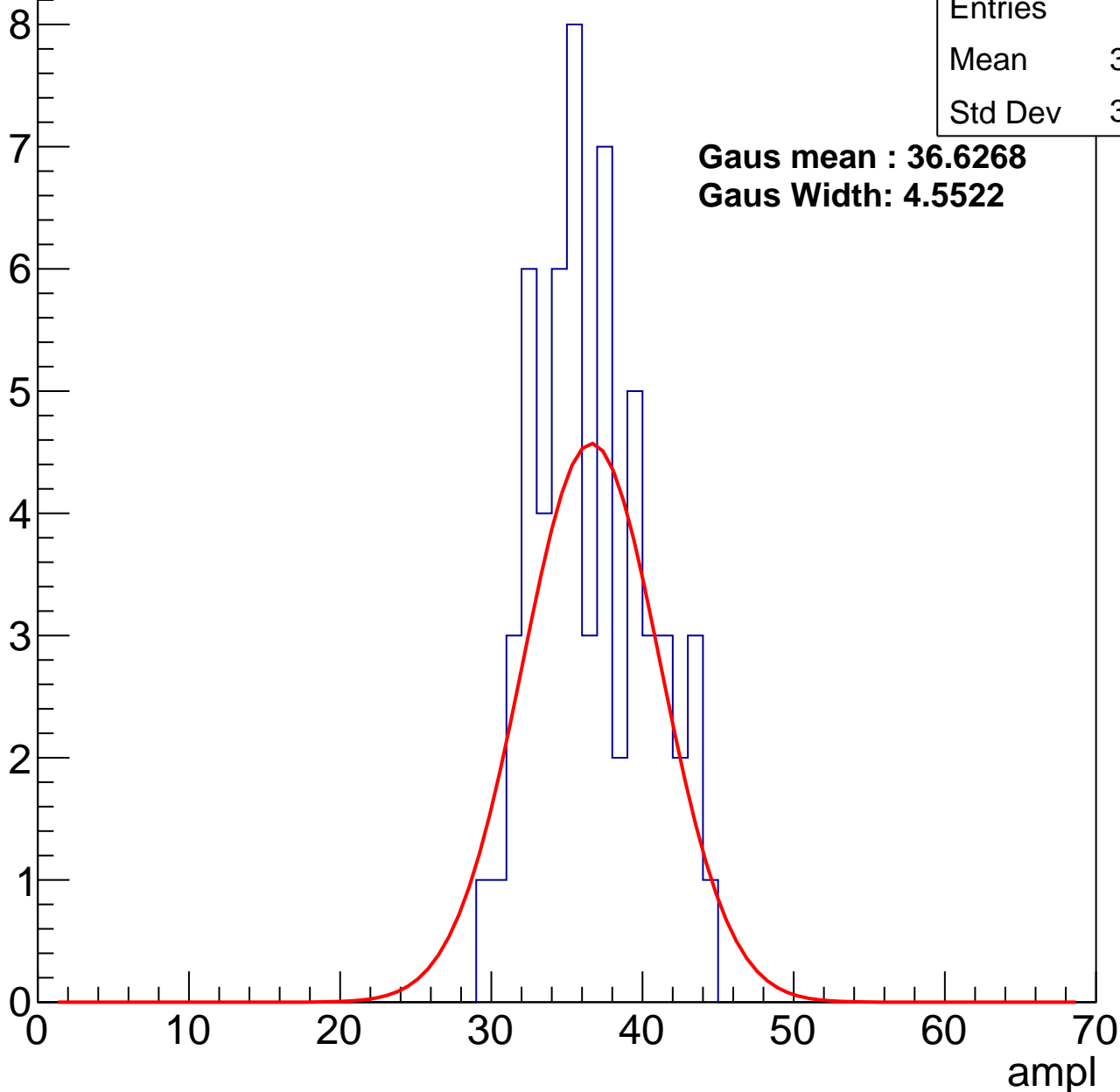
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	36.17
Std Dev	3.687

Gaus mean : 36.6268

Gaus Width: 4.5522



B1L103S, U10-ch114, adc2

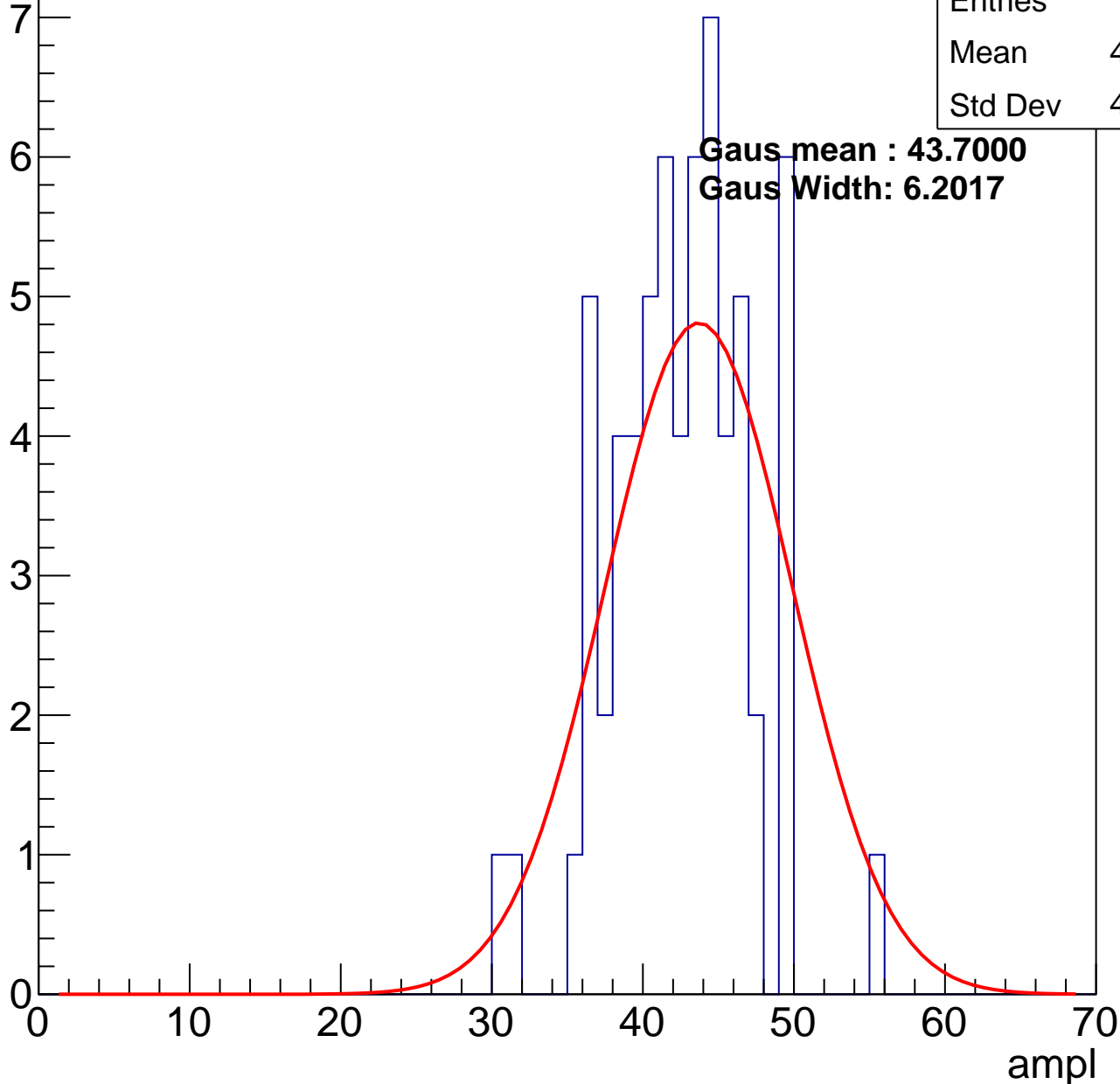
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	42.05
Std Dev	4.564

Gaus mean : 43.7000

Gaus Width: 6.2017

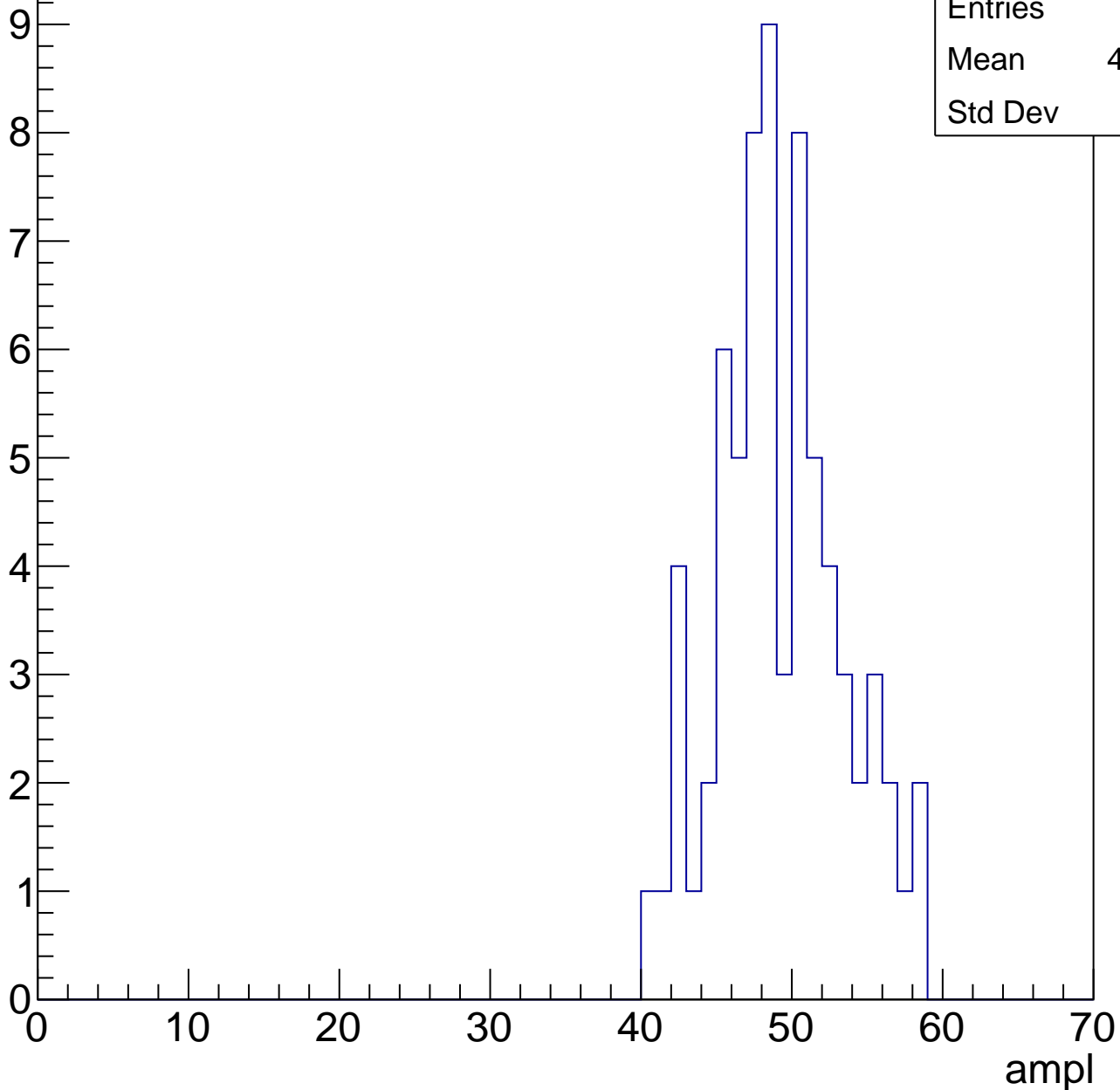


B1L103S, U10-ch114, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	48.79
Std Dev	4.14

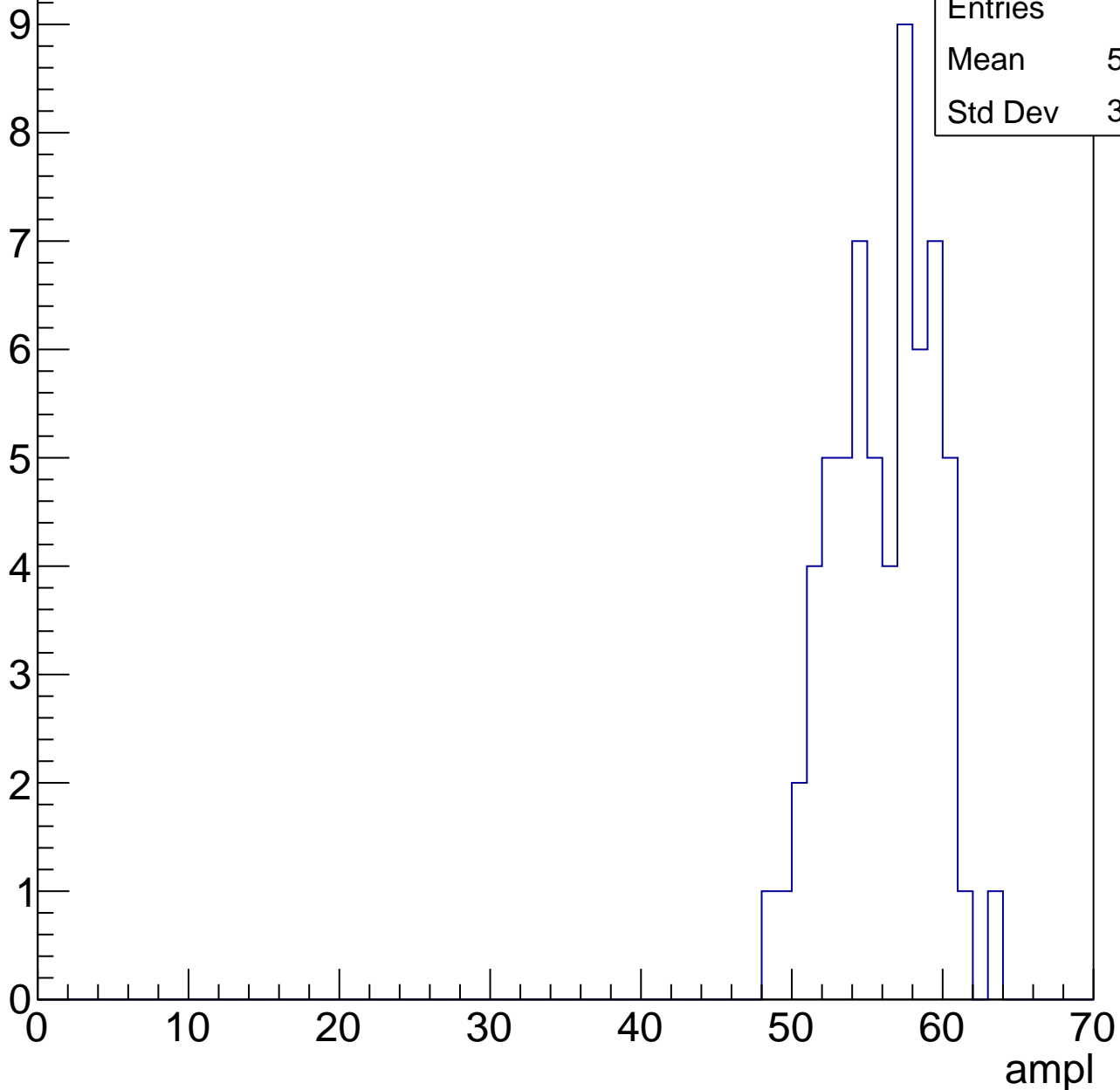


B1L103S, U10-ch114, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	55.57
Std Dev	3.289

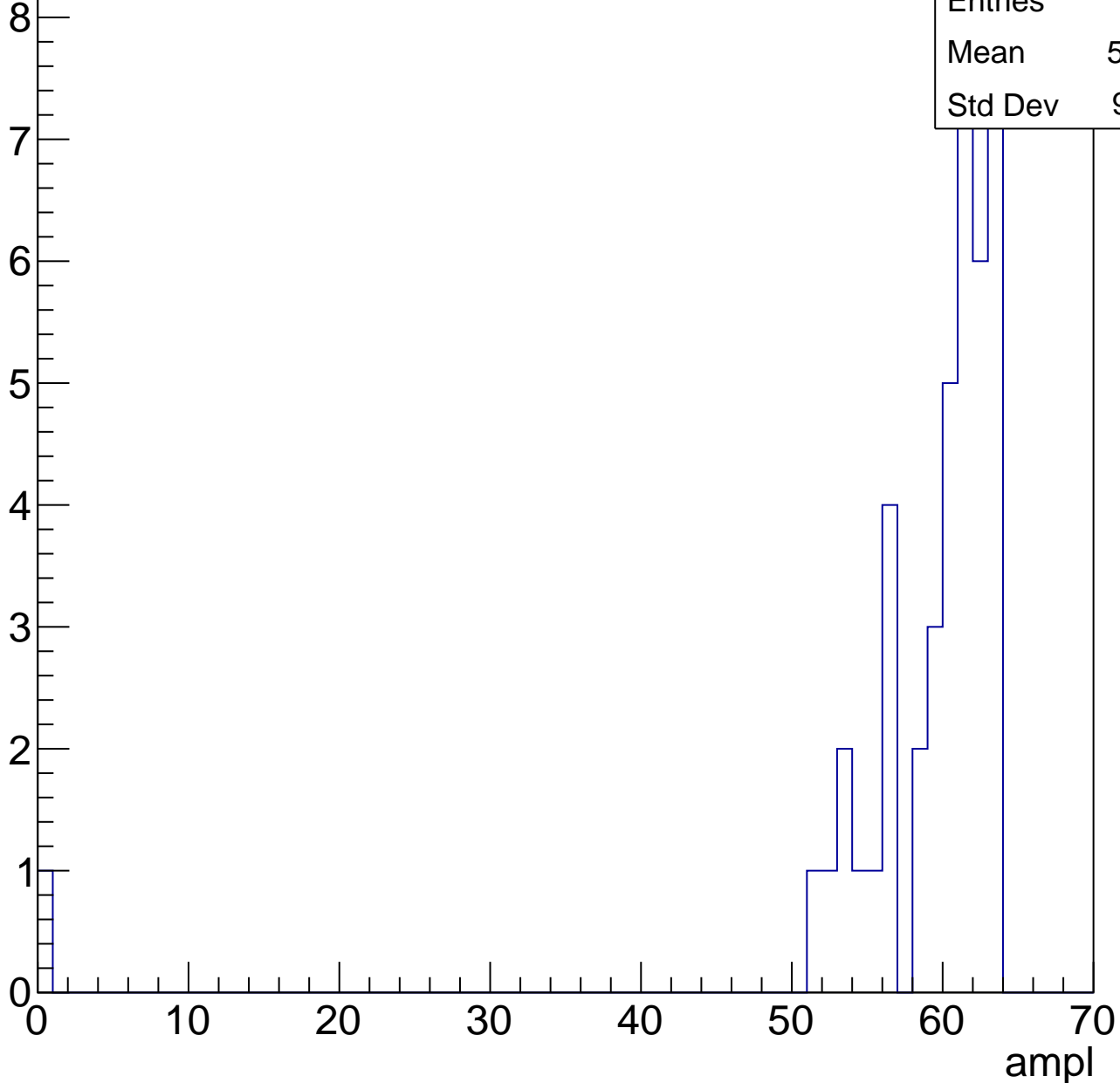


B1L103S, U10-ch114, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

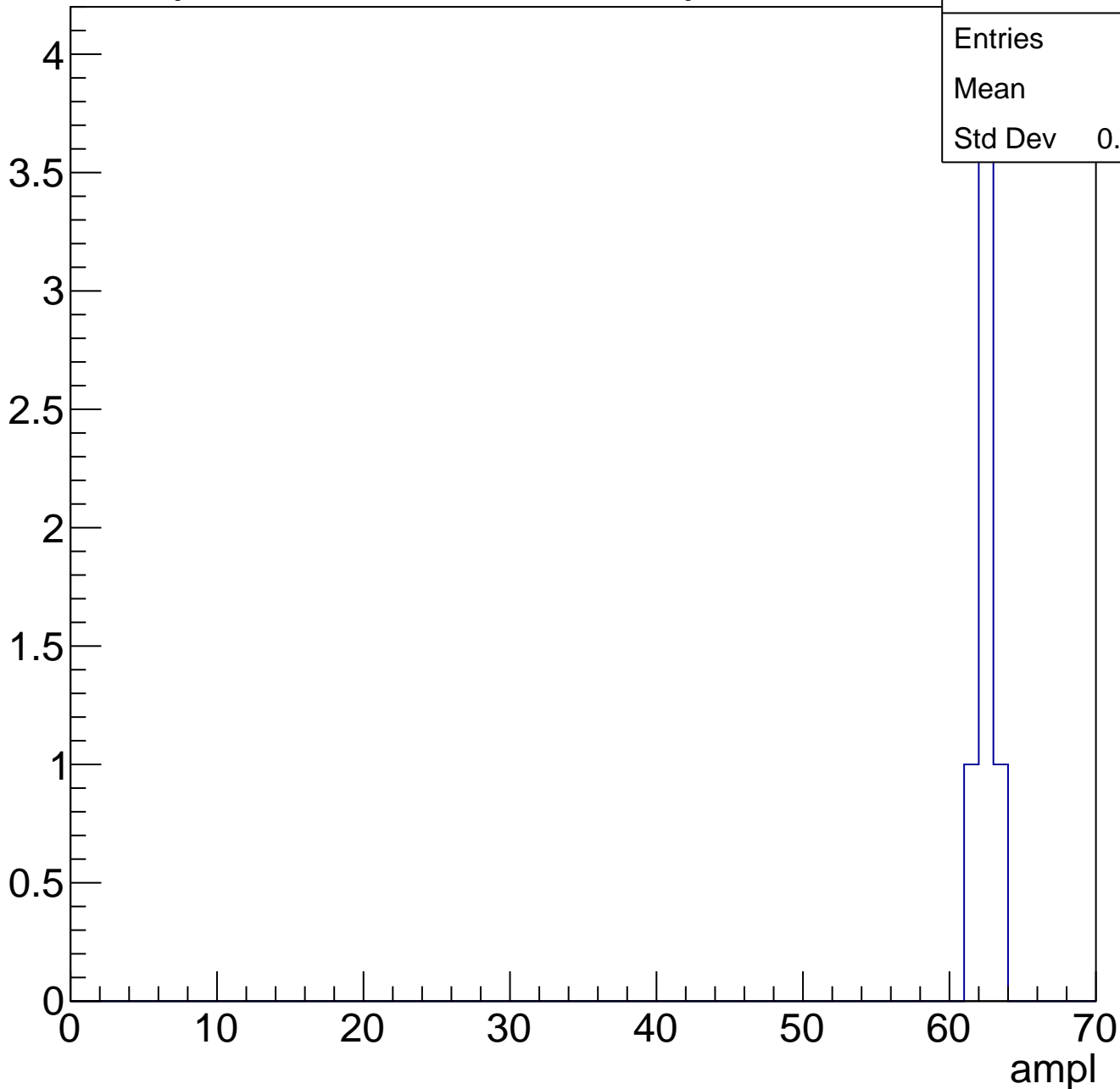
Entries	43
Mean	58.12
Std Dev	9.561



B1L103S, U10-ch114, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch114, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch115, adc0

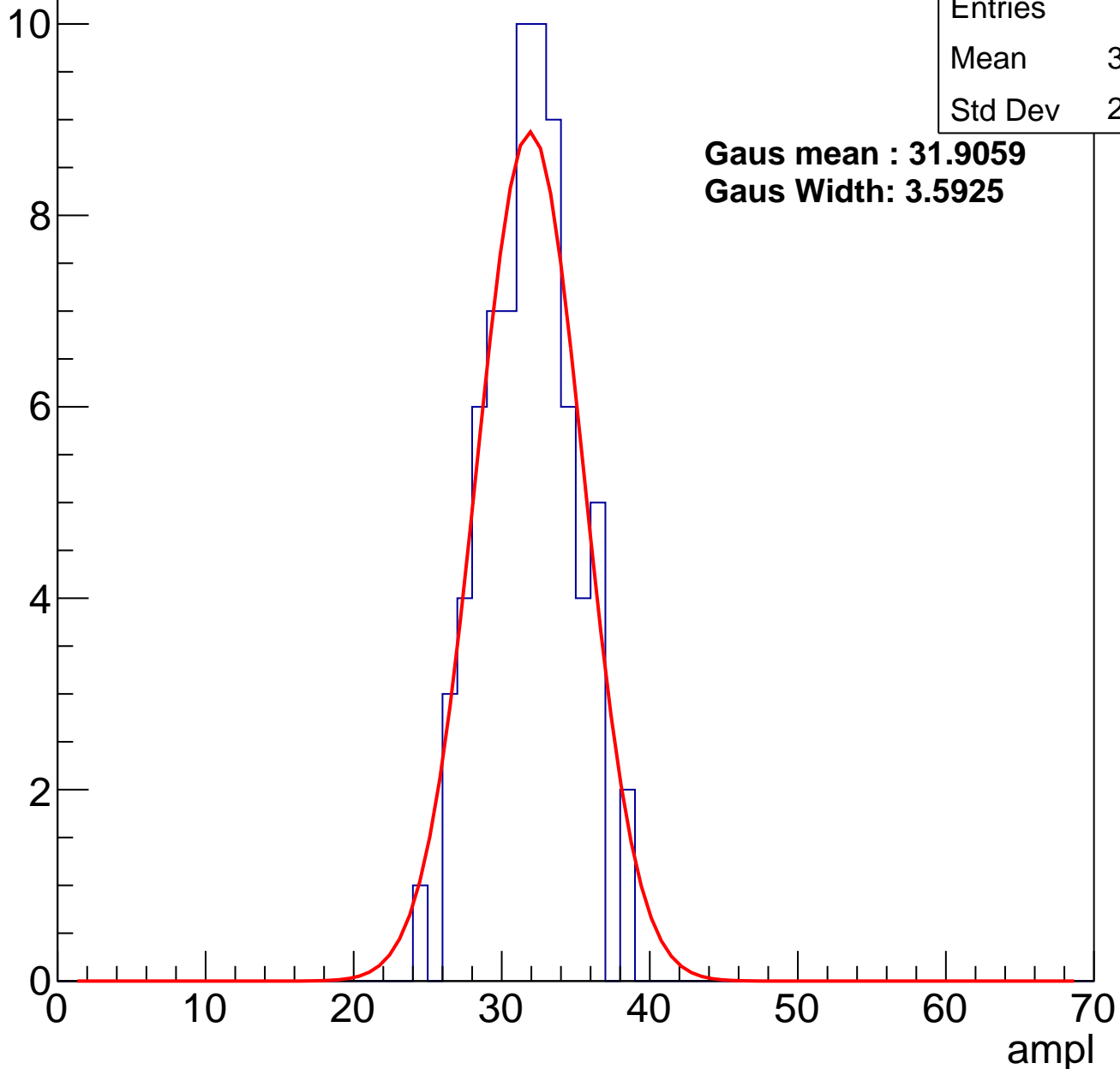
calib_packv5_042523_0143.root, FC#7, port C2

Entries	74
Mean	31.32
Std Dev	2.978

Gaus mean : 31.9059

Gaus Width: 3.5925

Entry



B1L103S, U10-ch115, adc1

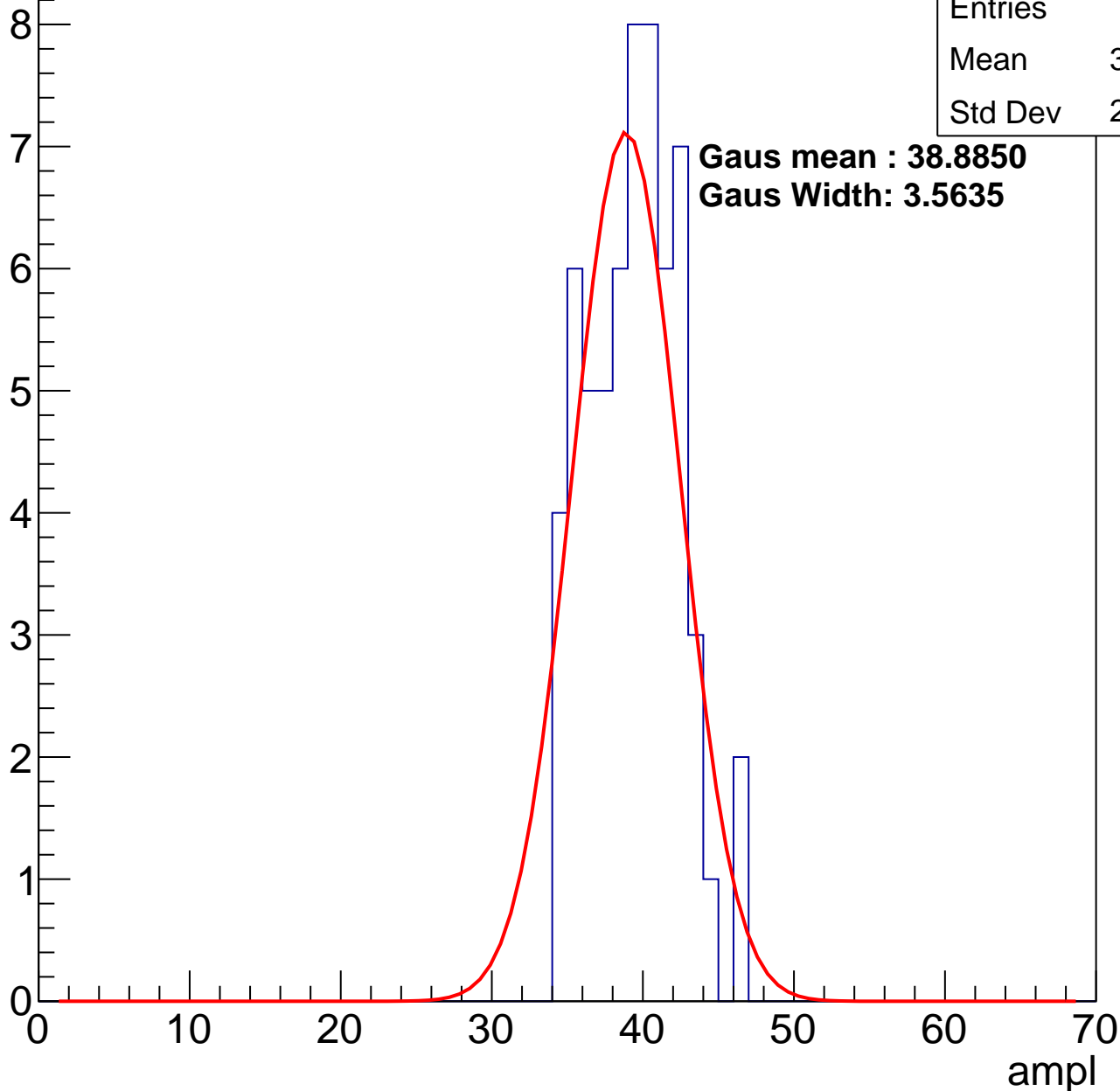
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	38.95
Std Dev	2.956

Gaus mean : 38.8850

Gaus Width: 3.5635



B1L103S, U10-ch115, adc2

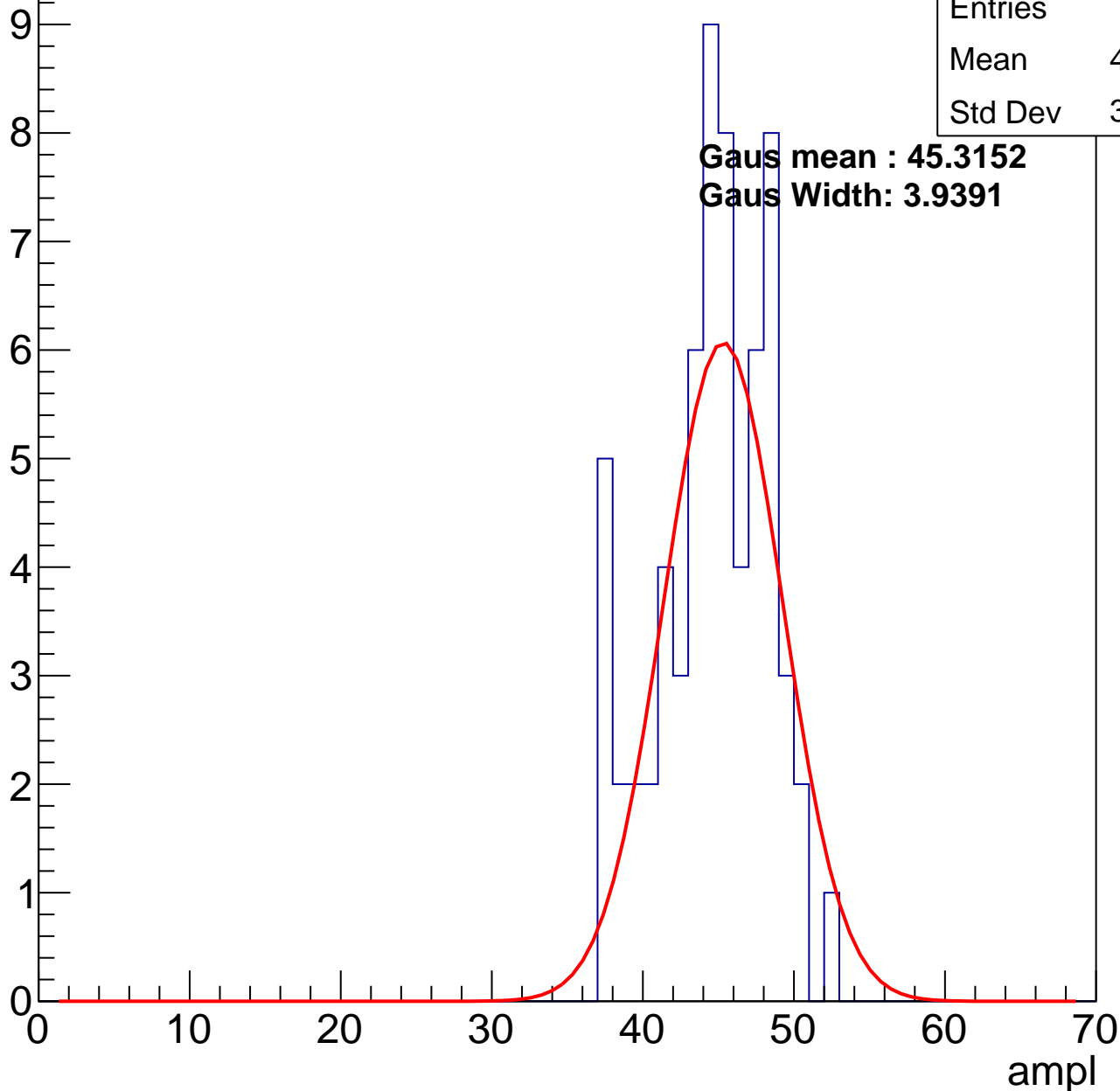
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.18
Std Dev	3.654

Gaus mean : 45.3152

Gaus Width: 3.9391

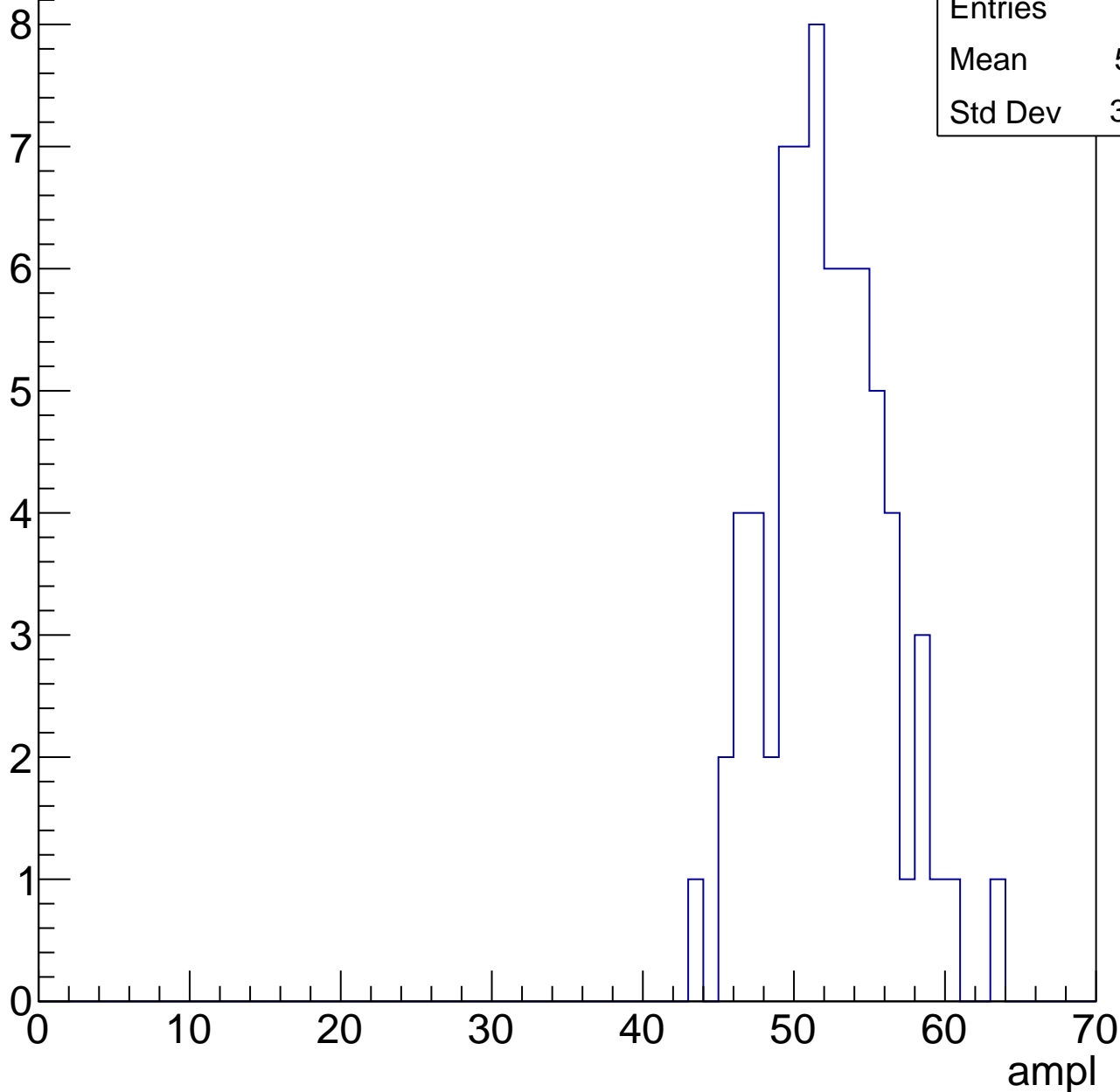


B1L103S, U10-ch115, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	69
Mean	51.71
Std Dev	3.909

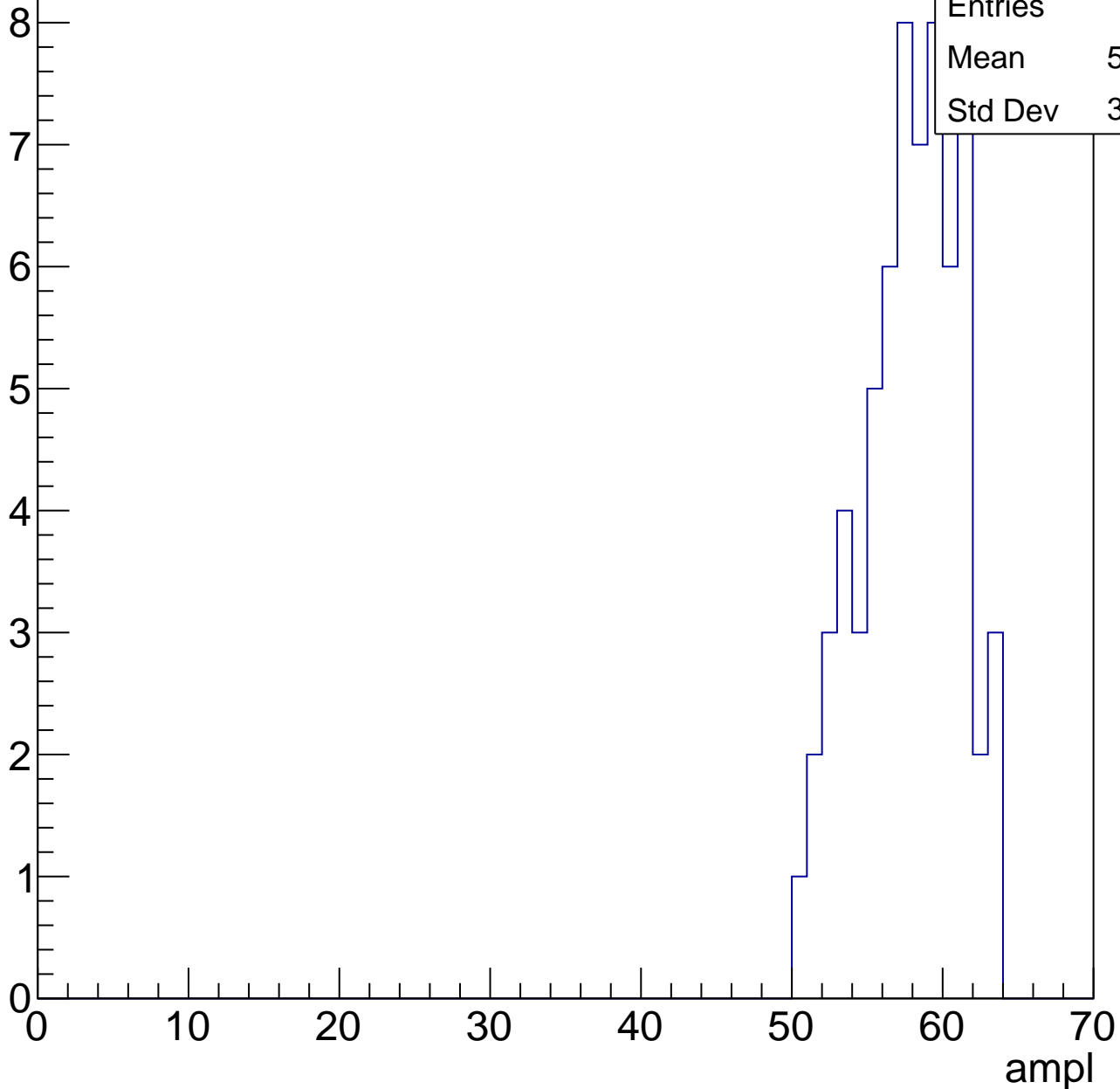


B1L103S, U10-ch115, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	57.39
Std Dev	3.214

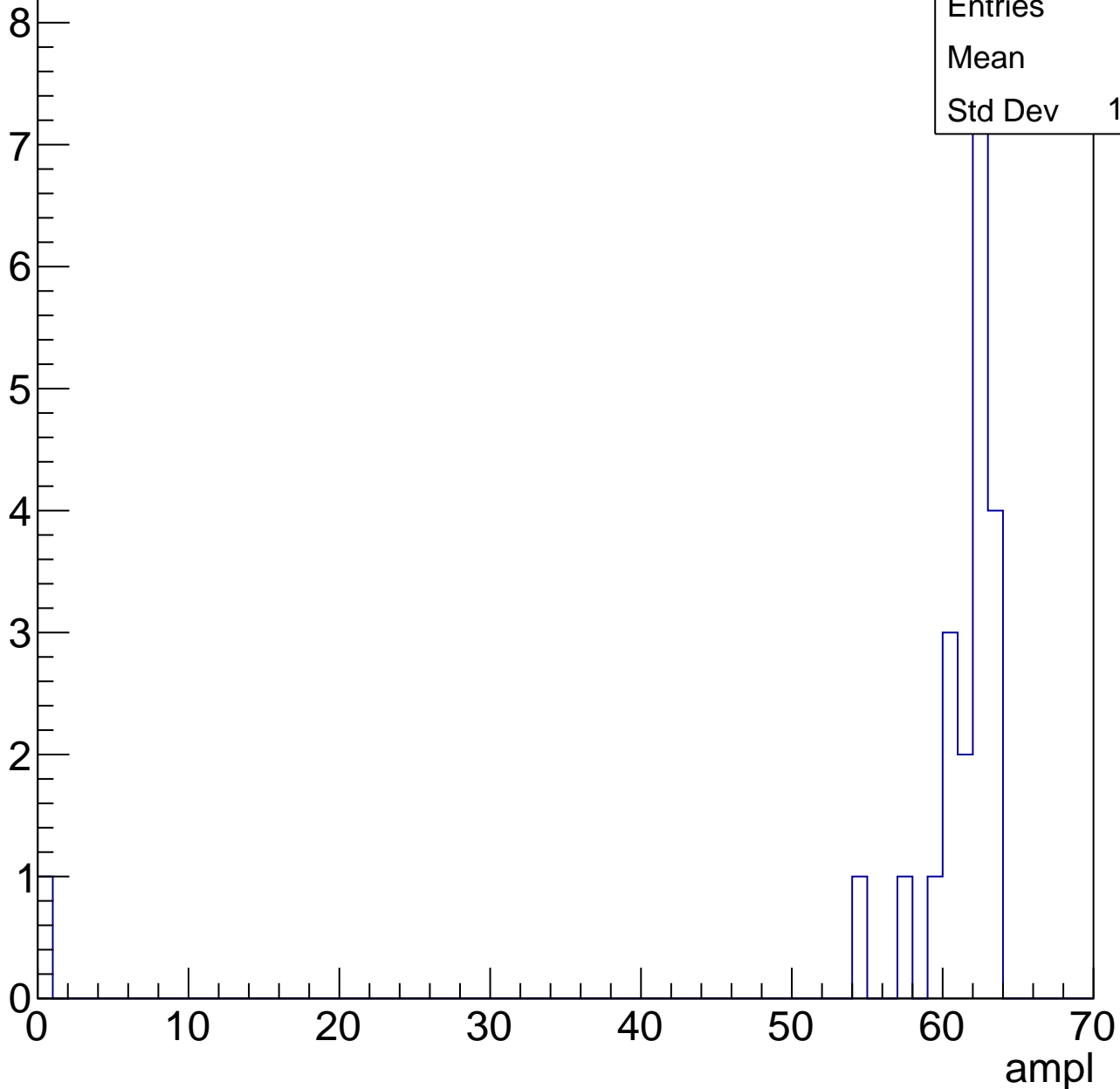


B1L103S, U10-ch115, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	21
Mean	58.1
Std Dev	13.17



B1L103S, U10-ch115, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch115, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	22
Std Dev	0

B1L103S, U10-ch116, adc0

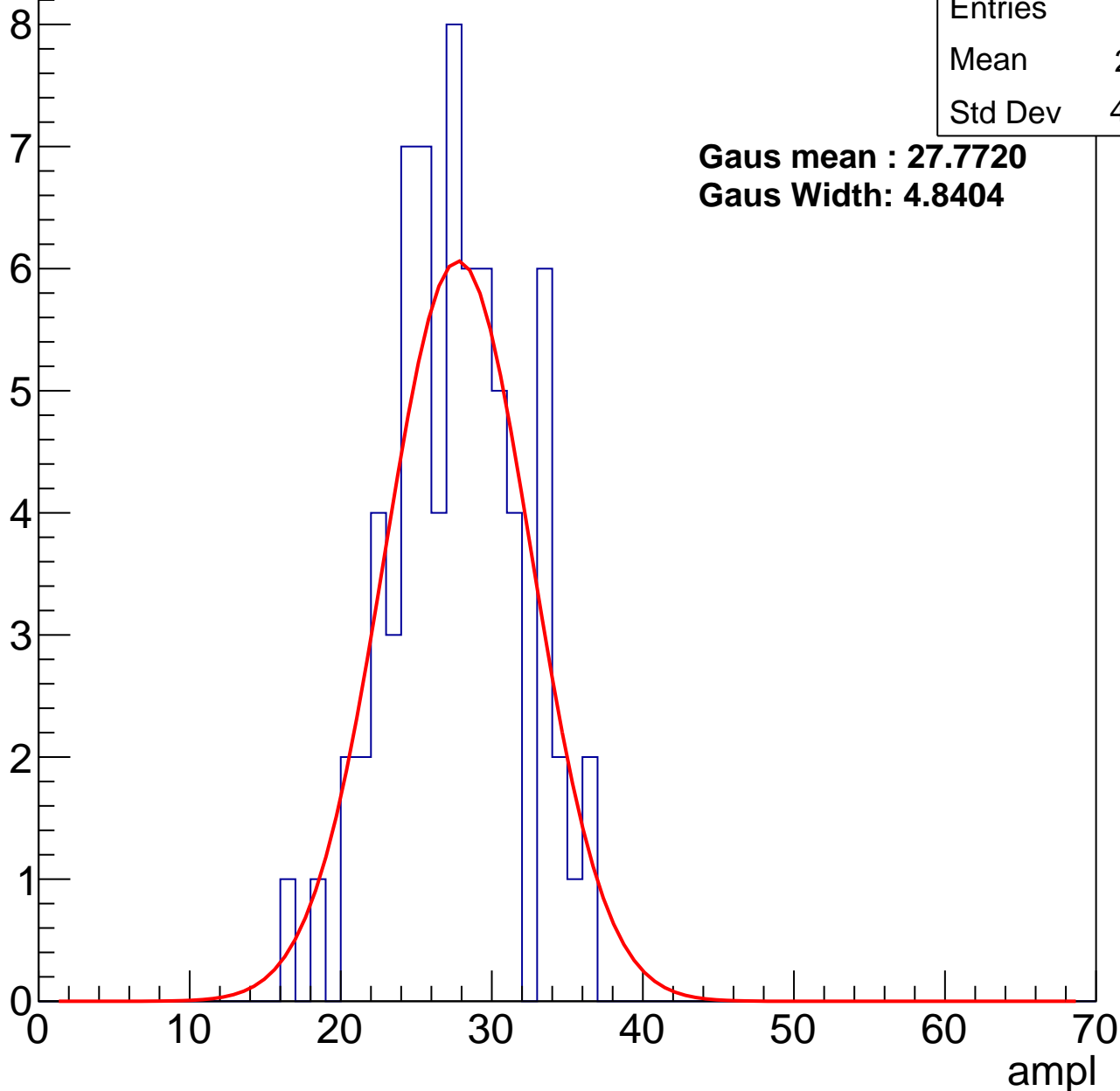
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	27.11
Std Dev	4.268

Gaus mean : 27.7720

Gaus Width: 4.8404



B1L103S, U10-ch116, adc1

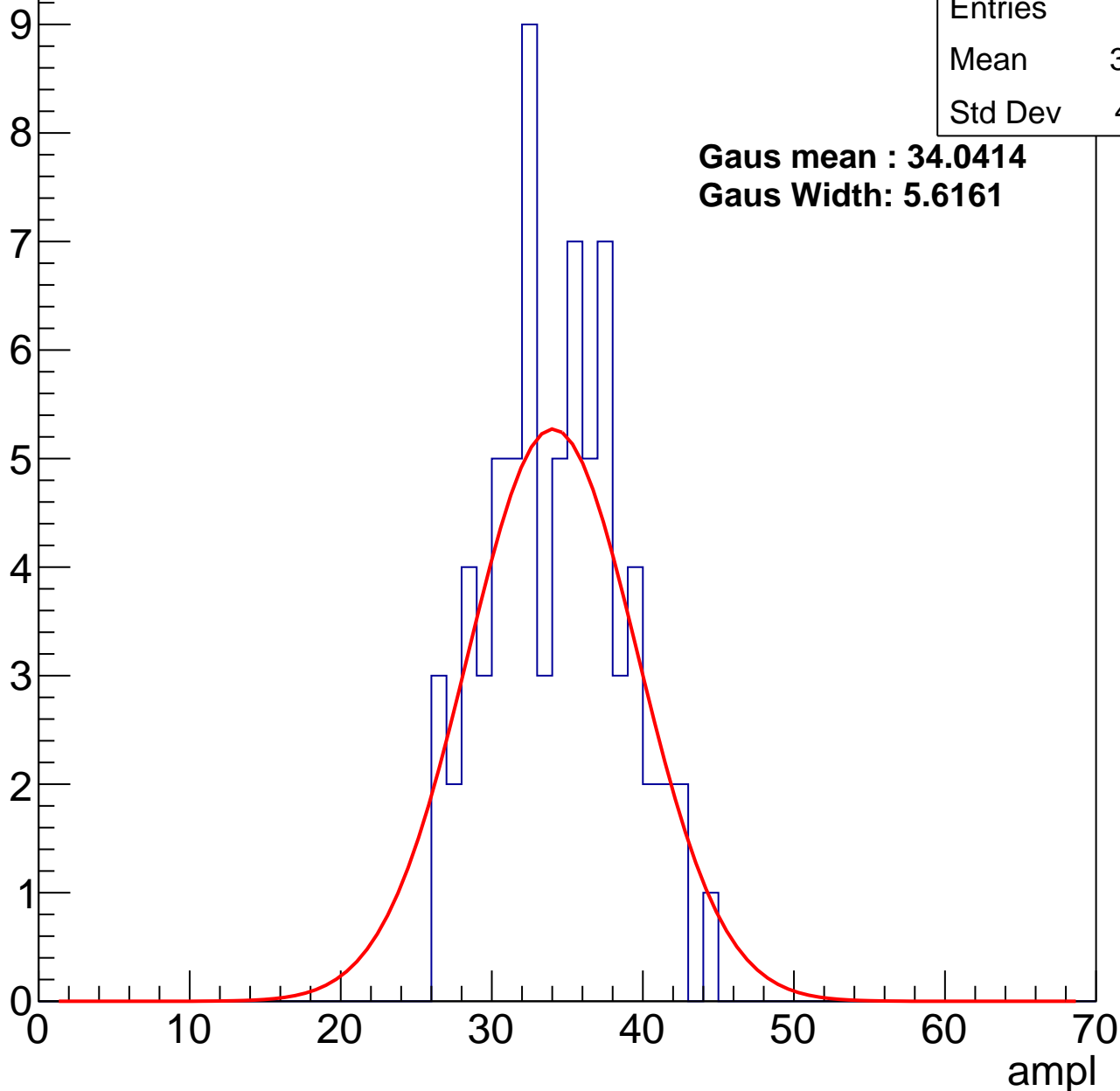
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	33.85
Std Dev	4.261

Gaus mean : 34.0414

Gaus Width: 5.6161



B1L103S, U10-ch116, adc2

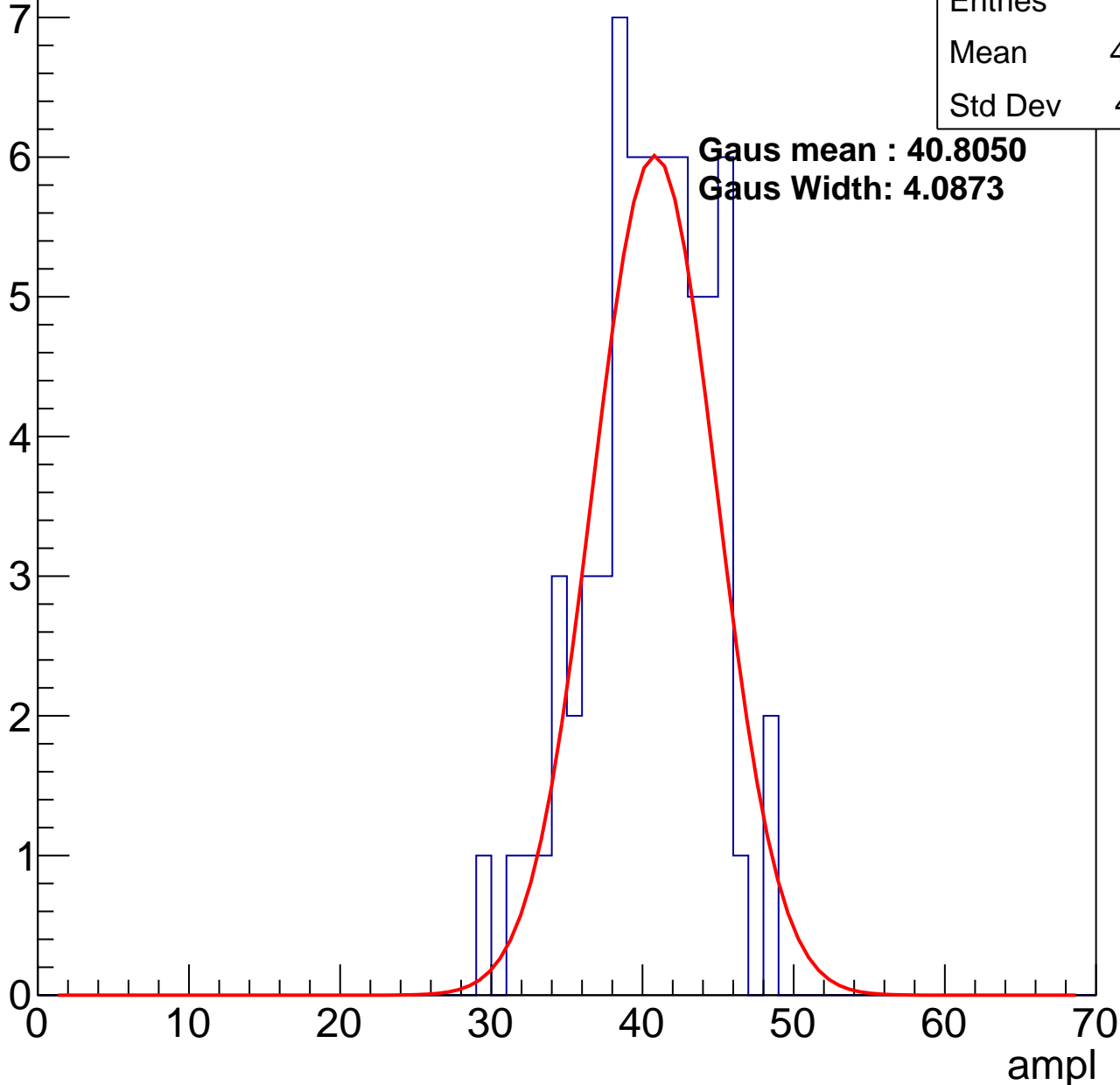
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	40.02
Std Dev	4.021

Gaus mean : 40.8050

Gaus Width: 4.0873

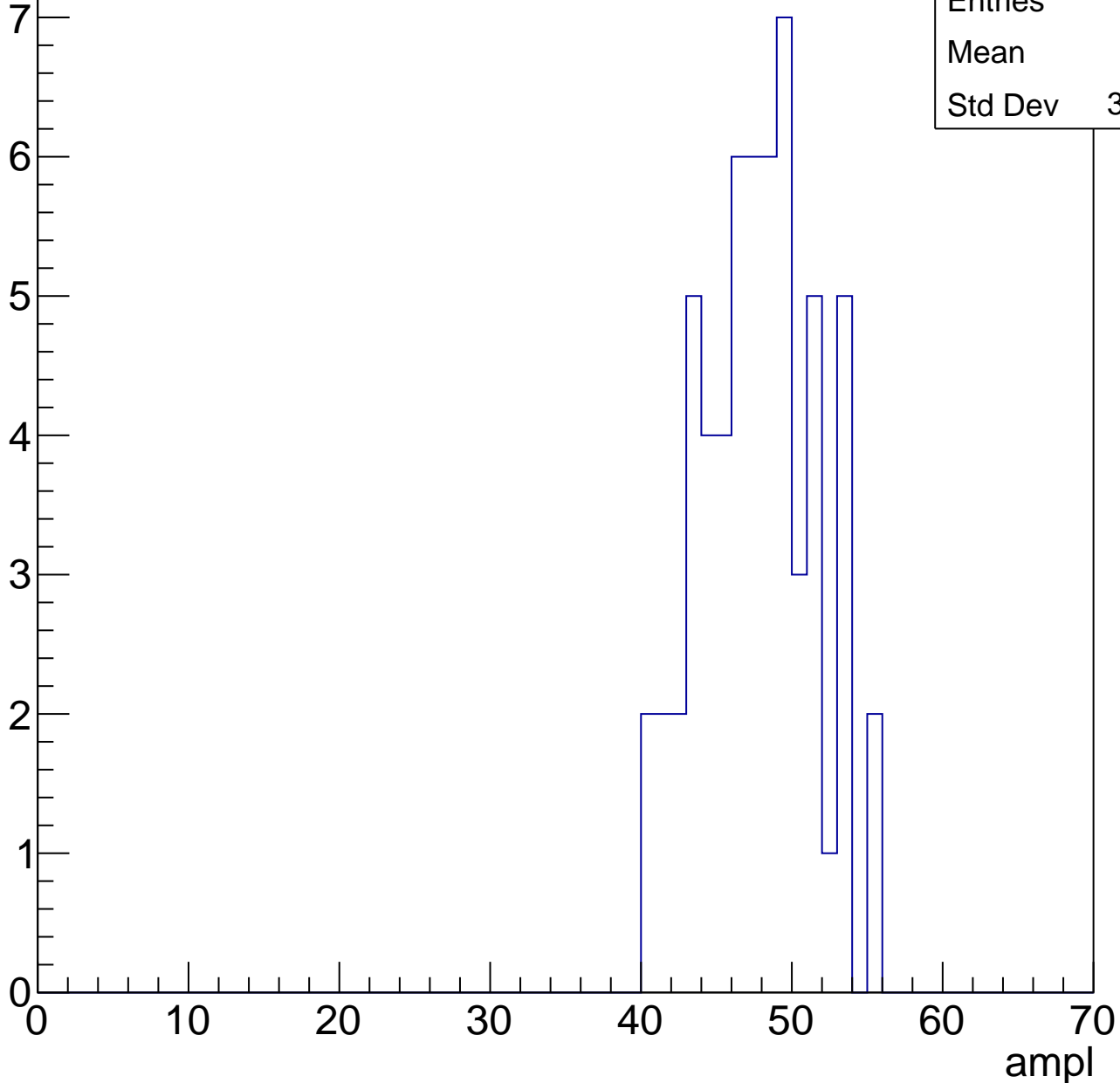


B1L103S, U10-ch116, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	47.3
Std Dev	3.712

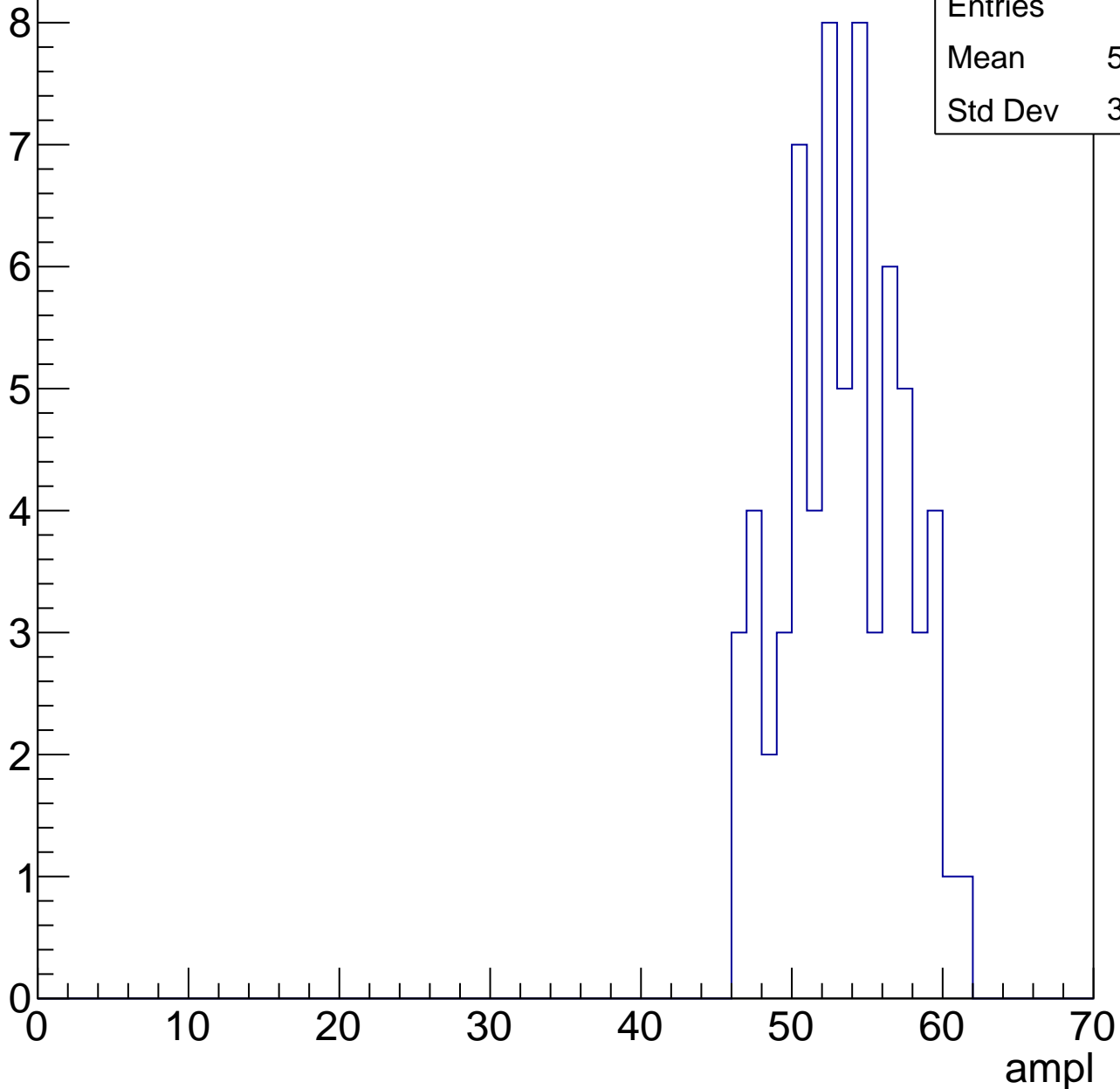


B1L103S, U10-ch116, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	53.03
Std Dev	3.789

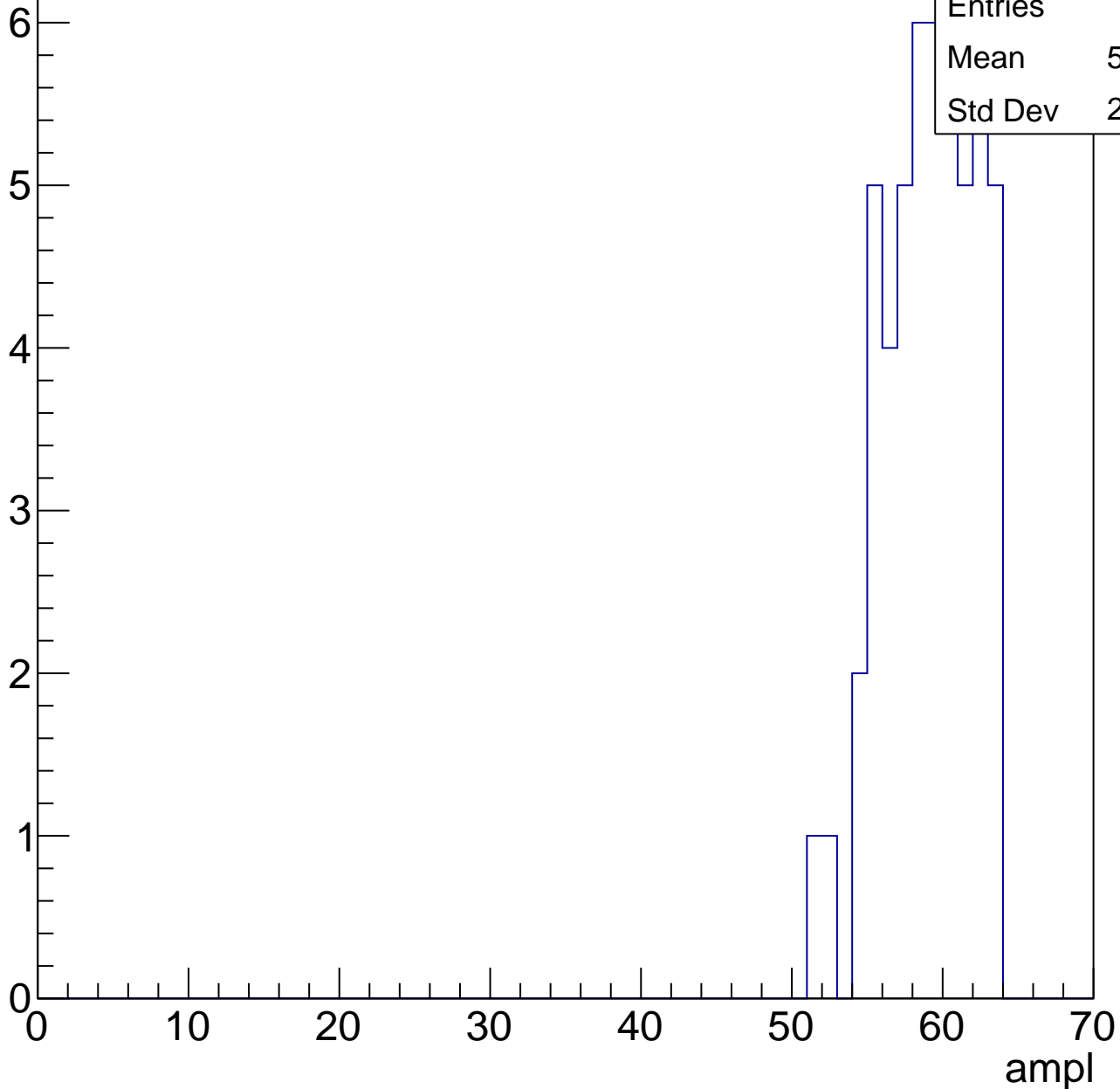


B1L103S, U10-ch116, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	52
Mean	58.63
Std Dev	2.968

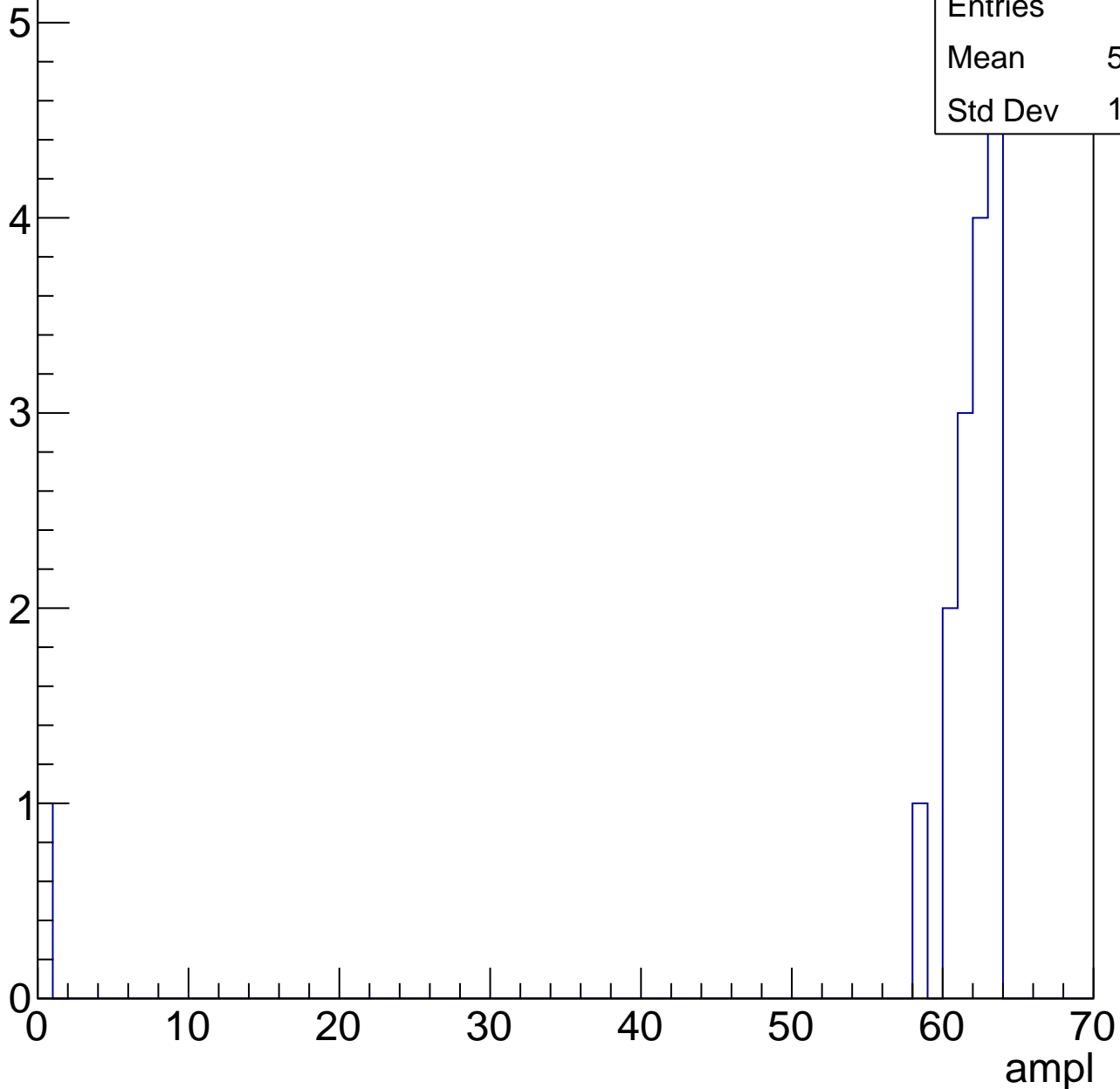


B1L103S, U10-ch116, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

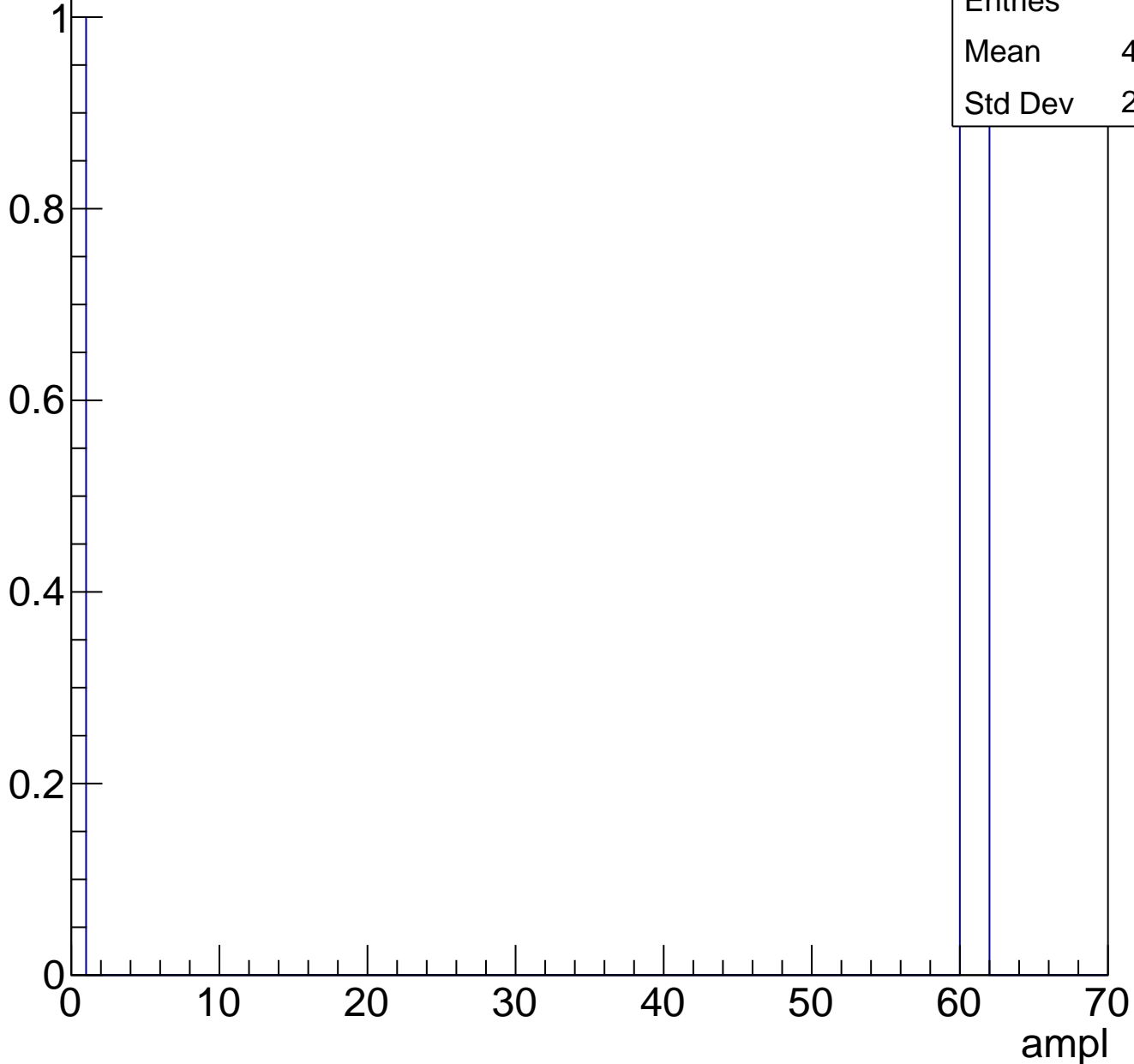
Entries	16
Mean	57.75
Std Dev	14.97



B1L103S, U10-ch116, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch117, adc0

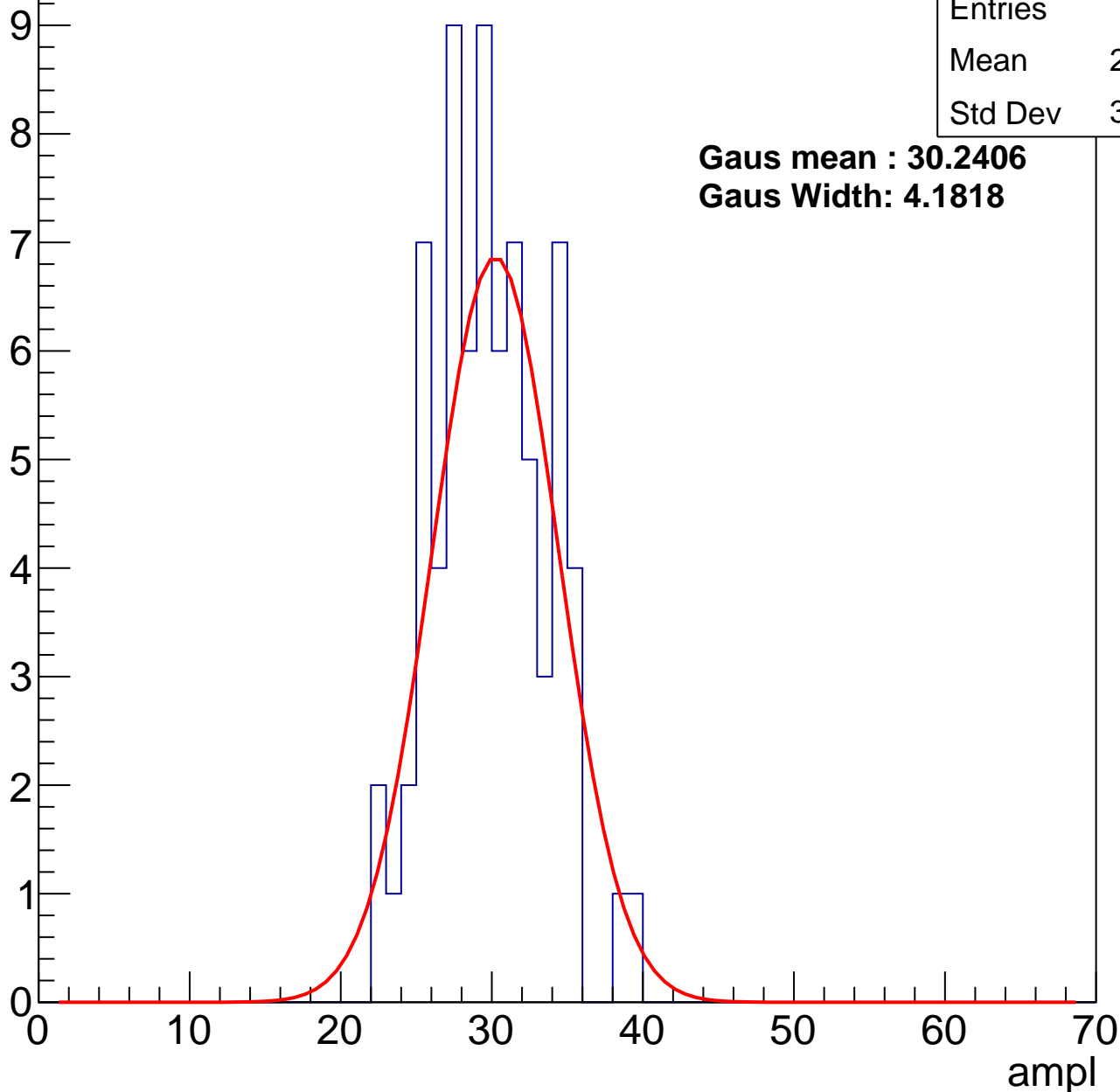
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	74
Mean	29.42
Std Dev	3.654

Gaus mean : 30.2406

Gaus Width: 4.1818



B1L103S, U10-ch117, adc1

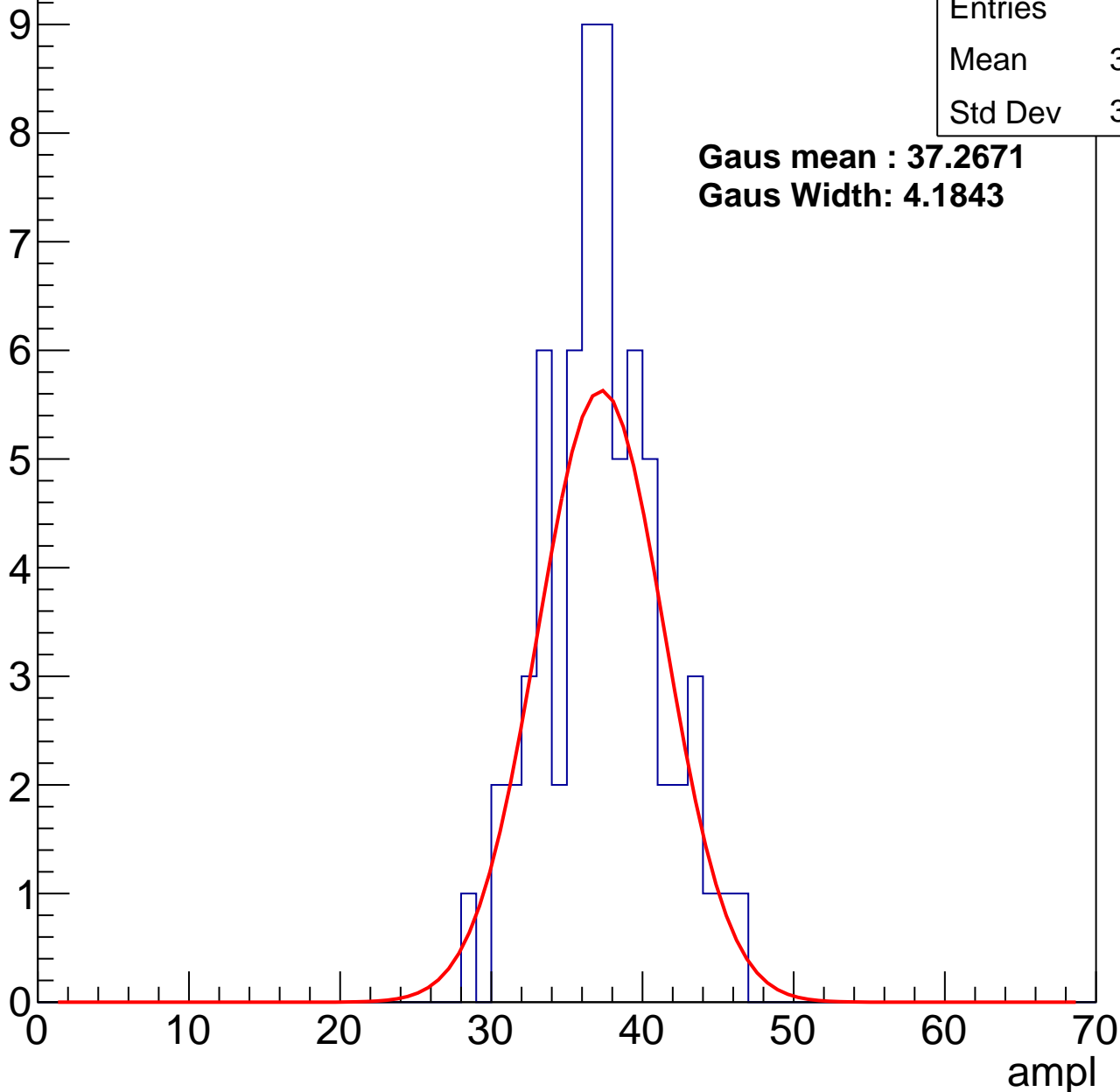
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	36.86
Std Dev	3.753

Gaus mean : 37.2671

Gaus Width: 4.1843



B1L103S, U10-ch117, adc2

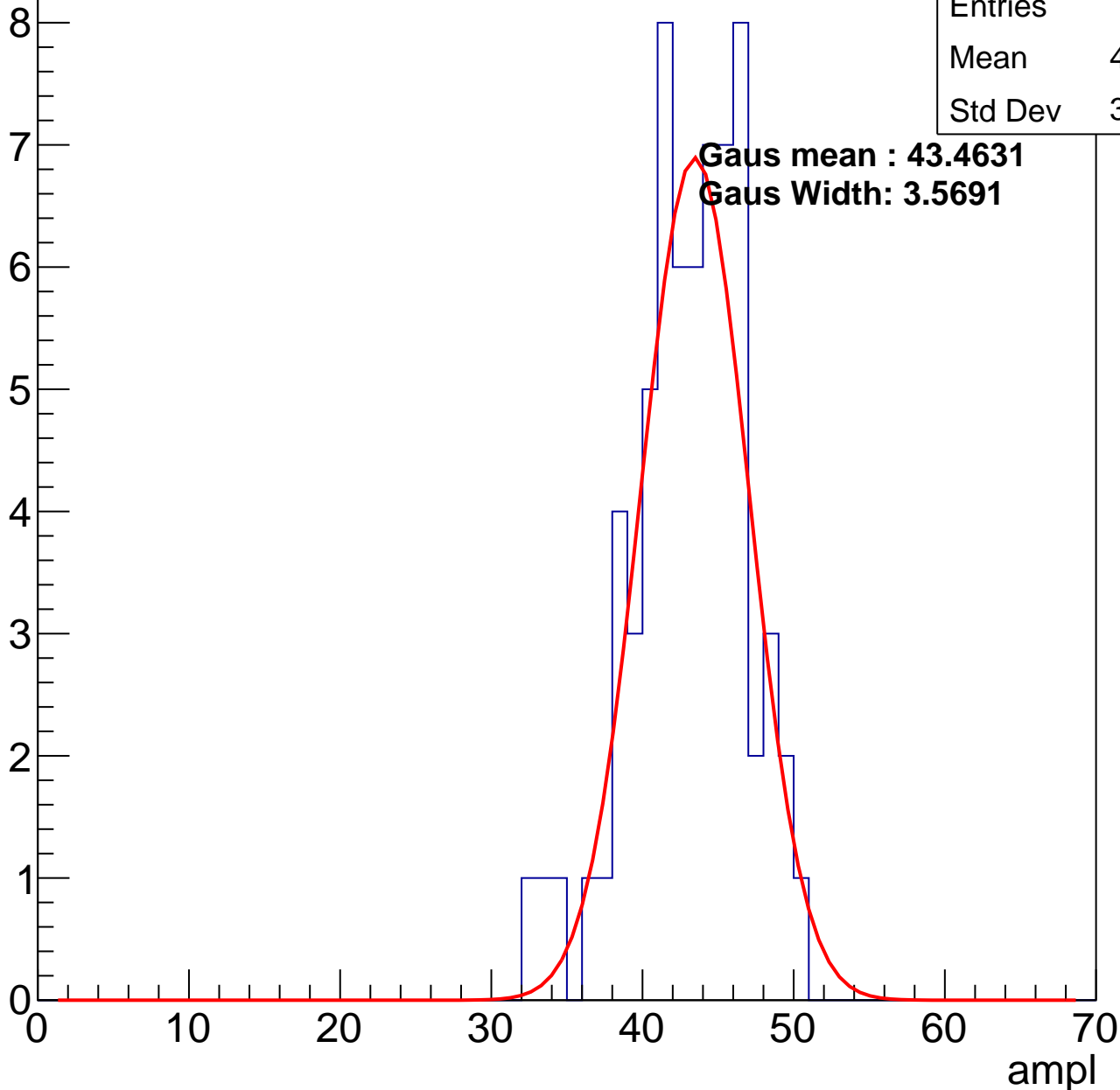
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	67
Mean	42.63
Std Dev	3.753

Gaus mean : 43.4631

Gaus Width: 3.5691

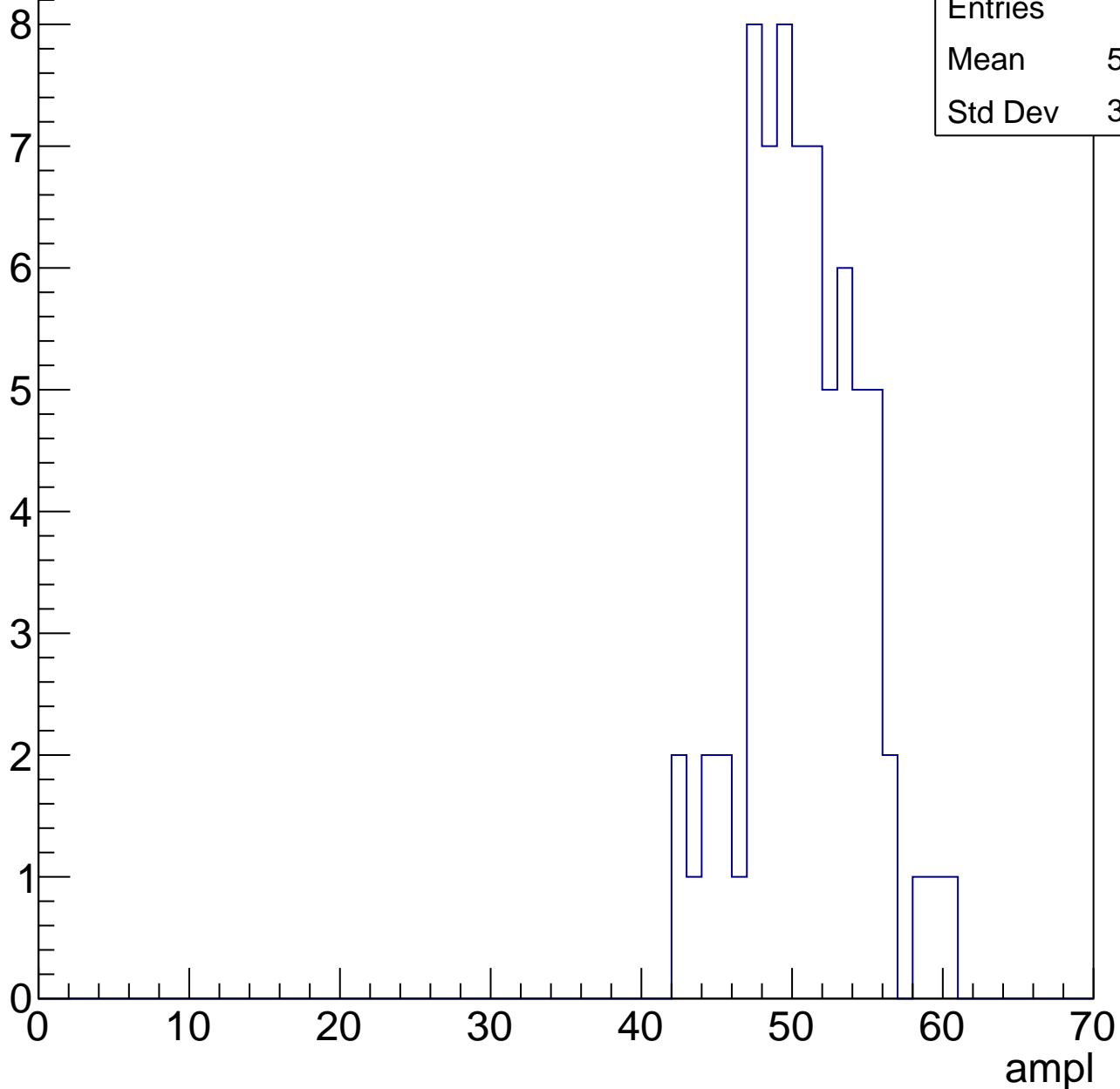


B1L103S, U10-ch117, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	50.34
Std Dev	3.786

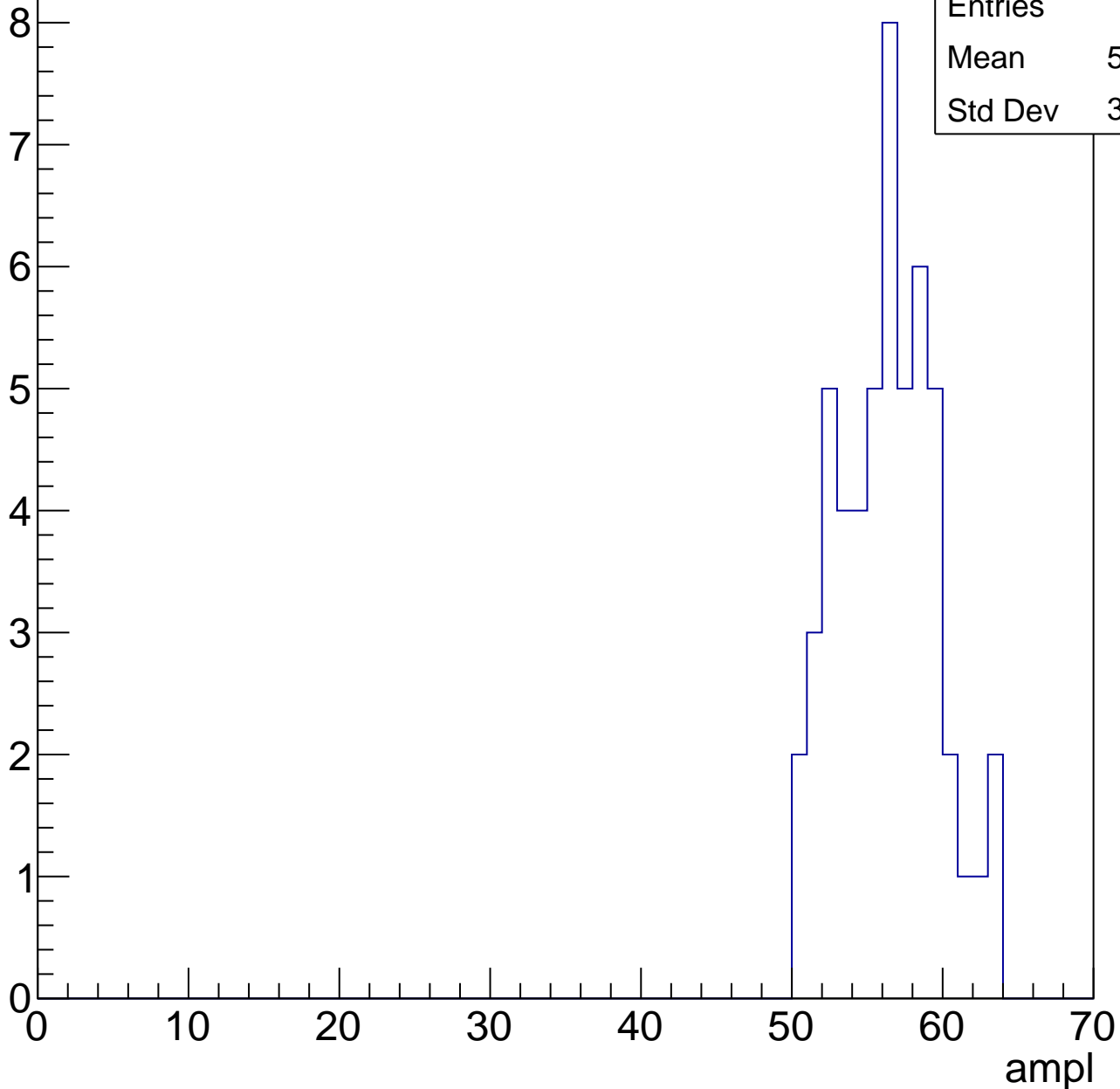


B1L103S, U10-ch117, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	53
Mean	55.87
Std Dev	3.216

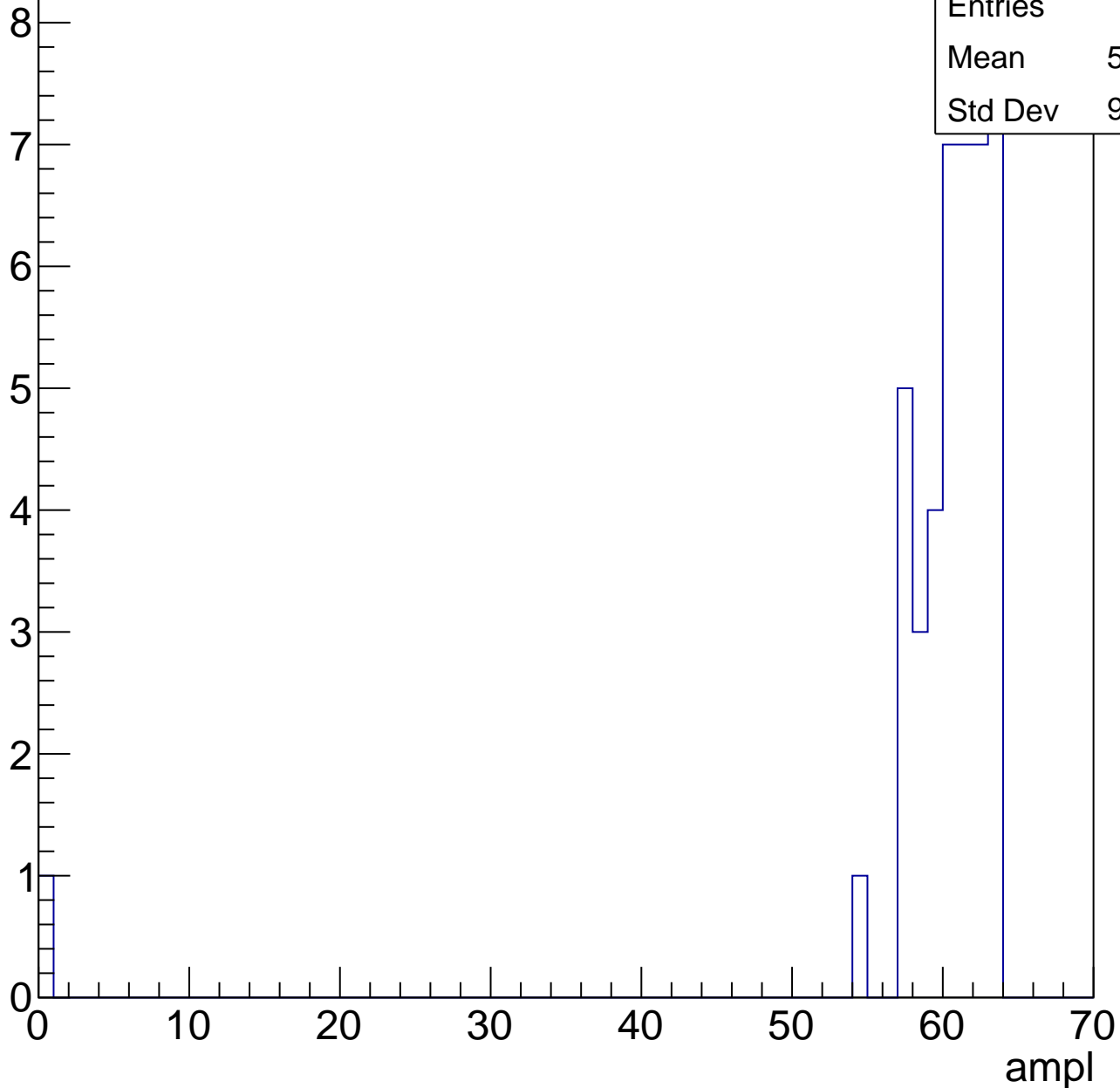


B1L103S, U10-ch117, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

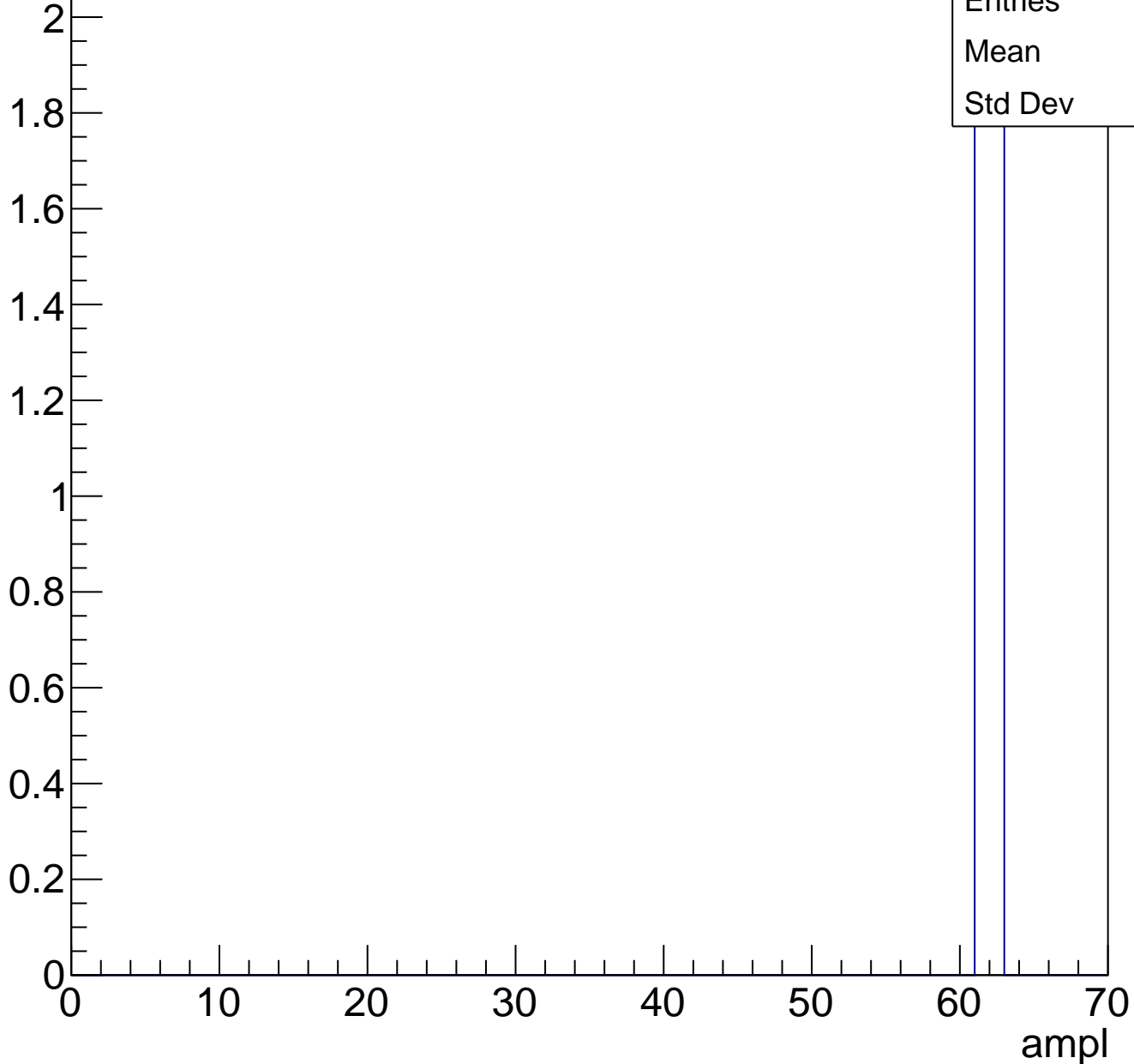
Entries	43
Mean	58.93
Std Dev	9.345



B1L103S, U10-ch117, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch117, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch118, adc0

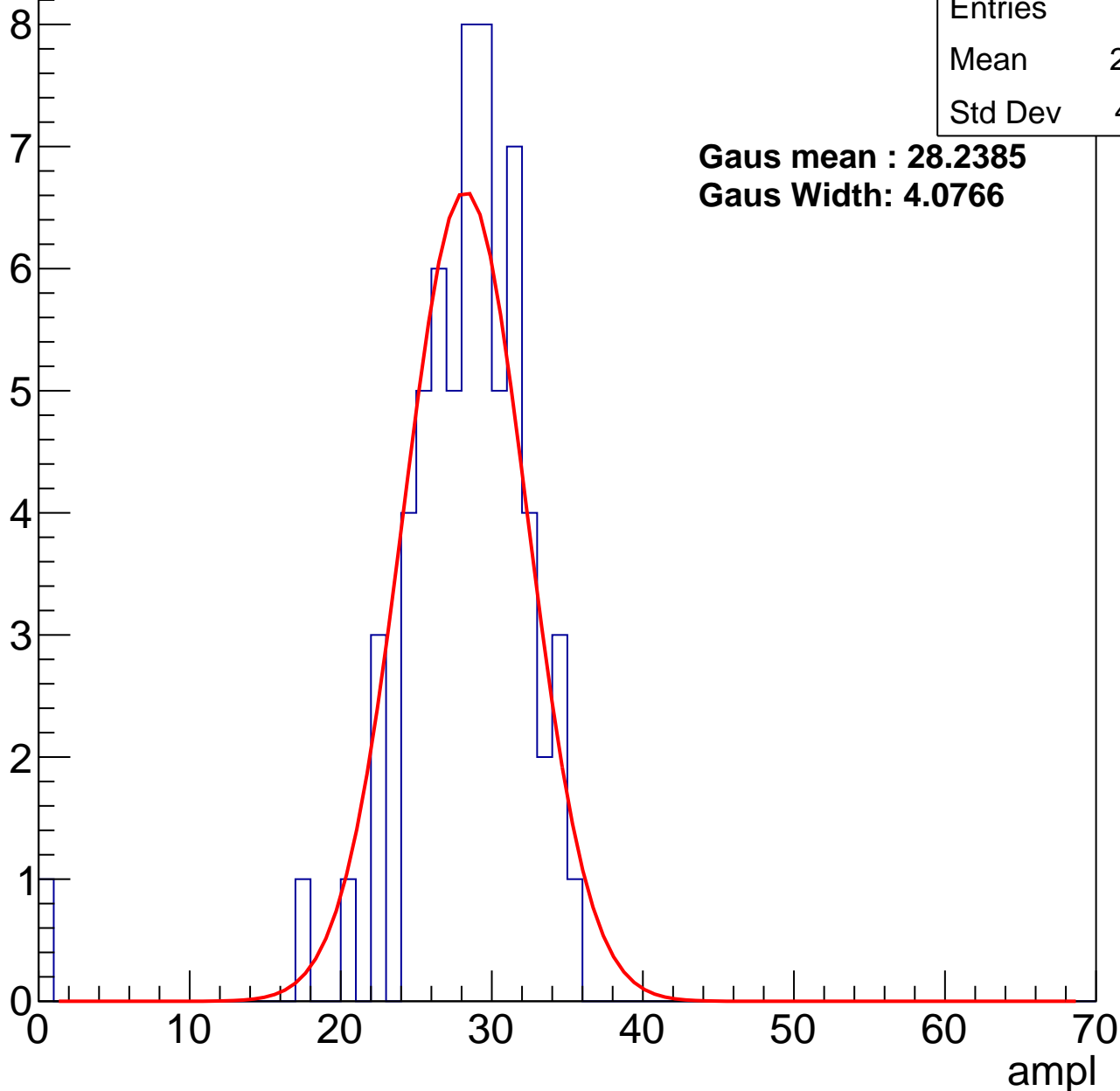
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	27.64
Std Dev	4.951

Gaus mean : 28.2385

Gaus Width: 4.0766



B1L103S, U10-ch118, adc1

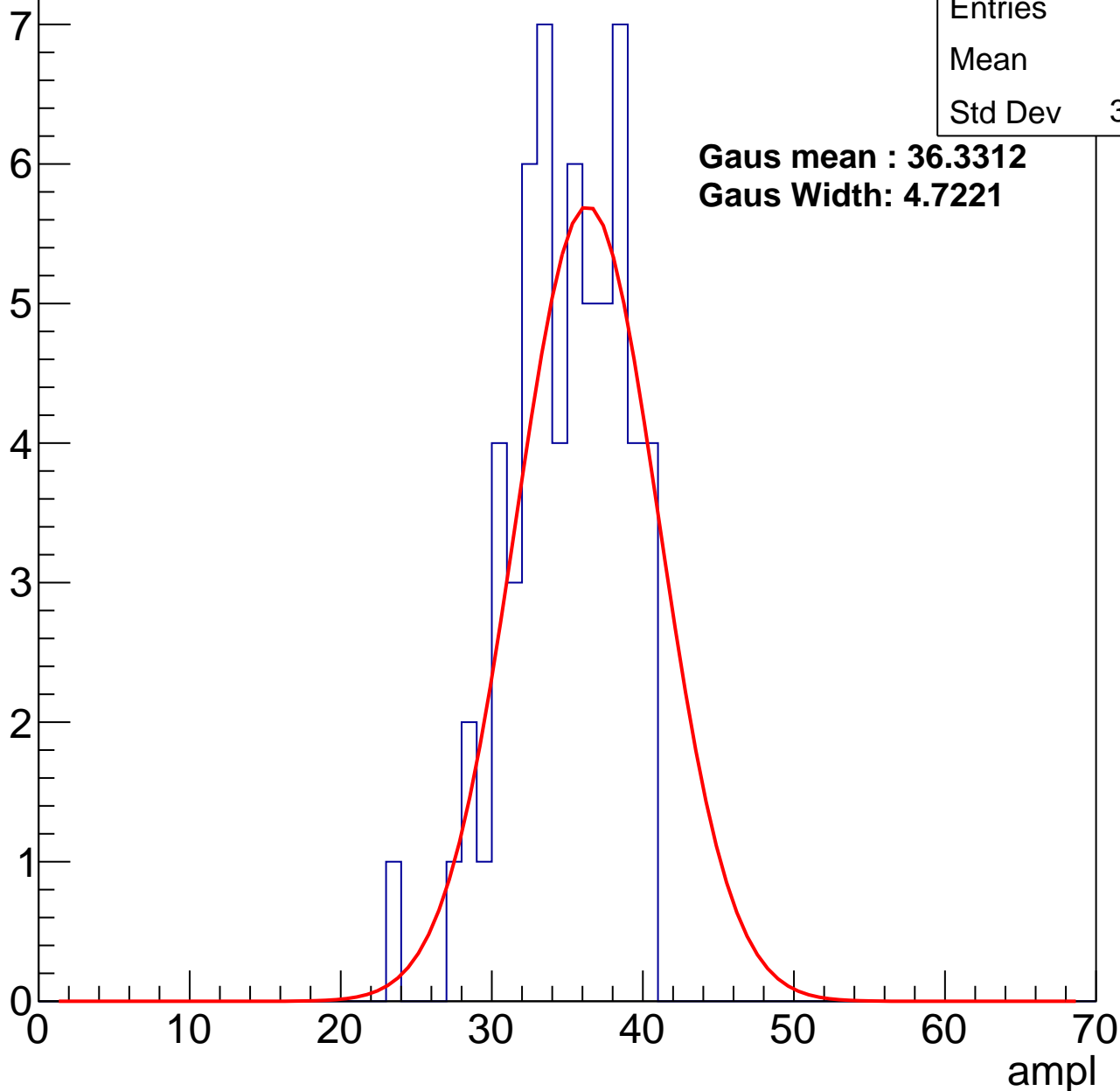
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	34.4
Std Dev	3.666

Gaus mean : 36.3312

Gaus Width: 4.7221



B1L103S, U10-ch118, adc2

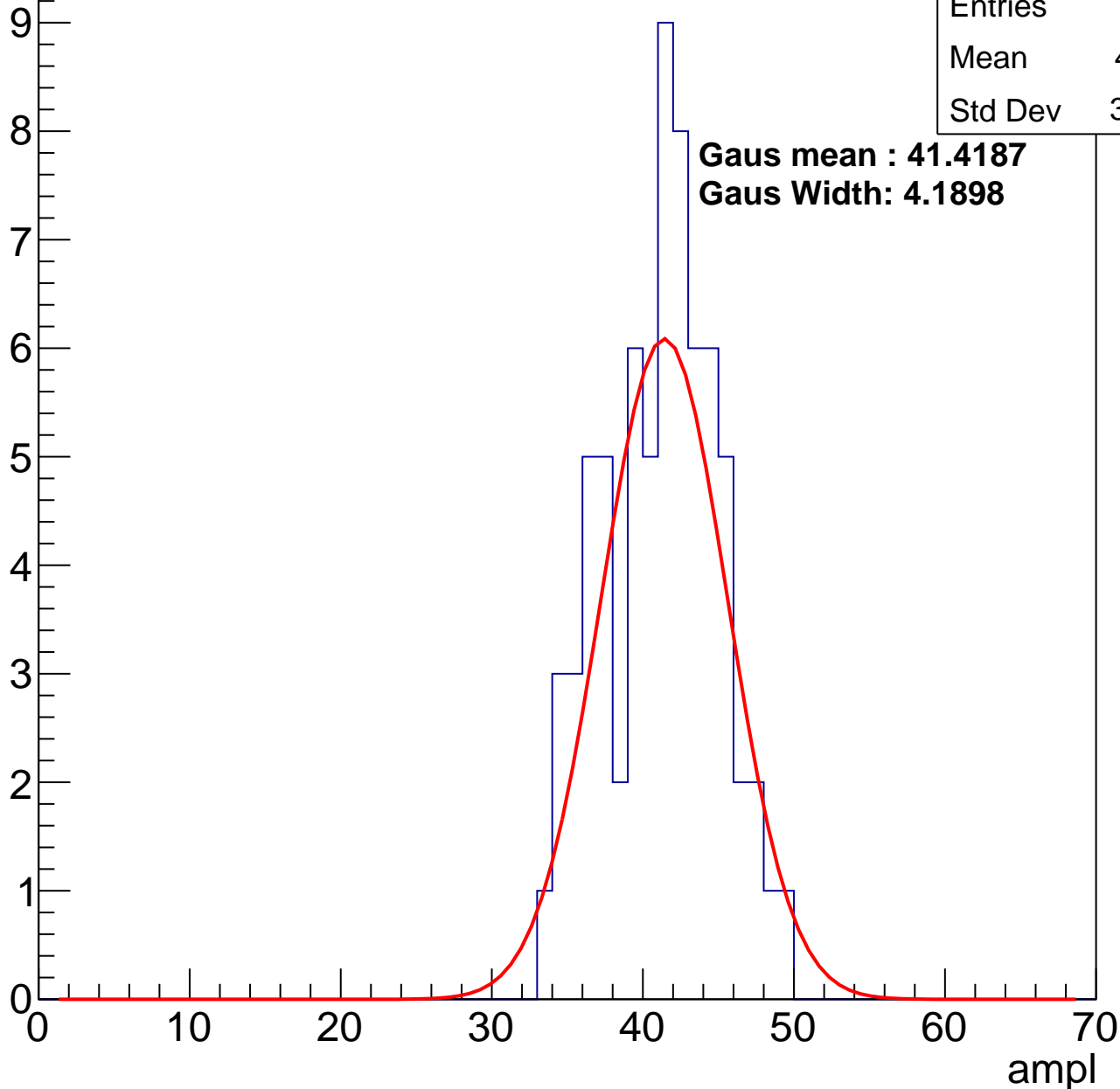
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	70
Mean	40.71
Std Dev	3.715

Gaus mean : 41.4187

Gaus Width: 4.1898

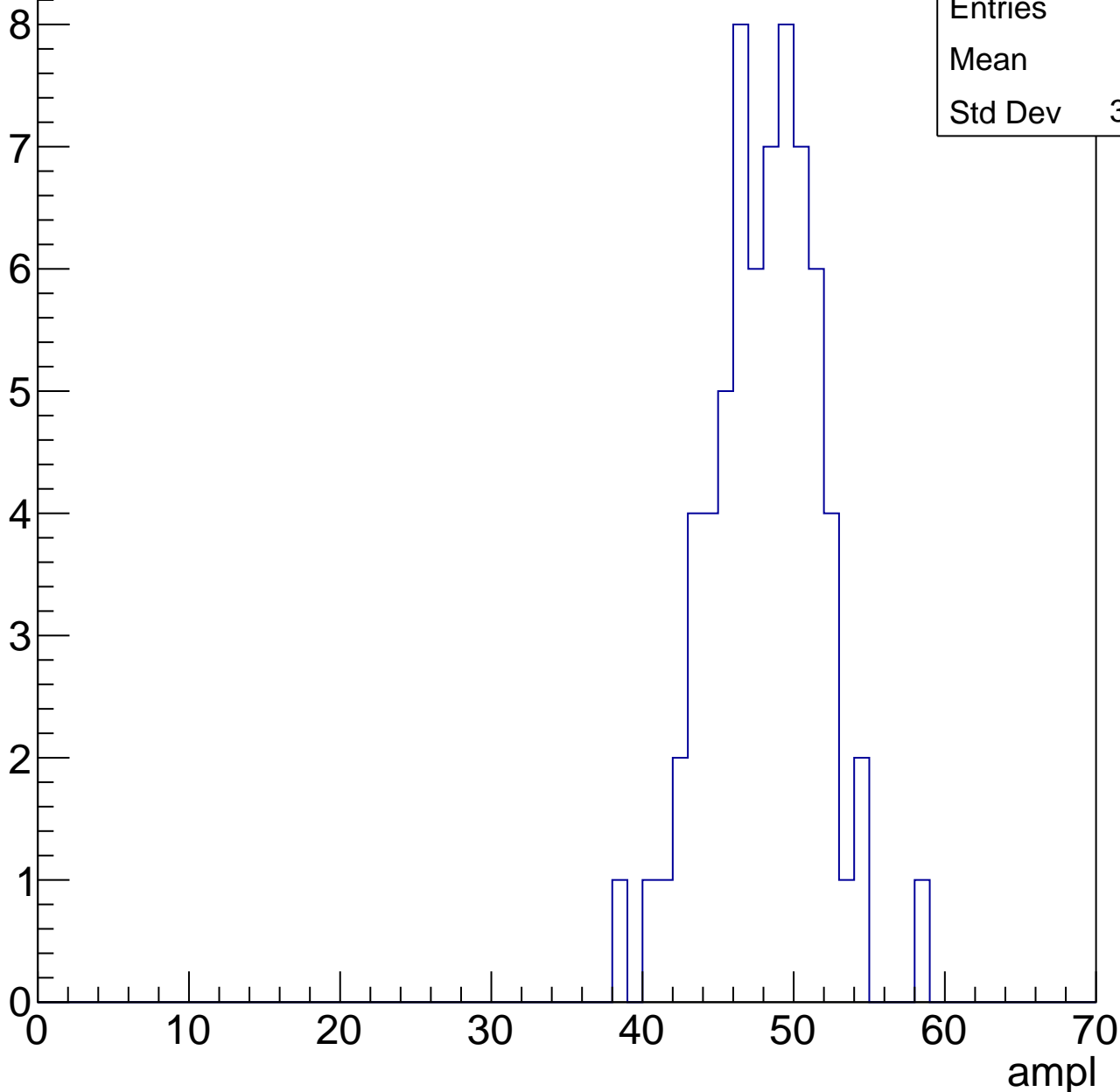


B1L103S, U10-ch118, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	47.6
Std Dev	3.582

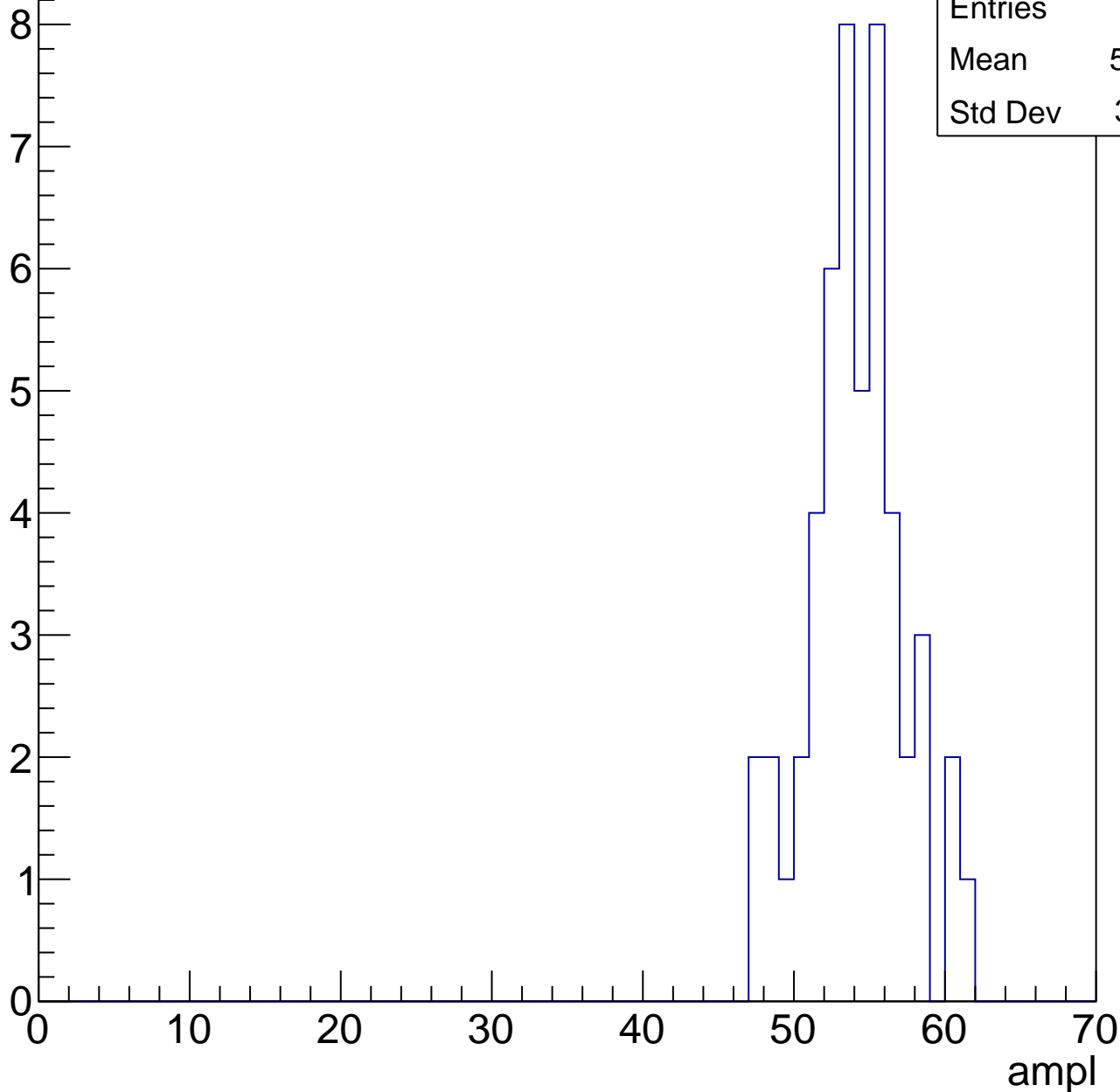


B1L103S, U10-ch118, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	53.64
Std Dev	3.161

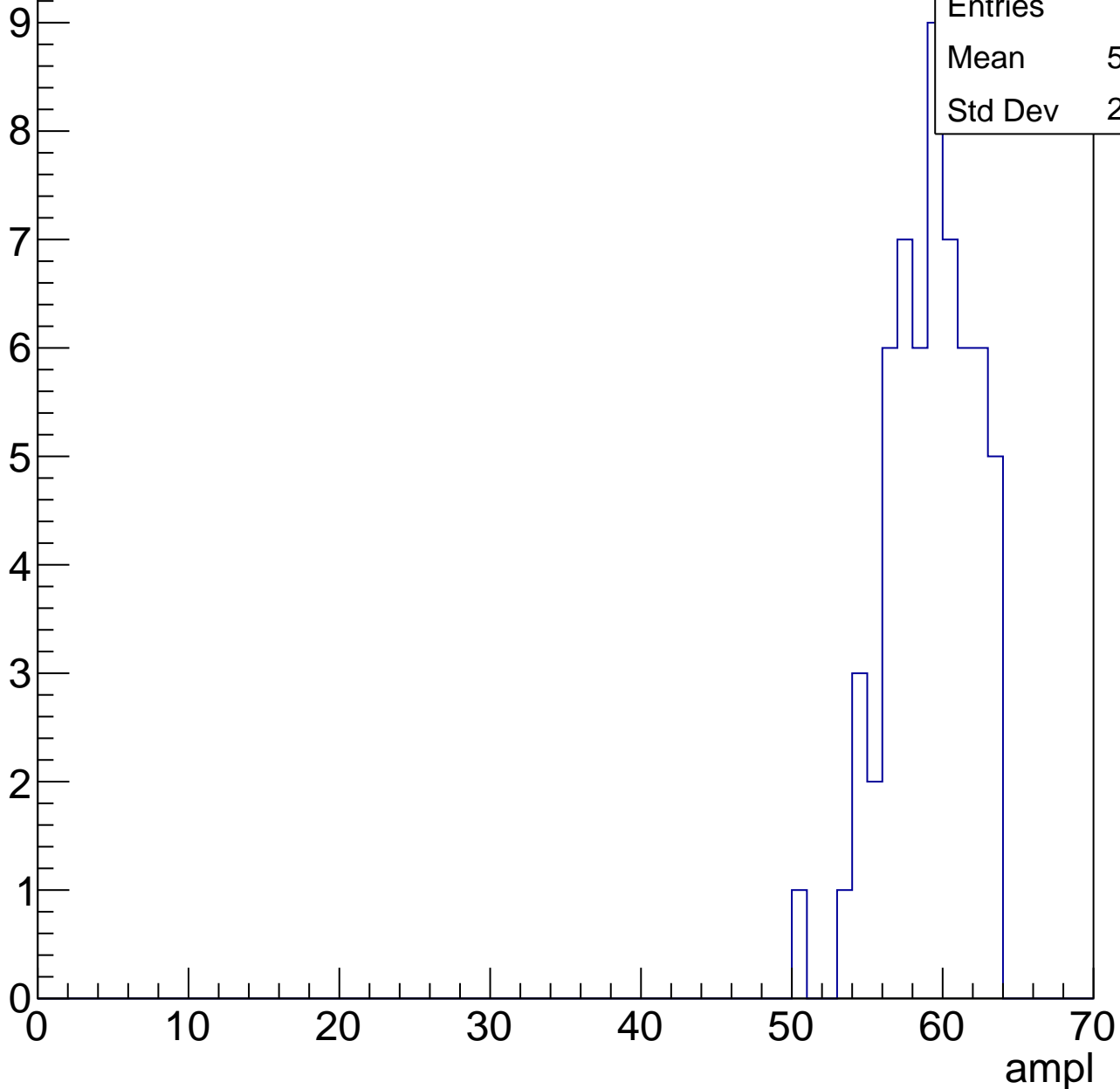


B1L103S, U10-ch118, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

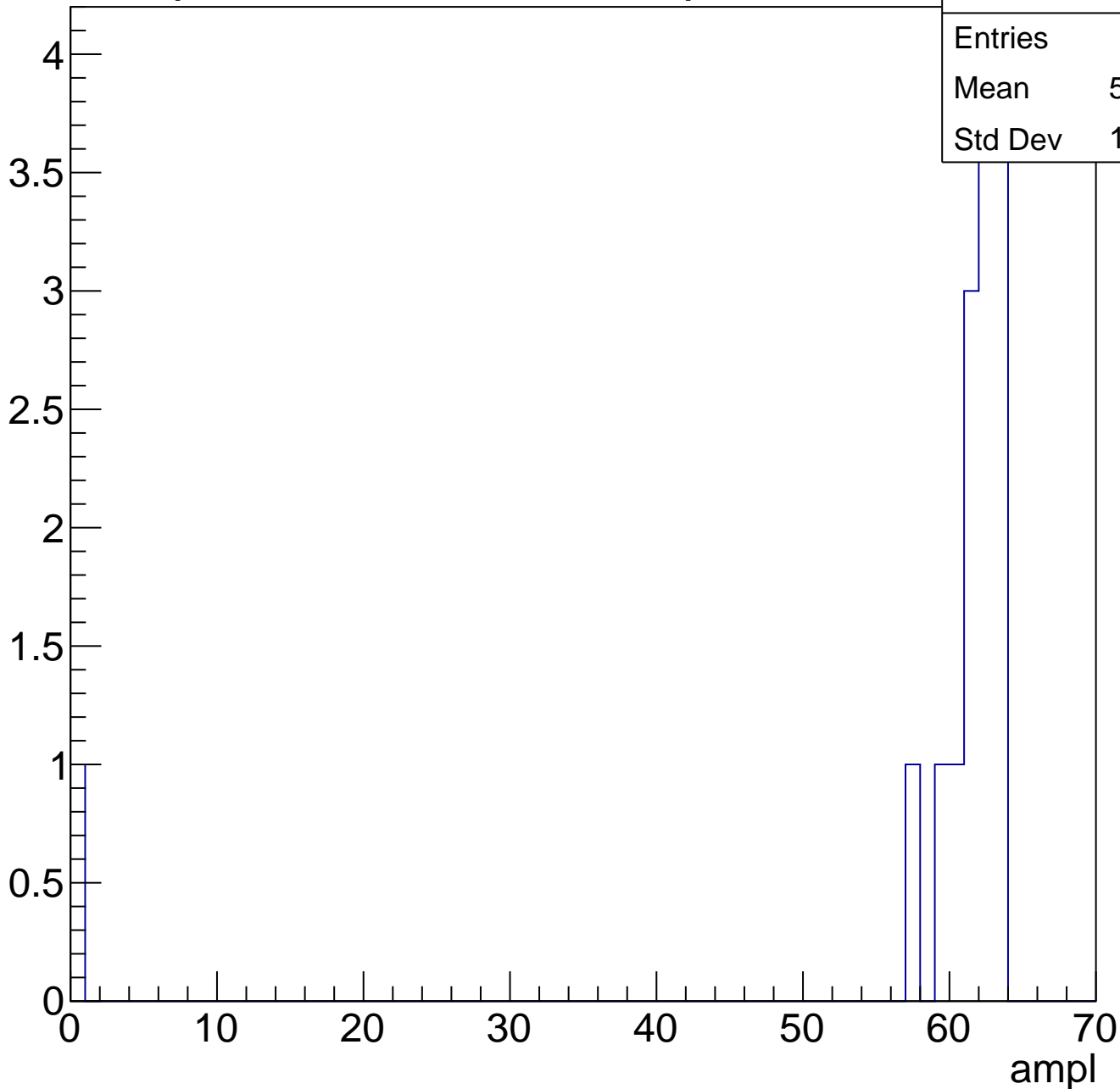
Entries	59
Mean	58.68
Std Dev	2.825



B1L103S, U10-ch118, adc6

calib_packv5_042523_0143.root, FC#7, port C2

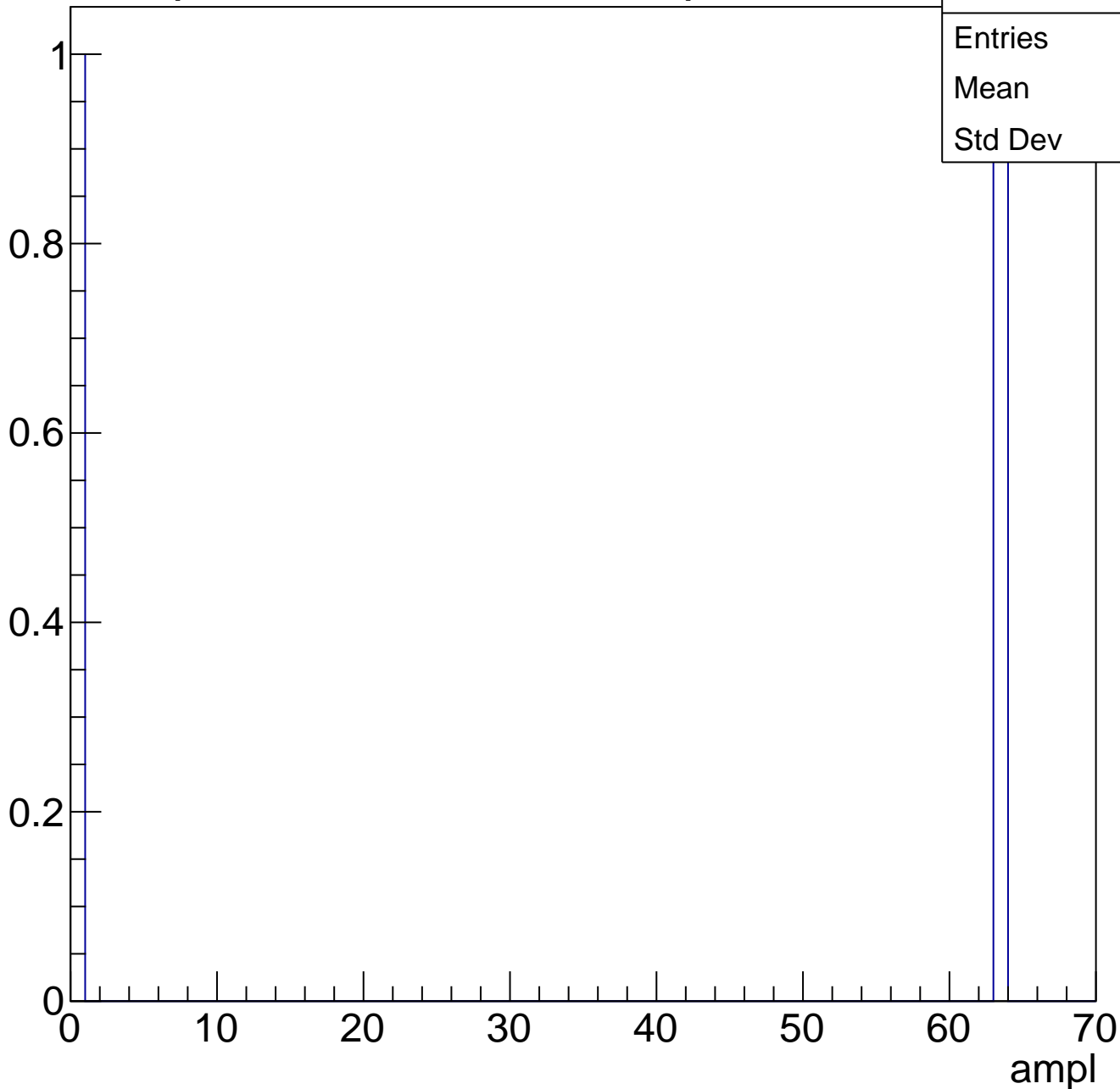
Entry



B1L103S, U10-ch118, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



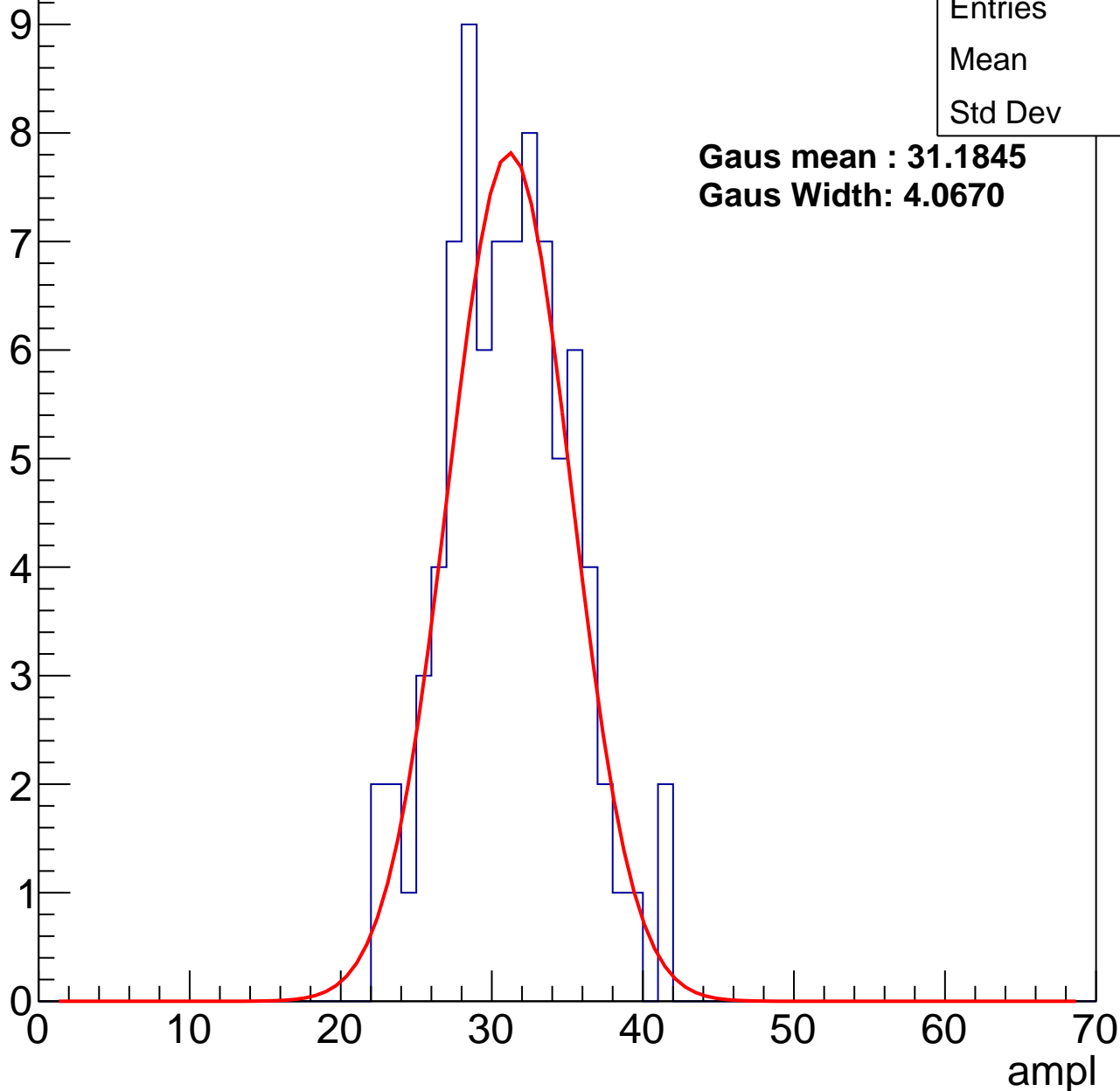
B1L103S, U10-ch119, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	84
Mean	30.7
Std Dev	4.12

Gaus mean : 31.1845
Gaus Width: 4.0670



B1L103S, U10-ch119, adc1

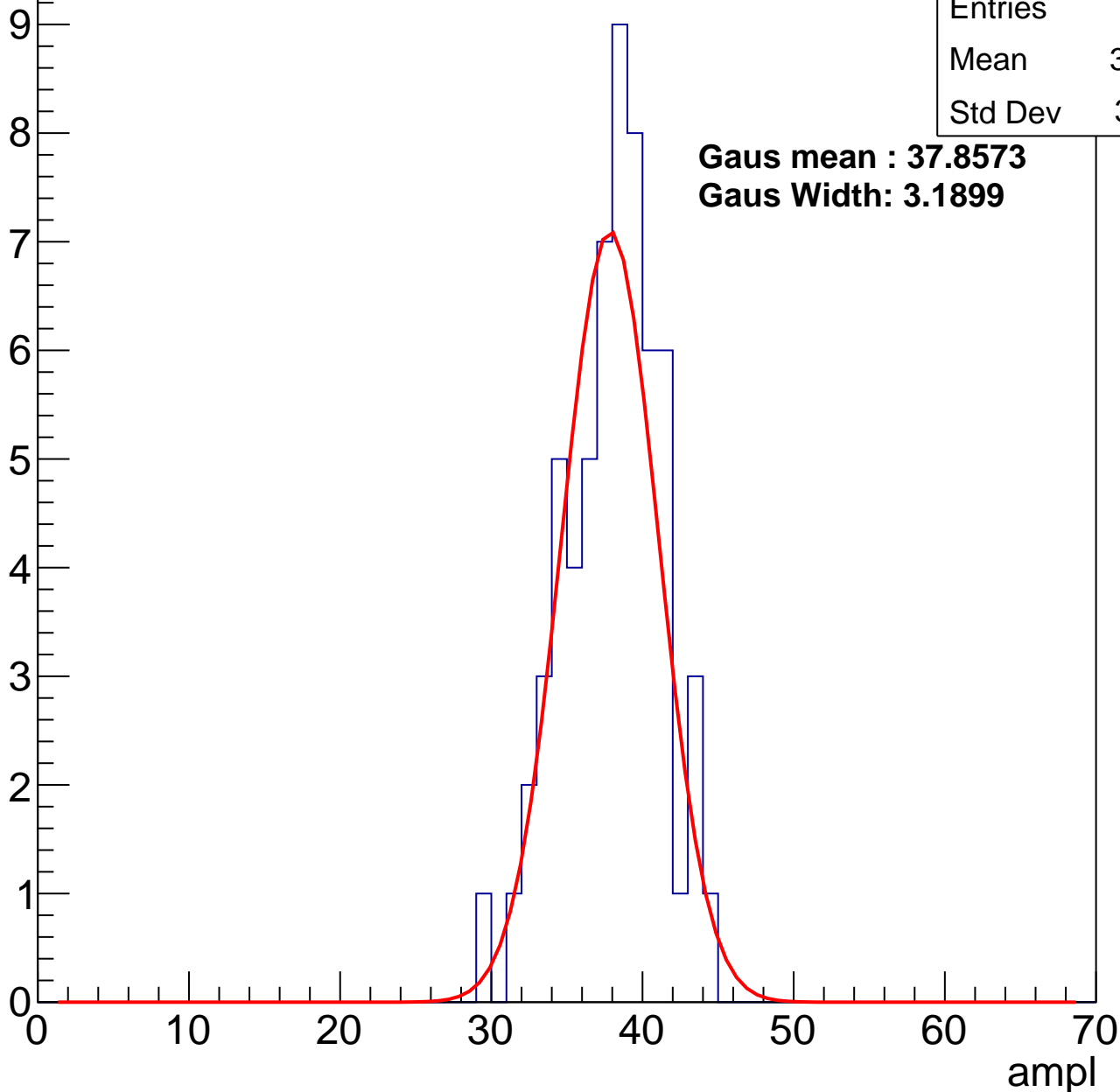
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	62
Mean	37.53
Std Dev	3.161

Gaus mean : 37.8573

Gaus Width: 3.1899



B1L103S, U10-ch119, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	65
Mean	44.52
Std Dev	3.163

Gaus mean : 45.1854

Gaus Width: 2.9841

Entry

10

8

6

4

2

0

0

10

20

30

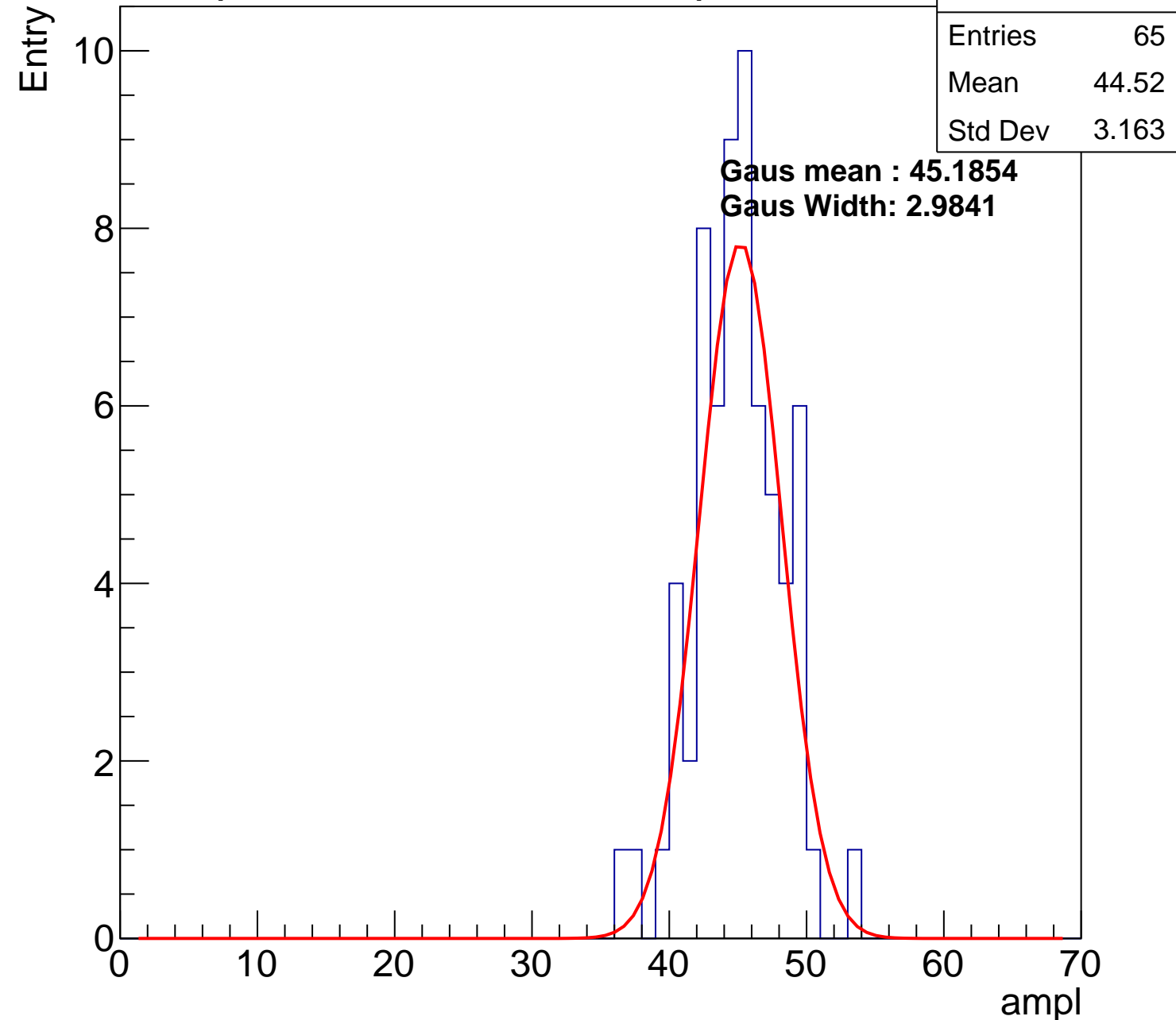
40

50

60

70

ampl

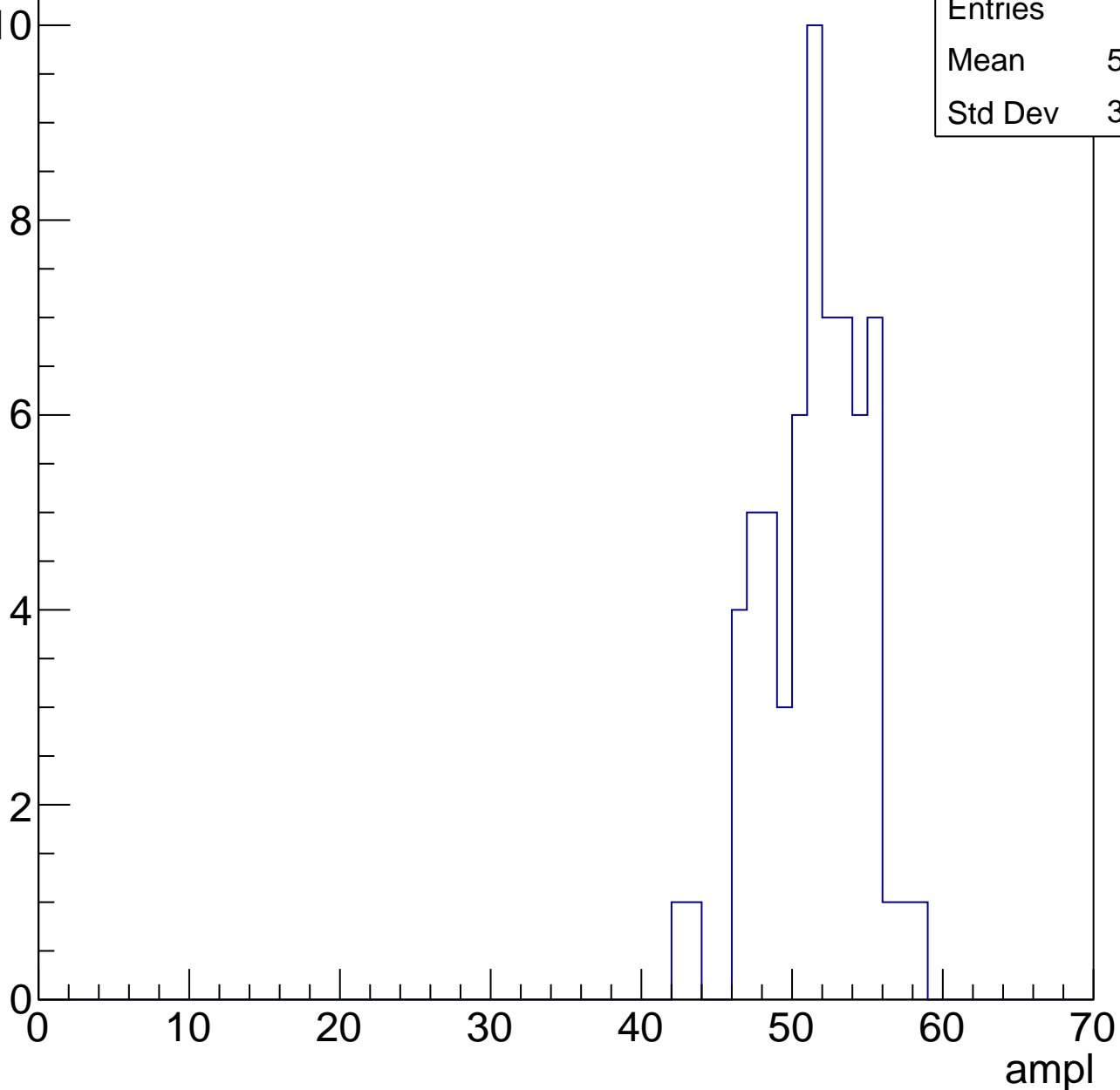


B1L103S, U10-ch119, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	51.02
Std Dev	3.279

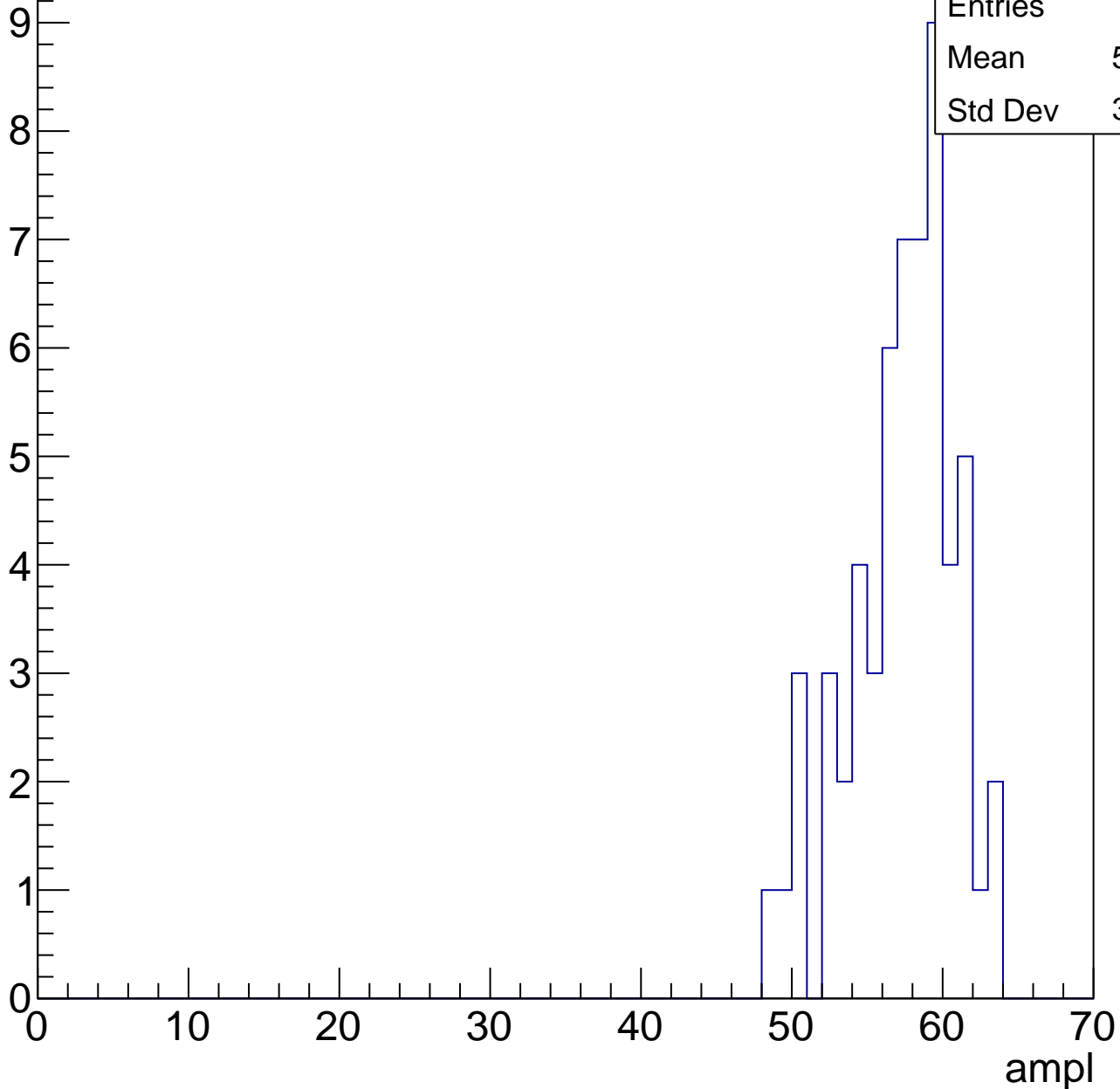


B1L103S, U10-ch119, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	58
Mean	56.81
Std Dev	3.491

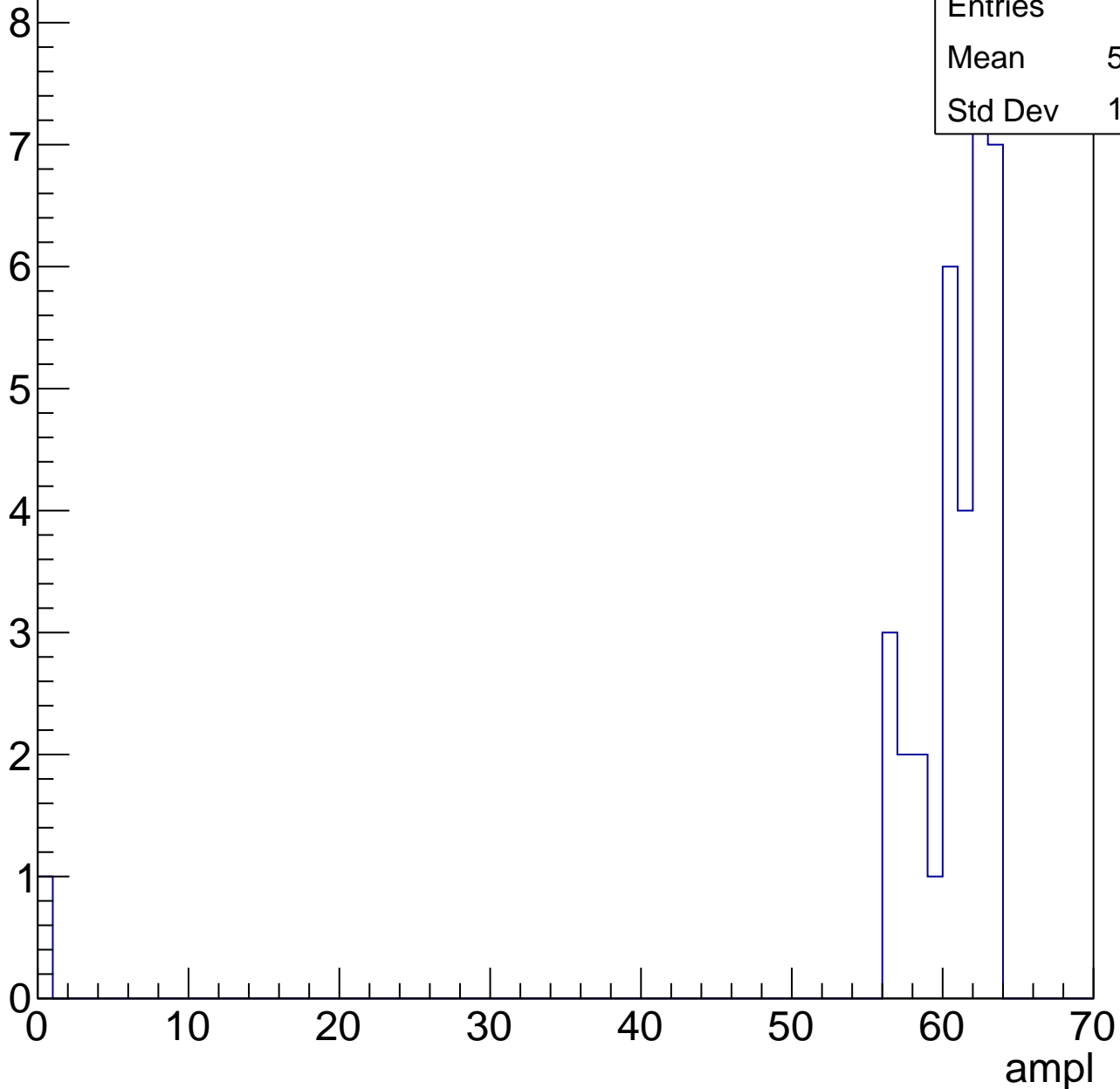


B1L103S, U10-ch119, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	34
Mean	58.76
Std Dev	10.46



B1L103S, U10-ch119, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch119, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

0

Mean

0

Std Dev

0

B1L103S, U10-ch120, adc0

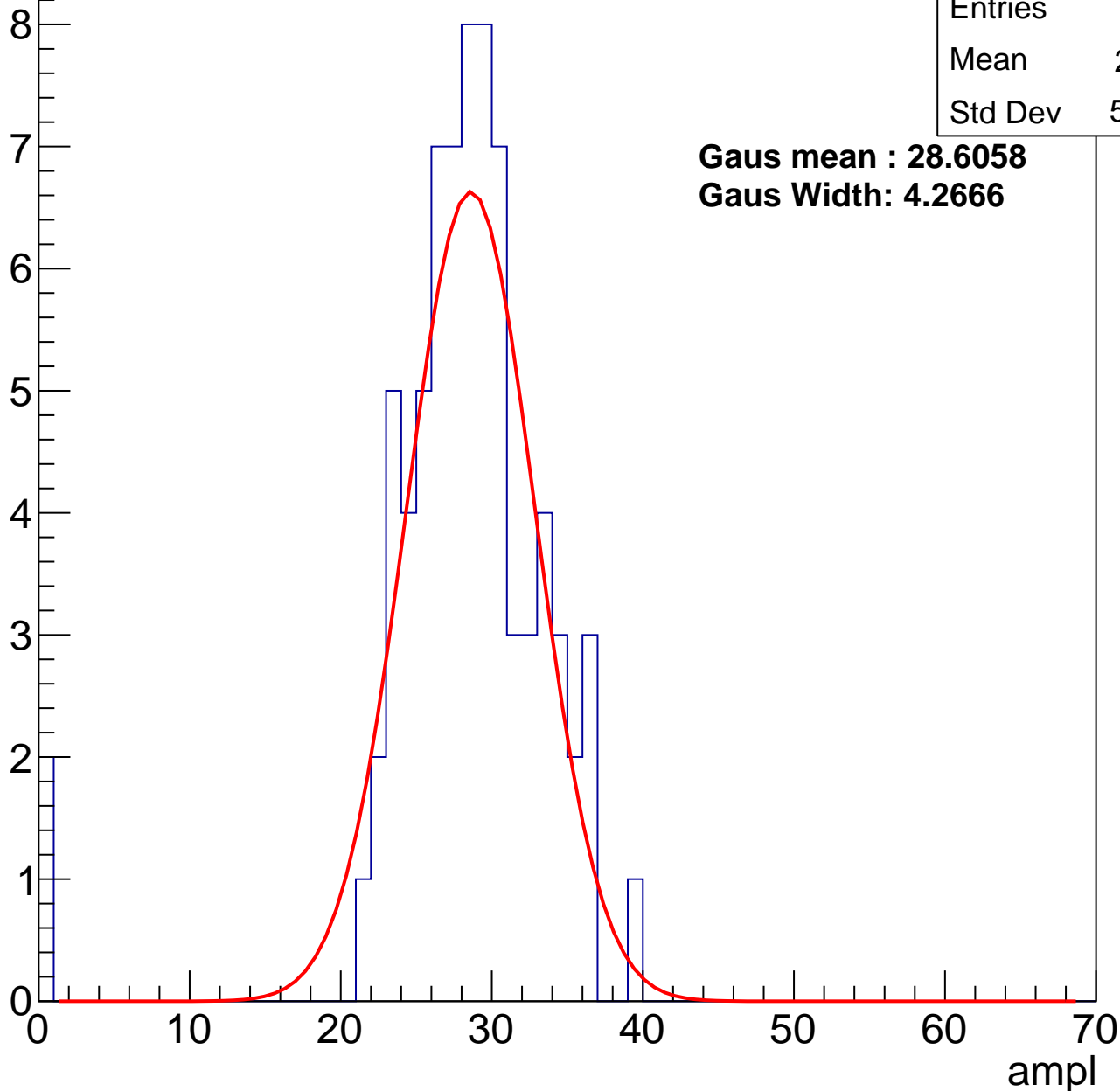
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	27.71
Std Dev	5.979

Gaus mean : 28.6058

Gaus Width: 4.2666



B1L103S, U10-ch120, adc1

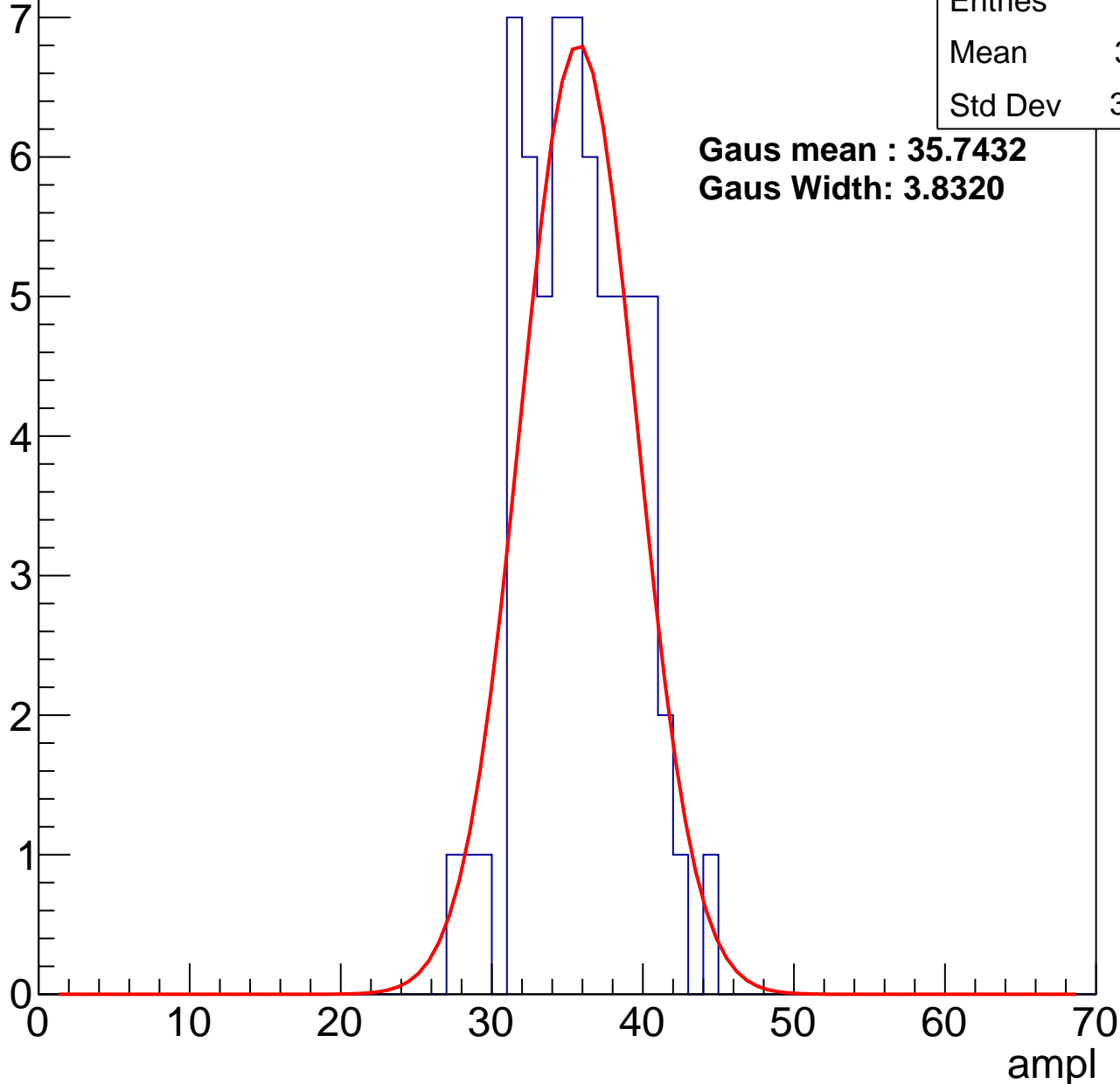
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.31
Std Dev	3.538

Gaus mean : 35.7432

Gaus Width: 3.8320



B1L103S, U10-ch120, adc2

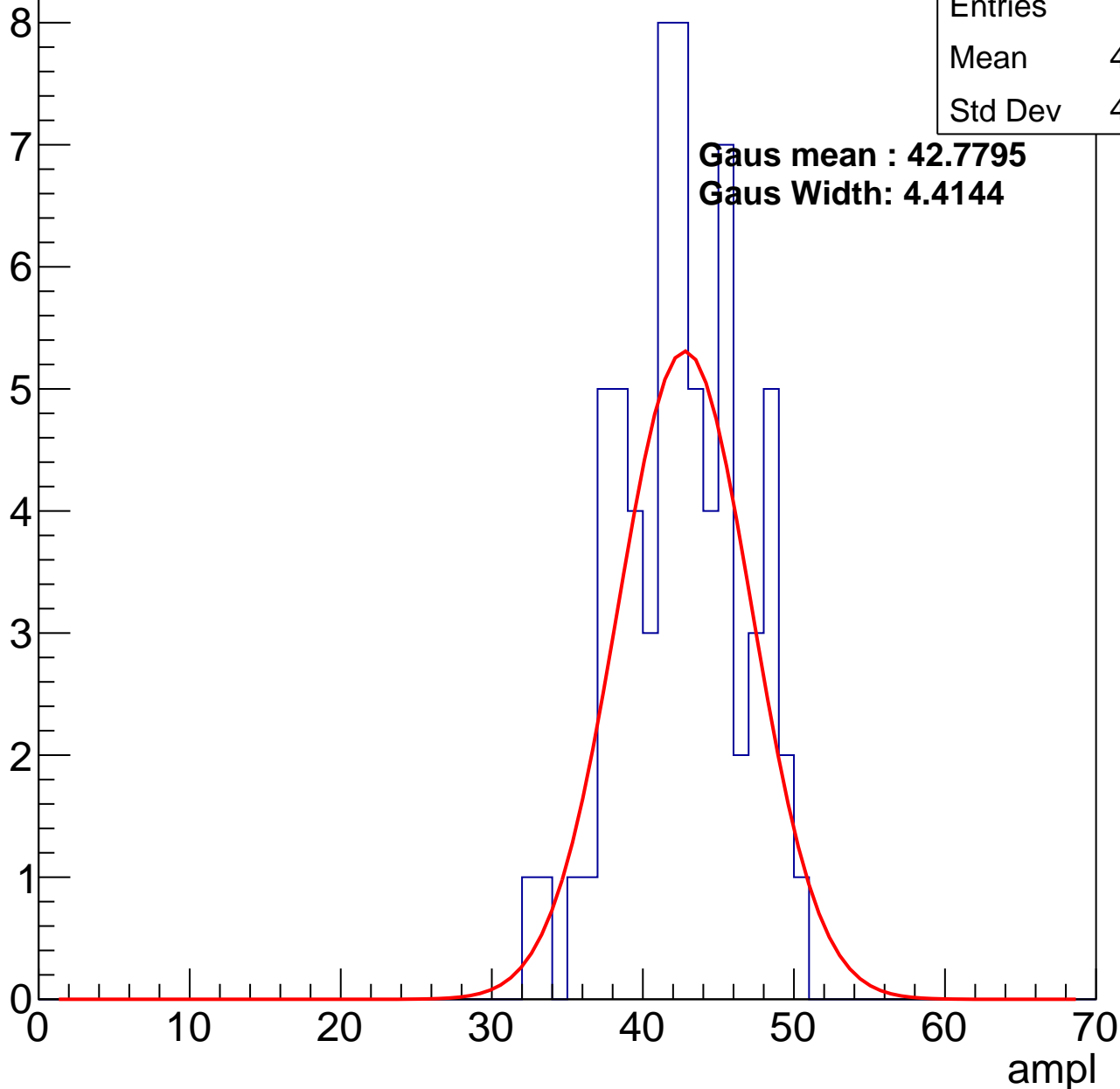
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	42.09
Std Dev	4.007

Gaus mean : 42.7795

Gaus Width: 4.4144

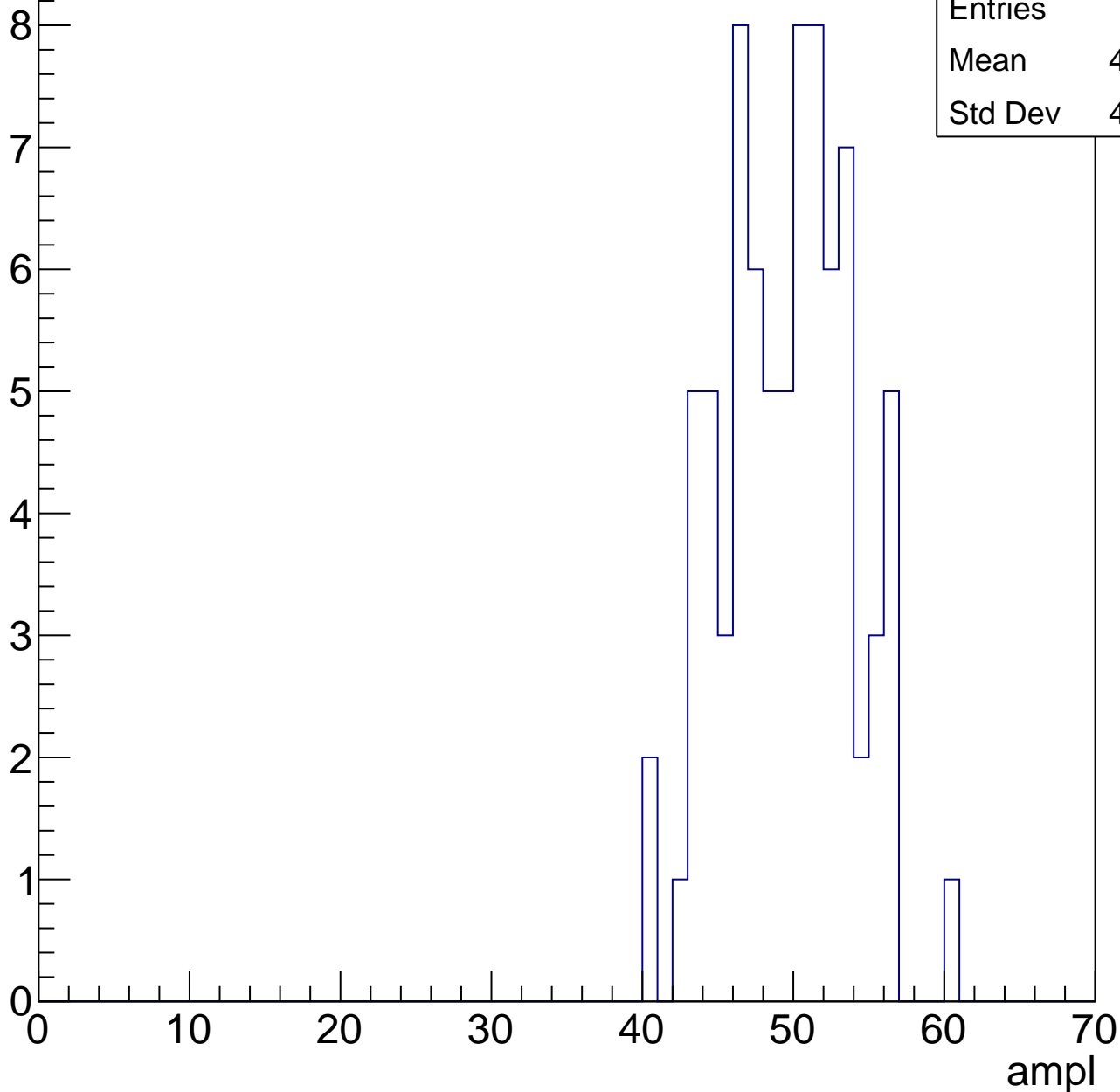


B1L103S, U10-ch120, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	80
Mean	49.14
Std Dev	4.174

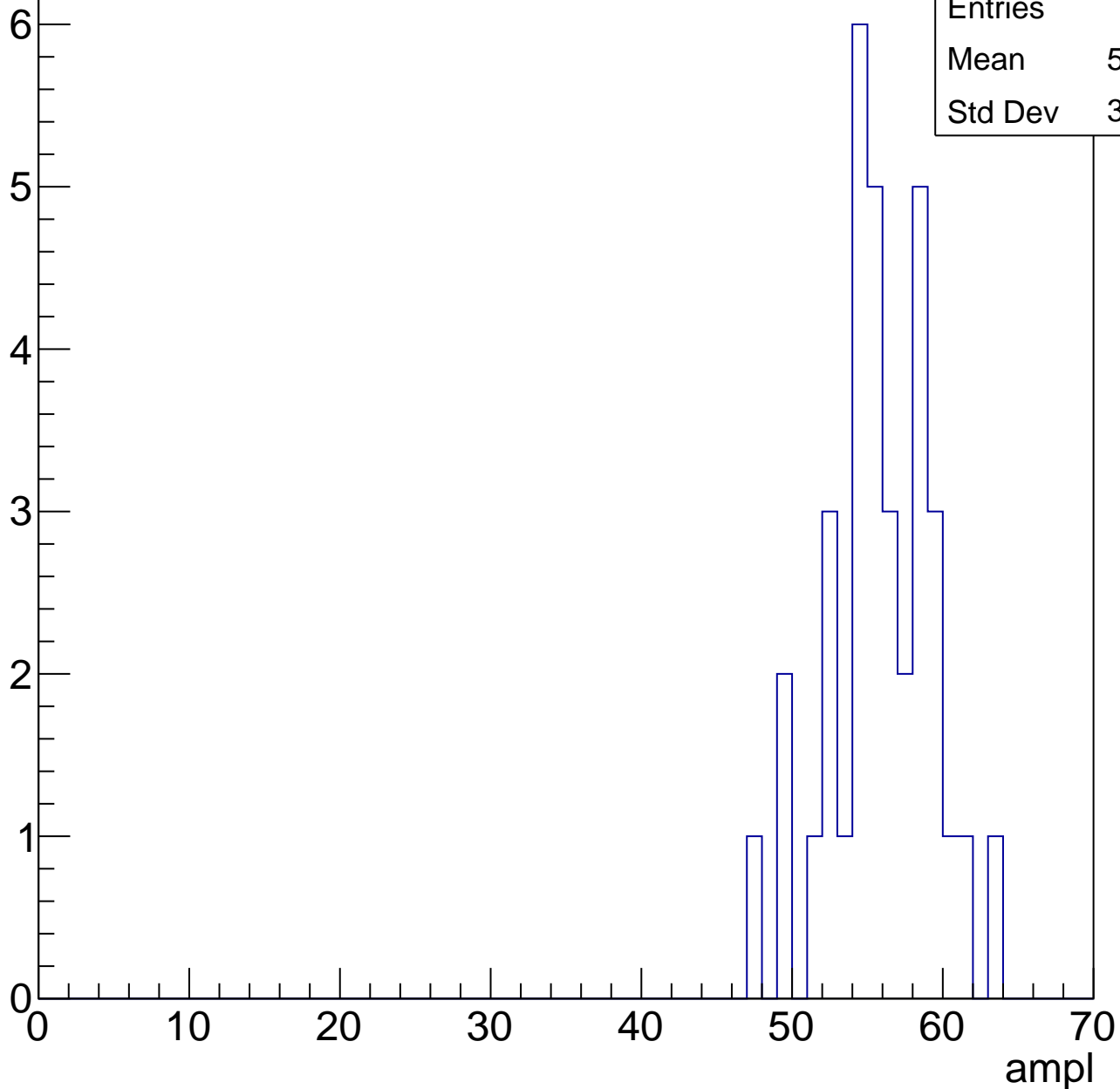


B1L103S, U10-ch120, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

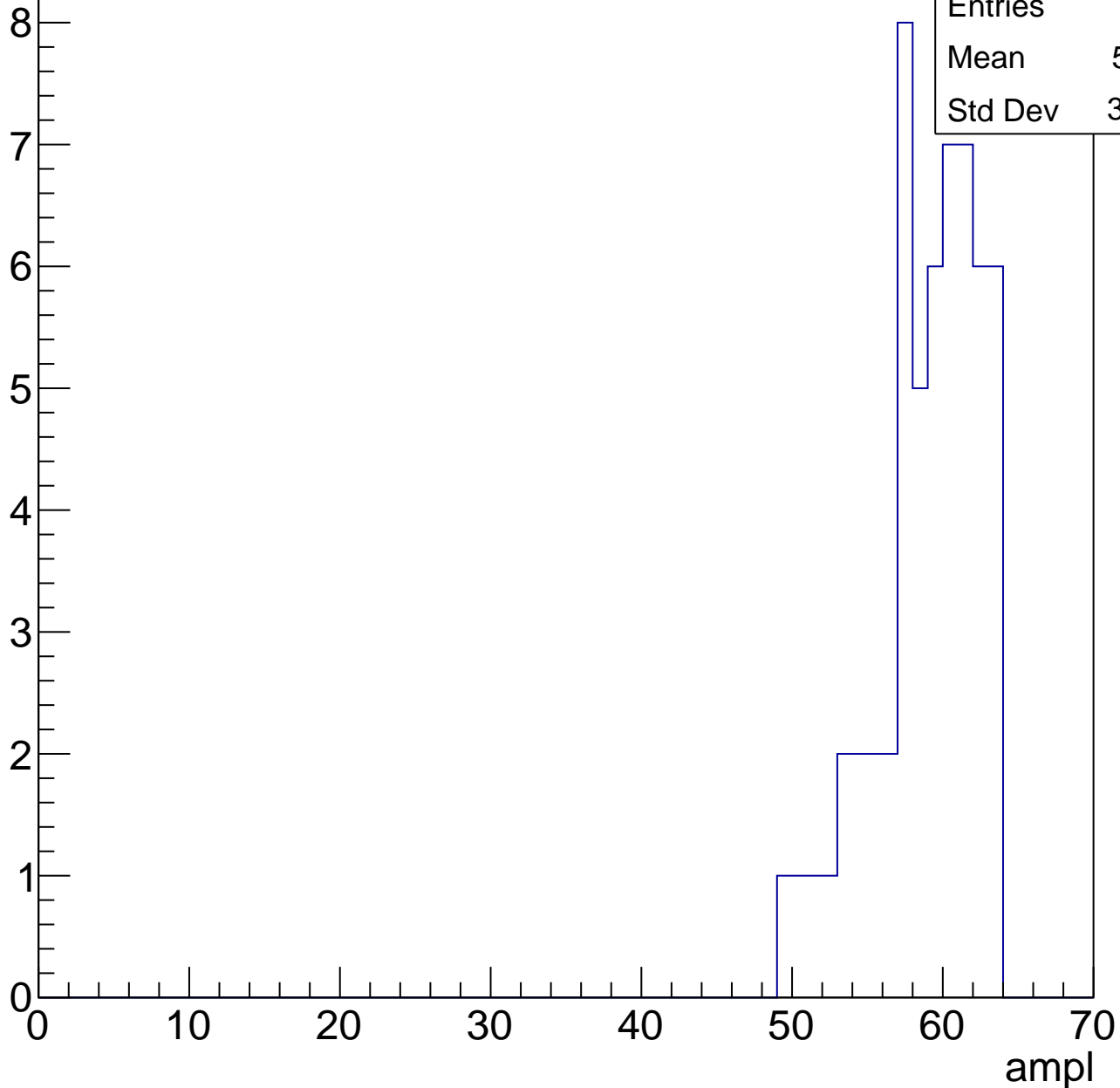
Entries	35
Mean	55.34
Std Dev	3.439



B1L103S, U10-ch120, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch120, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

3

2.5

2

1.5

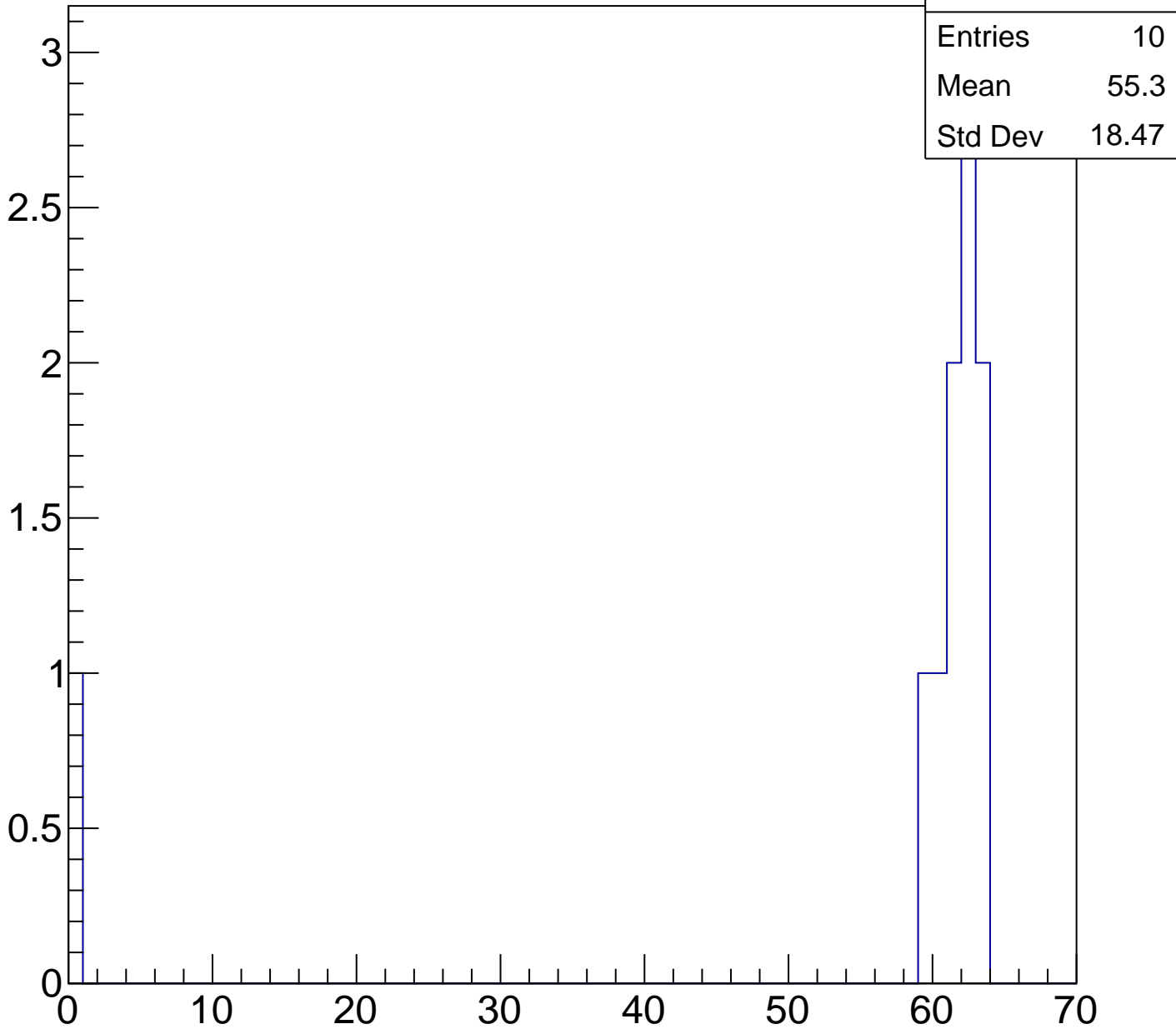
1

0.5

0

ampl

Entries	10
Mean	55.3
Std Dev	18.47



B1L103S, U10-ch120, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch121, adc0

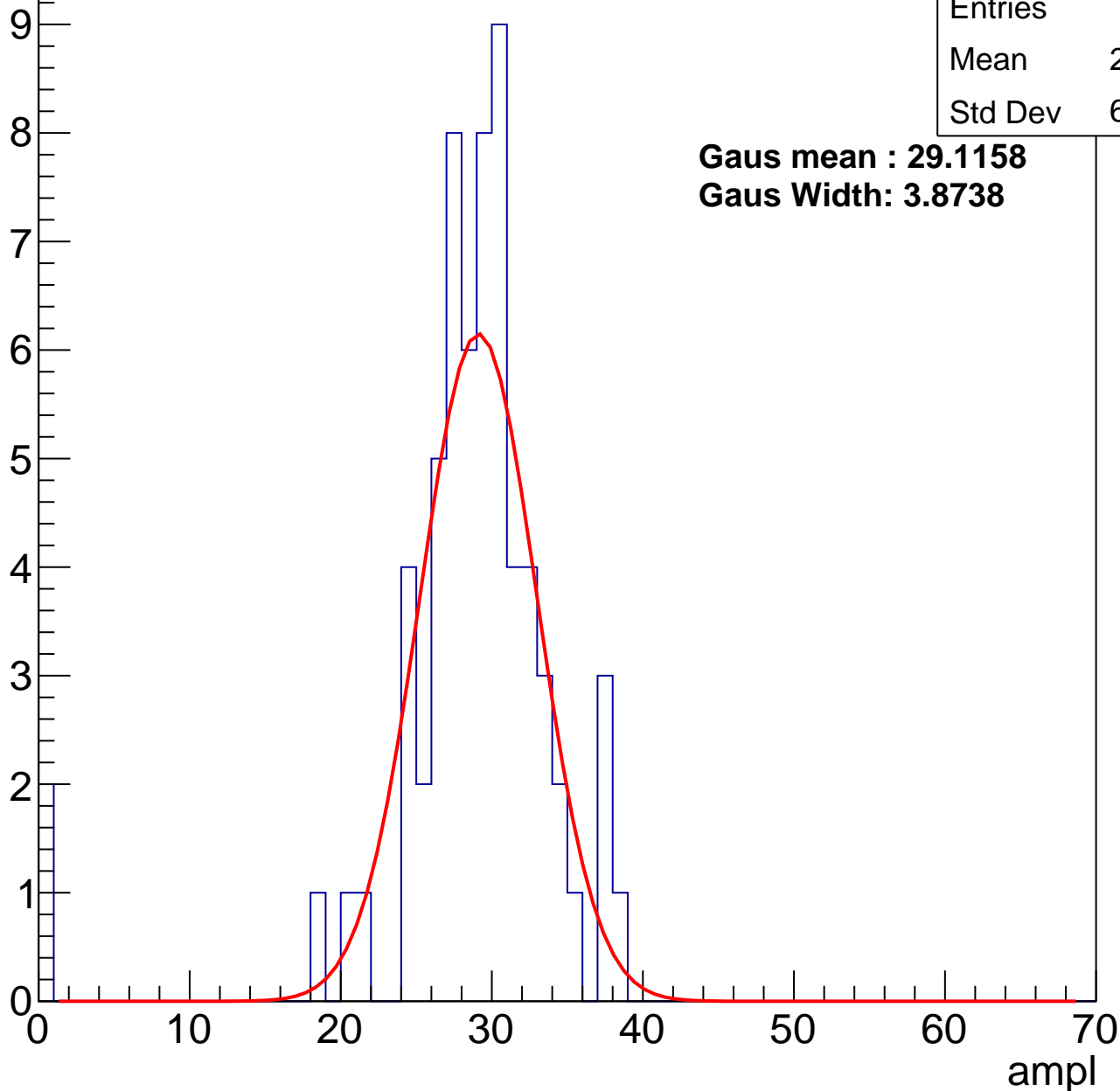
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	28.06
Std Dev	6.293

Gaus mean : 29.1158

Gaus Width: 3.8738



B1L103S, U10-ch121, adc1

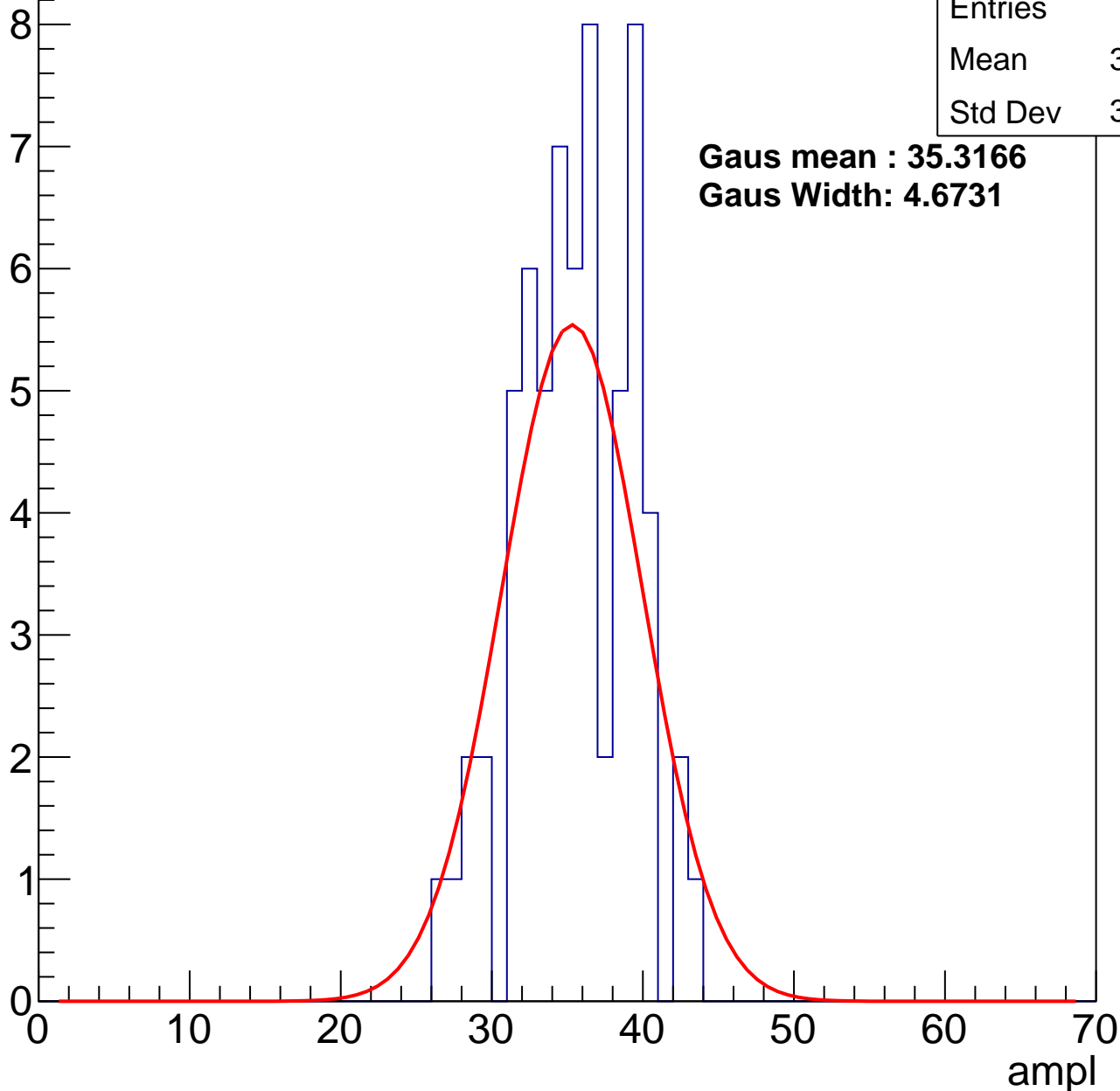
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	35.05
Std Dev	3.776

Gaus mean : 35.3166

Gaus Width: 4.6731



B1L103S, U10-ch121, adc2

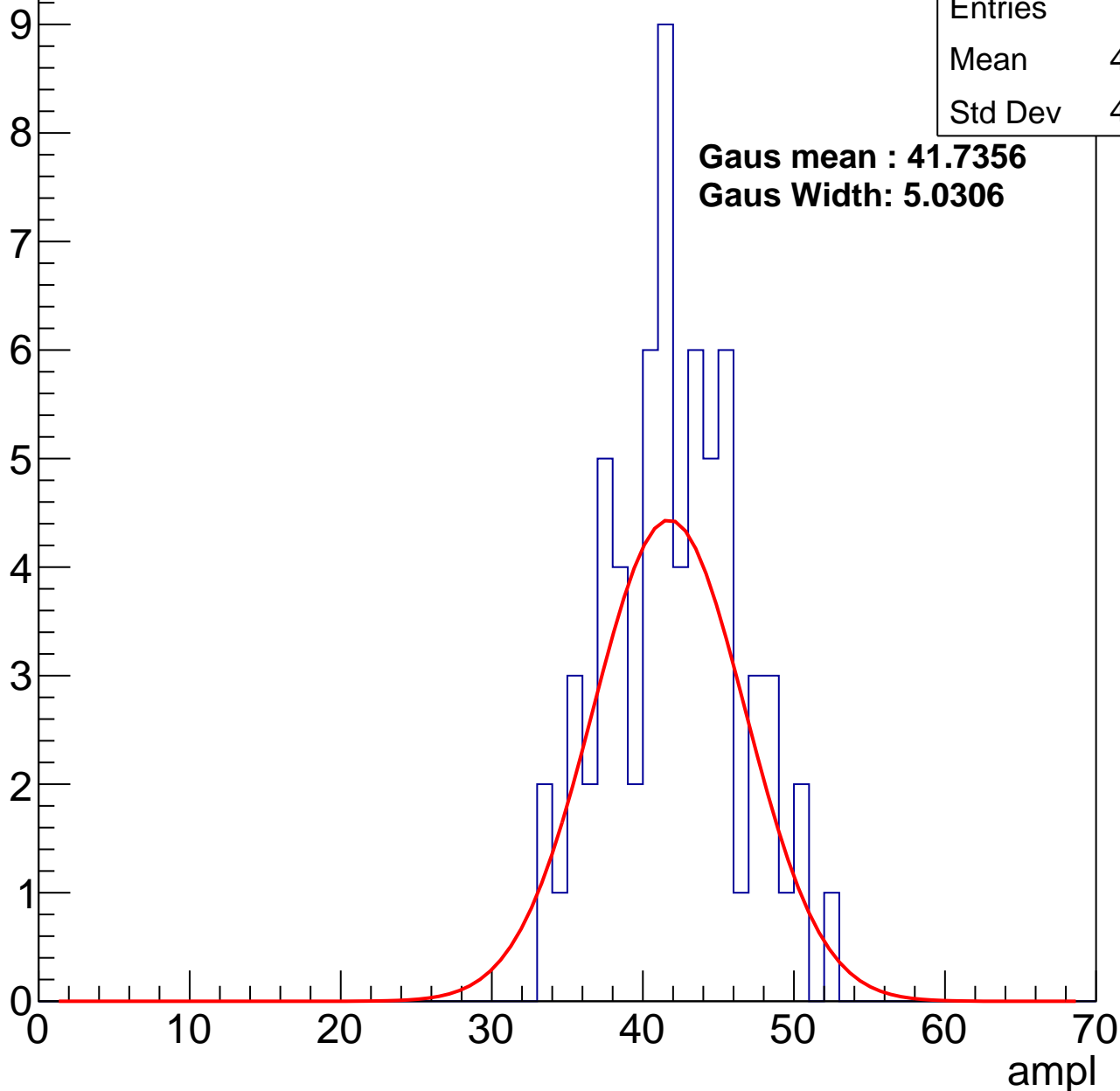
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	66
Mean	41.65
Std Dev	4.322

Gaus mean : 41.7356

Gaus Width: 5.0306

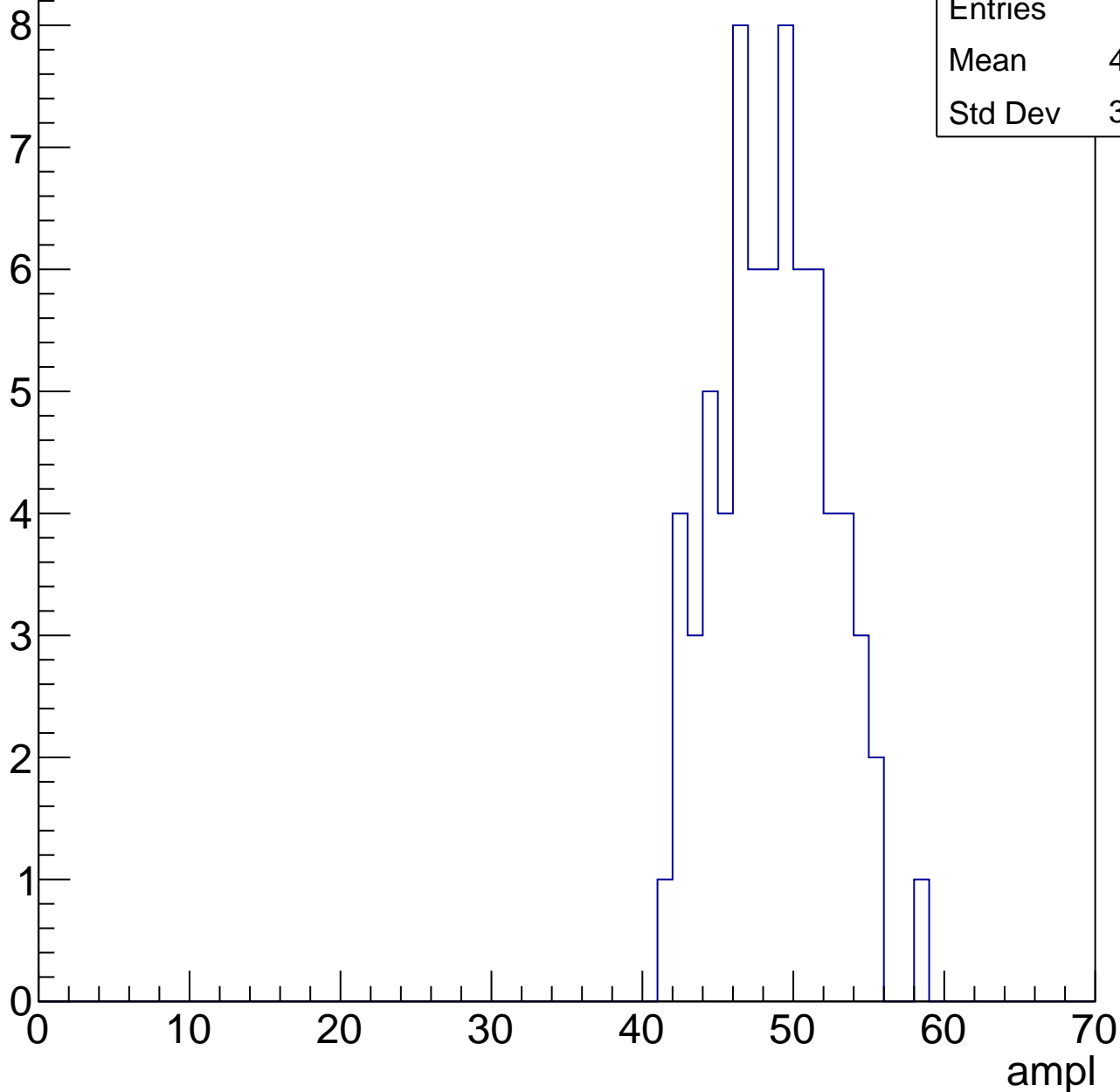


B1L103S, U10-ch121, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

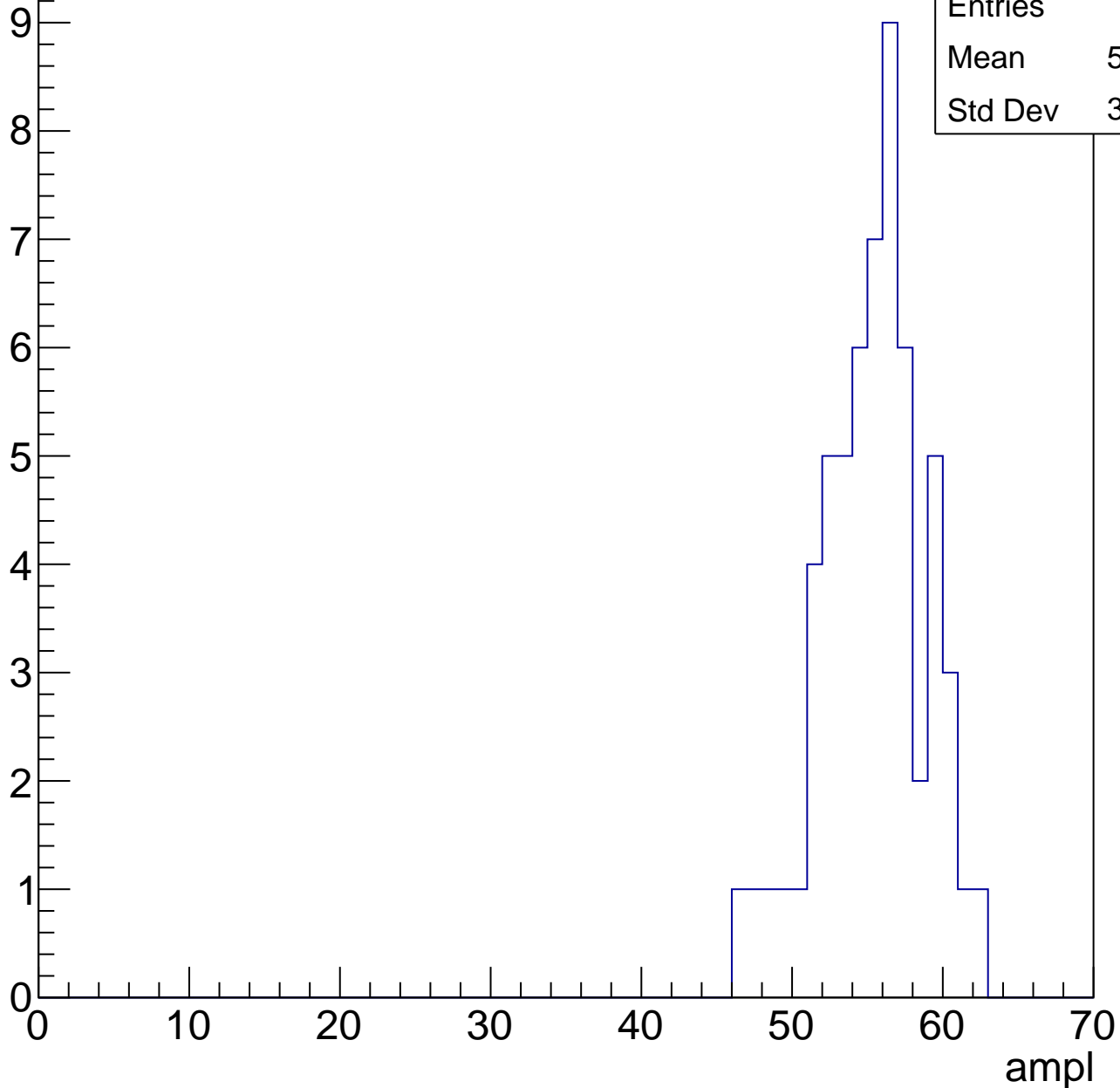
Entries	71
Mean	48.23
Std Dev	3.705



B1L103S, U10-ch121, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

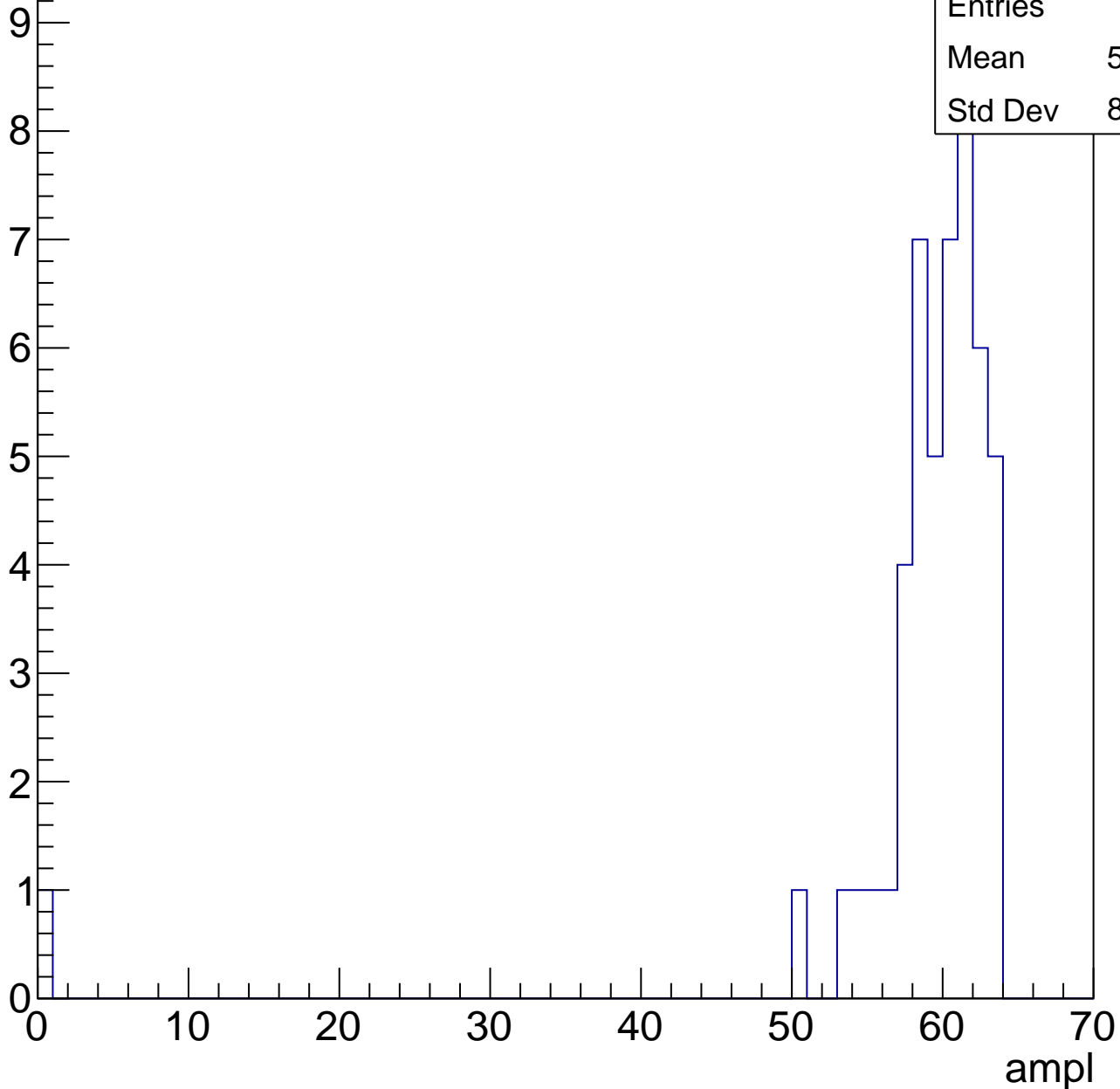


B1L103S, U10-ch121, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	58.22
Std Dev	8.828



B1L103S, U10-ch121, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch121, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch122, adc0

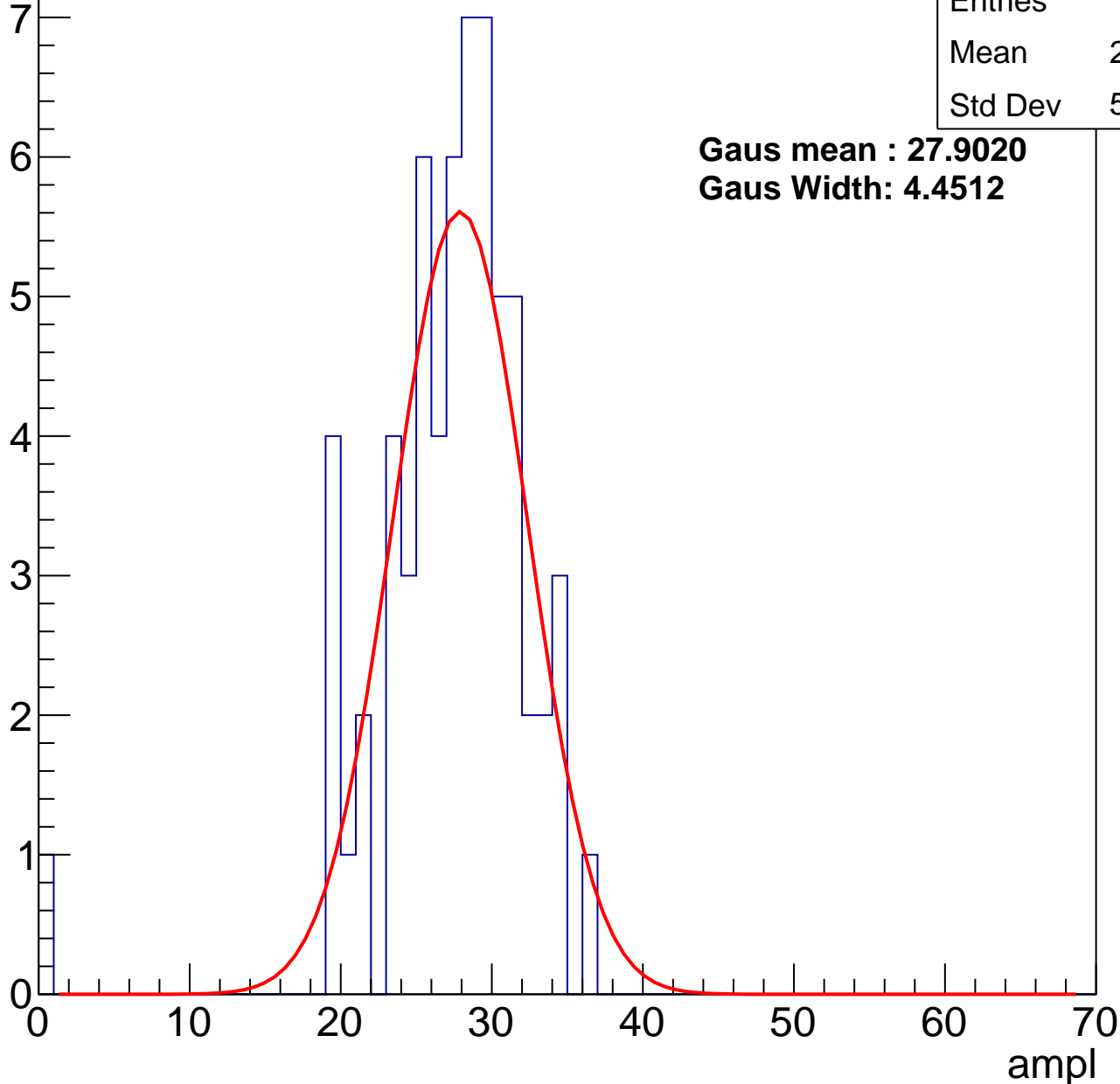
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	63
Mean	26.83
Std Dev	5.245

Gaus mean : 27.9020

Gaus Width: 4.4512



B1L103S, U10-ch122, adc1

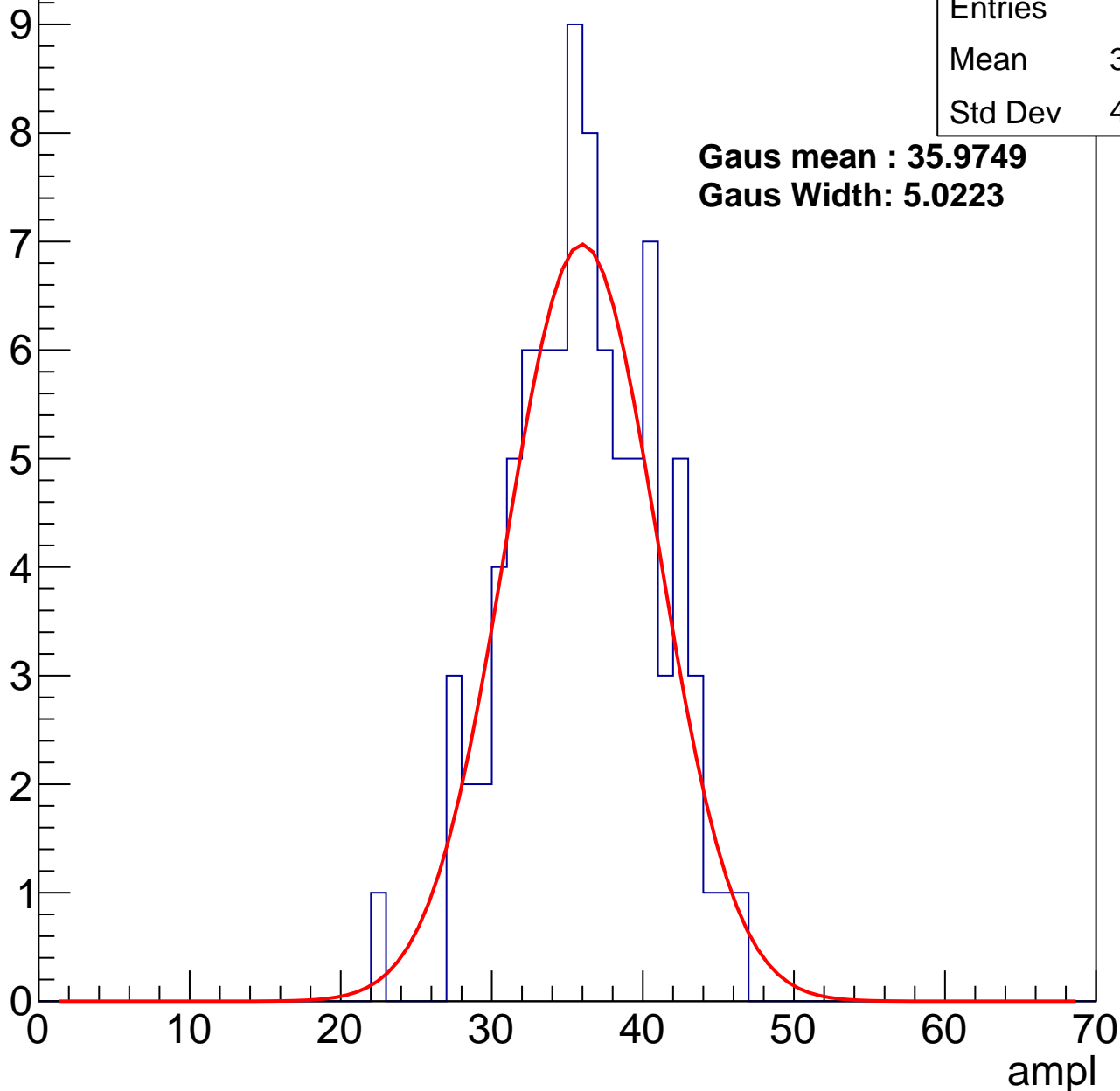
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	89
Mean	35.65
Std Dev	4.664

Gaus mean : 35.9749

Gaus Width: 5.0223



B1L103S, U10-ch122, adc2

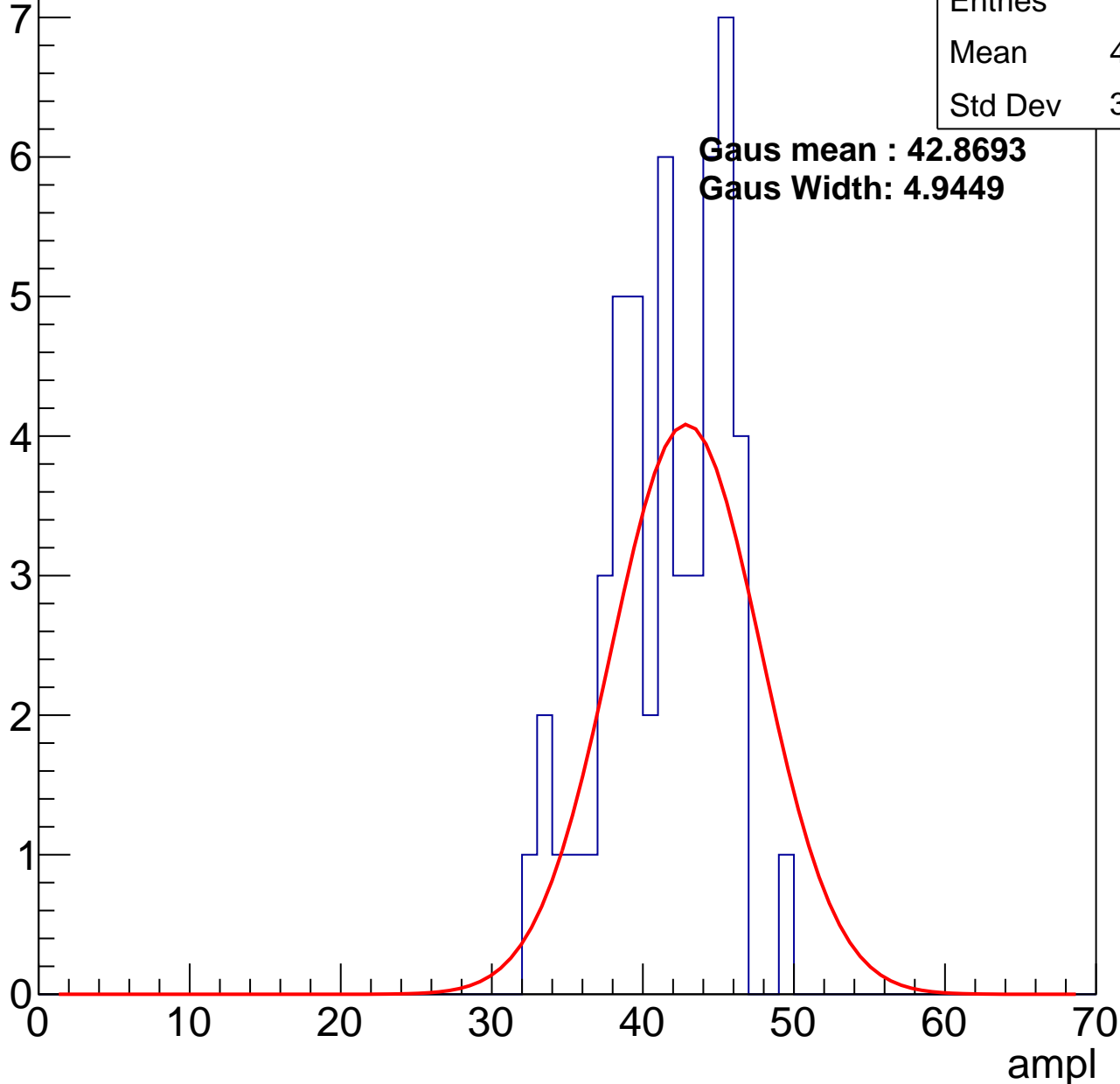
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	51
Mean	41.02
Std Dev	3.908

Gaus mean : 42.8693

Gaus Width: 4.9449

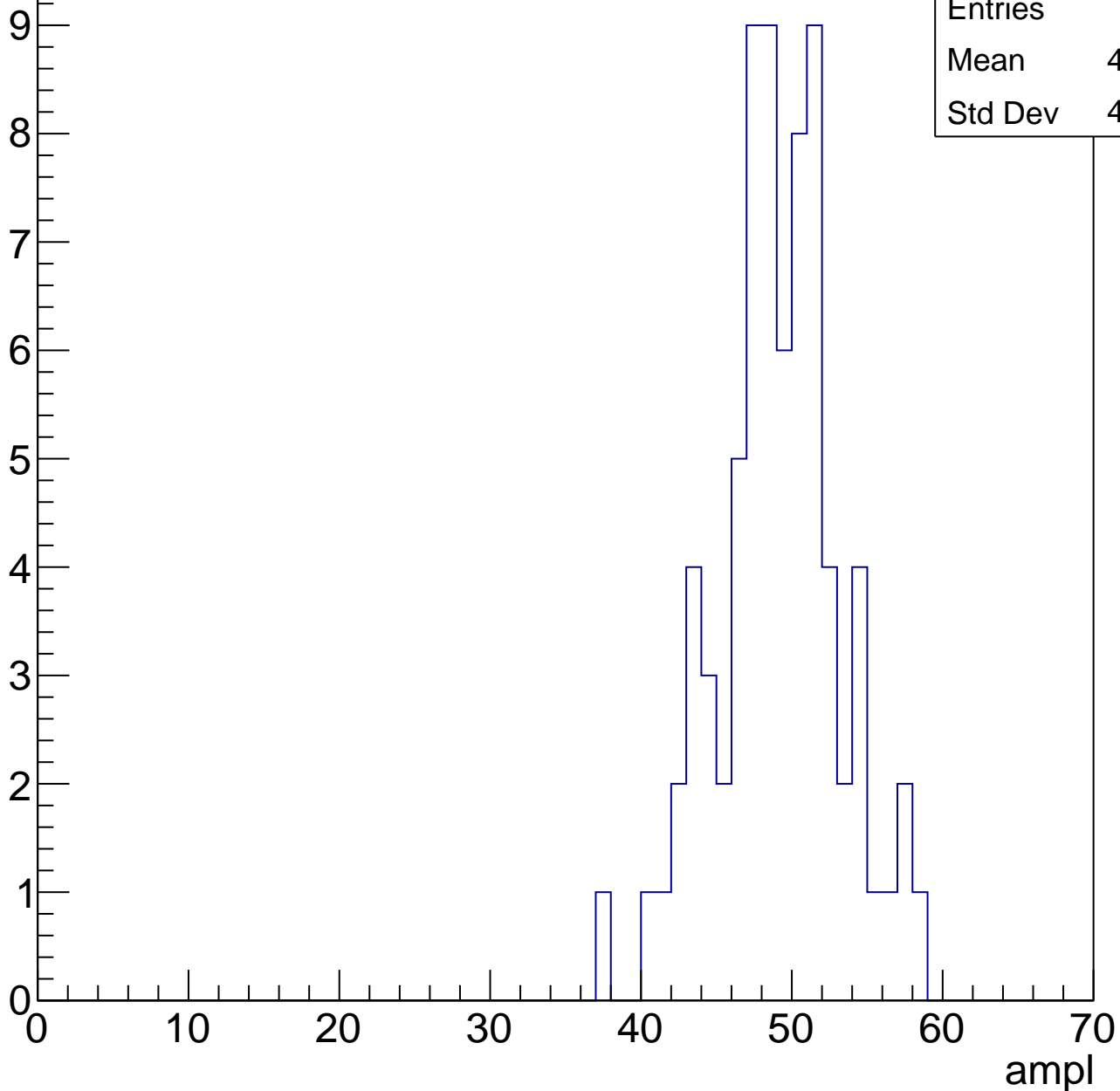


B1L103S, U10-ch122, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	48.63
Std Dev	4.039

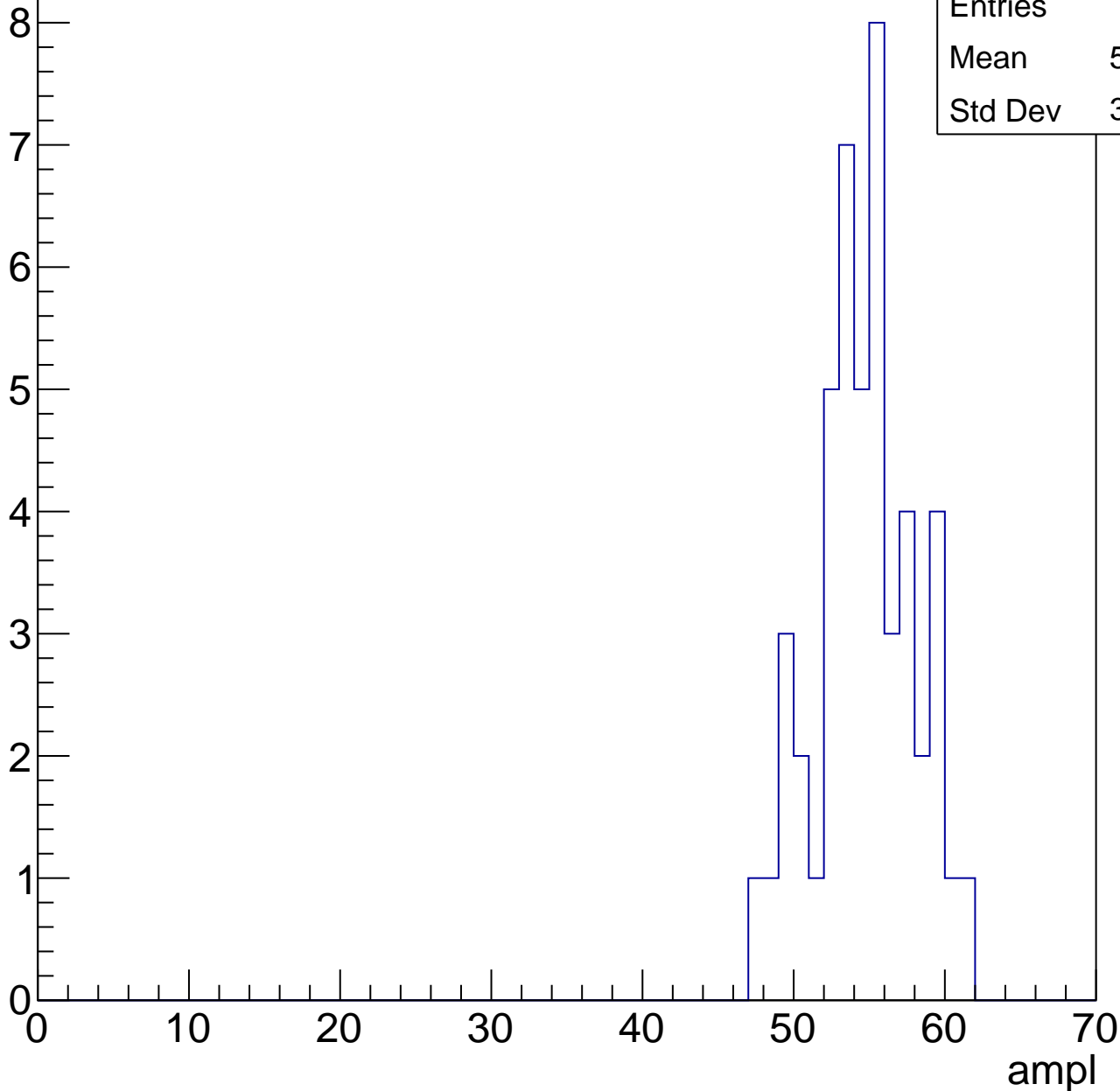


B1L103S, U10-ch122, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	48
Mean	54.23
Std Dev	3.223



B1L103S, U10-ch122, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	57.96
Std Dev	8.607

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

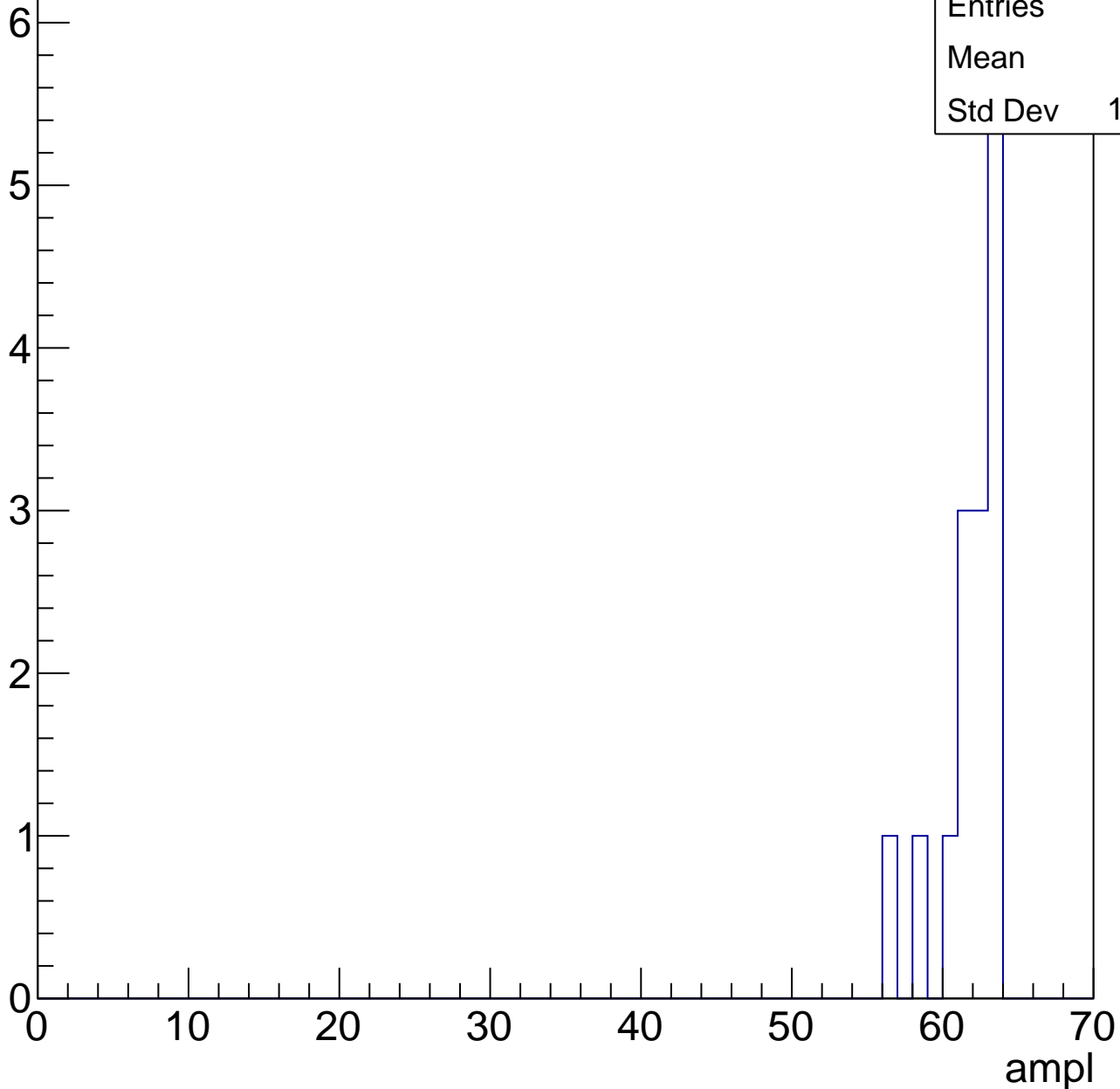
8

B1L103S, U10-ch122, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	15
Mean	61.4
Std Dev	1.993



B1L103S, U10-ch122, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	1
Mean	0
Std Dev	0

B1L103S, U10-ch123, adc0

calib_packv5_042523_0143.root, FC#7, port C2

Entries	75
Mean	31.79
Std Dev	5.259

Gaus mean : 33.5193

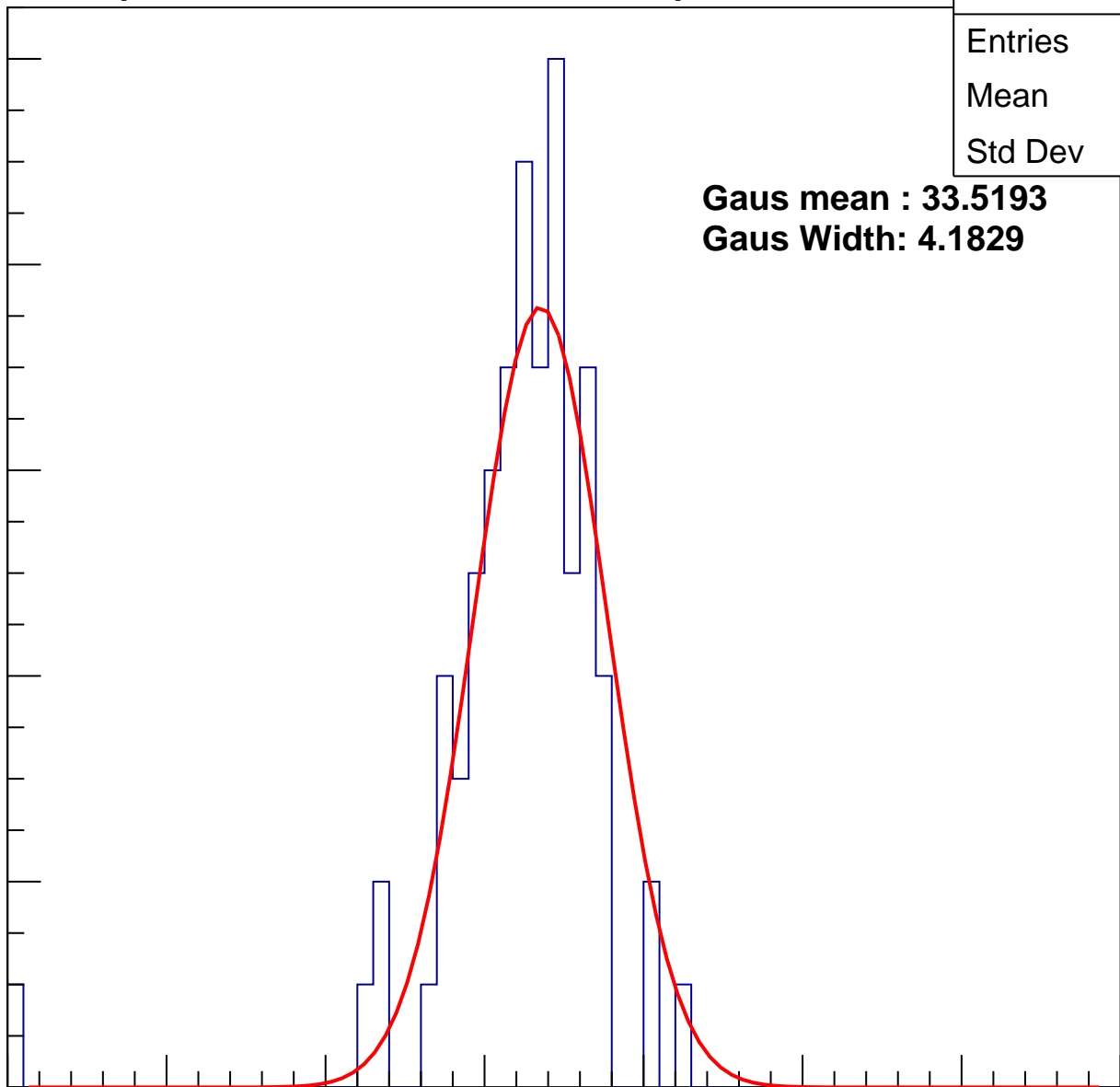
Gaus Width: 4.1829

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch123, adc1

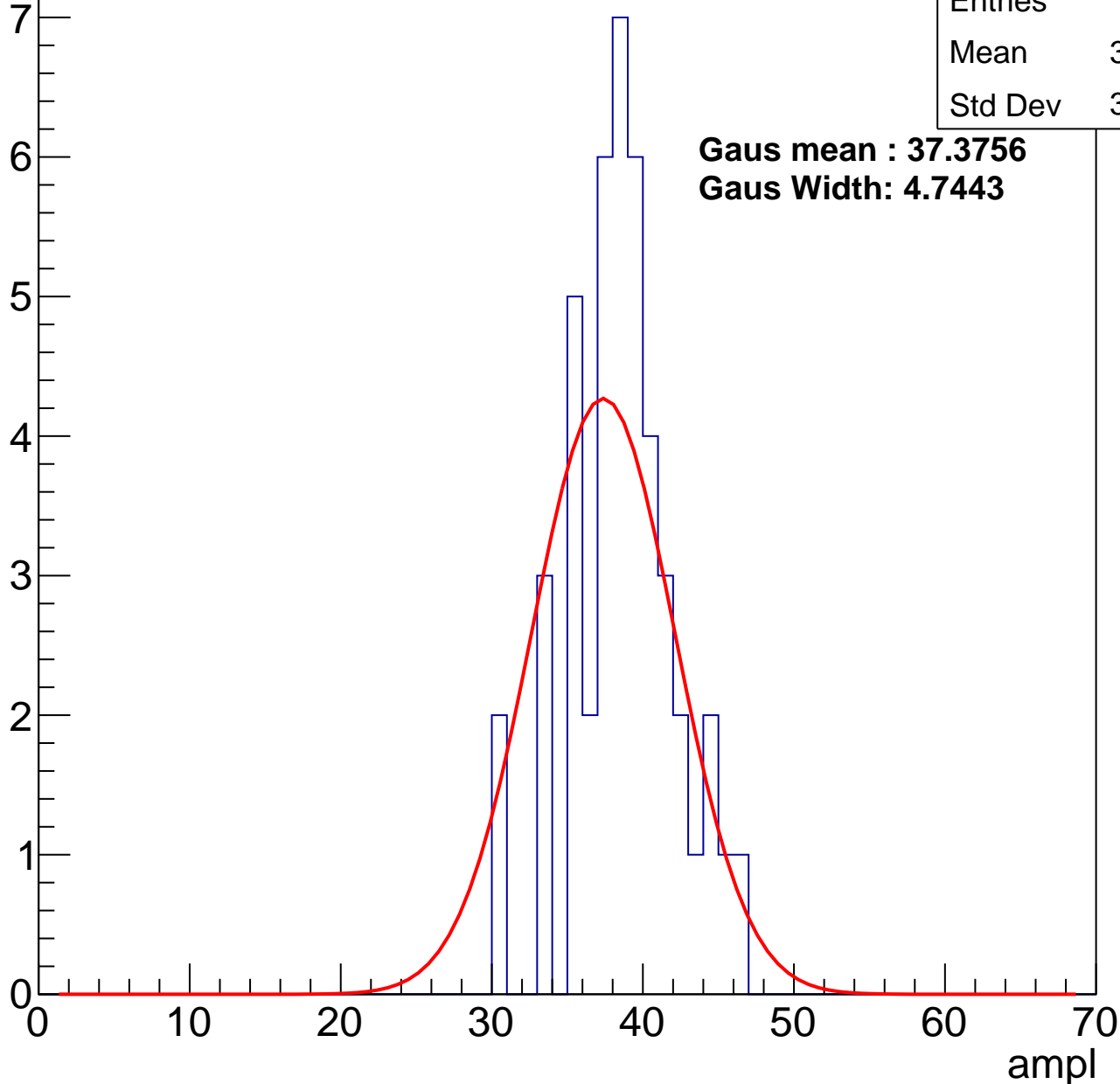
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	38.16
Std Dev	3.502

Gaus mean : 37.3756

Gaus Width: 4.7443



B1L103S, U10-ch123, adc2

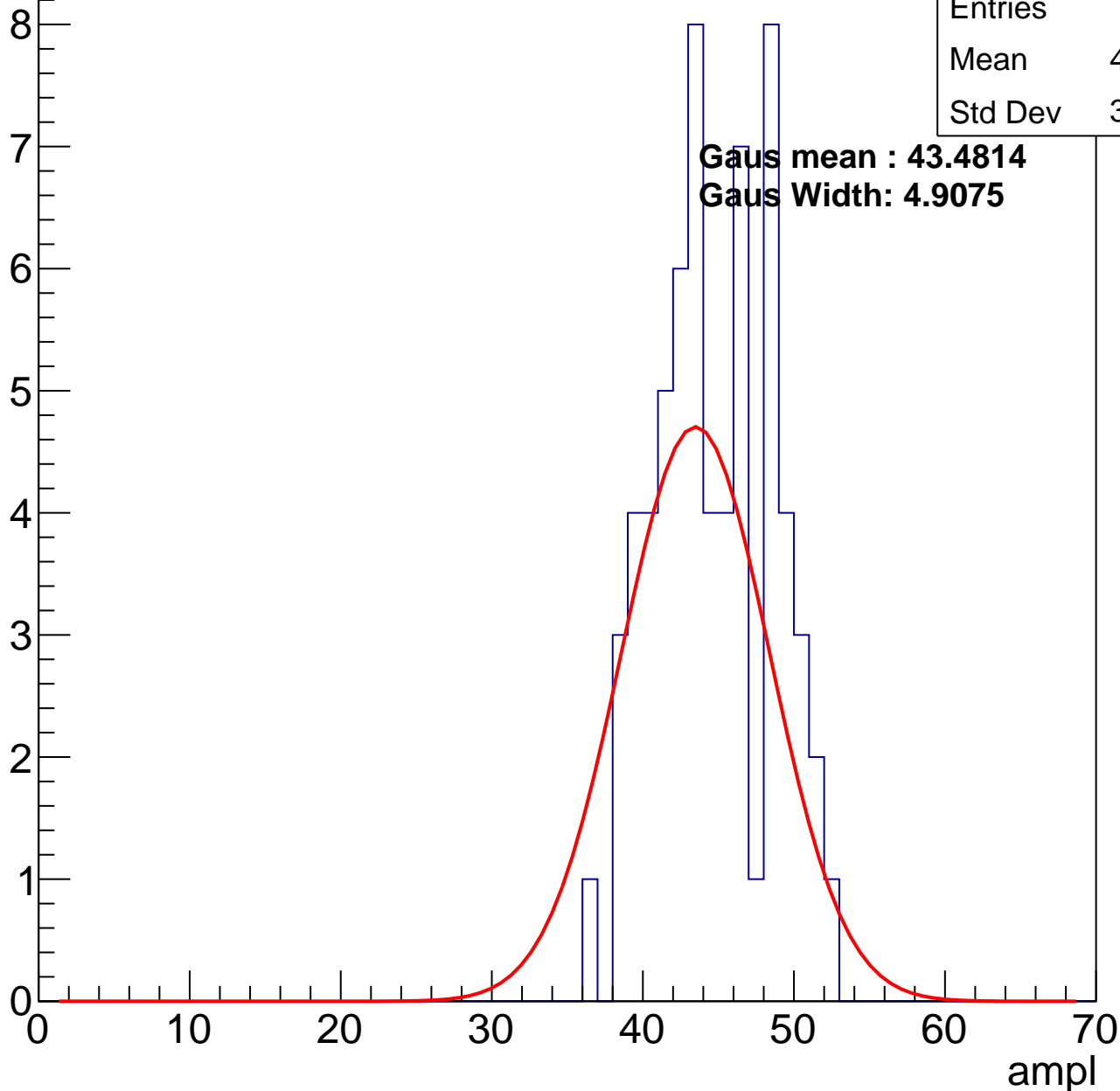
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	65
Mean	44.25
Std Dev	3.819

Gaus mean : 43.4814

Gaus Width: 4.9075

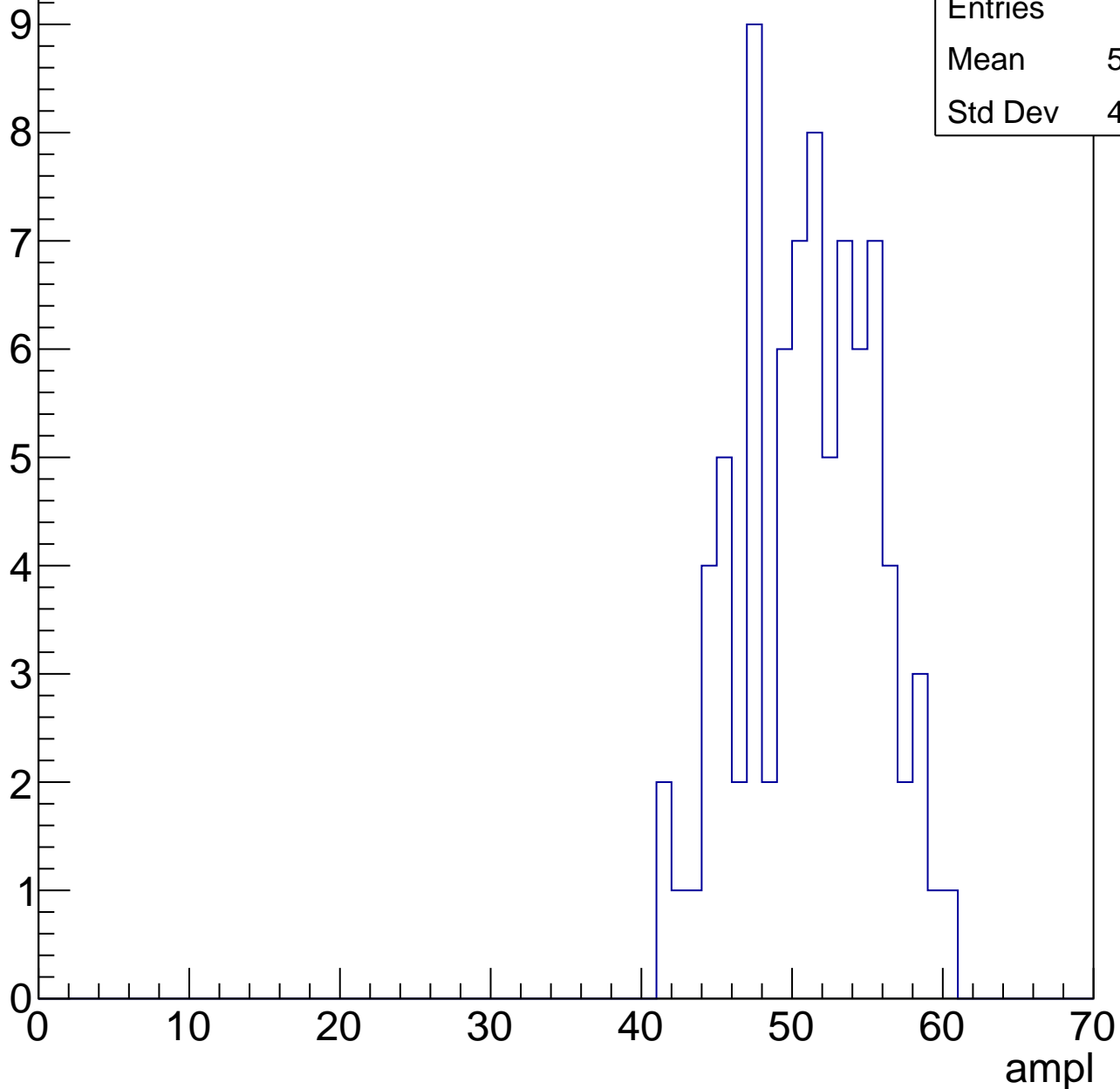


B1L103S, U10-ch123, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	83
Mean	50.63
Std Dev	4.412

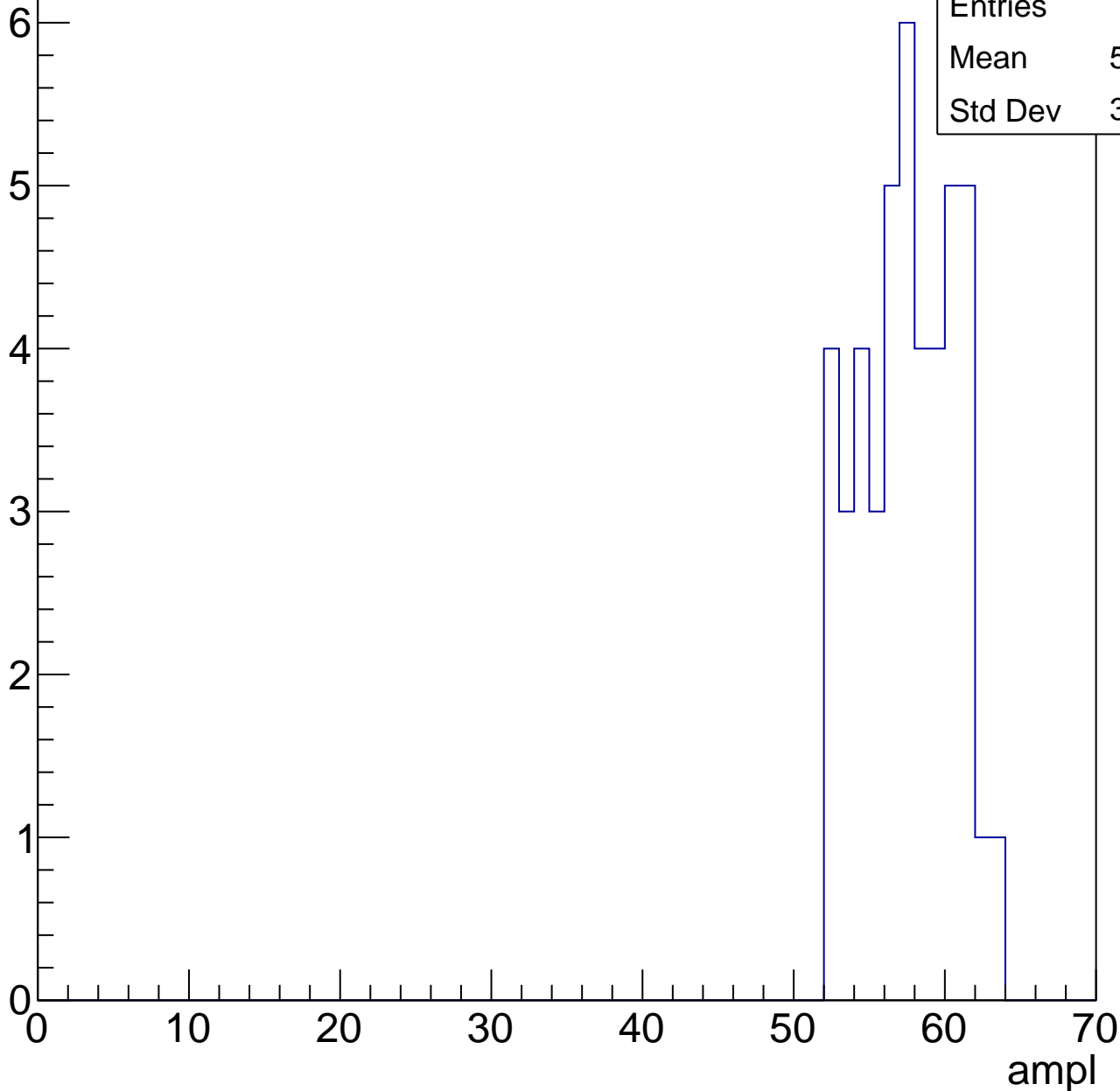


B1L103S, U10-ch123, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	45
Mean	57.07
Std Dev	3.007

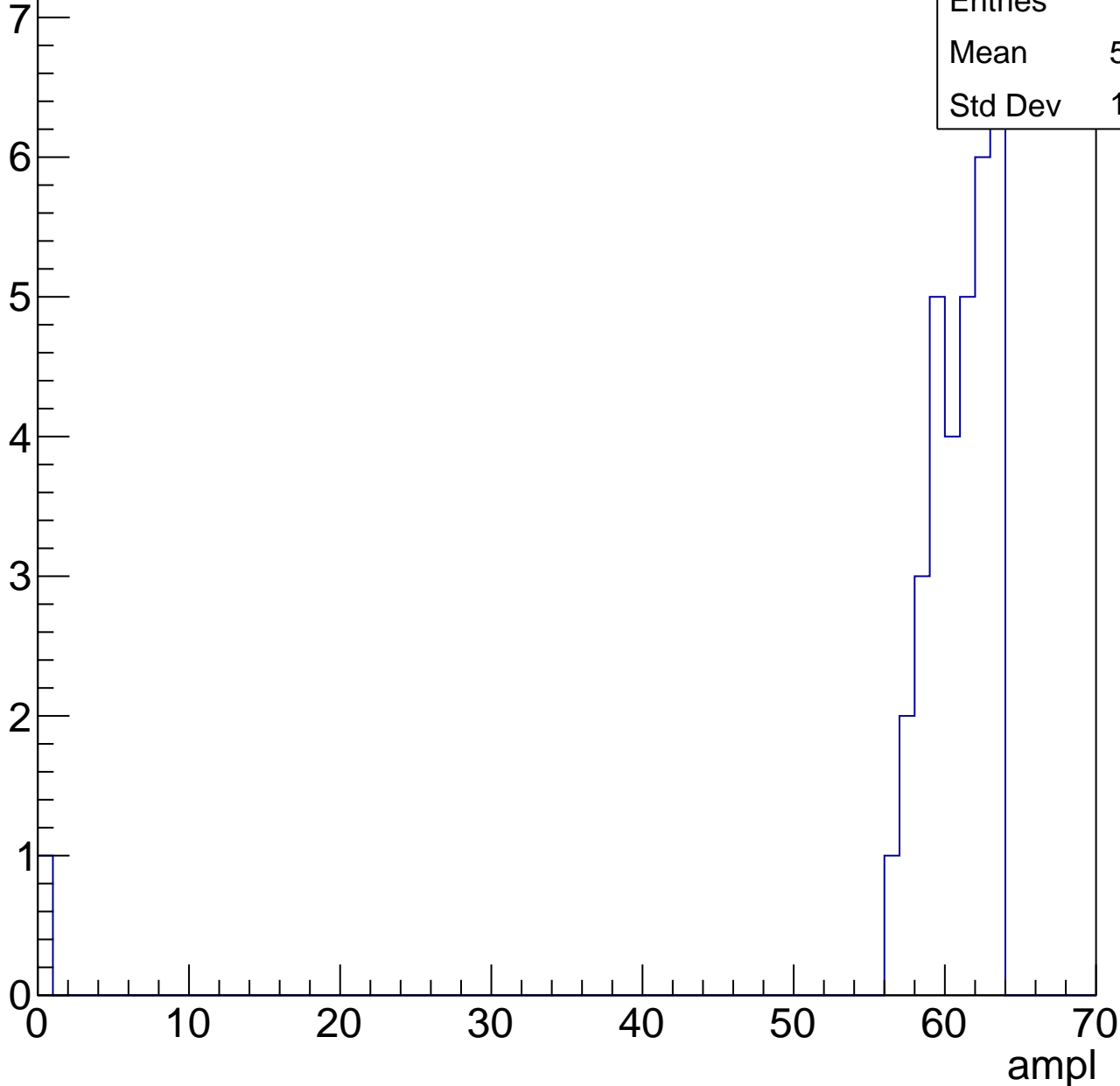


B1L103S, U10-ch123, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

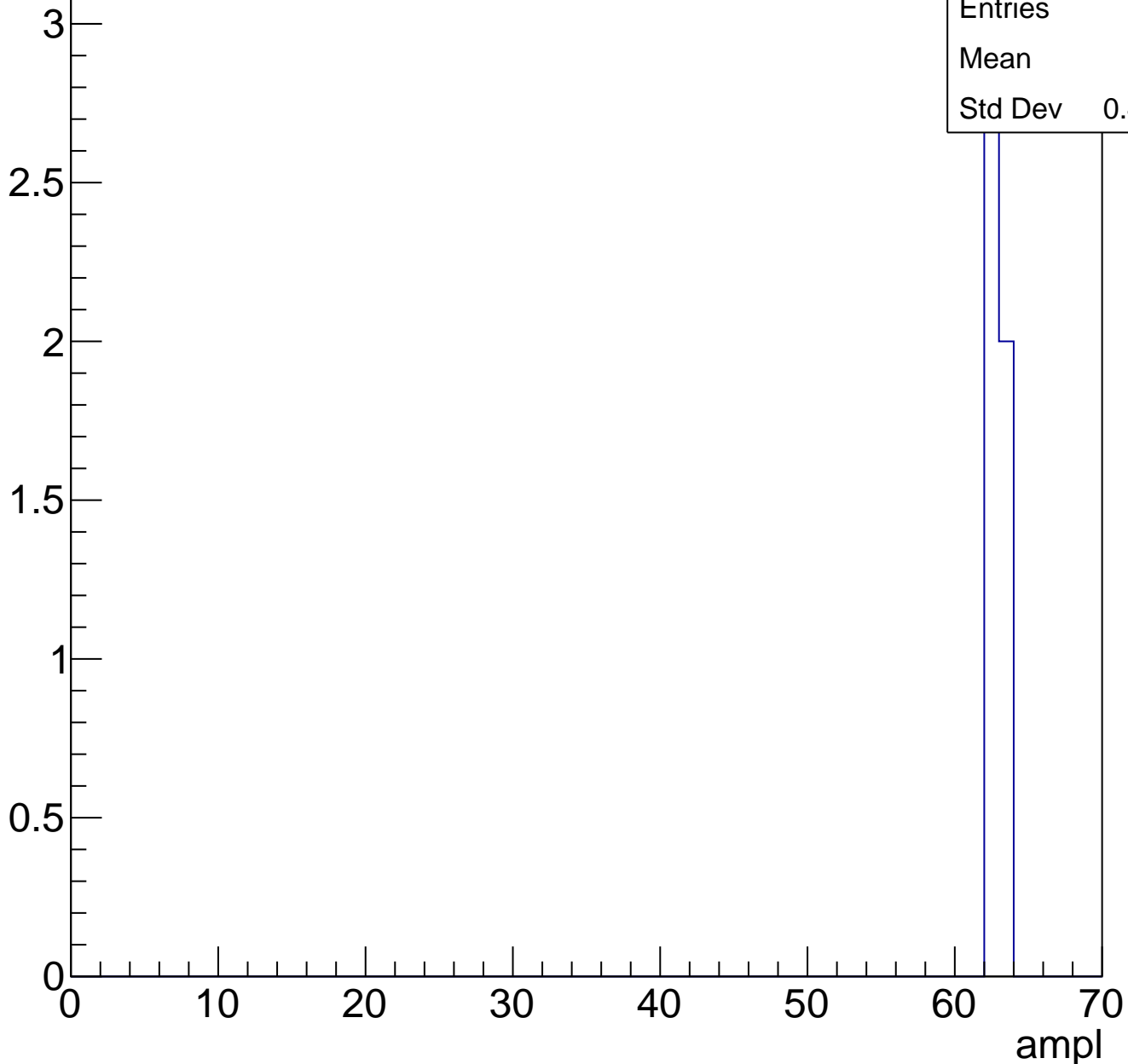
Entries	34
Mean	58.74
Std Dev	10.42



B1L103S, U10-ch123, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	5
Mean	62.4
Std Dev	0.4899

B1L103S, U10-ch123, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch124, adc0

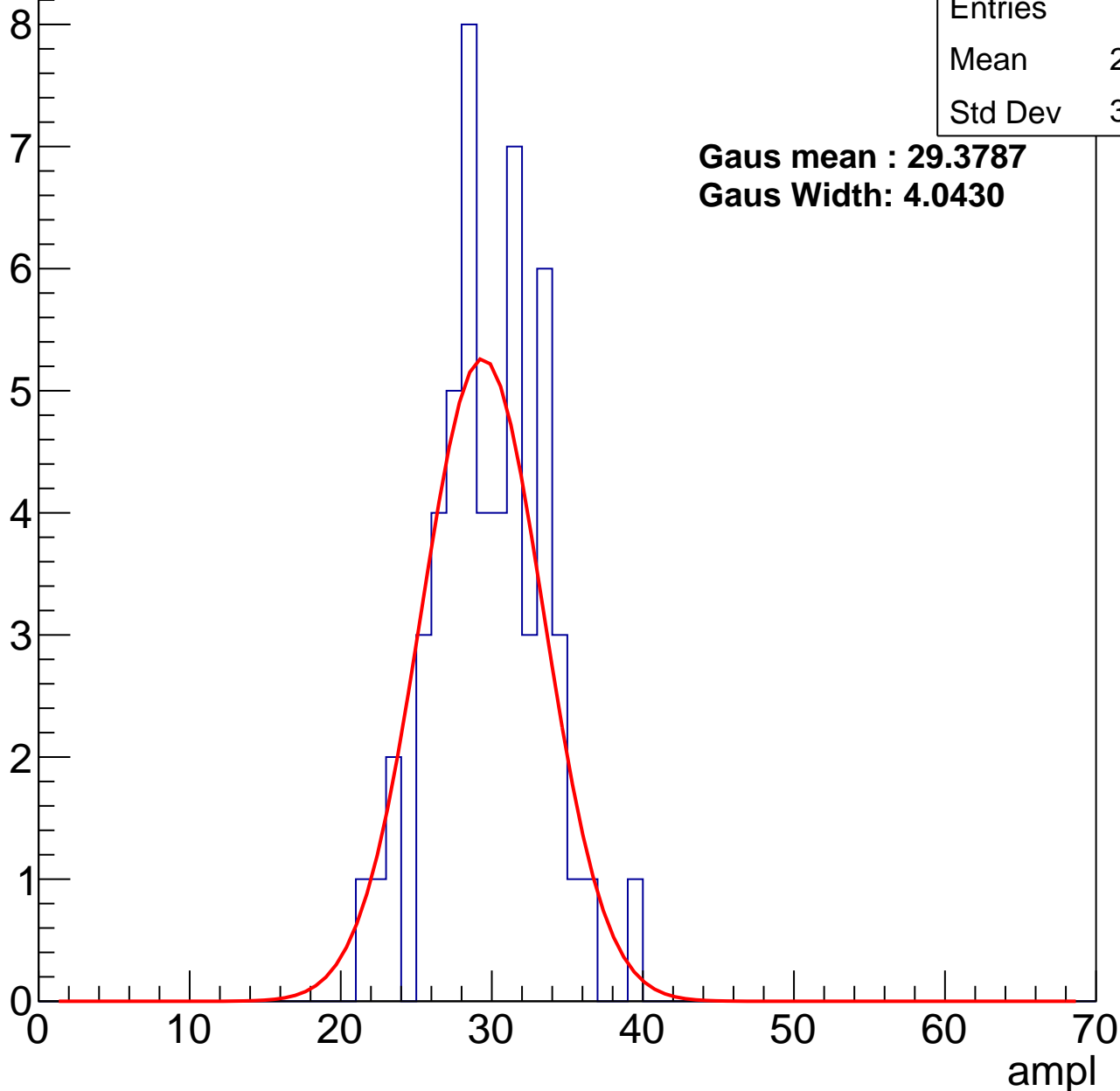
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	54
Mean	29.37
Std Dev	3.612

Gaus mean : 29.3787

Gaus Width: 4.0430



B1L103S, U10-ch124, adc1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	78
Mean	36.29
Std Dev	3.718

Gaus mean : 36.8703

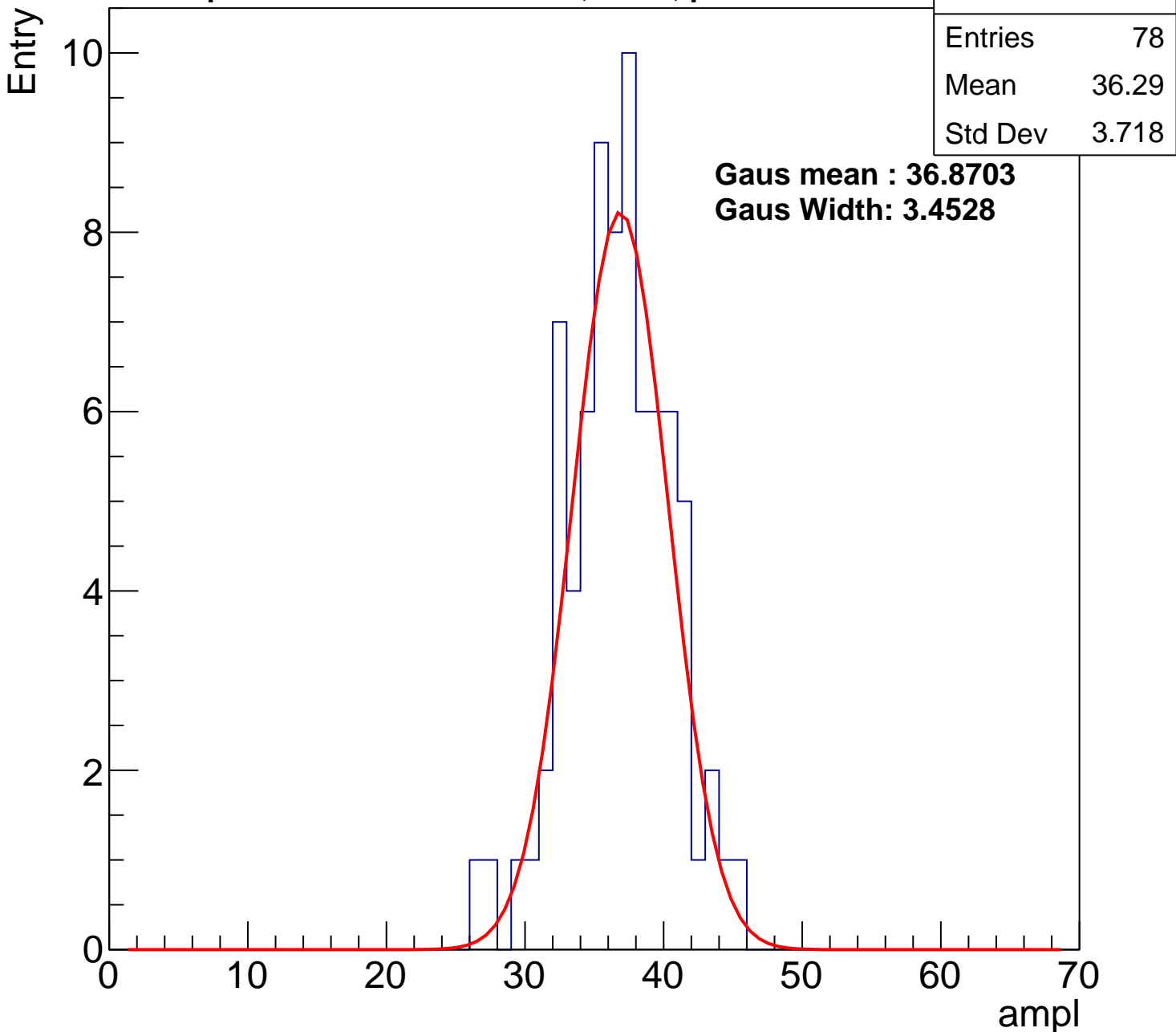
Gaus Width: 3.4528

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U10-ch124, adc2

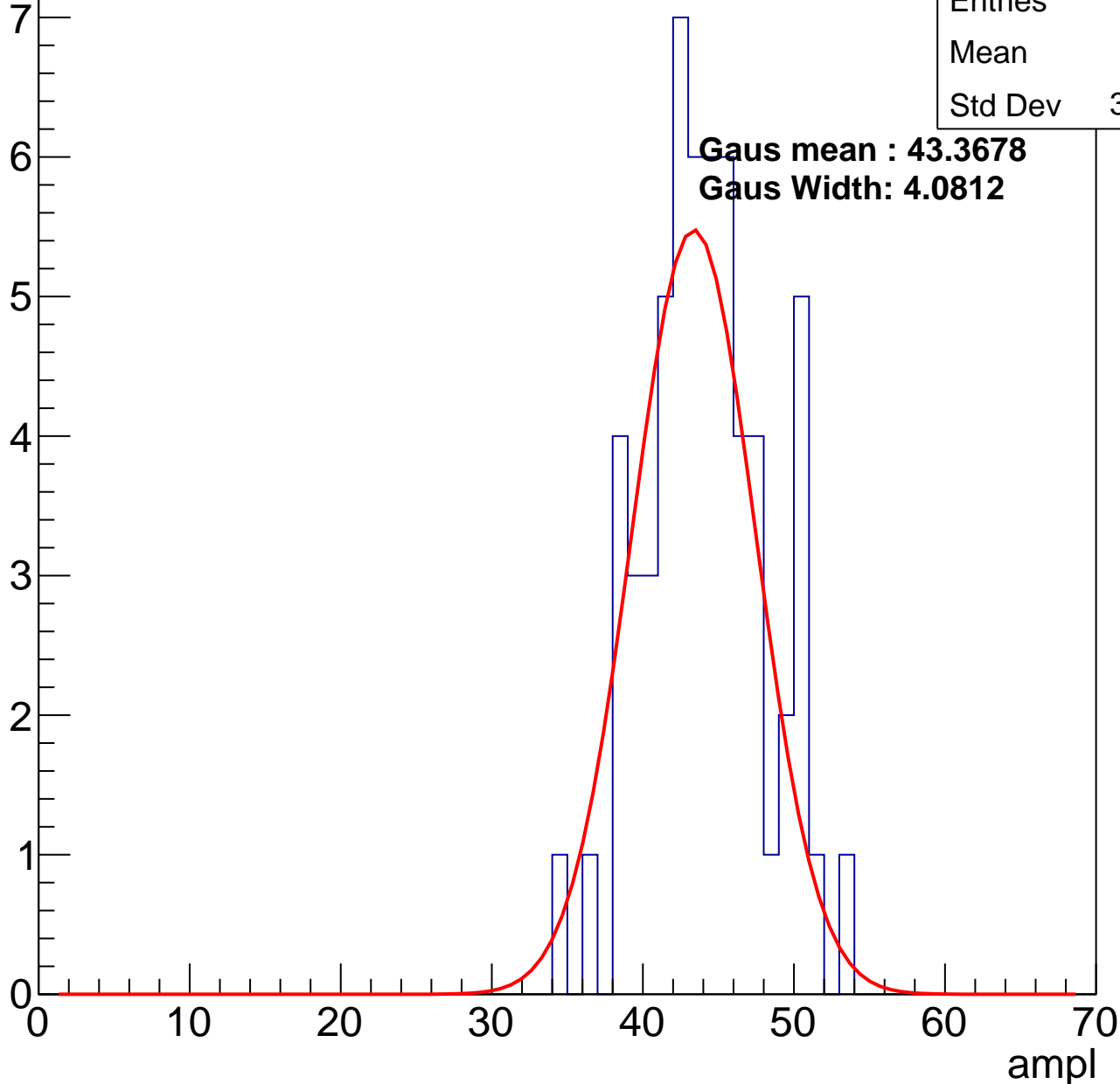
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	60
Mean	43.7
Std Dev	3.955

Gaus mean : 43.3678

Gaus Width: 4.0812

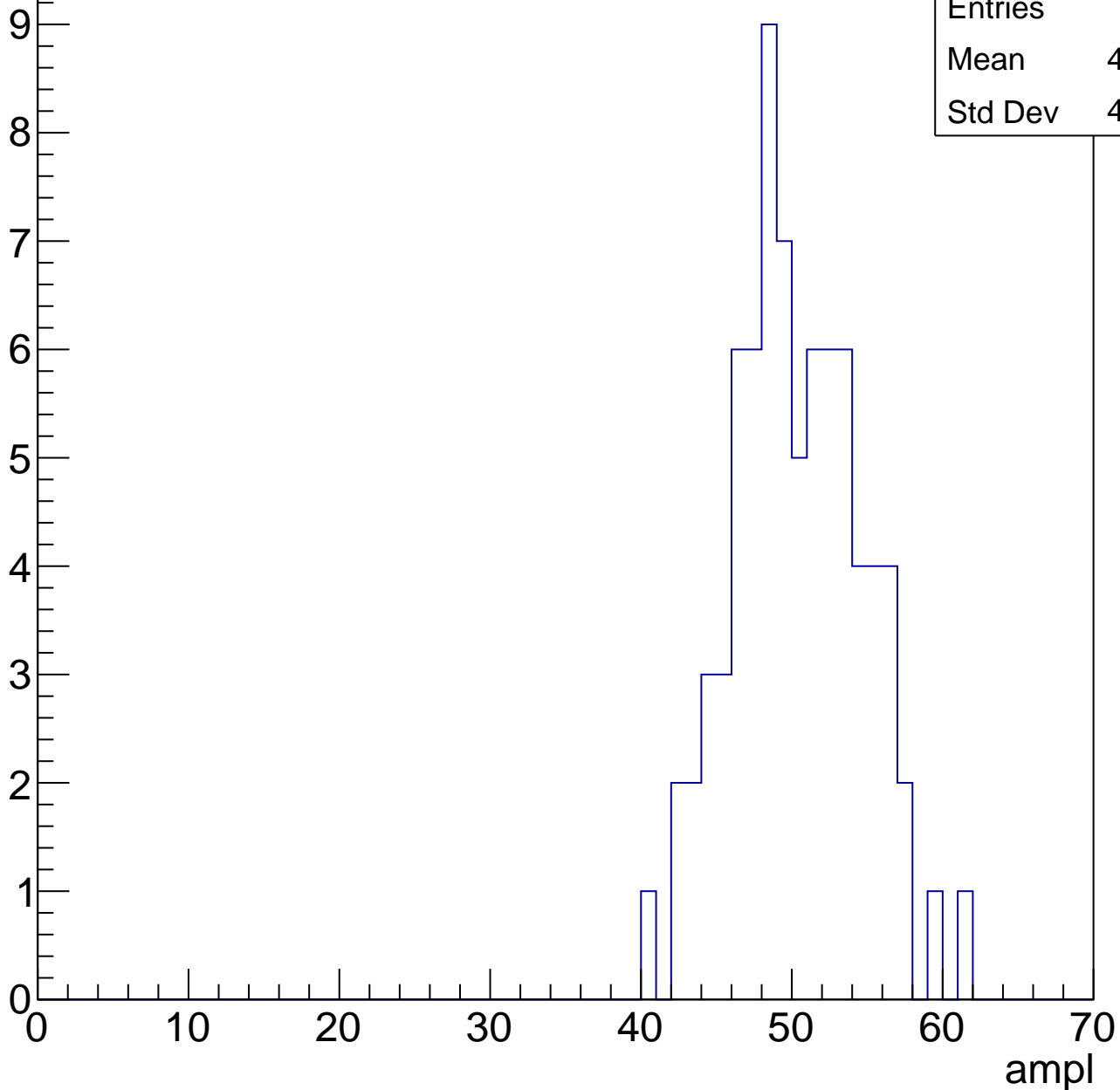


B1L103S, U10-ch124, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	78
Mean	49.87
Std Dev	4.226

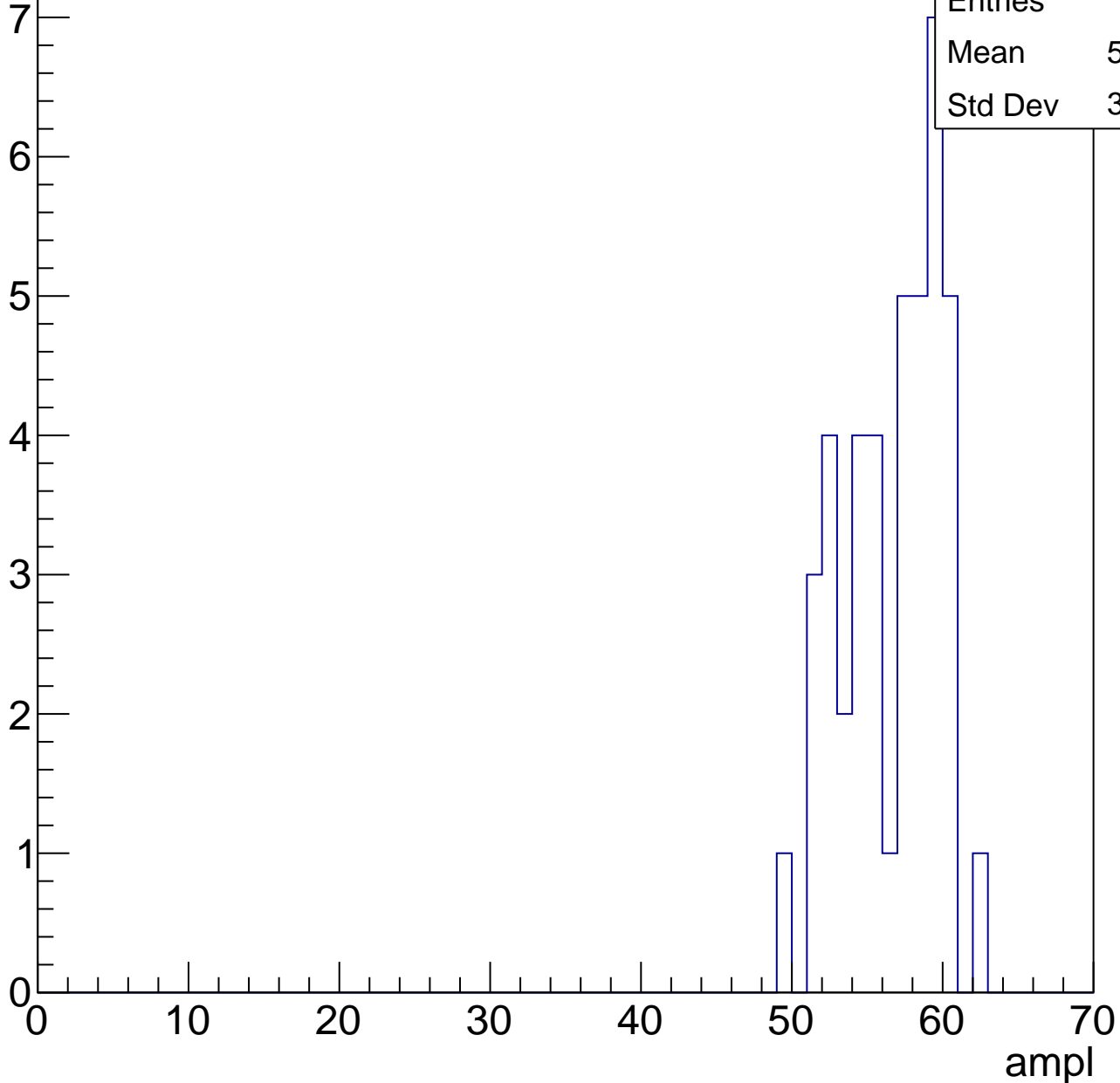


B1L103S, U10-ch124, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	42
Mean	56.14
Std Dev	3.197

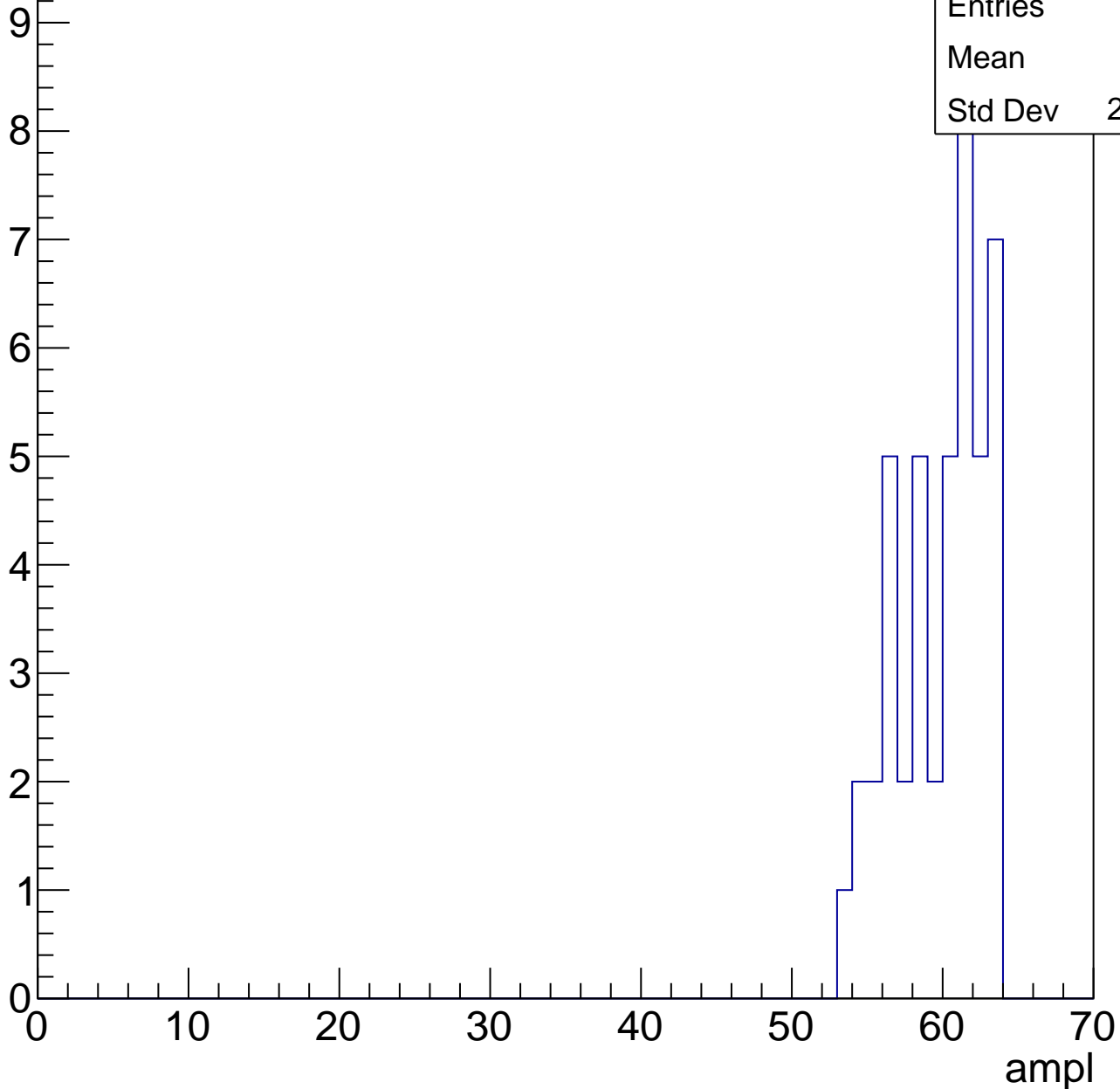


B1L103S, U10-ch124, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

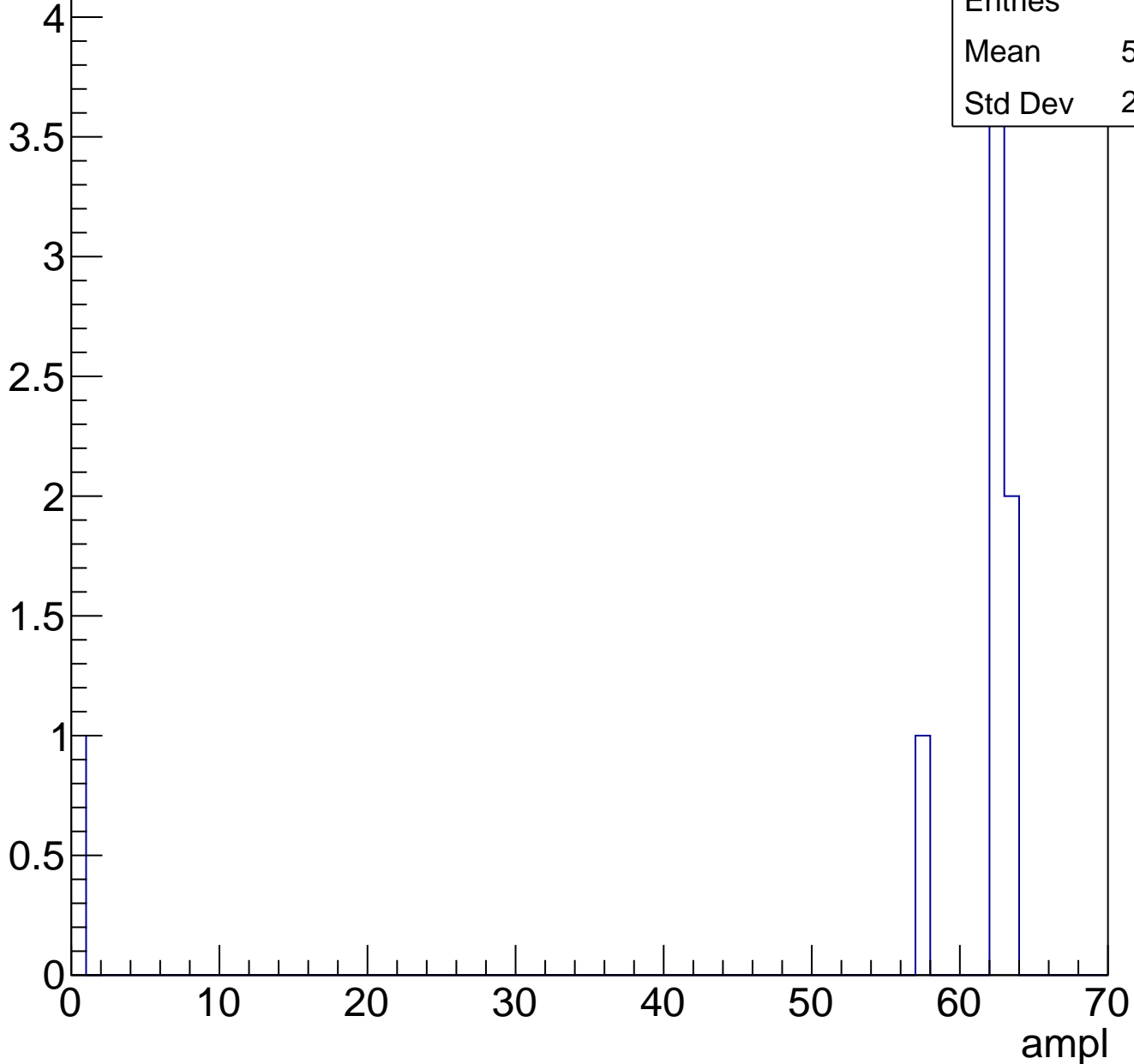
Entries	45
Mean	59.4
Std Dev	2.855



B1L103S, U10-ch124, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch124, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch125, adc0

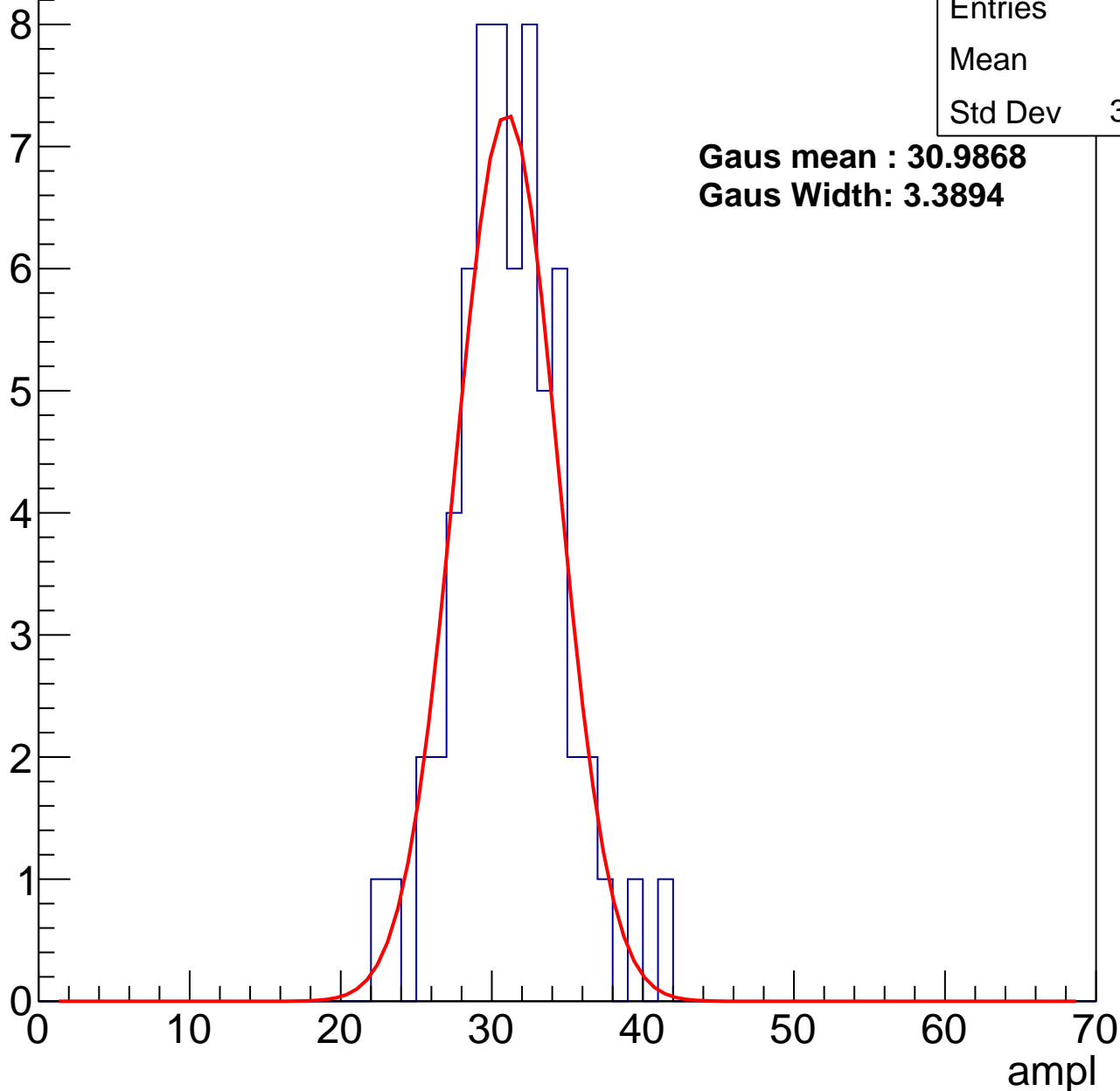
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	64
Mean	30.7
Std Dev	3.499

Gaus mean : 30.9868

Gaus Width: 3.3894



B1L103S, U10-ch125, adc1

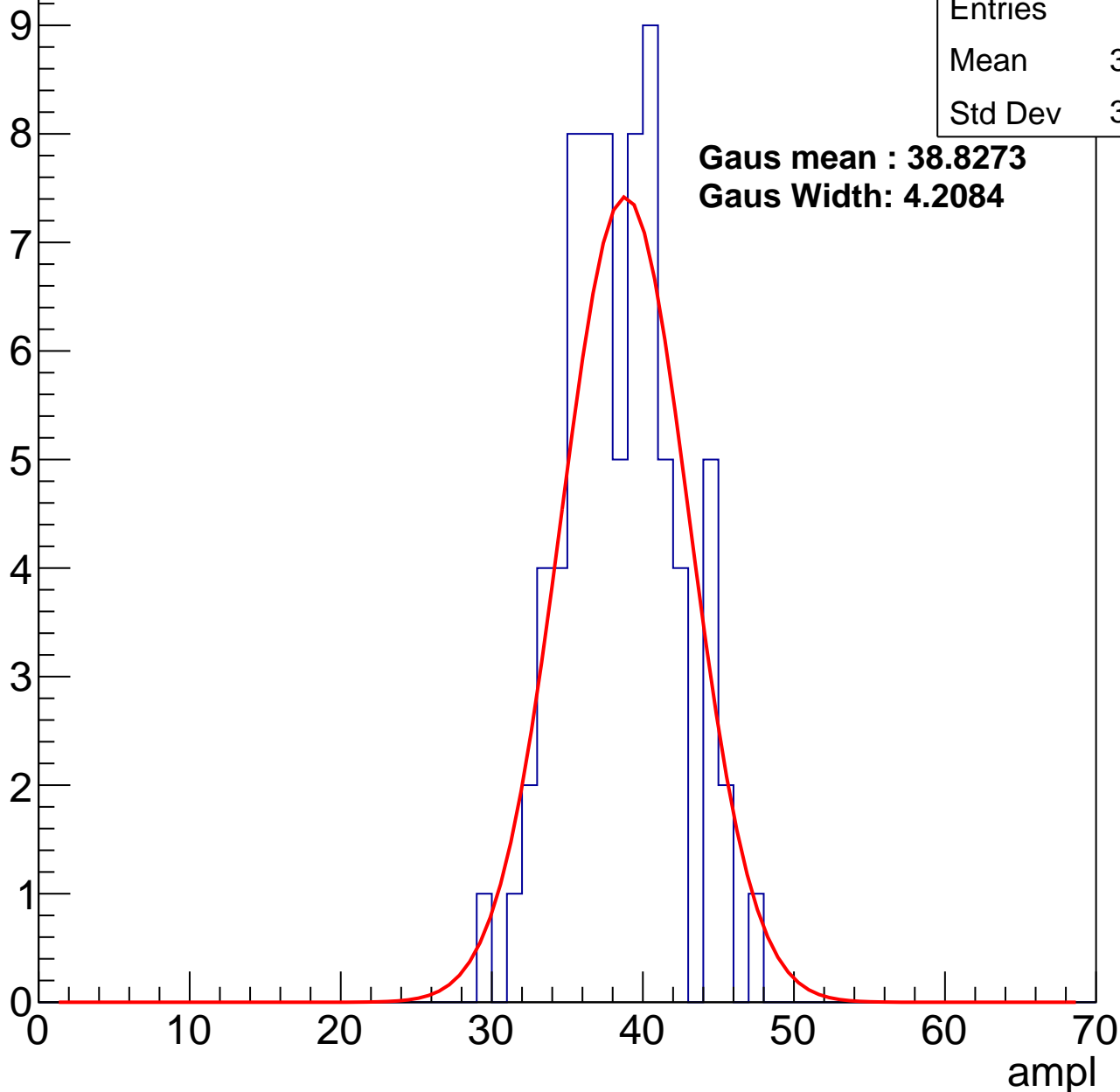
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	75
Mean	37.97
Std Dev	3.633

Gaus mean : 38.8273

Gaus Width: 4.2084



B1L103S, U10-ch125, adc2

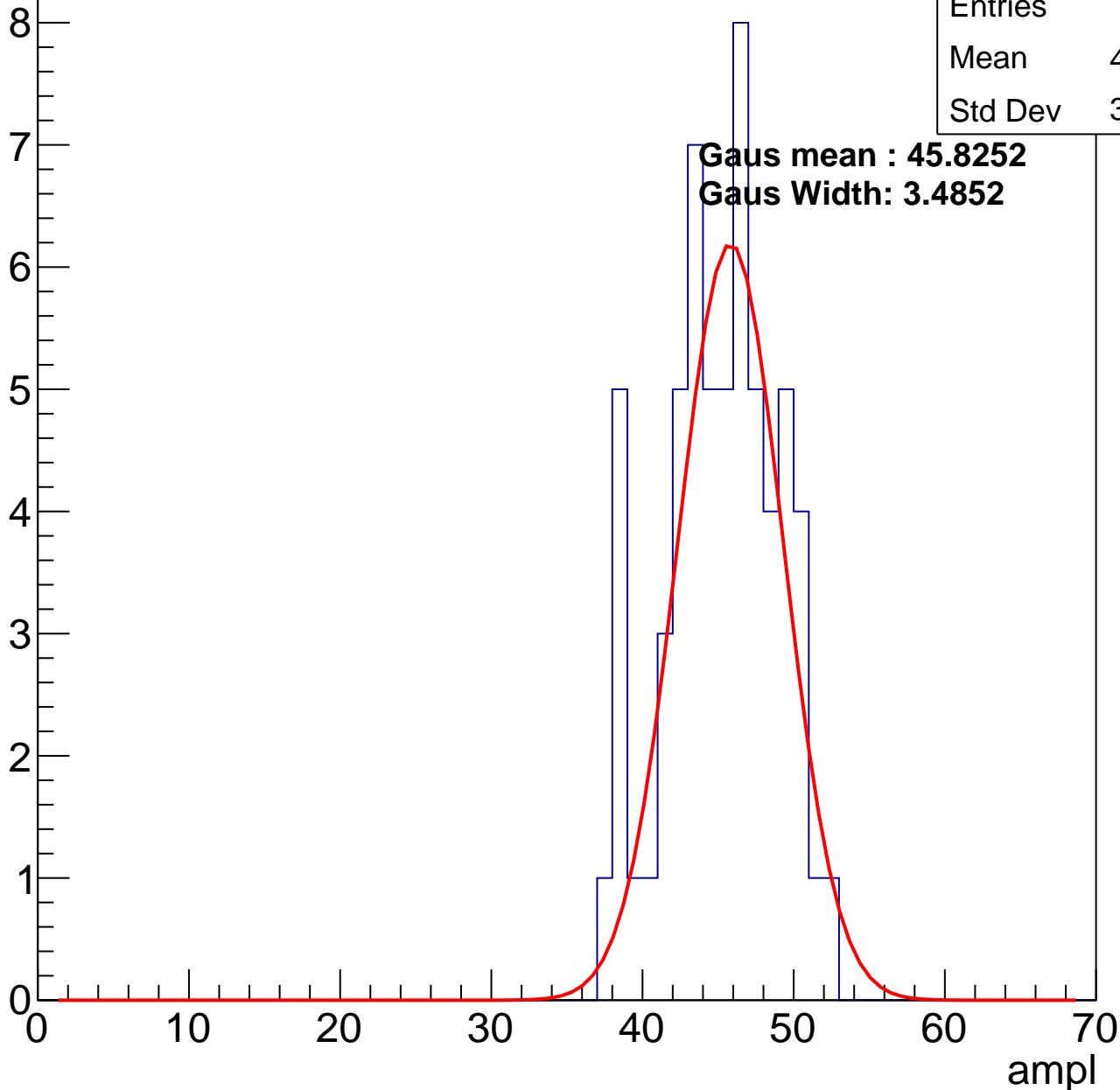
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	61
Mean	44.72
Std Dev	3.667

Gaus mean : 45.8252

Gaus Width: 3.4852

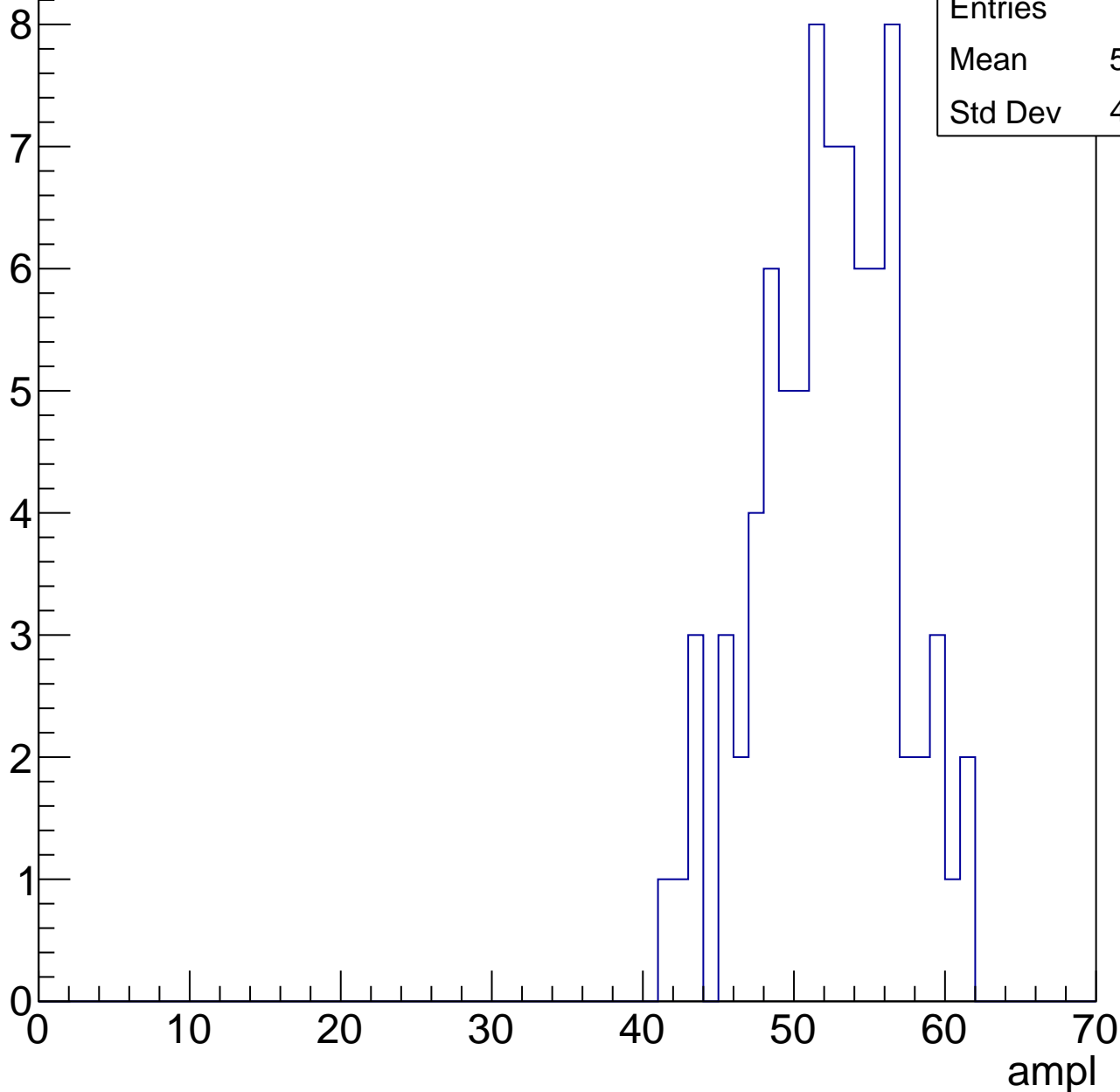


B1L103S, U10-ch125, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	82
Mean	51.76
Std Dev	4.493

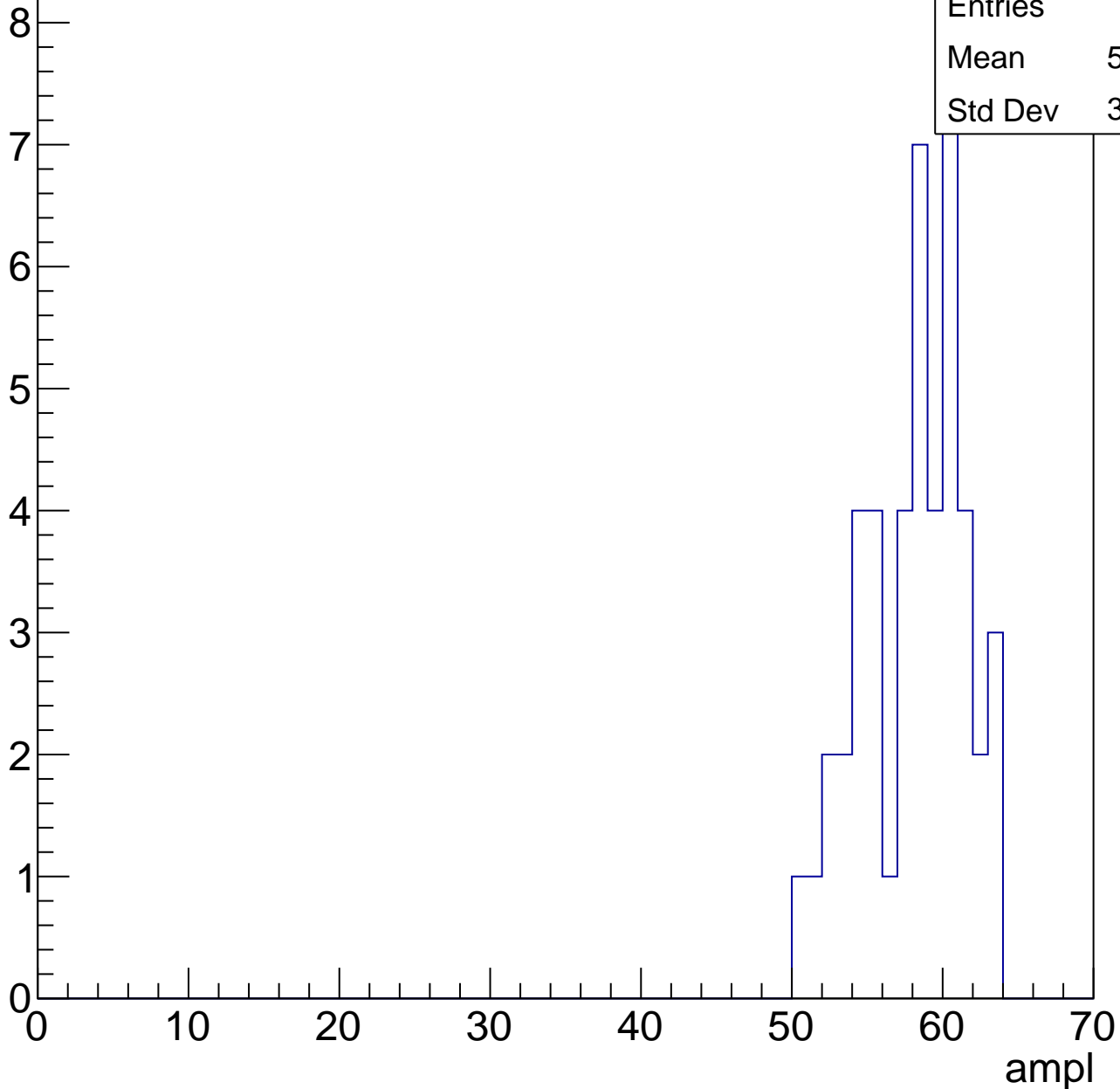


B1L103S, U10-ch125, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	47
Mean	57.66
Std Dev	3.315

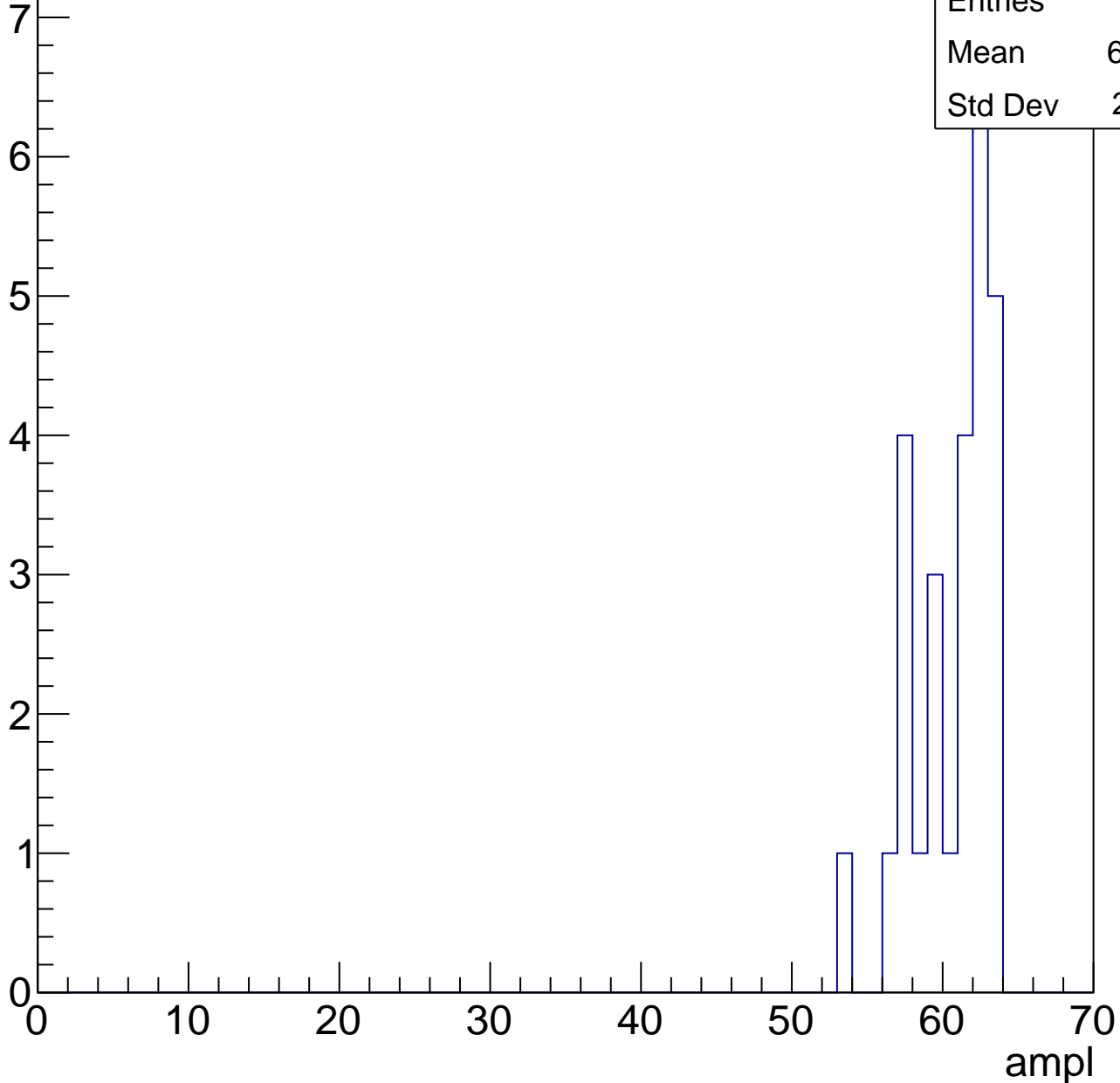


B1L103S, U10-ch125, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	27
Mean	60.19
Std Dev	2.611



B1L103S, U10-ch125, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch125, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



Entries	0
Mean	0
Std Dev	0

B1L103S, U10-ch126, adc0

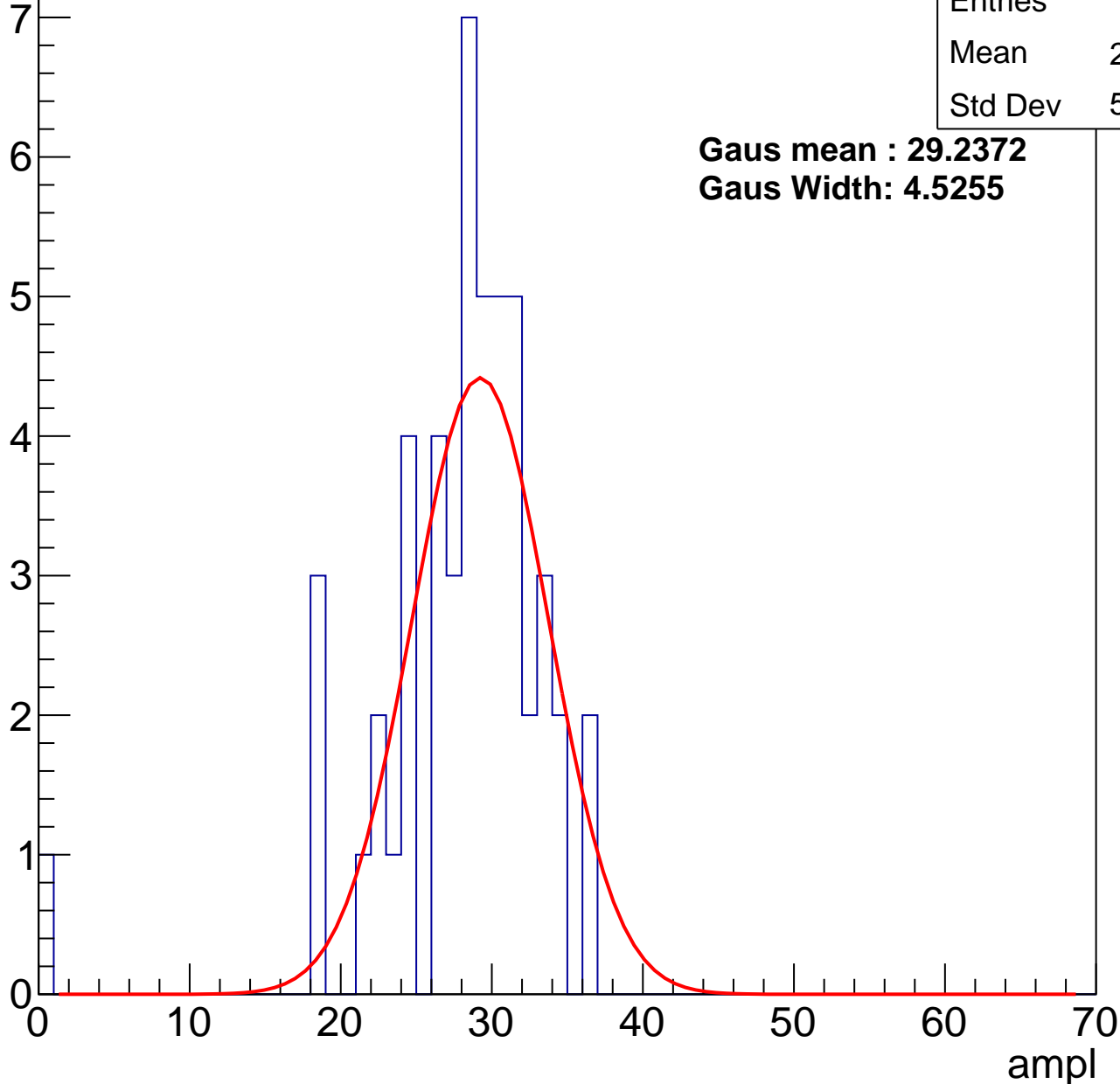
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	50
Mean	27.44
Std Dev	5.787

Gaus mean : 29.2372

Gaus Width: 4.5255



B1L103S, U10-ch126, adc1

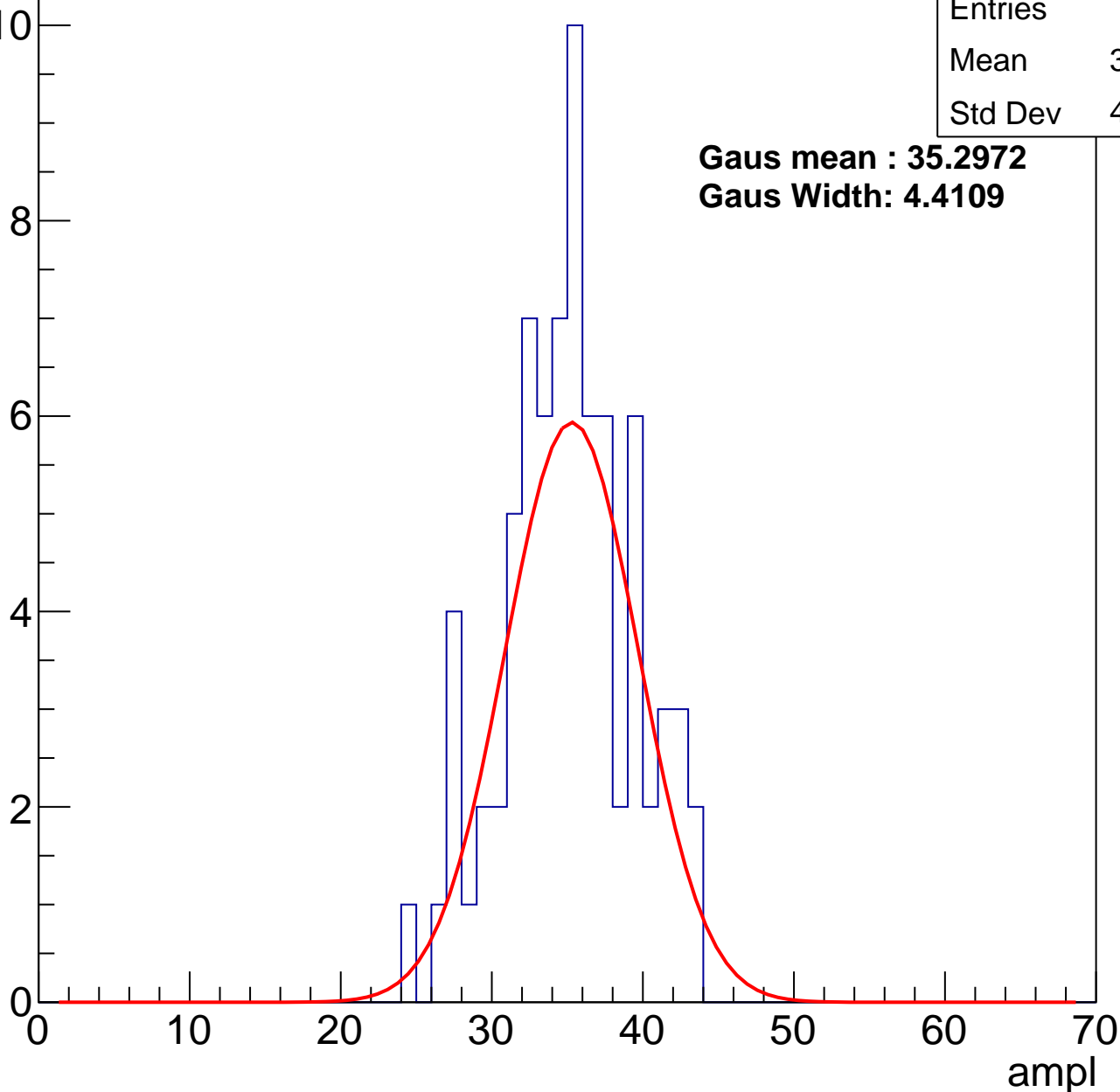
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	76
Mean	34.63
Std Dev	4.245

Gaus mean : 35.2972

Gaus Width: 4.4109

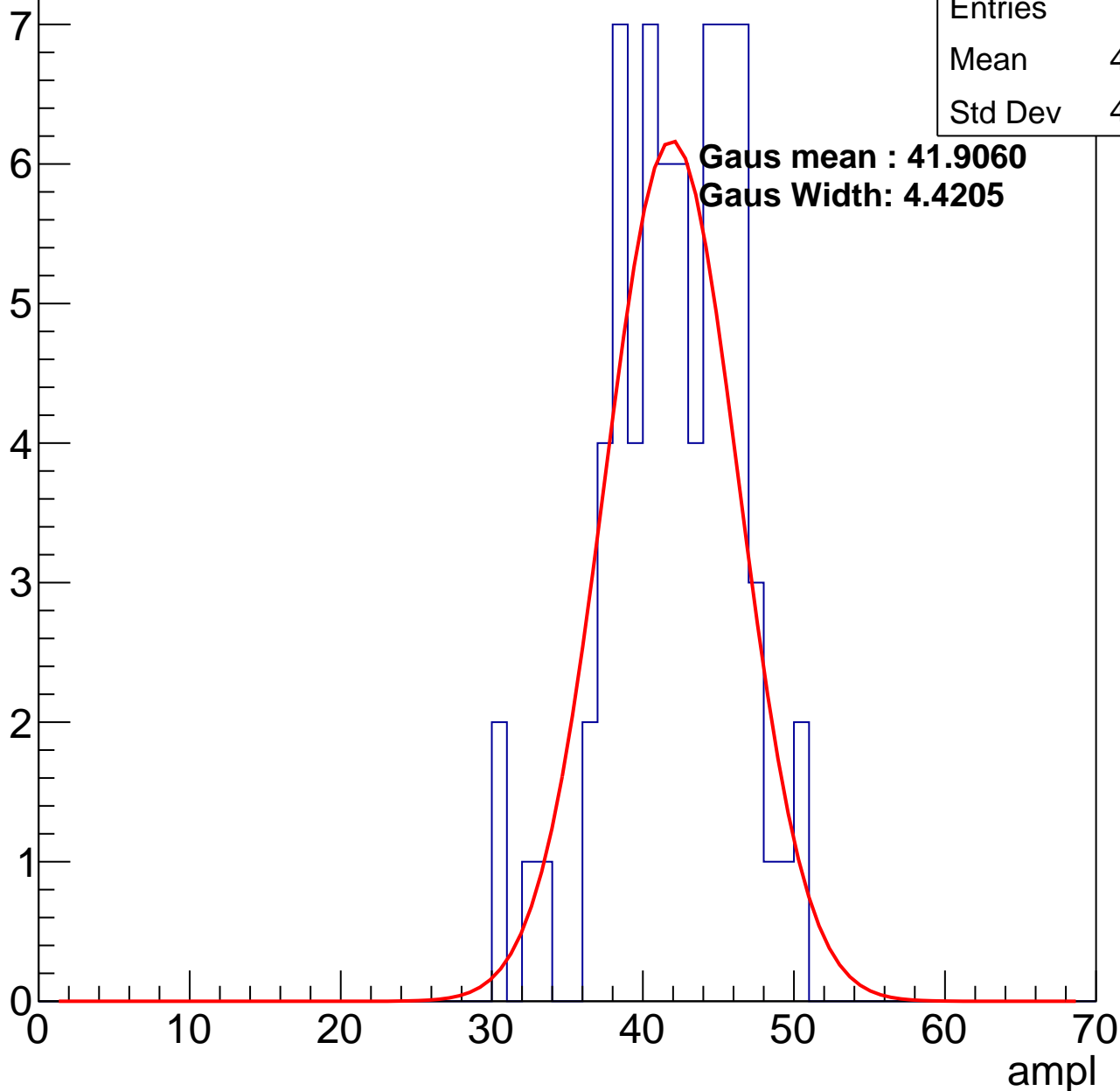


B1L103S, U10-ch126, adc2

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	72
Mean	41.67
Std Dev	4.265

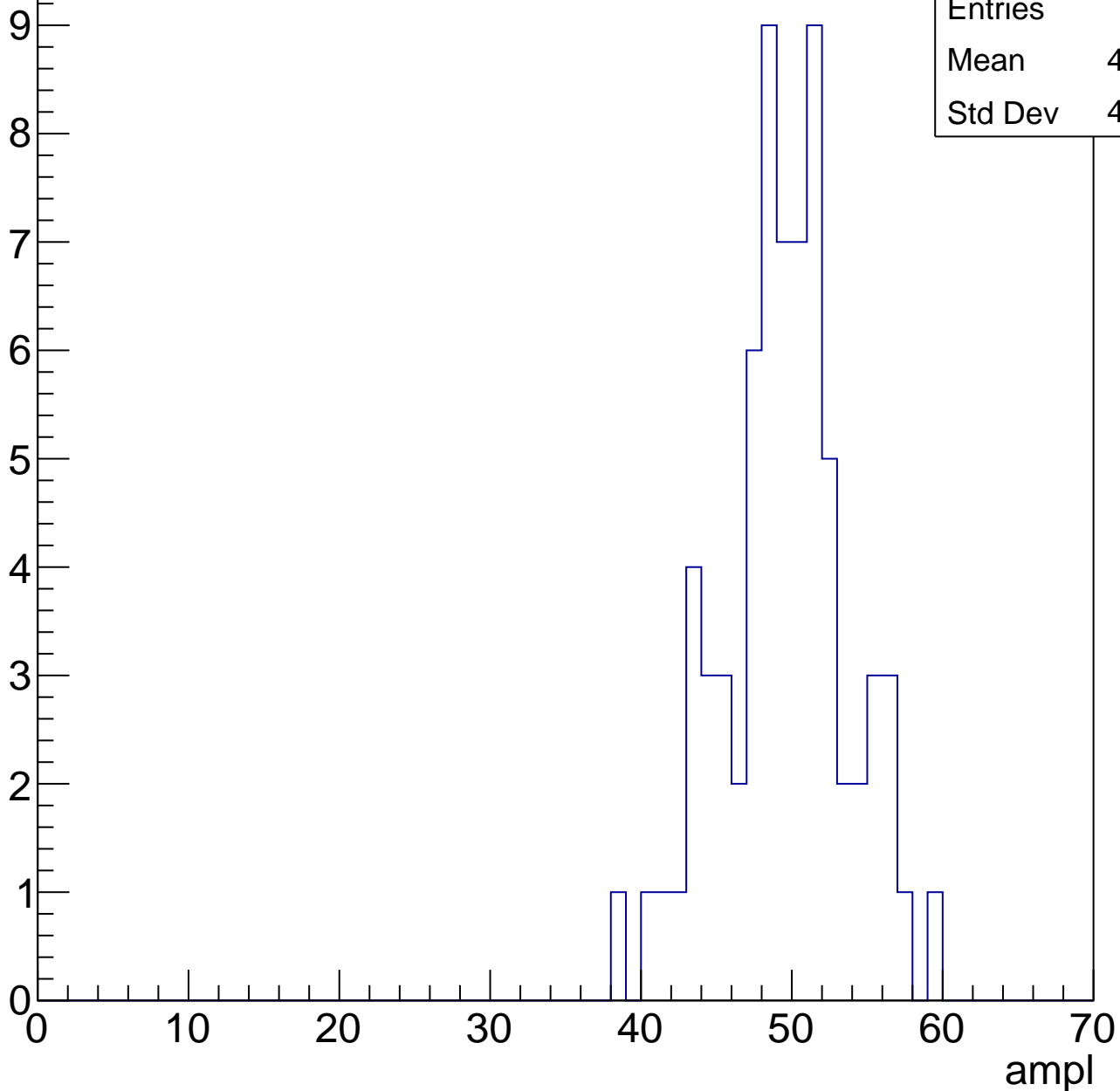


B1L103S, U10-ch126, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	71
Mean	49.03
Std Dev	4.149

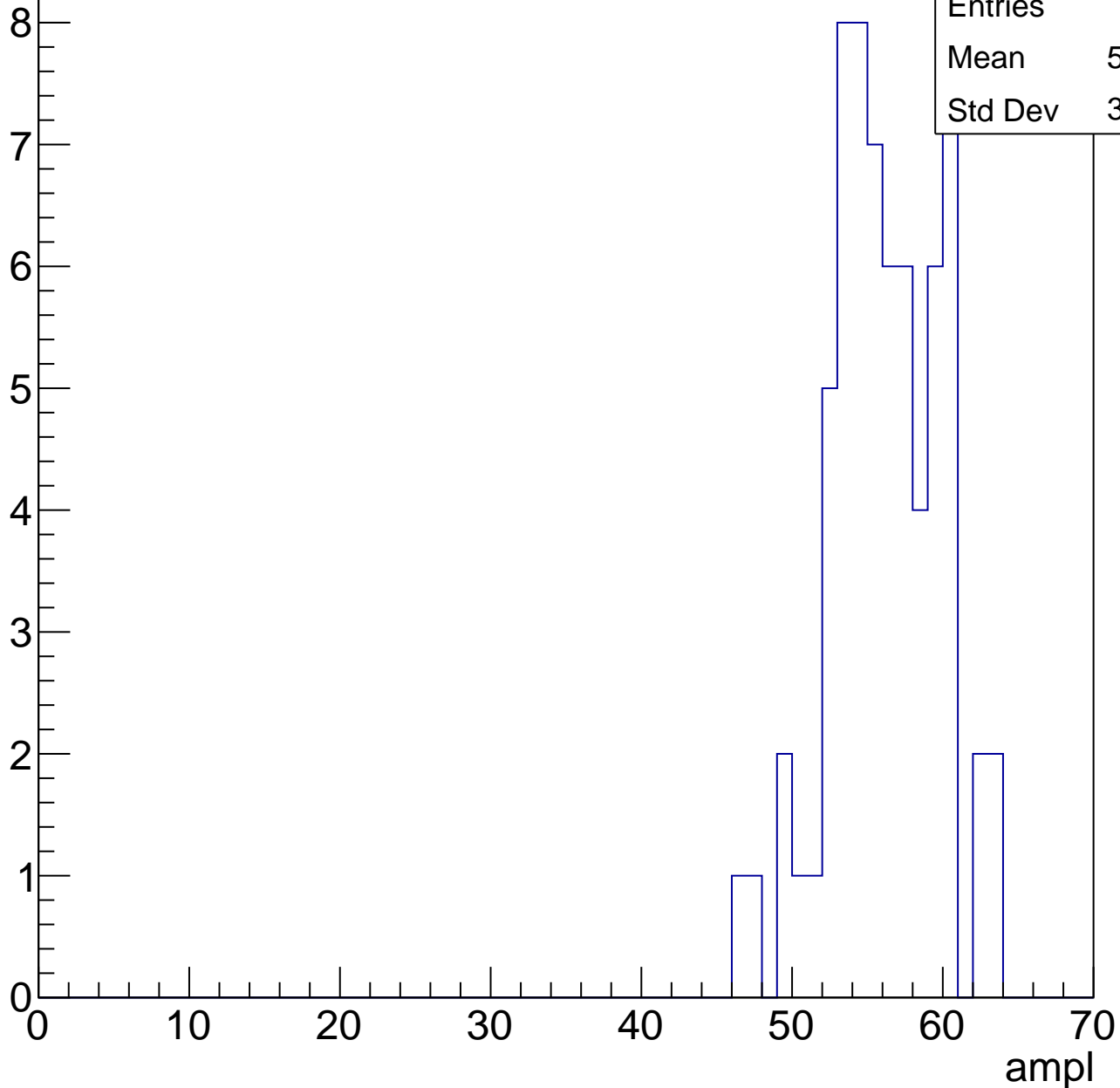


B1L103S, U10-ch126, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	68
Mean	55.69
Std Dev	3.635

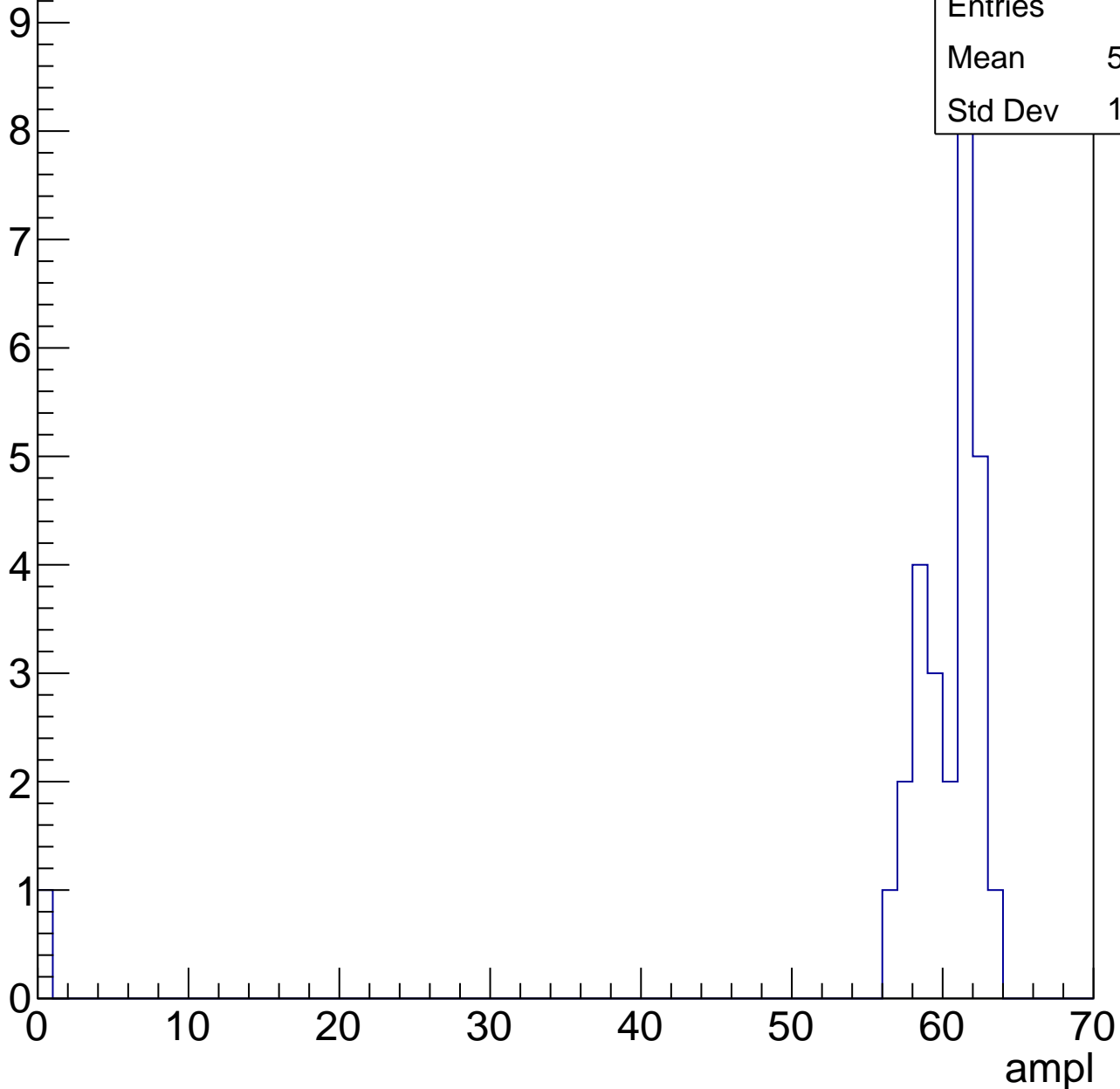


B1L103S, U10-ch126, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	28
Mean	57.89
Std Dev	11.29

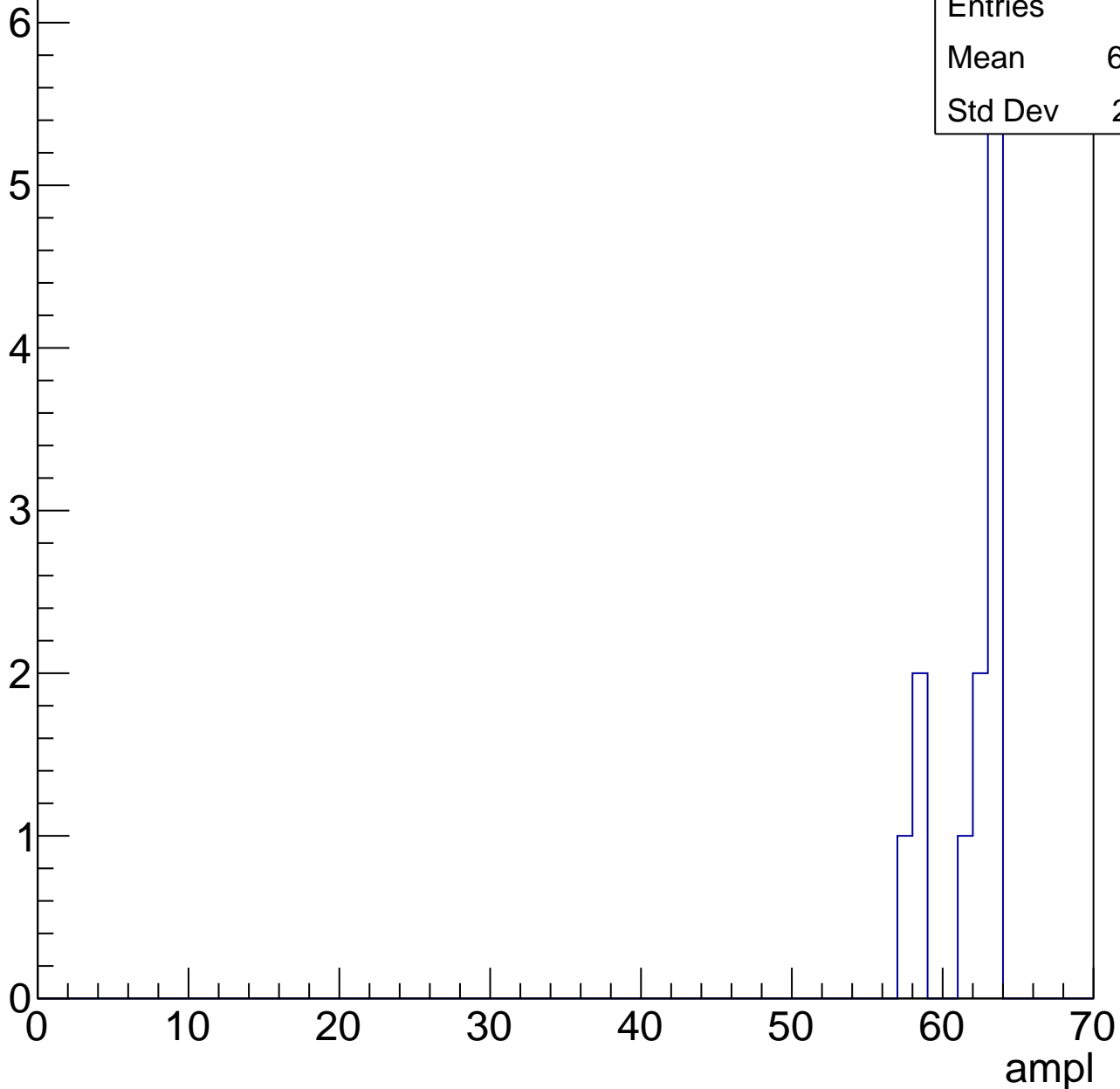


B1L103S, U10-ch126, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	12
Mean	61.33
Std Dev	2.211



B1L103S, U10-ch126, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch127, adc0

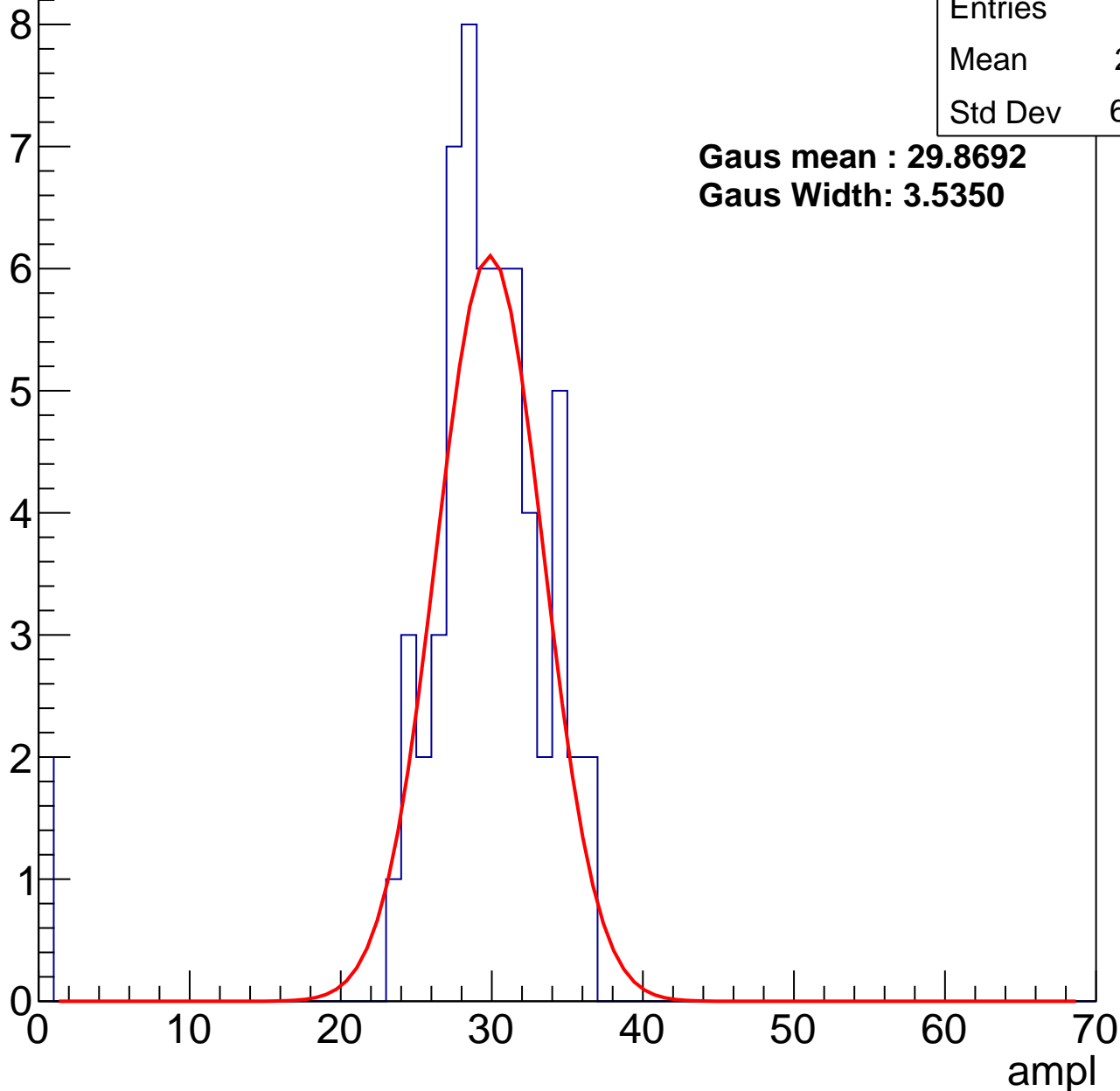
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	59
Mean	28.51
Std Dev	6.198

Gaus mean : 29.8692

Gaus Width: 3.5350



B1L103S, U10-ch127, adc1

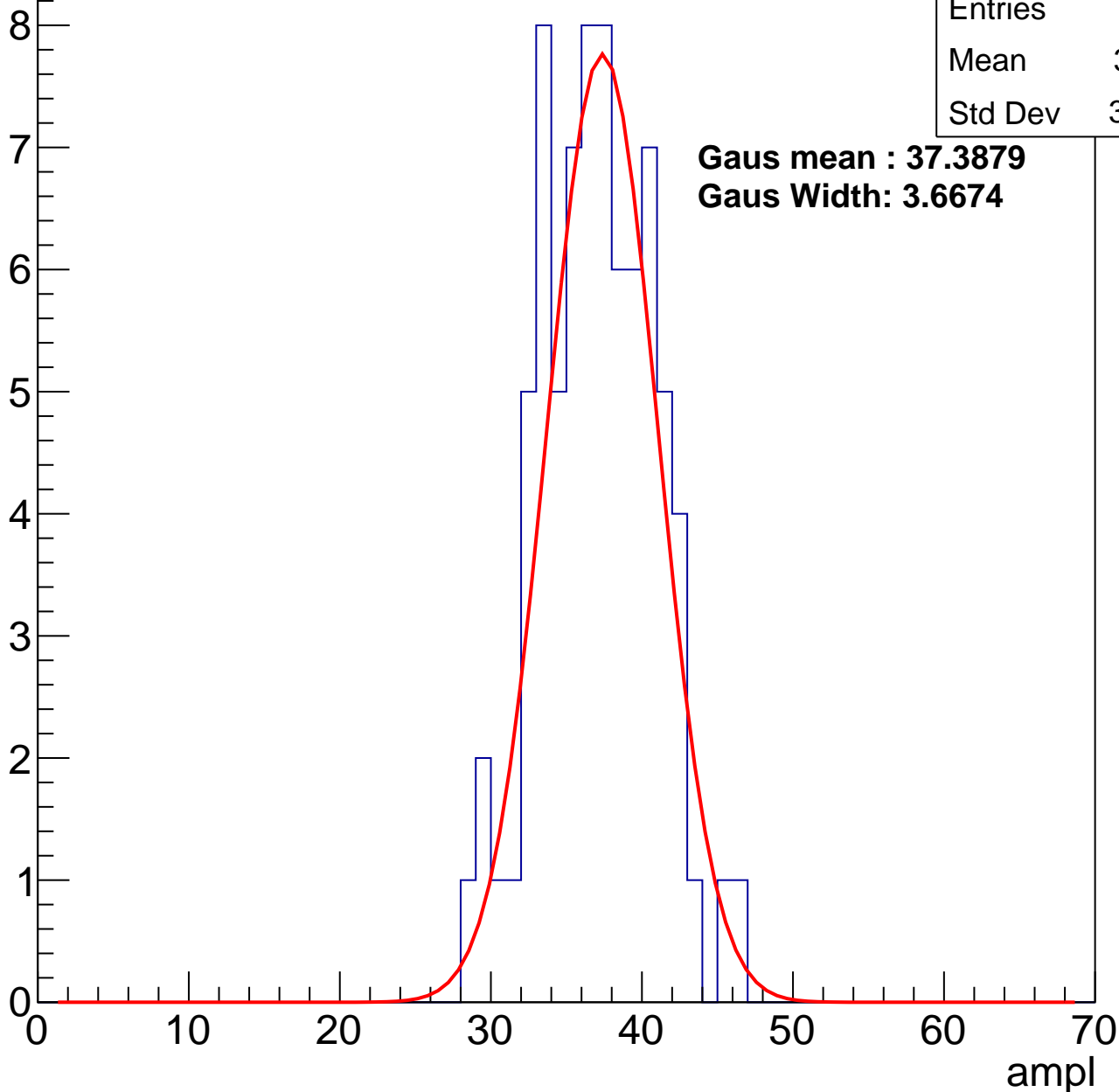
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	77
Mean	36.61
Std Dev	3.728

Gaus mean : 37.3879

Gaus Width: 3.6674



B1L103S, U10-ch127, adc2

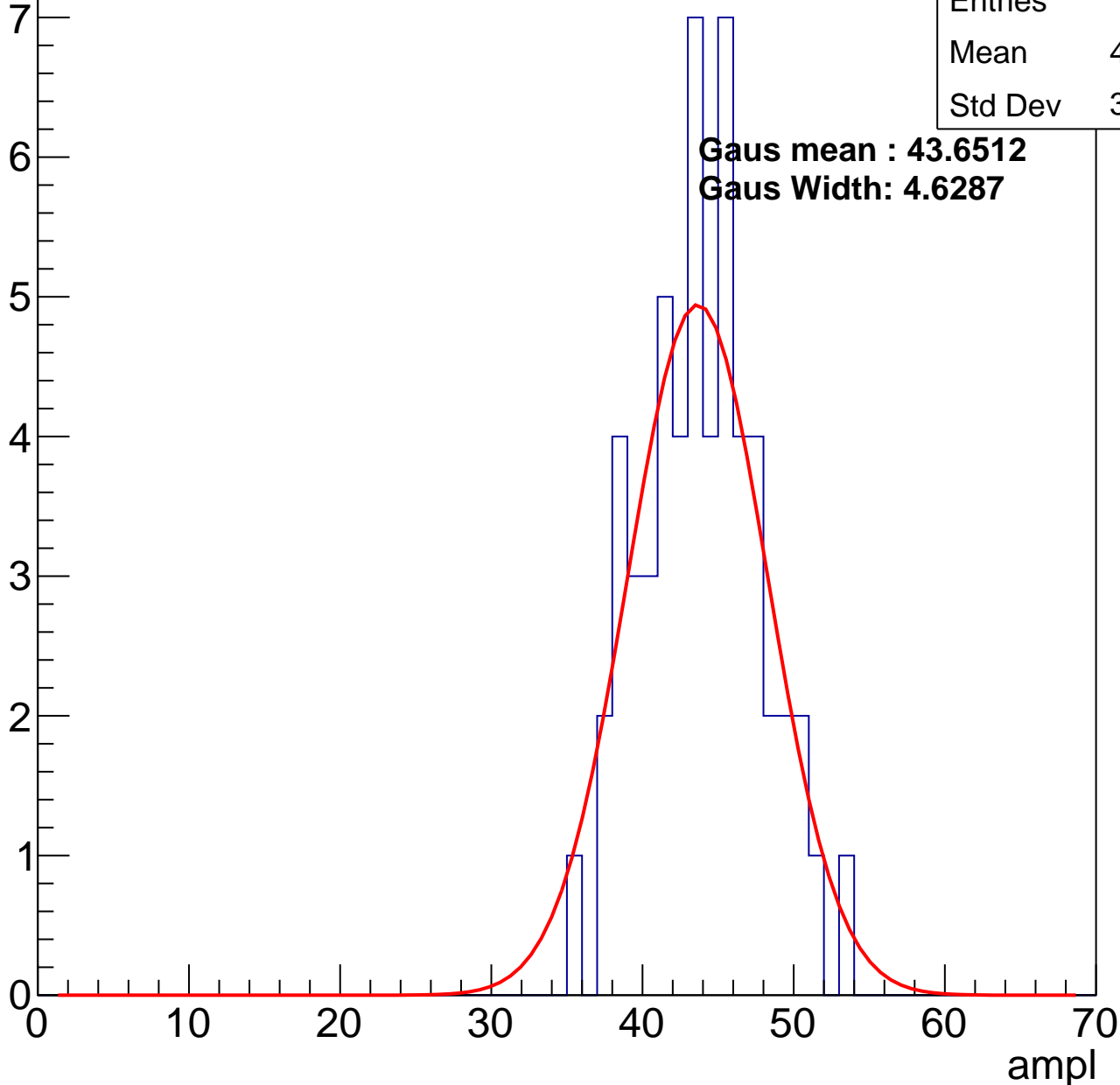
calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	56
Mean	43.45
Std Dev	3.873

Gaus mean : 43.6512

Gaus Width: 4.6287

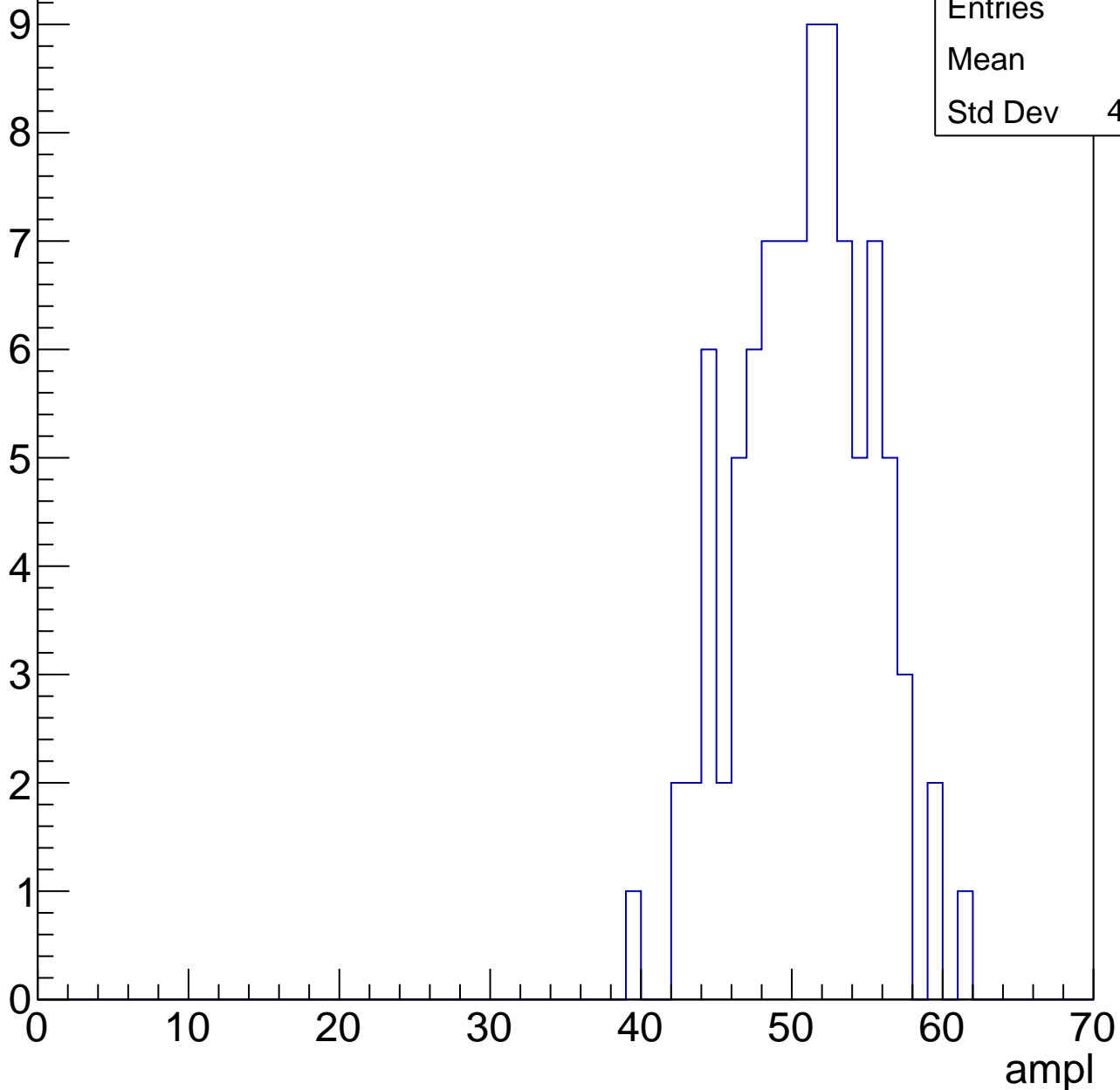


B1L103S, U10-ch127, adc3

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	93
Mean	50.4
Std Dev	4.326

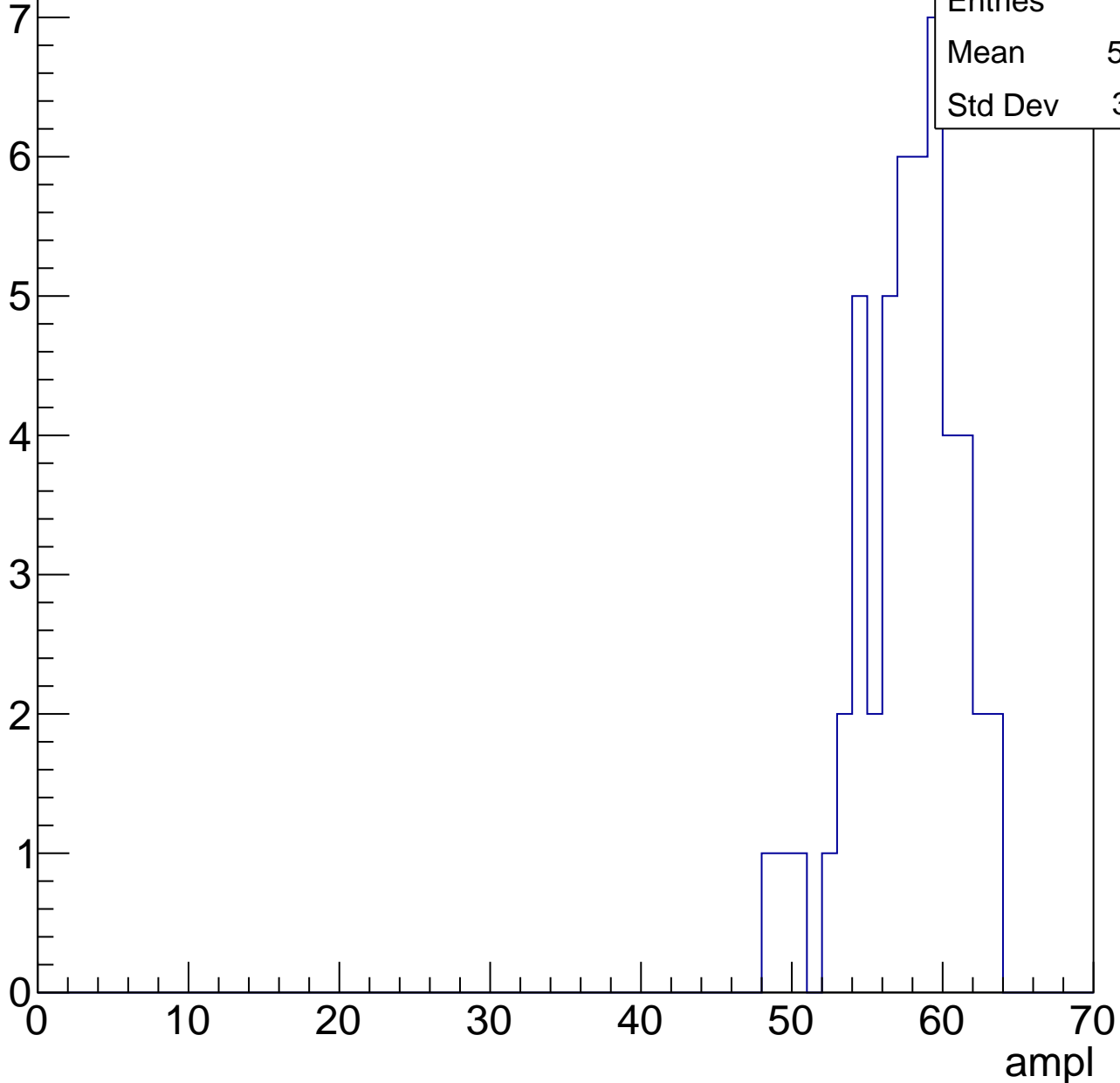


B1L103S, U10-ch127, adc4

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	49
Mean	57.18
Std Dev	3.391

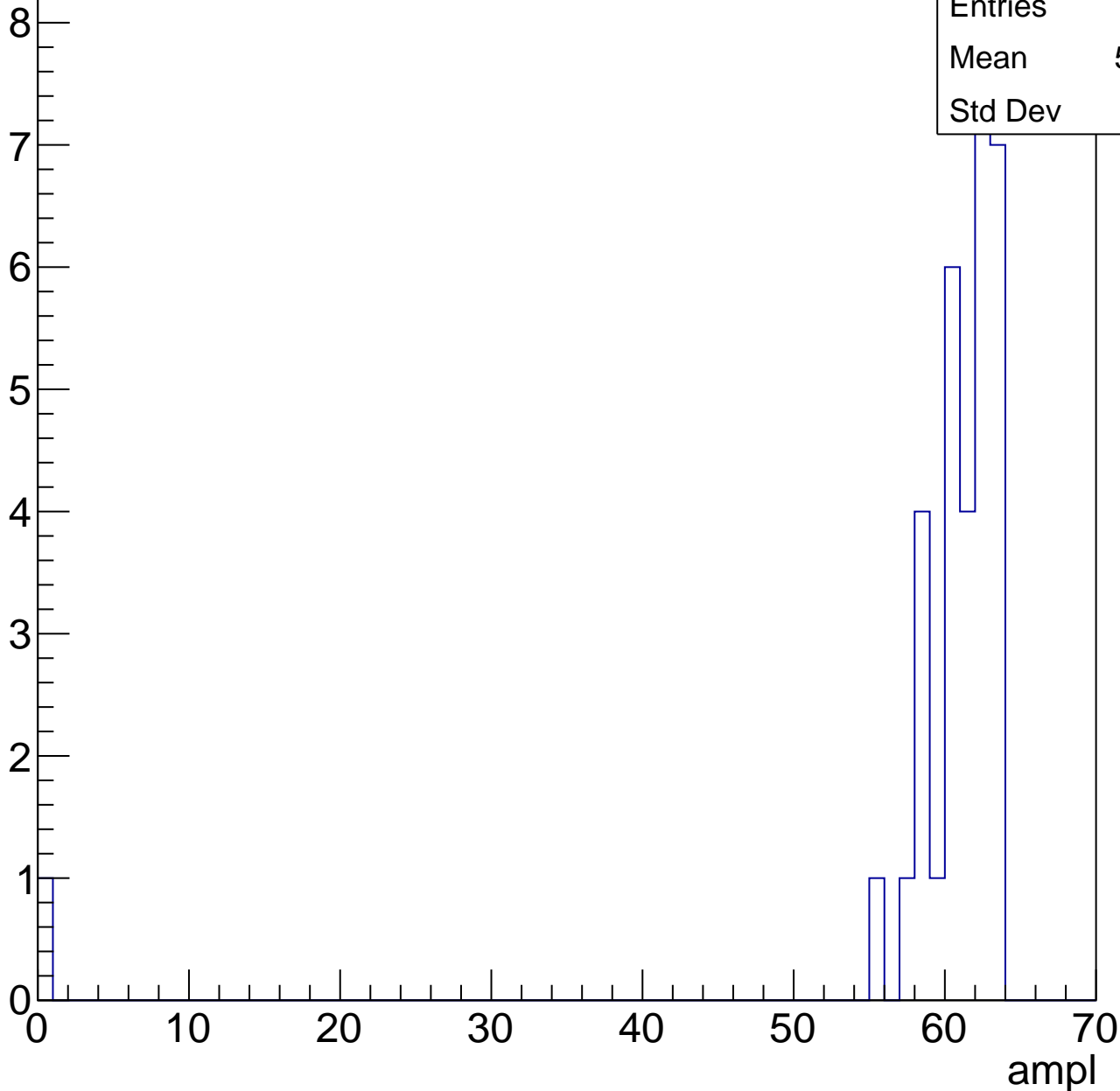


B1L103S, U10-ch127, adc5

calib_packv5_042523_0143.root, FC#7, port C2

Entry

Entries	33
Mean	58.91
Std Dev	10.6



B1L103S, U10-ch127, adc6

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry



B1L103S, U10-ch127, adc7

calib_packv5_042523_0143.root, FC#7, port C2

Entry

