



# B1L002S, U3-ch0, adc0

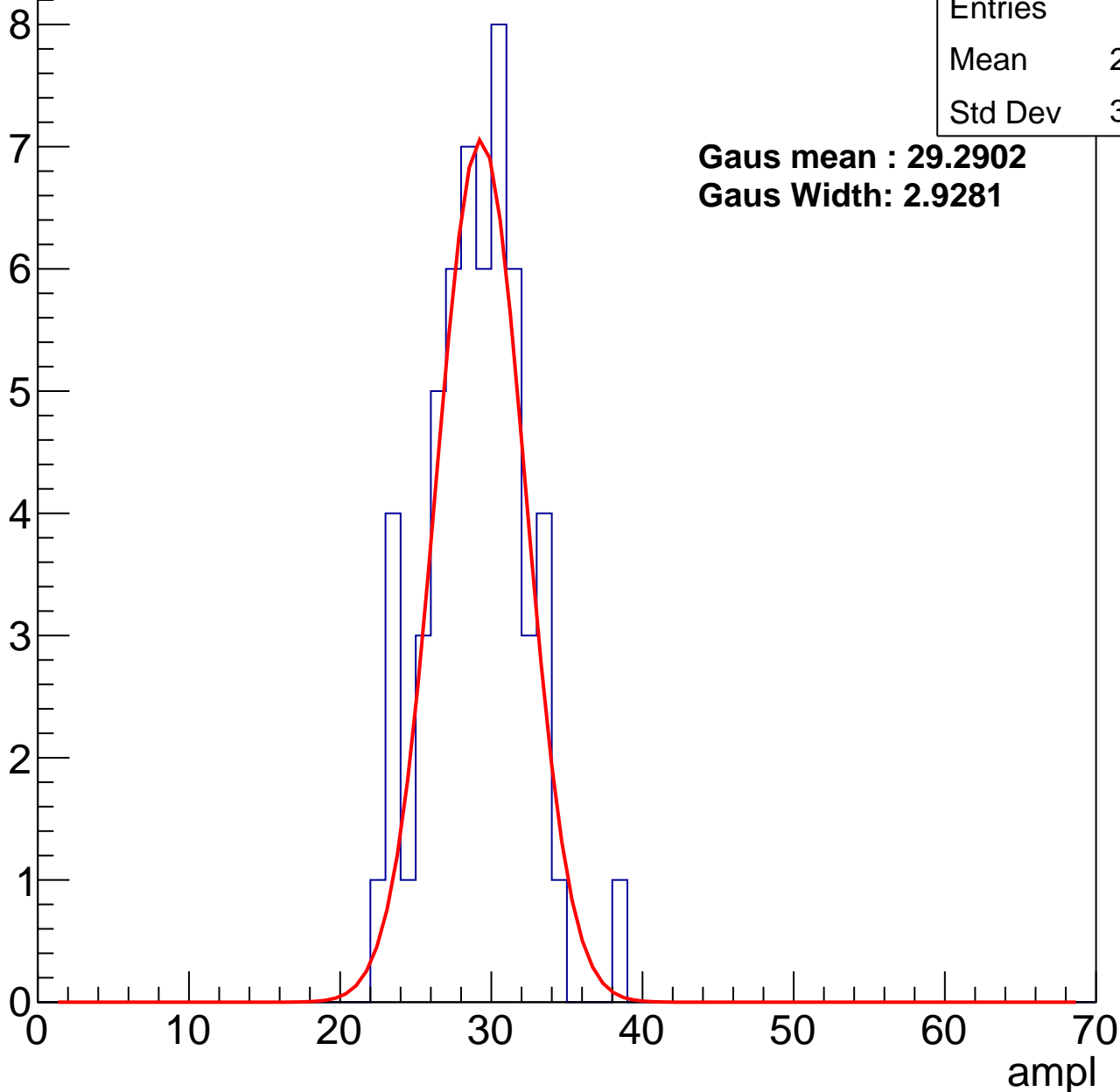
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	28.59
Std Dev	3.167

**Gaus mean : 29.2902**

**Gaus Width: 2.9281**

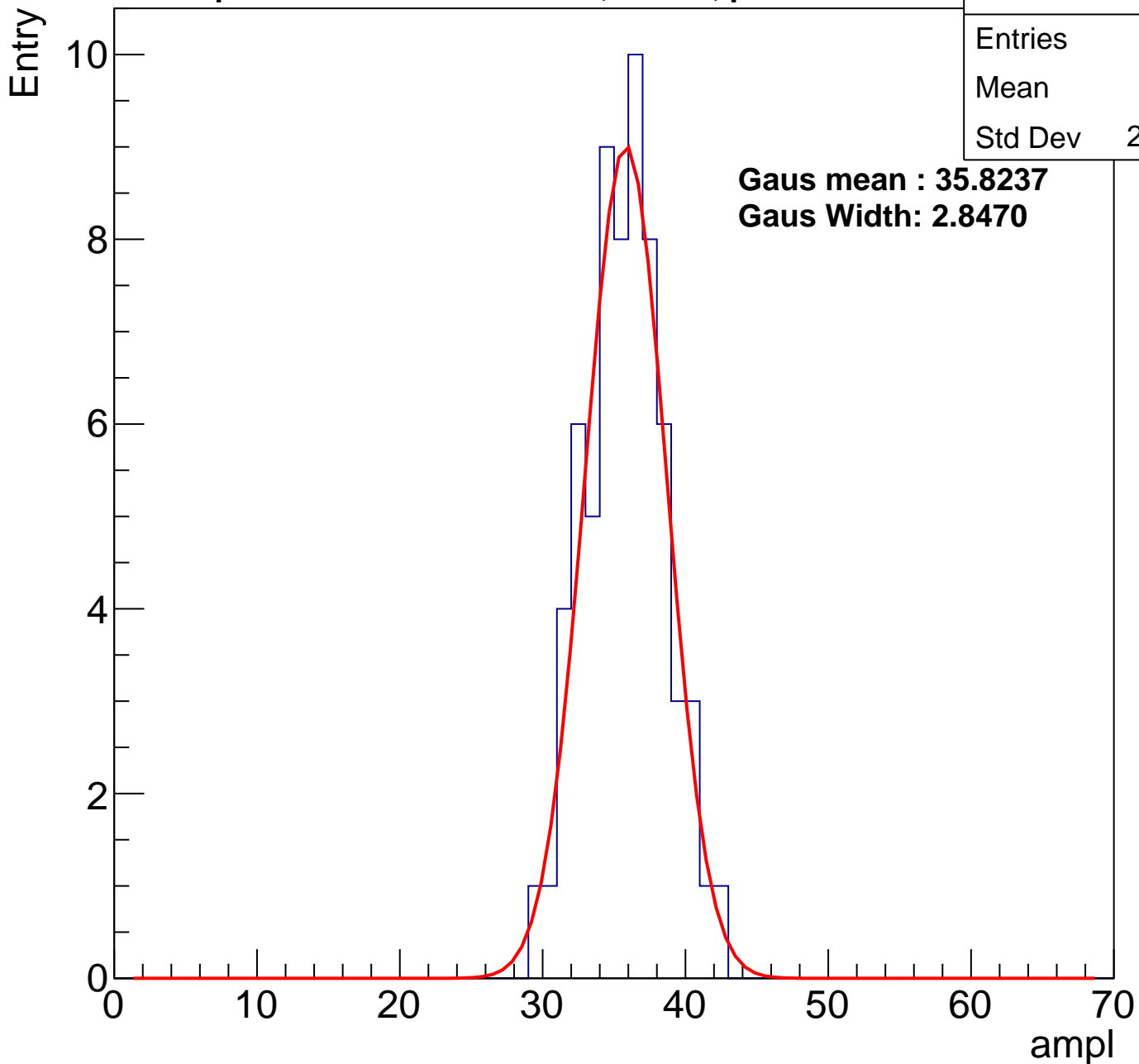


# B1L002S, U3-ch0, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	35.3
Std Dev	2.769

**Gaus mean : 35.8237**  
**Gaus Width: 2.8470**



# B1L002S, U3-ch0, adc2

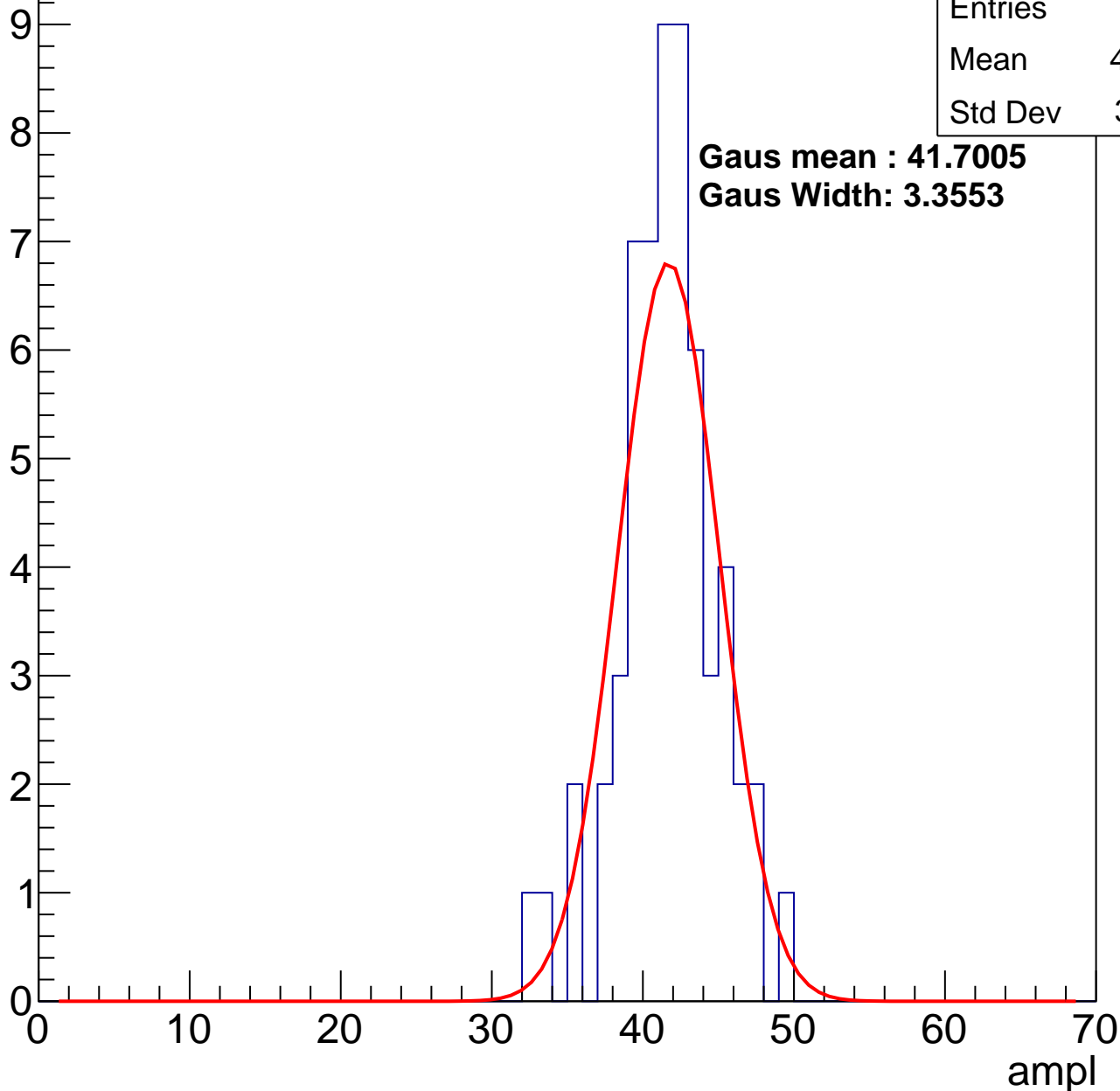
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.15
Std Dev	3.241

**Gaus mean : 41.7005**

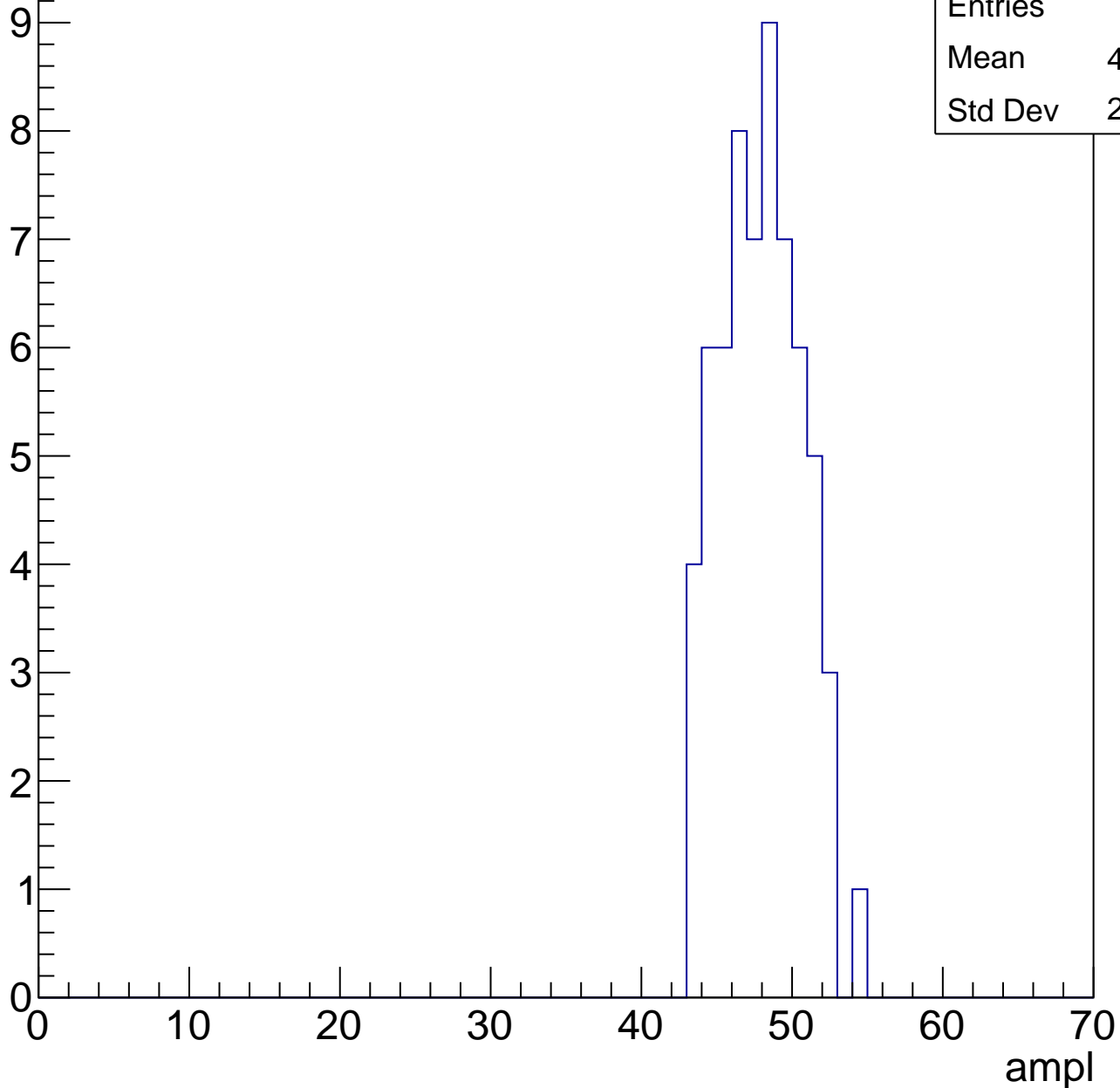
**Gaus Width: 3.3553**



# B1L002S, U3-ch0, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

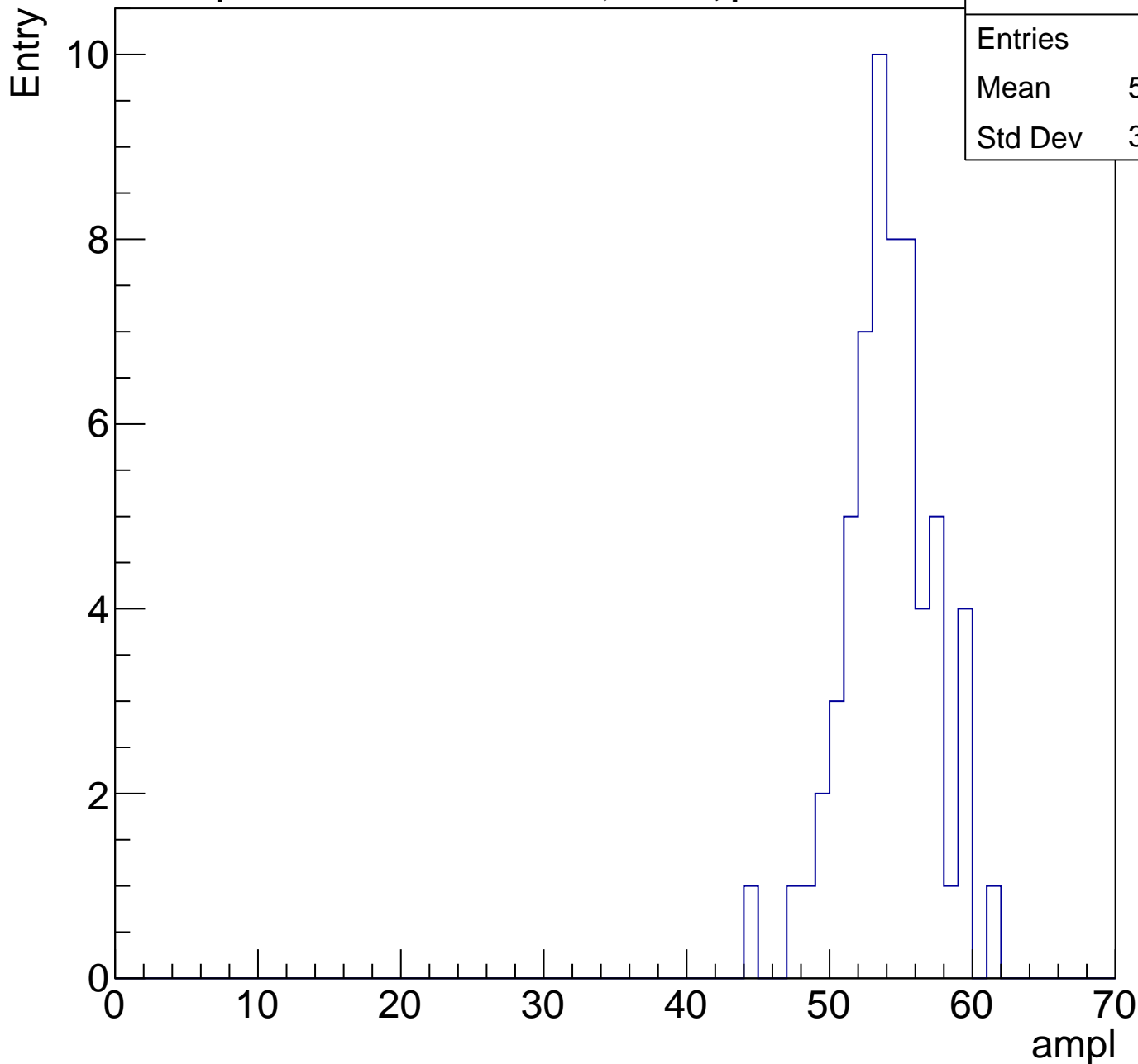
Entry



# B1L002S, U3-ch0, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	53.64
Std Dev	3.115

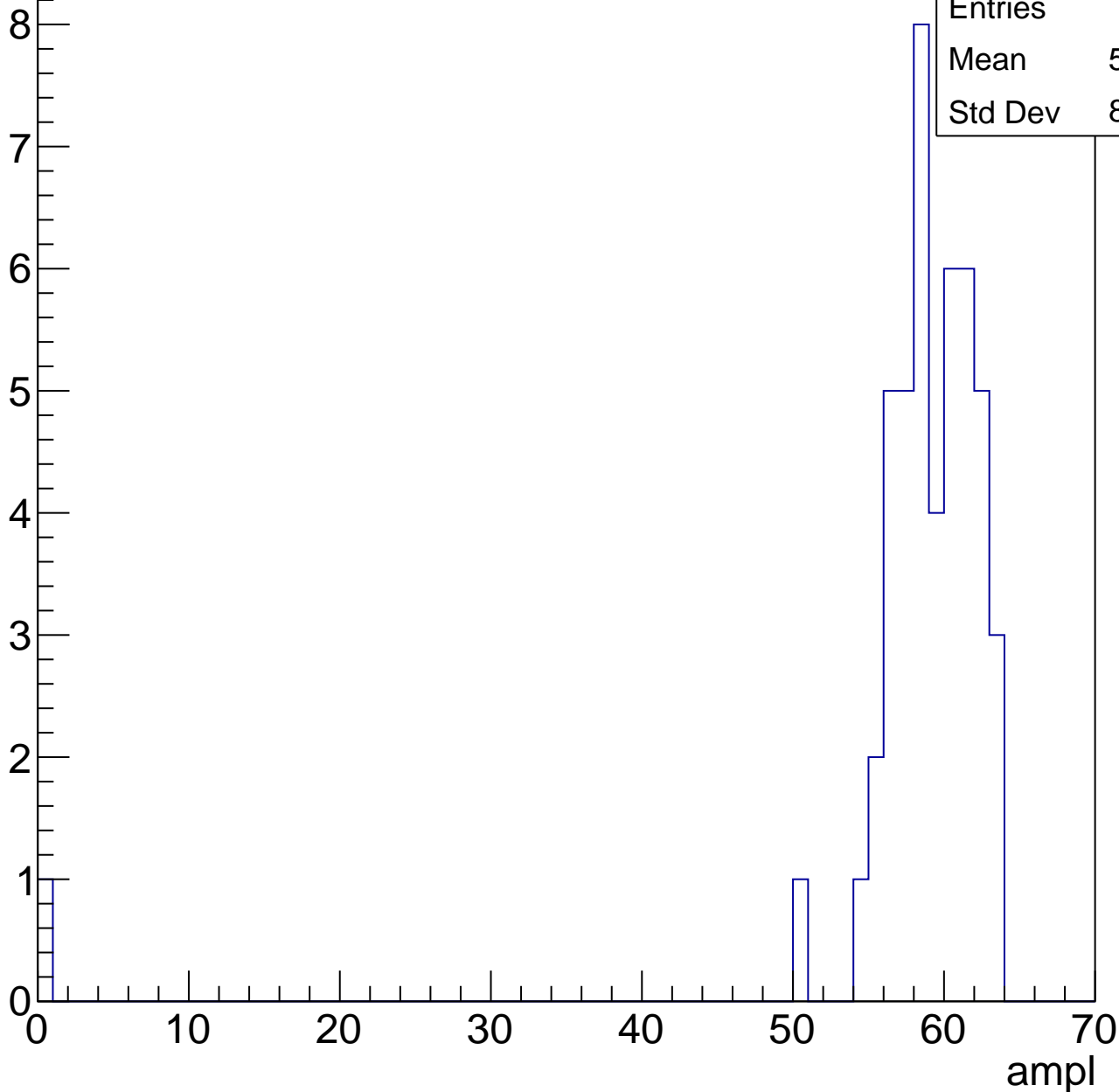


# B1L002S, U3-ch0, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.53
Std Dev	8.889

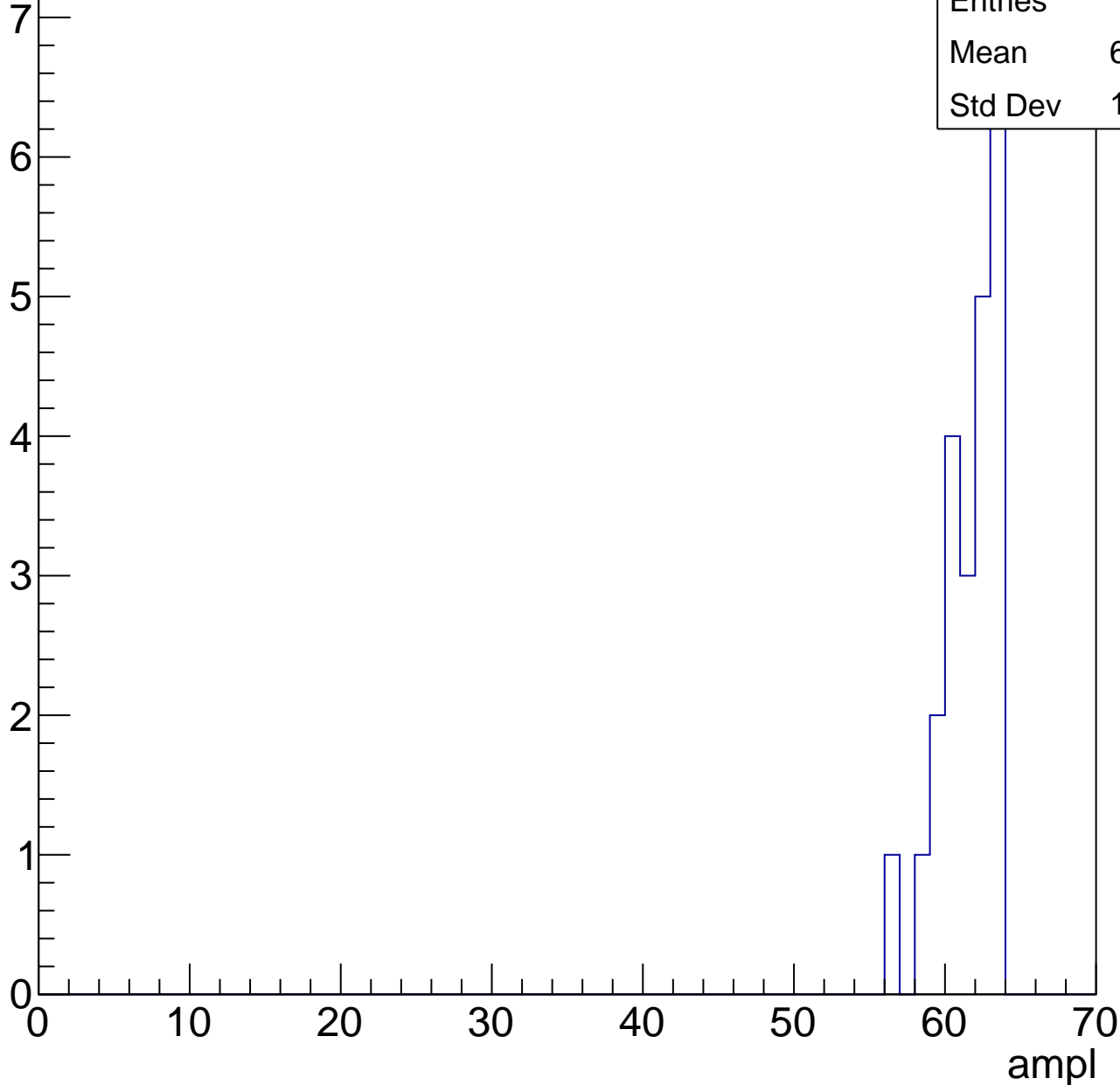


# B1L002S, U3-ch0, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.13
Std Dev	1.849

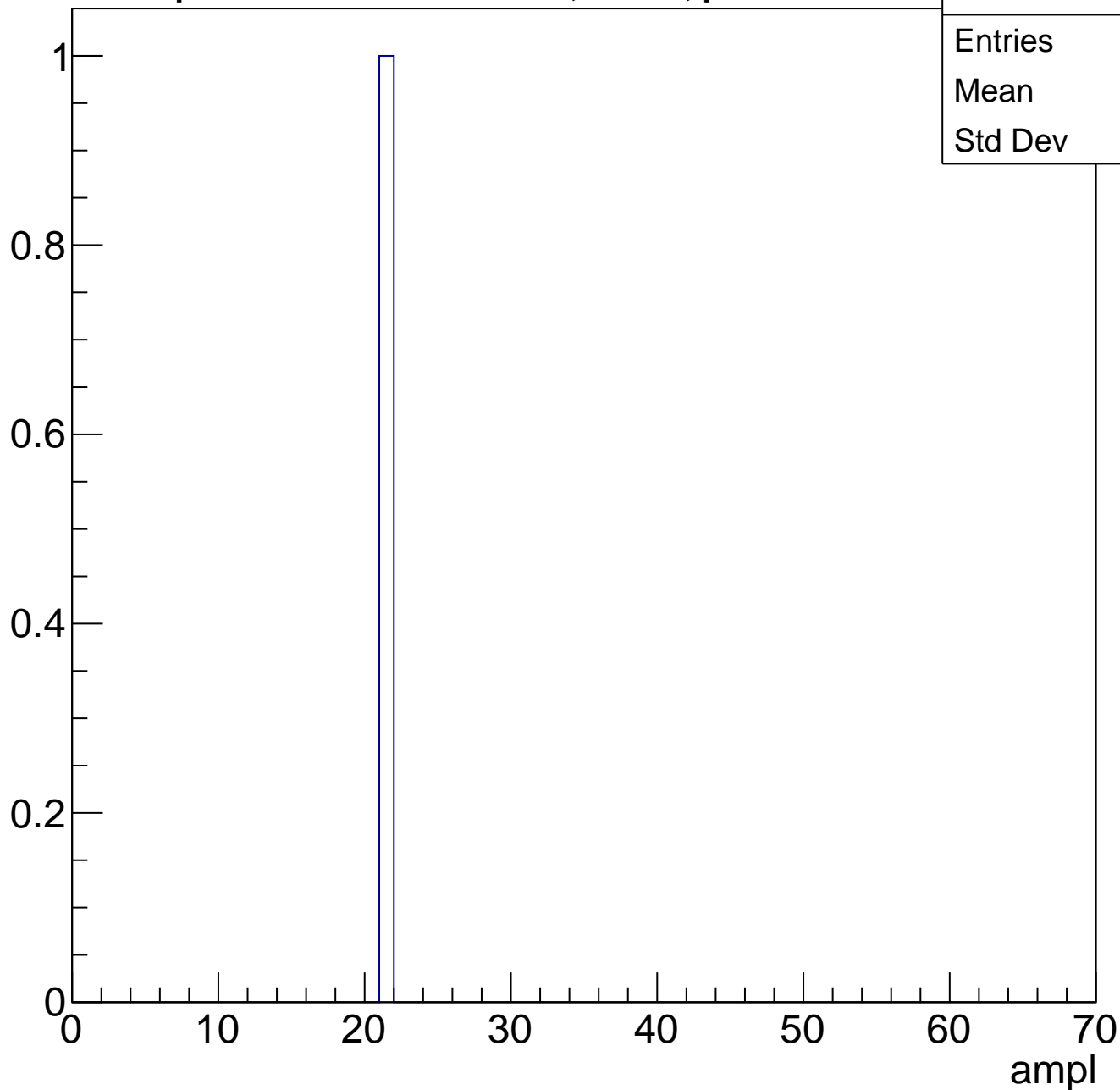




# B1L002S, U3-ch0, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch1, adc0

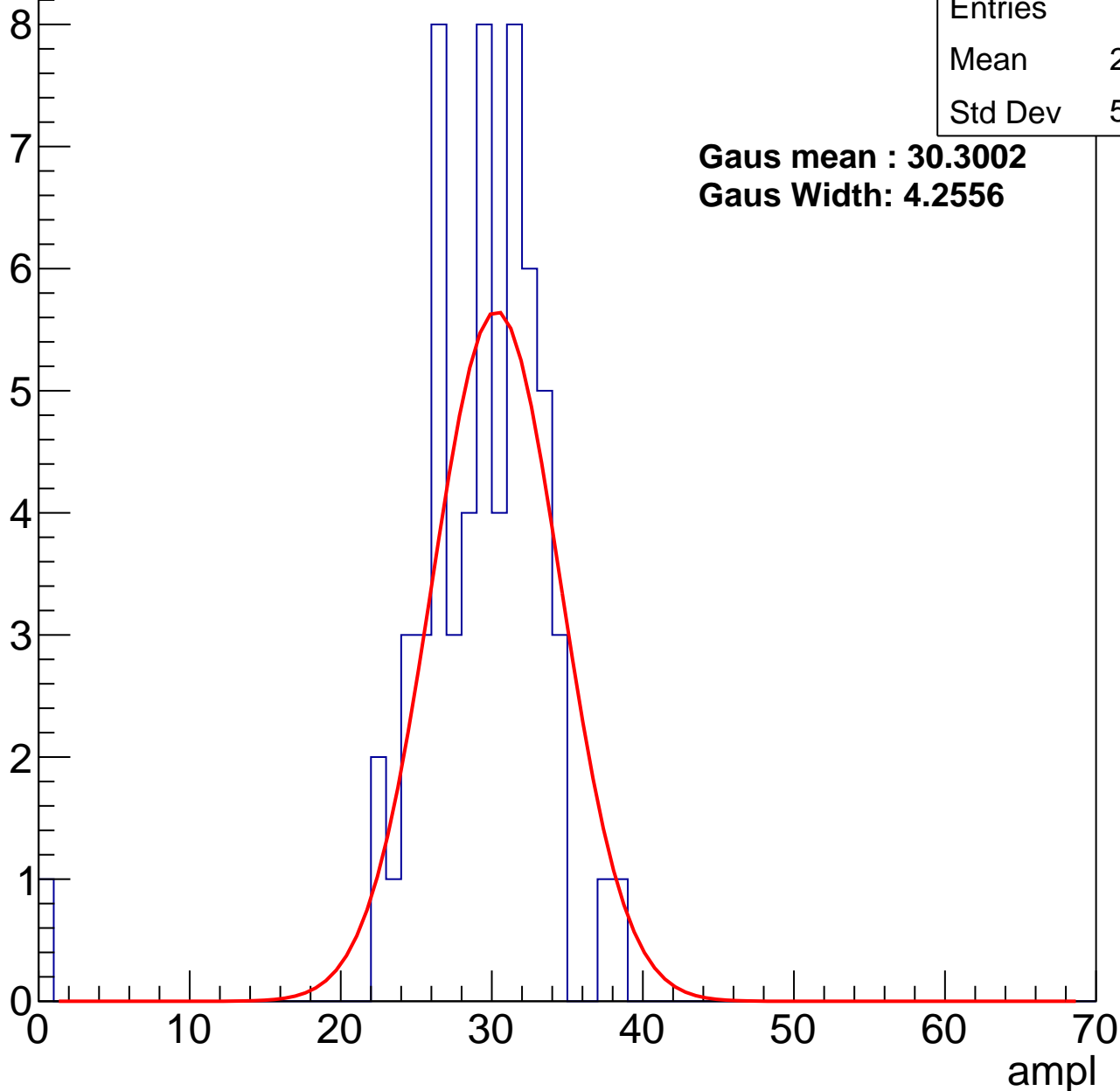
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	28.67
Std Dev	5.072

**Gaus mean : 30.3002**

**Gaus Width: 4.2556**

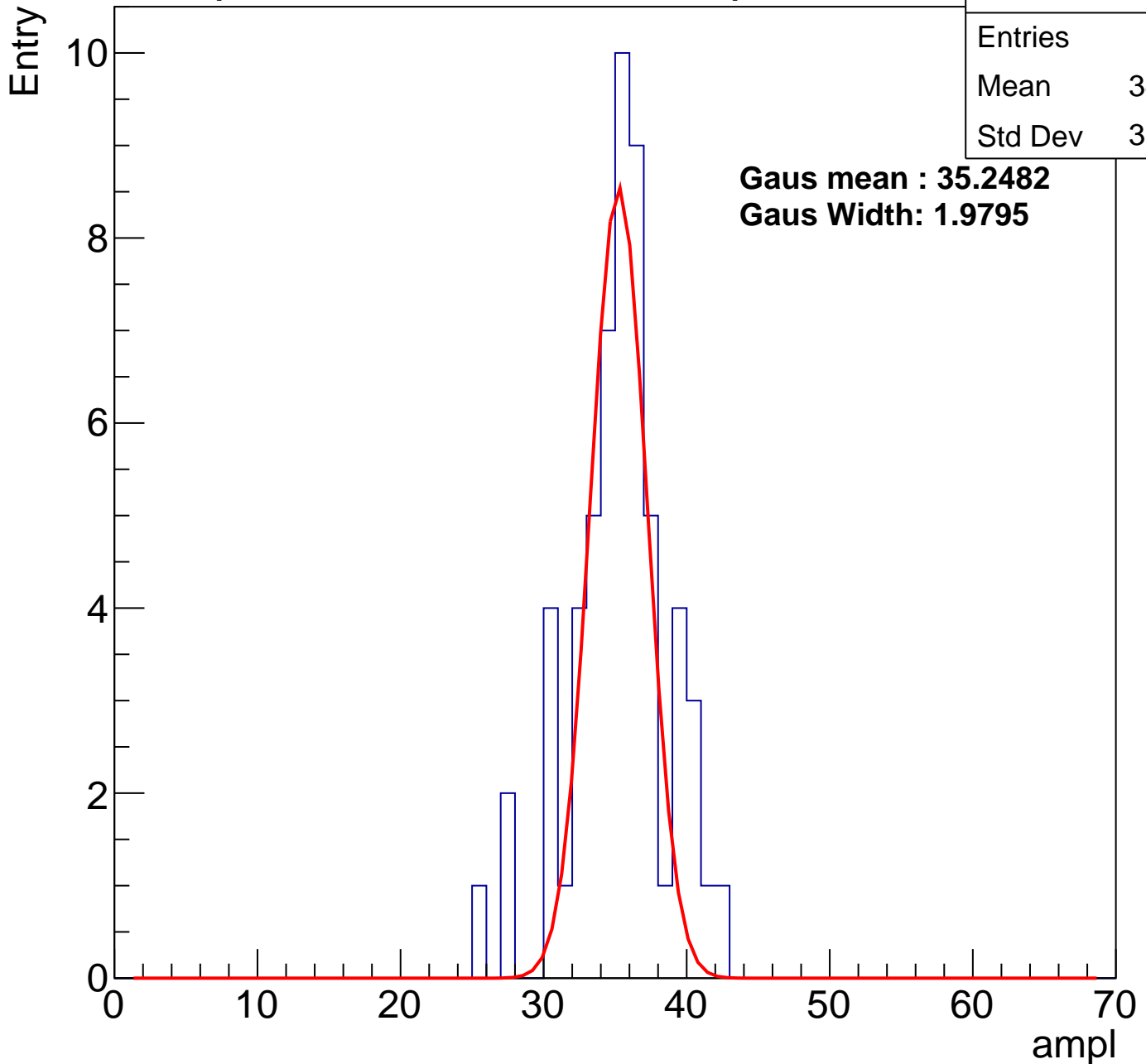


# B1L002S, U3-ch1, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	58
Mean	34.78
Std Dev	3.389

**Gaus mean : 35.2482**  
**Gaus Width: 1.9795**



# B1L002S, U3-ch1, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	41.76
Std Dev	3.089

**Gaus mean : 42.0579**

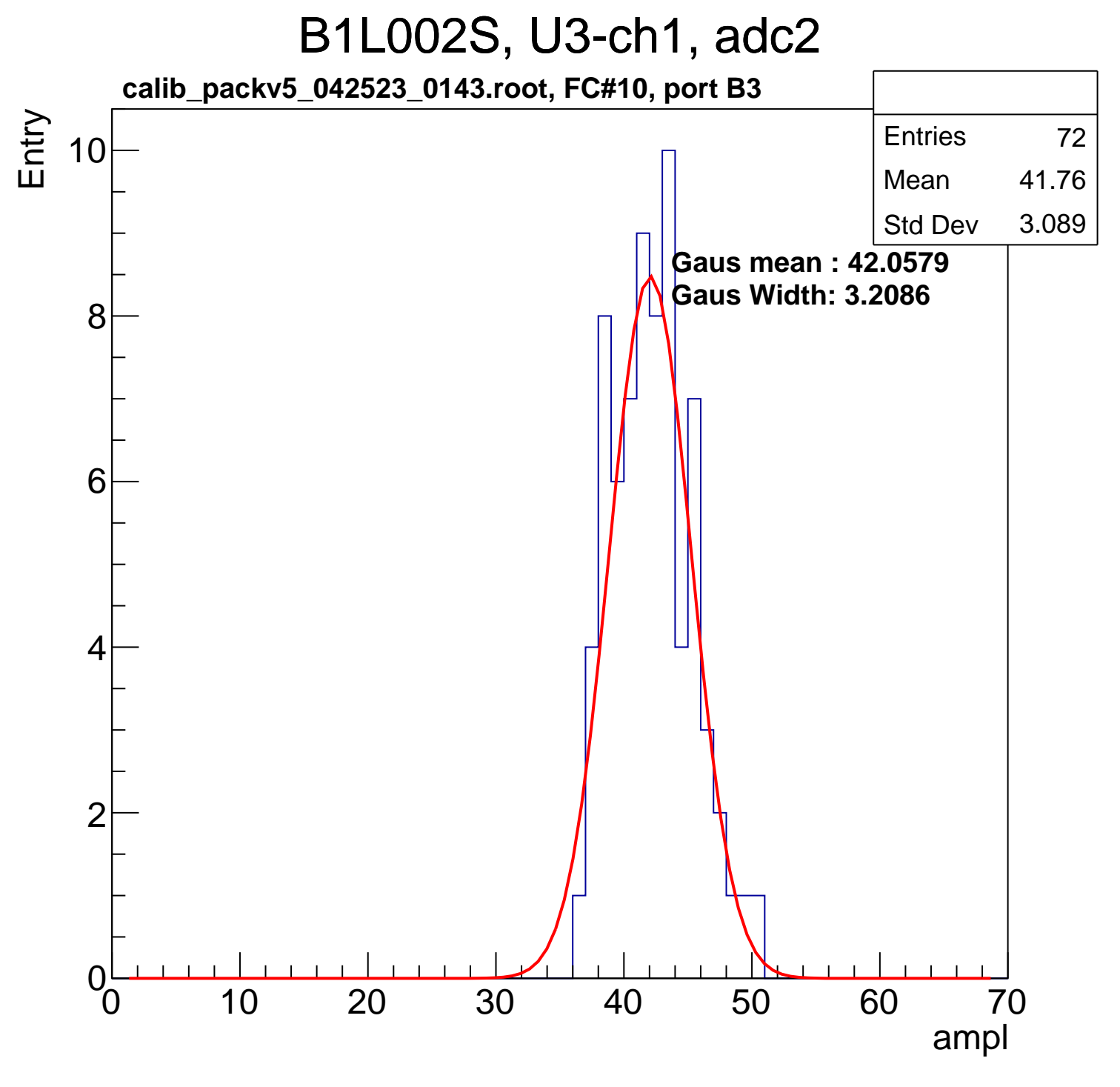
**Gaus Width: 3.2086**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

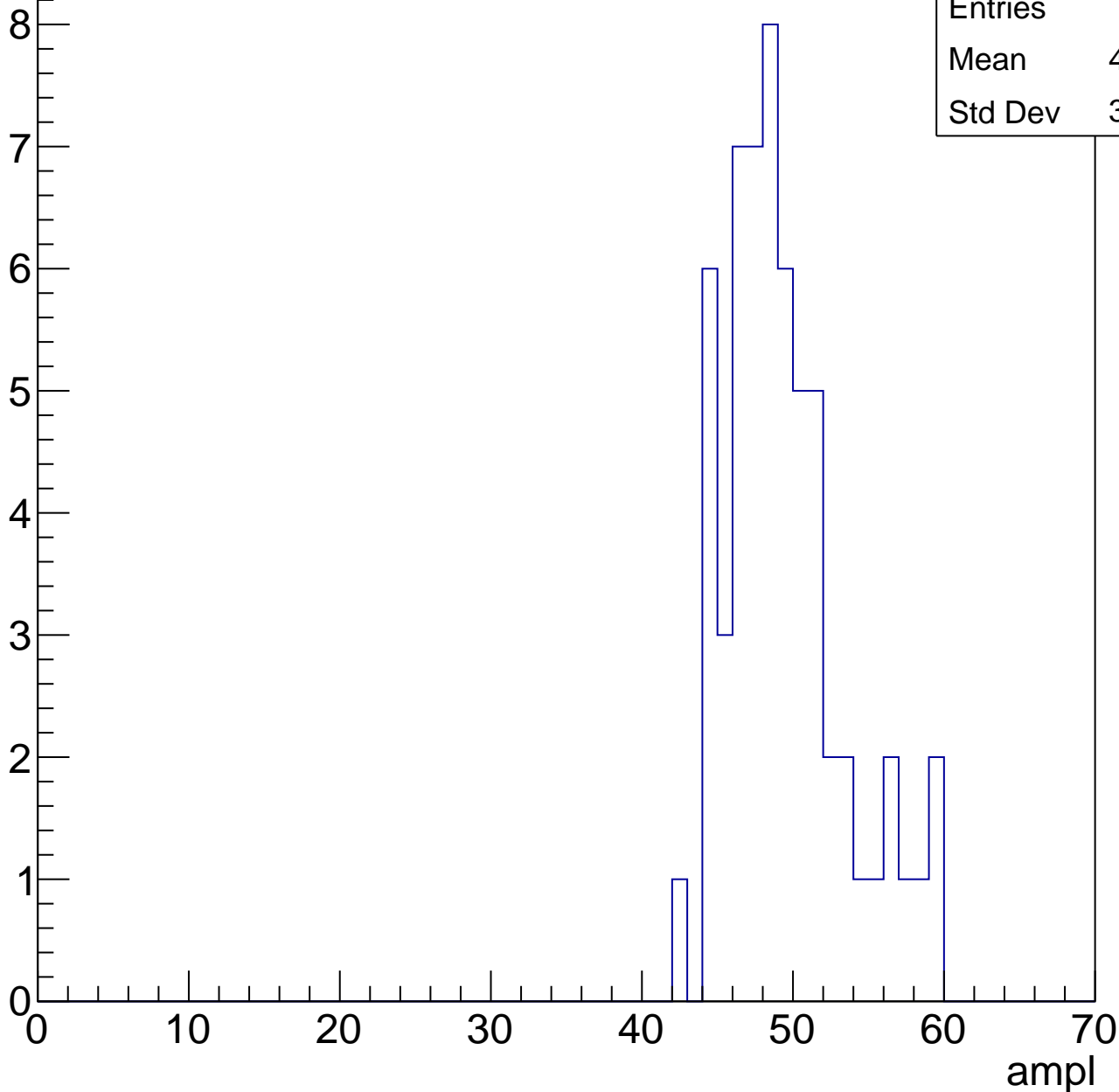


# B1L002S, U3-ch1, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	48.98
Std Dev	3.926

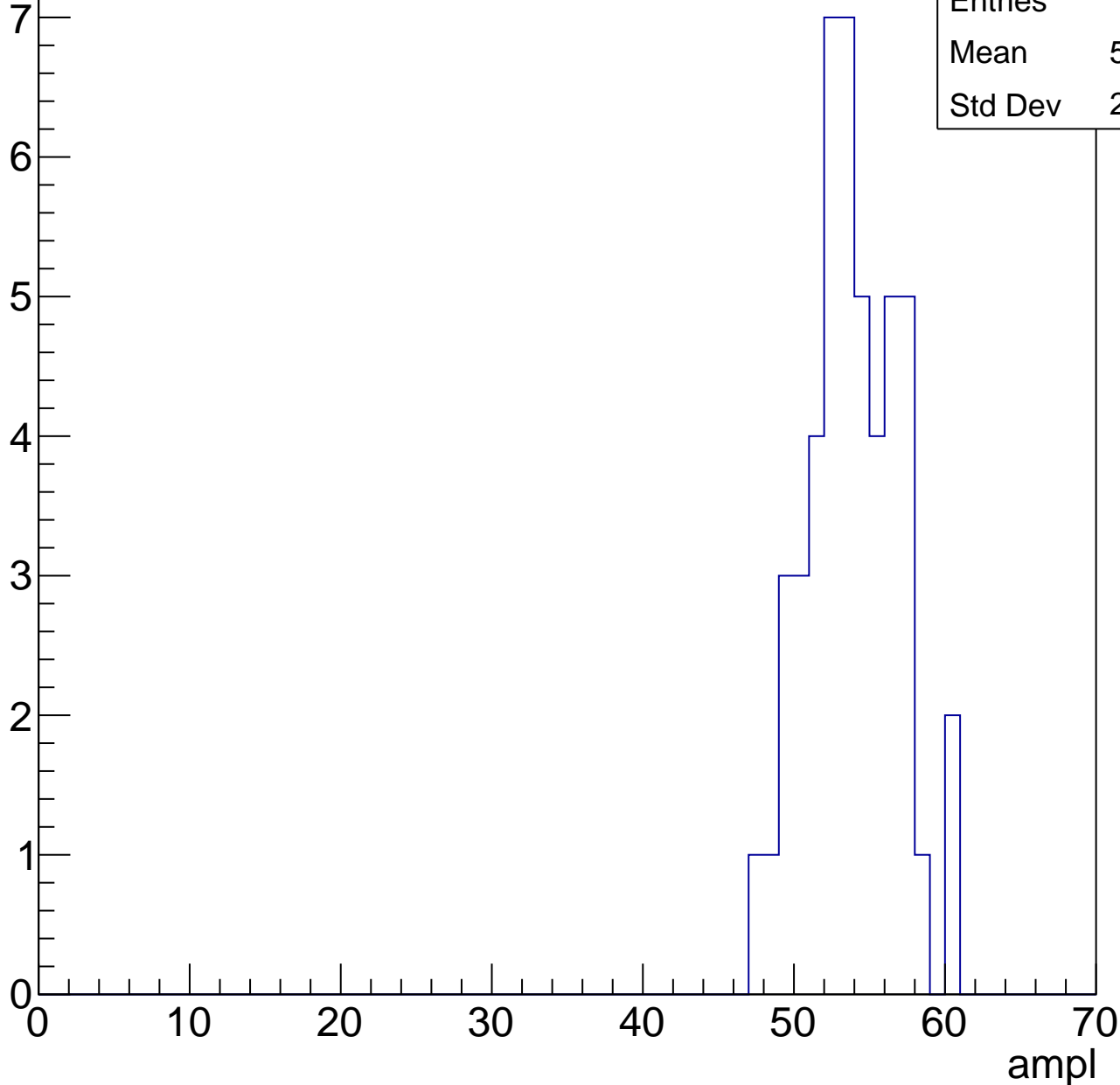


# B1L002S, U3-ch1, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	53.42
Std Dev	2.957

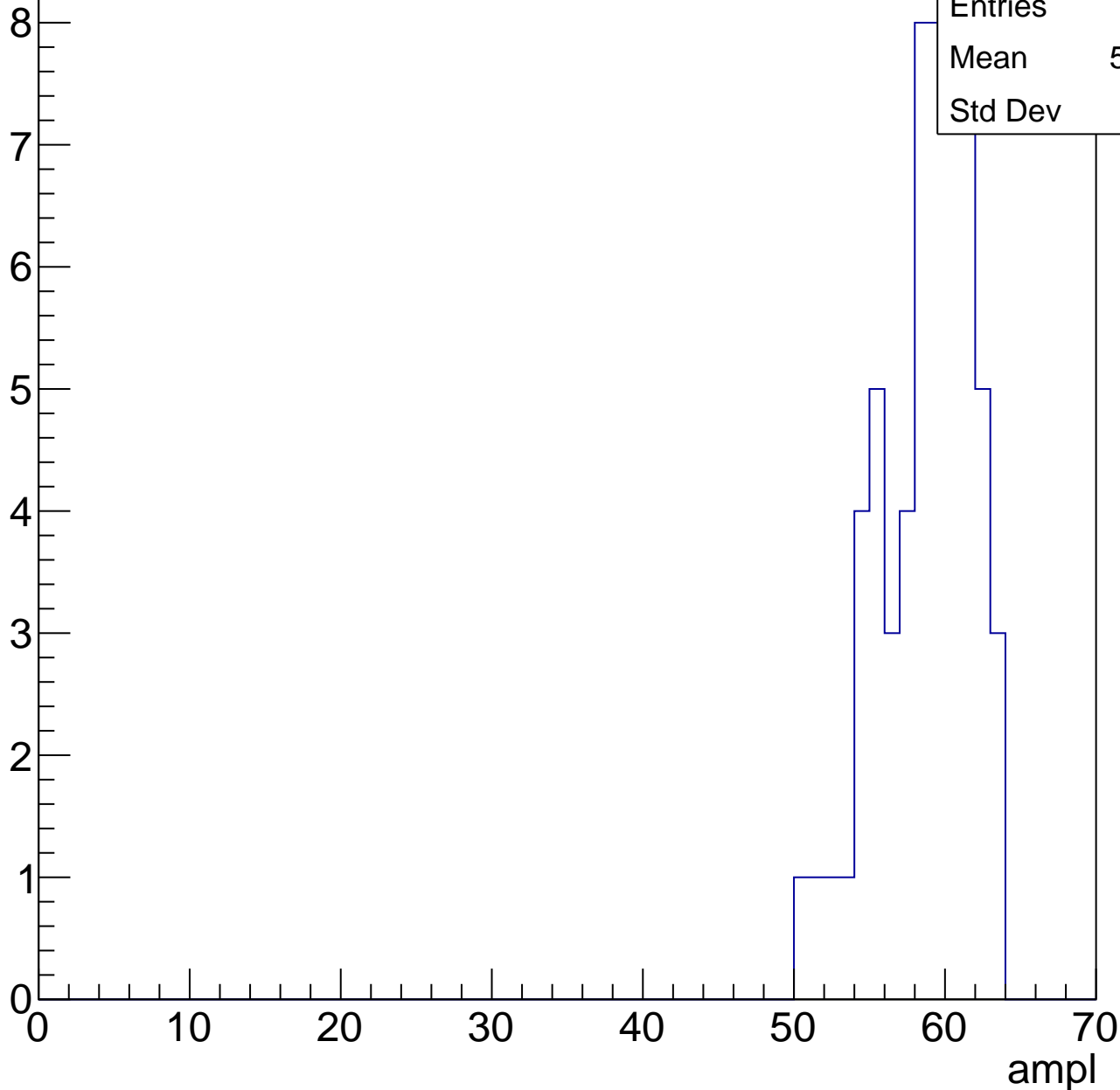


# B1L002S, U3-ch1, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

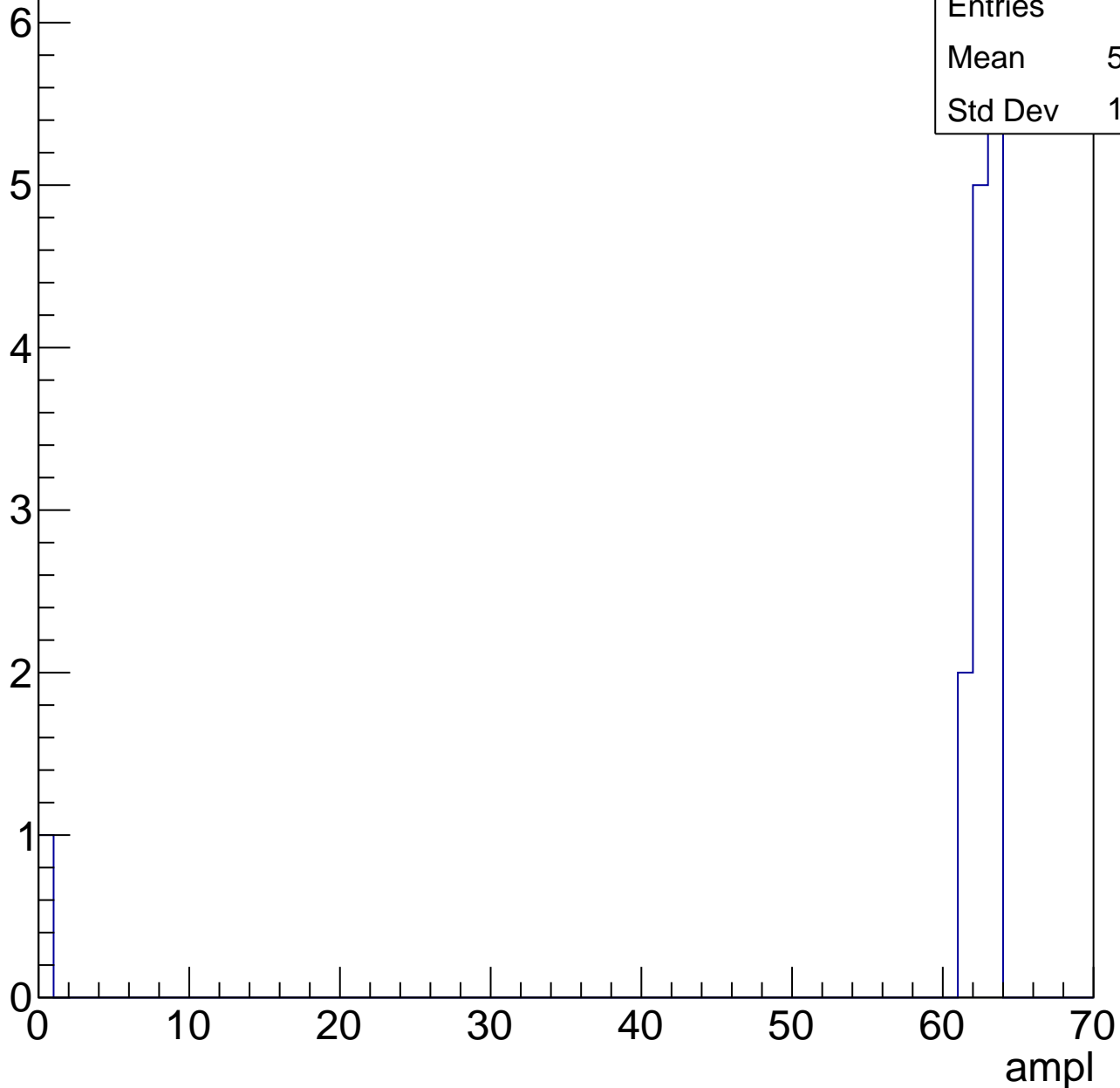
Entries	60
Mean	58.27
Std Dev	3.06



# B1L002S, U3-ch1, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

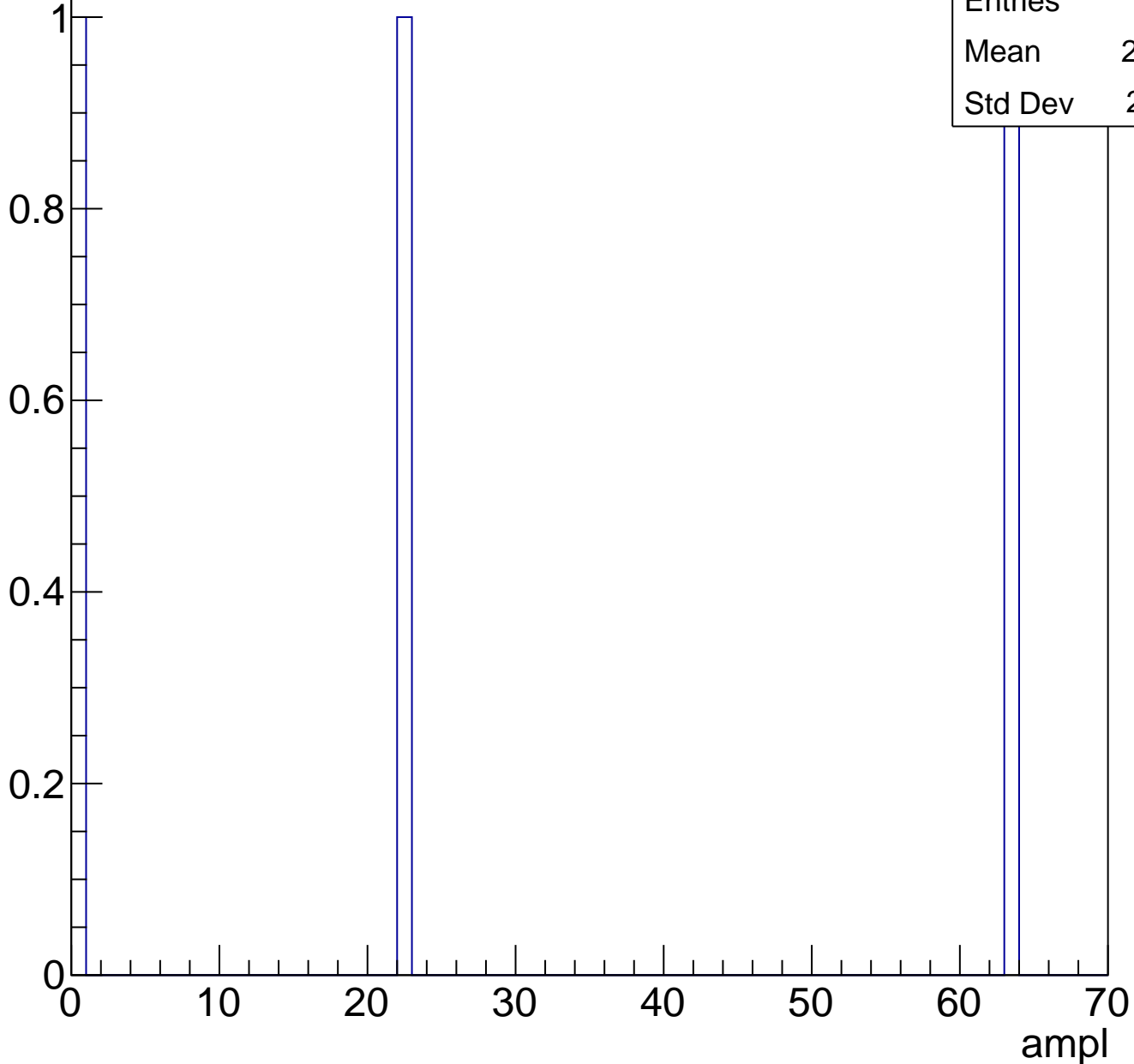




# B1L002S, U3-ch1, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	28.33
Std Dev	26.11

# B1L002S, U3-ch2, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	26.63
Std Dev	3.041

**Gaus mean : 27.1719**

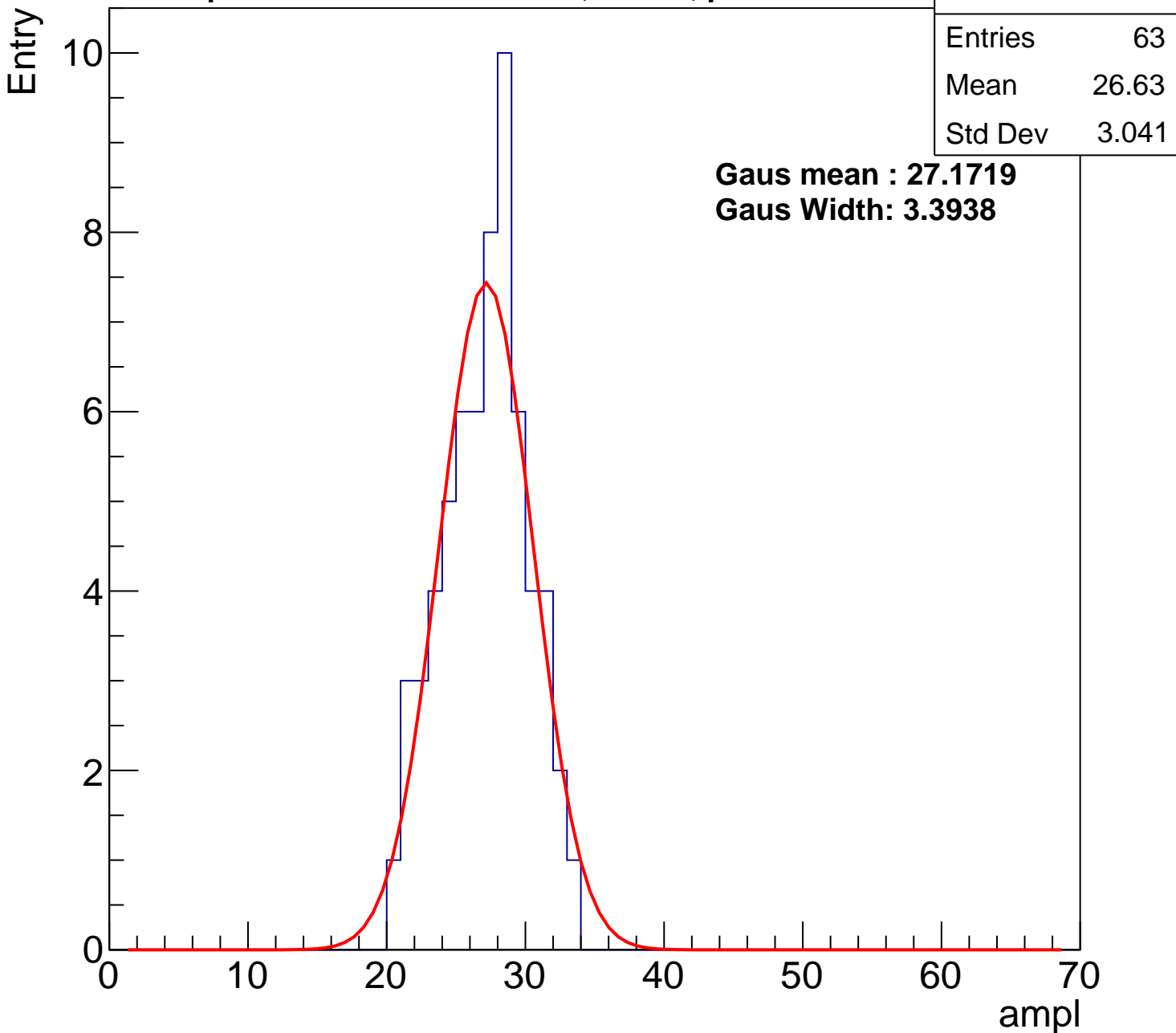
**Gaus Width: 3.3938**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch2, adc1

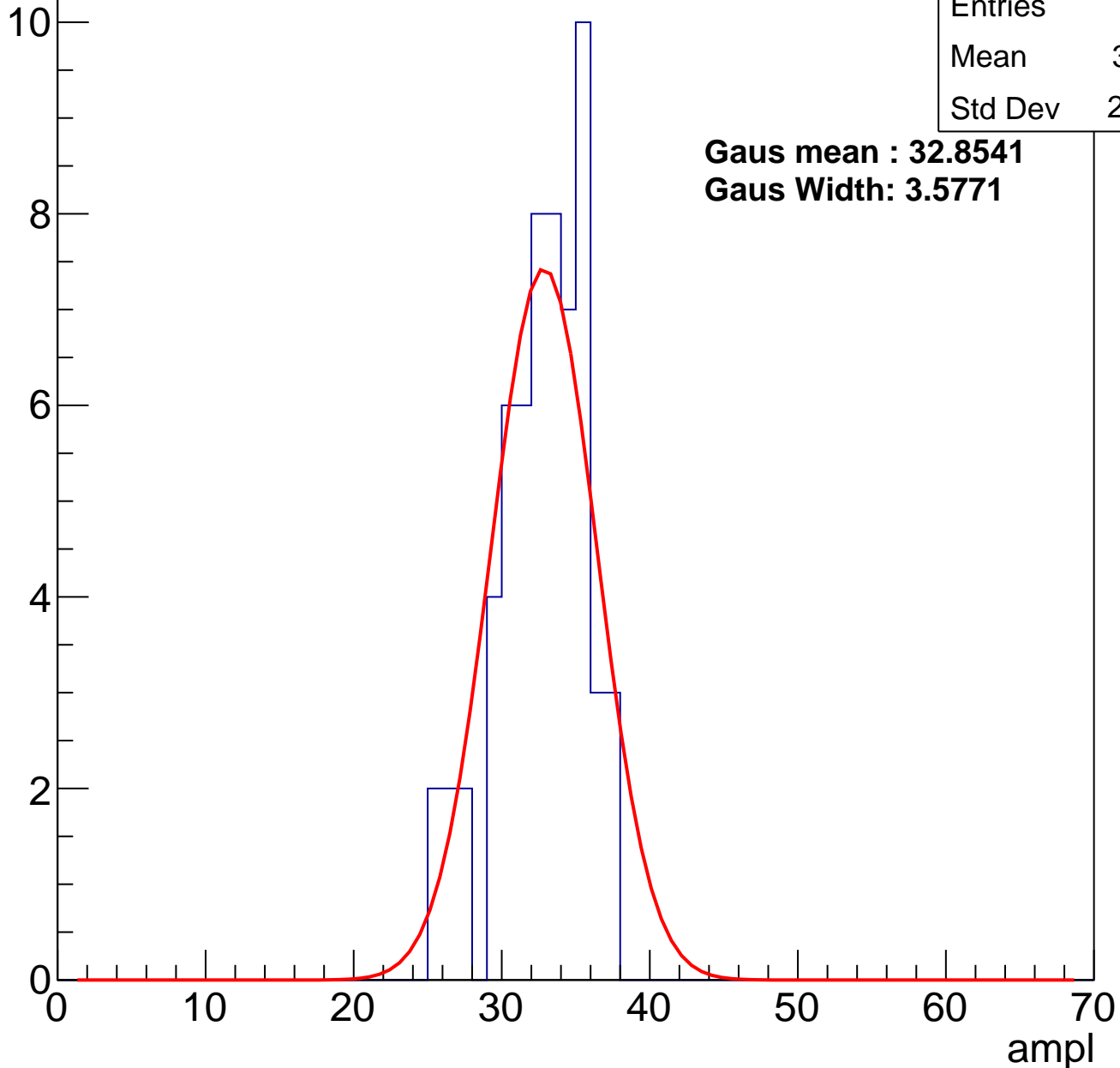
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	32.21
Std Dev	2.954

**Gaus mean : 32.8541**

**Gaus Width: 3.5771**

Entry

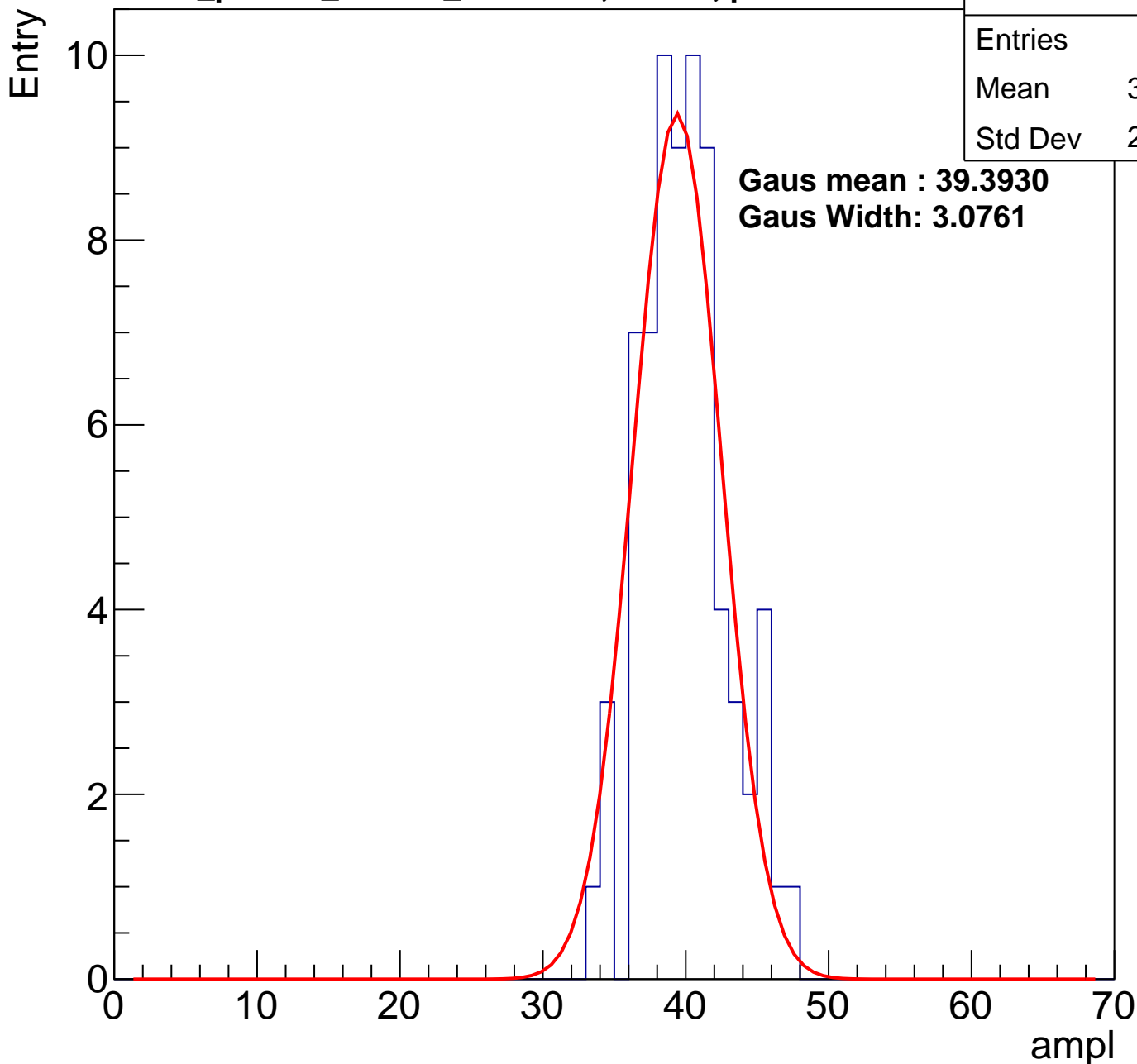


# B1L002S, U3-ch2, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	39.49
Std Dev	2.983

**Gaus mean : 39.3930**  
**Gaus Width: 3.0761**

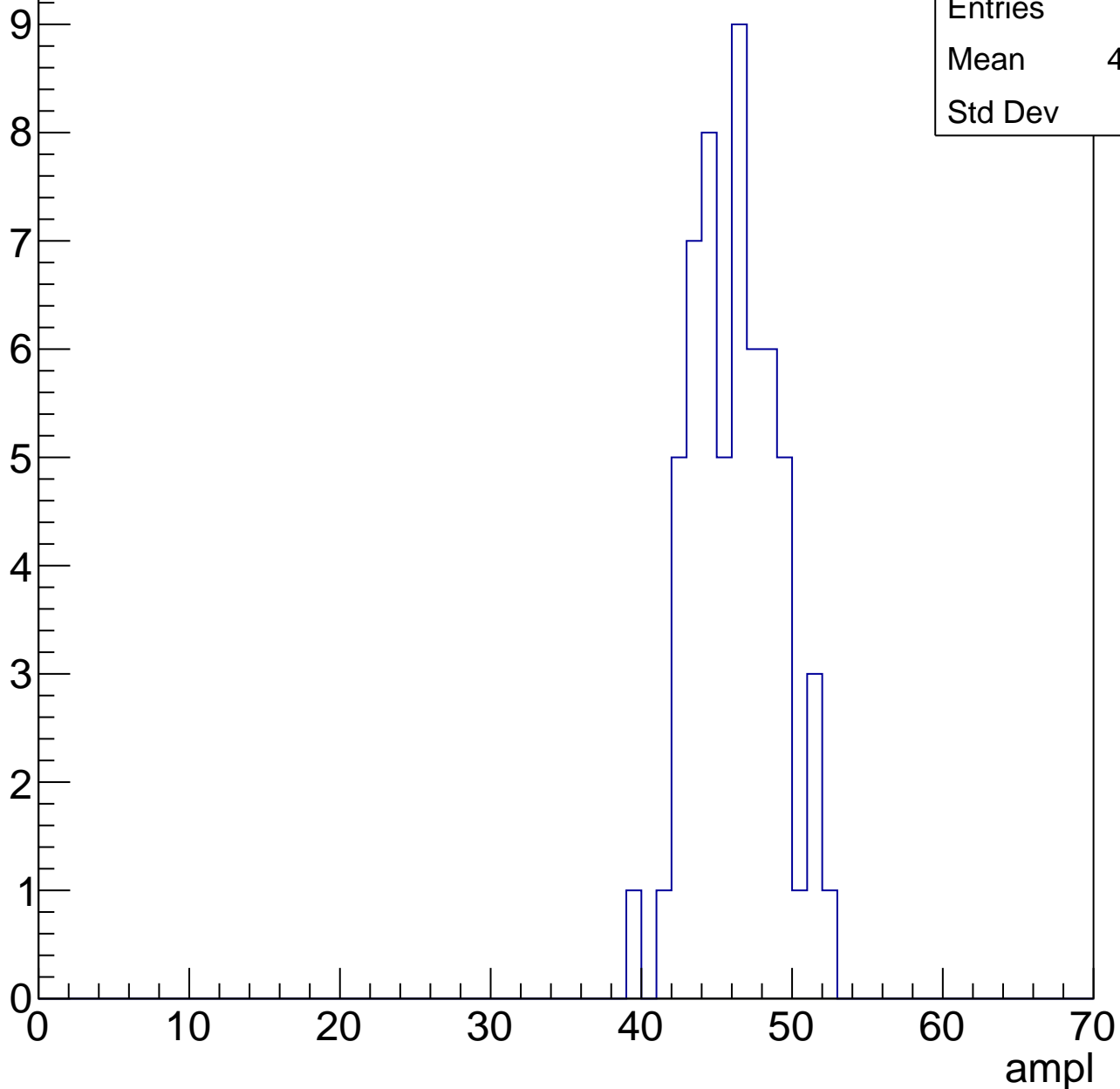


# B1L002S, U3-ch2, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	45.72
Std Dev	2.79

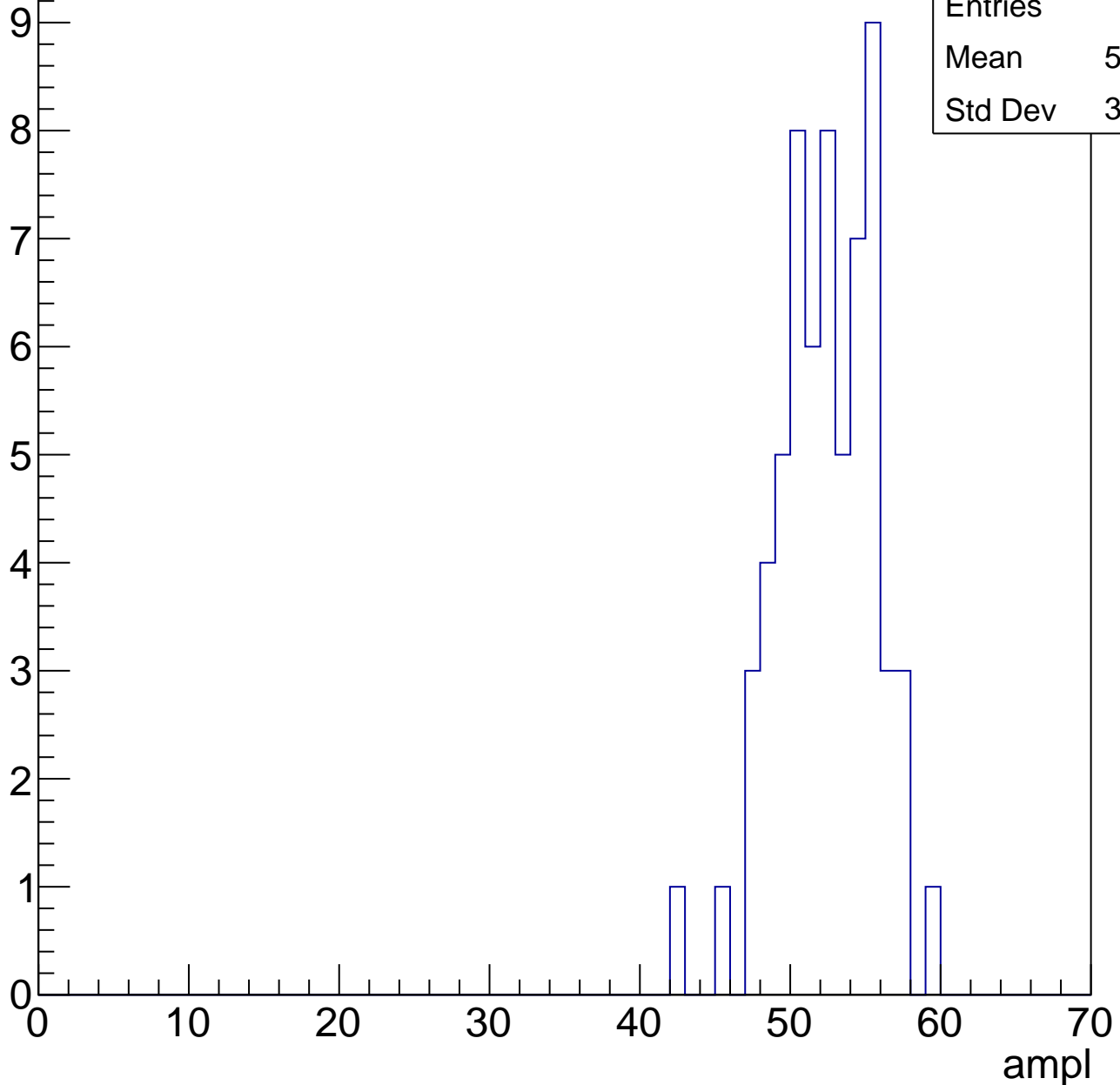


# B1L002S, U3-ch2, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

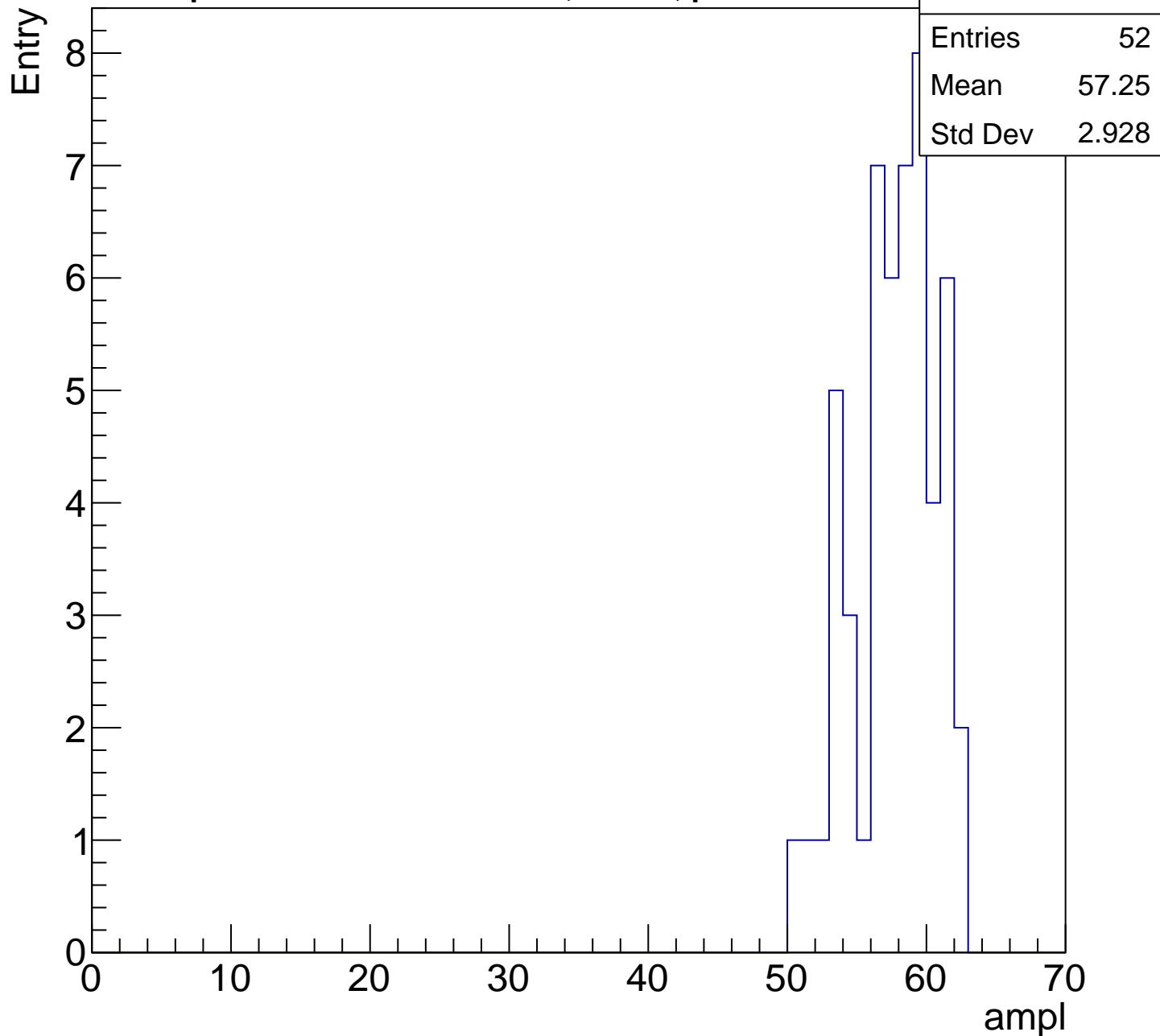
Entry

Entries	64
Mean	51.92
Std Dev	3.203



# B1L002S, U3-ch2, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

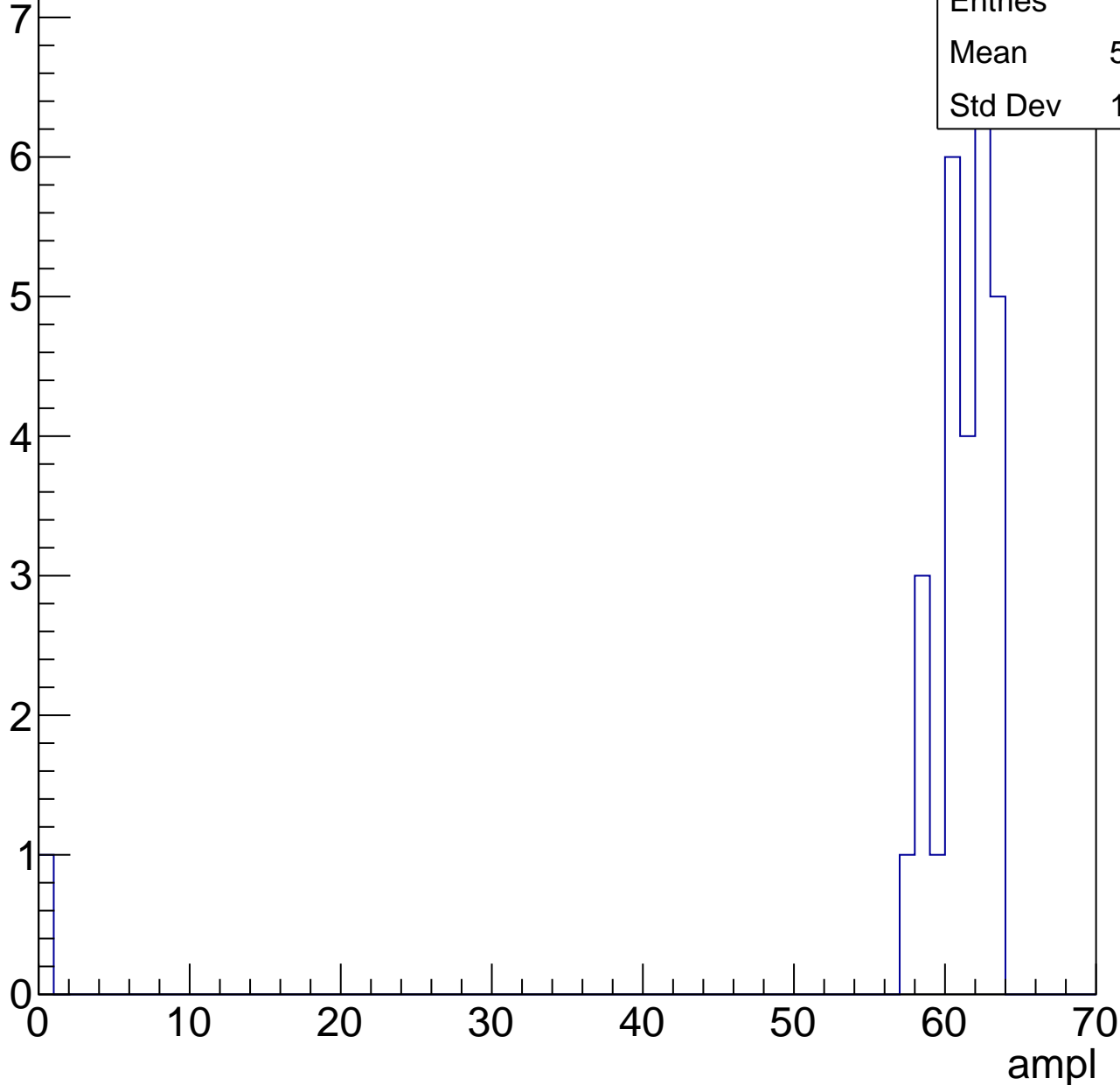


# B1L002S, U3-ch2, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.68
Std Dev	11.42



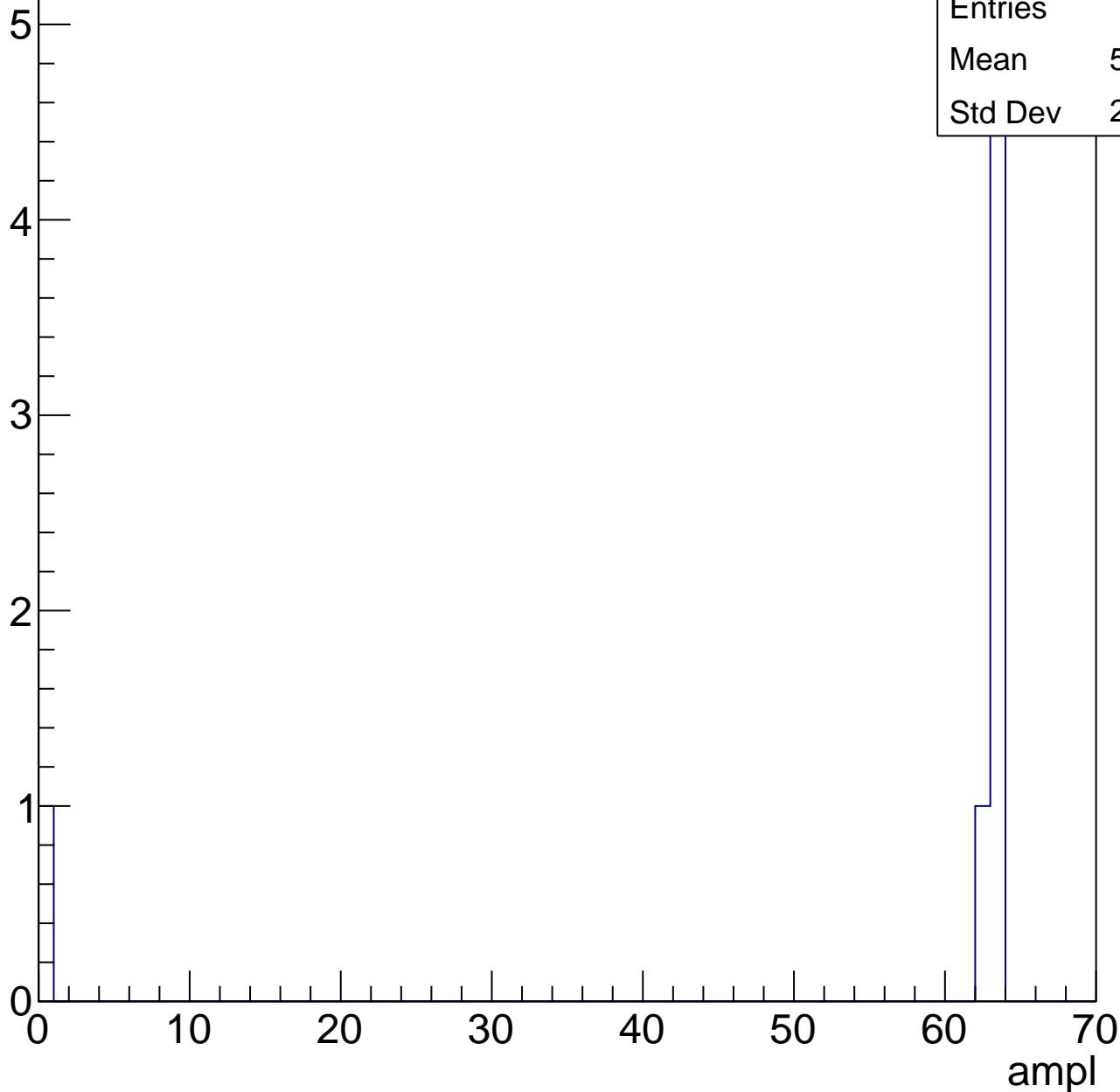


# B1L002S, U3-ch2, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	7
Mean	53.86
Std Dev	21.99



# B1L002S, U3-ch3, adc0

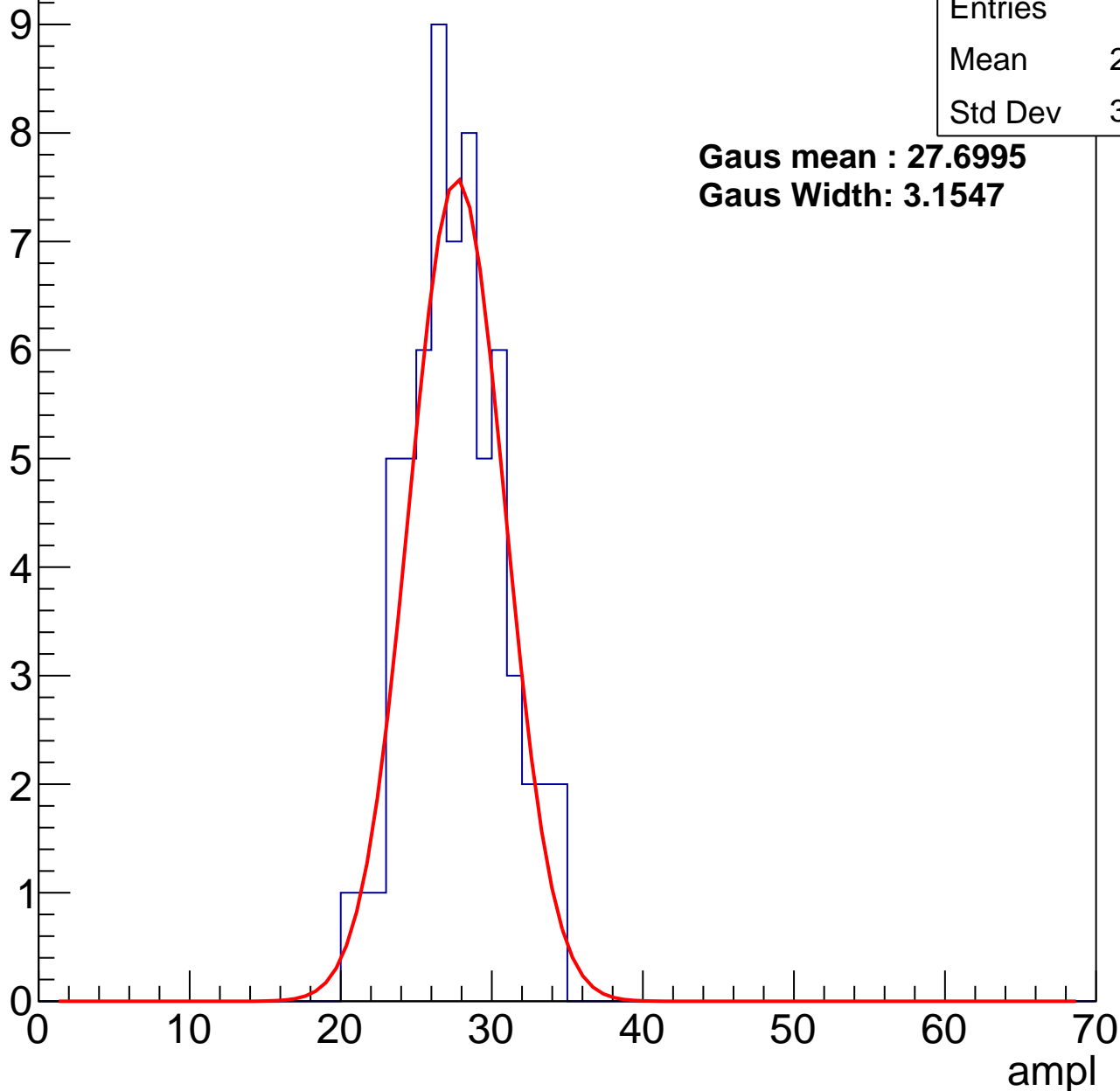
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	27.16
Std Dev	3.128

**Gaus mean : 27.6995**

**Gaus Width: 3.1547**



# B1L002S, U3-ch3, adc1

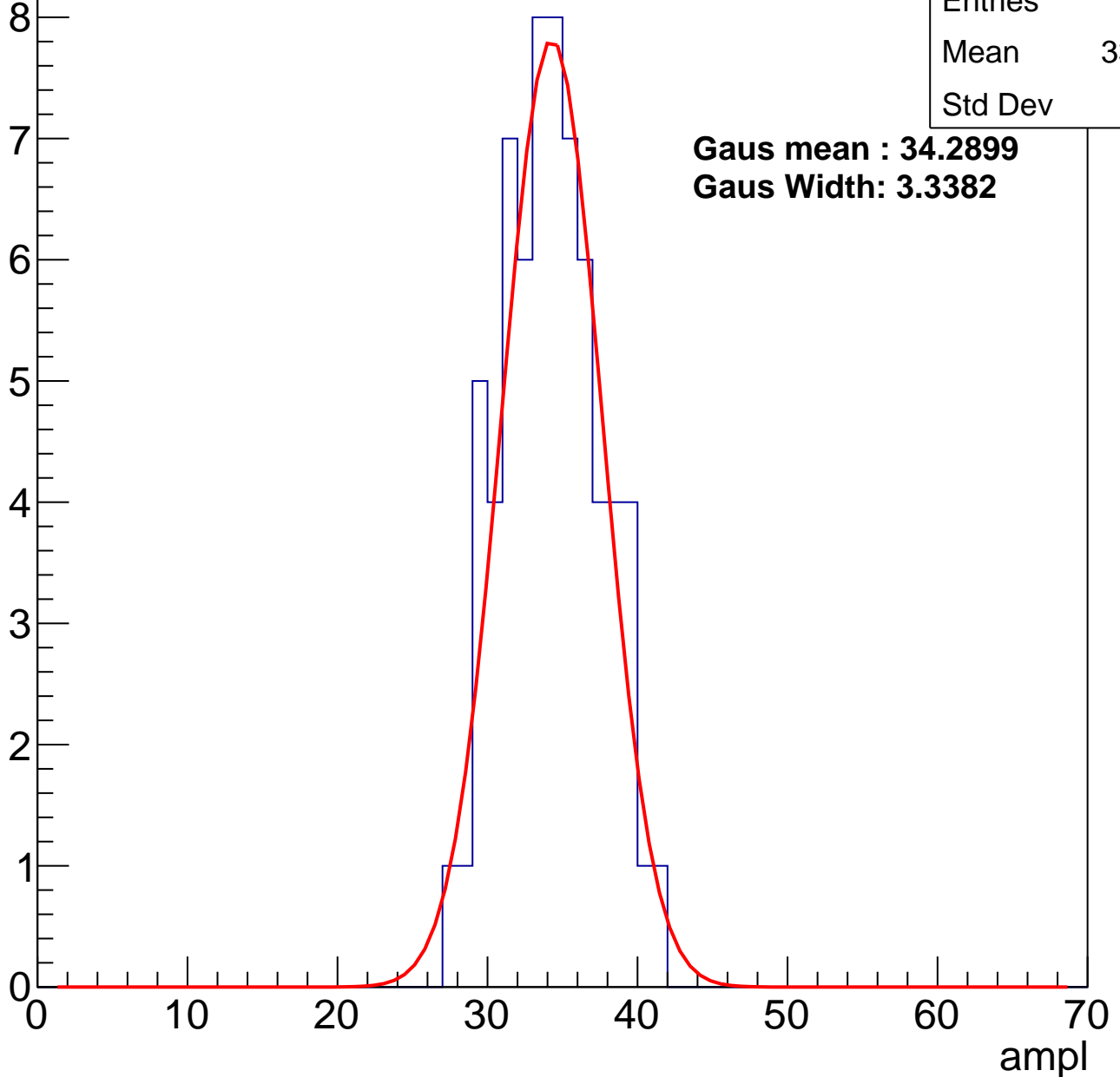
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.78
Std Dev	3.19

**Gaus mean : 34.2899**

**Gaus Width: 3.3382**



# B1L002S, U3-ch3, adc2

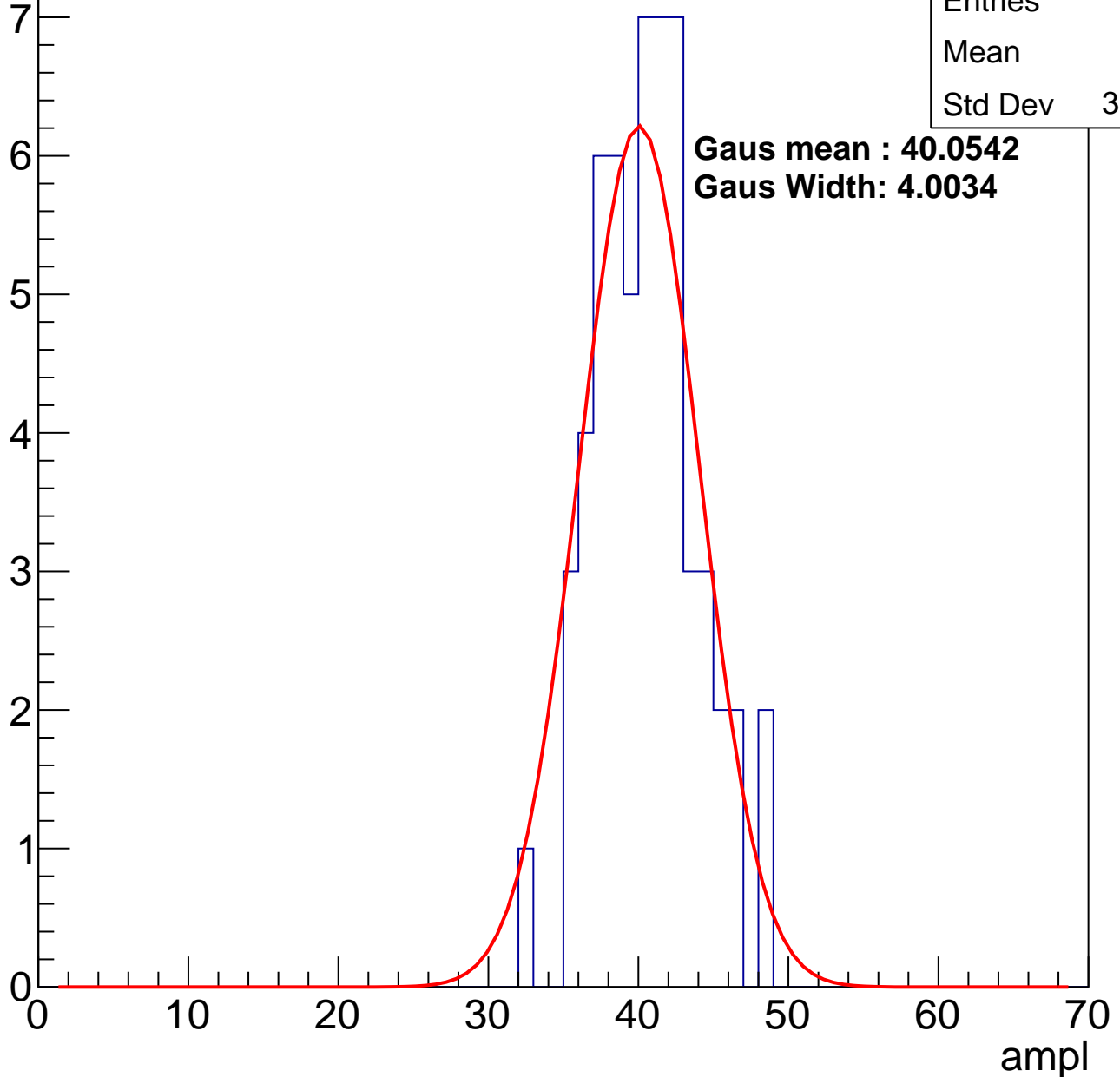
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.1
Std Dev	3.336

**Gaus mean : 40.0542**

**Gaus Width: 4.0034**

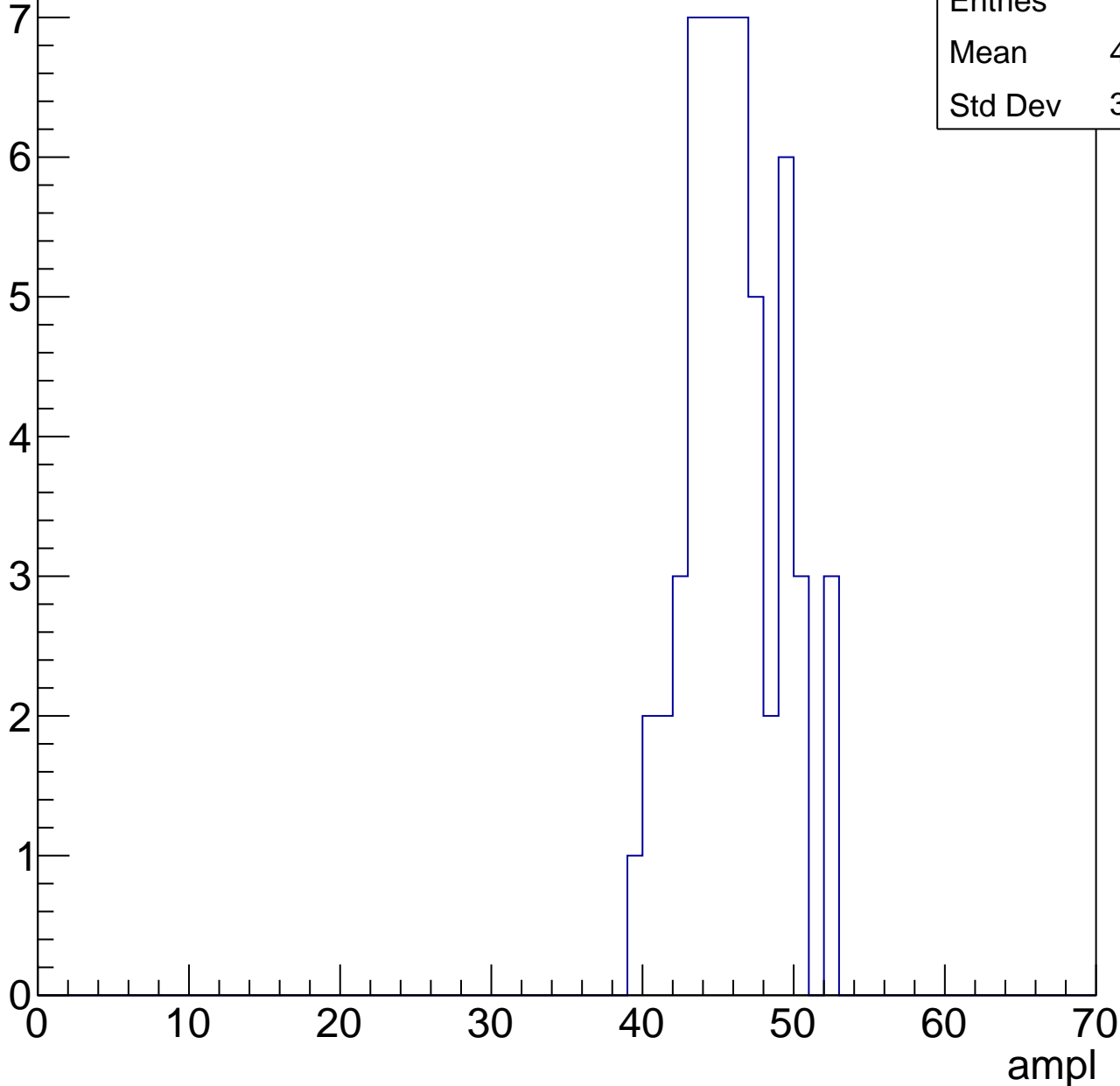


# B1L002S, U3-ch3, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	45.53
Std Dev	3.098

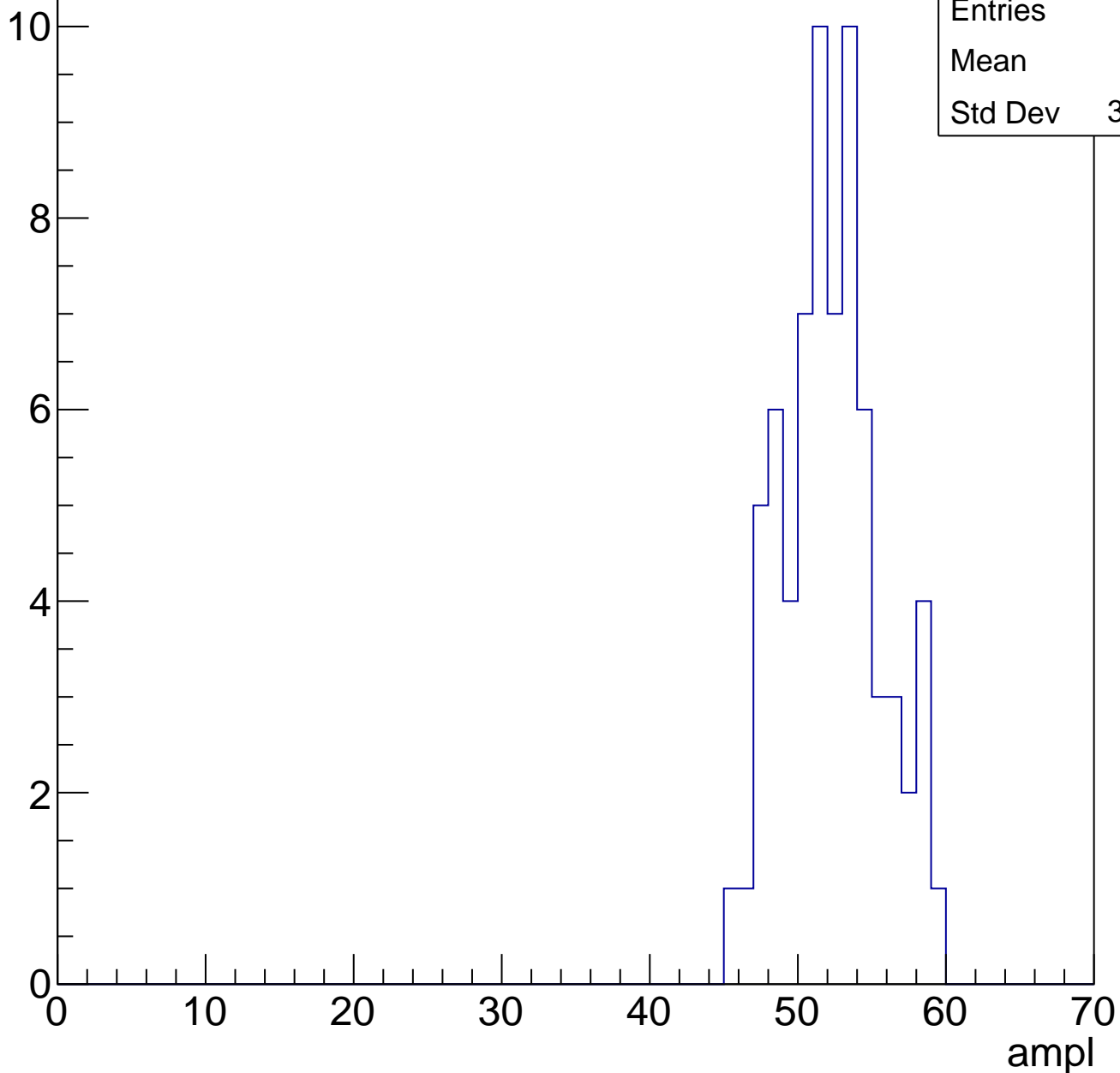


# B1L002S, U3-ch3, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	51.8
Std Dev	3.228

Entry

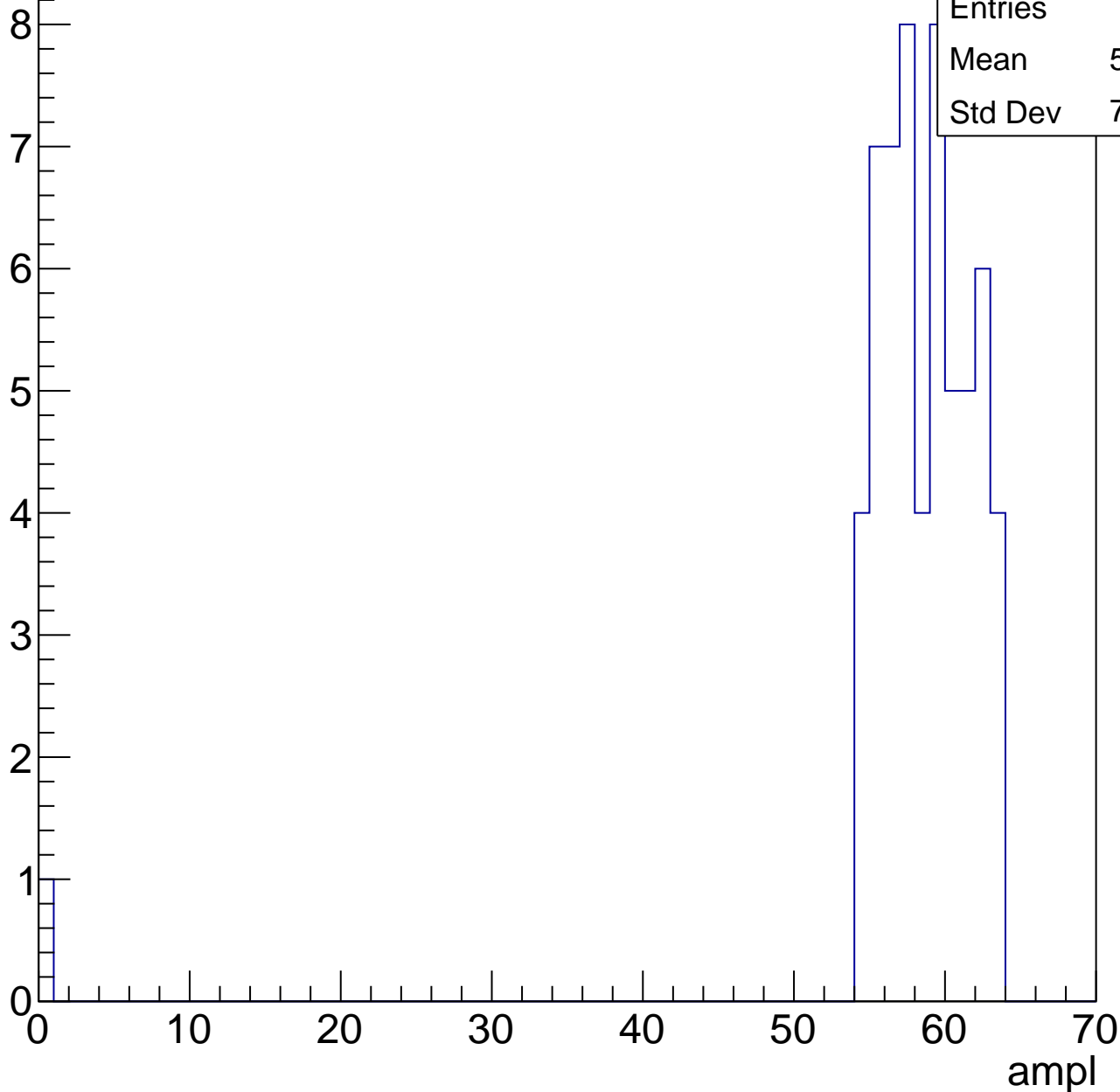


# B1L002S, U3-ch3, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

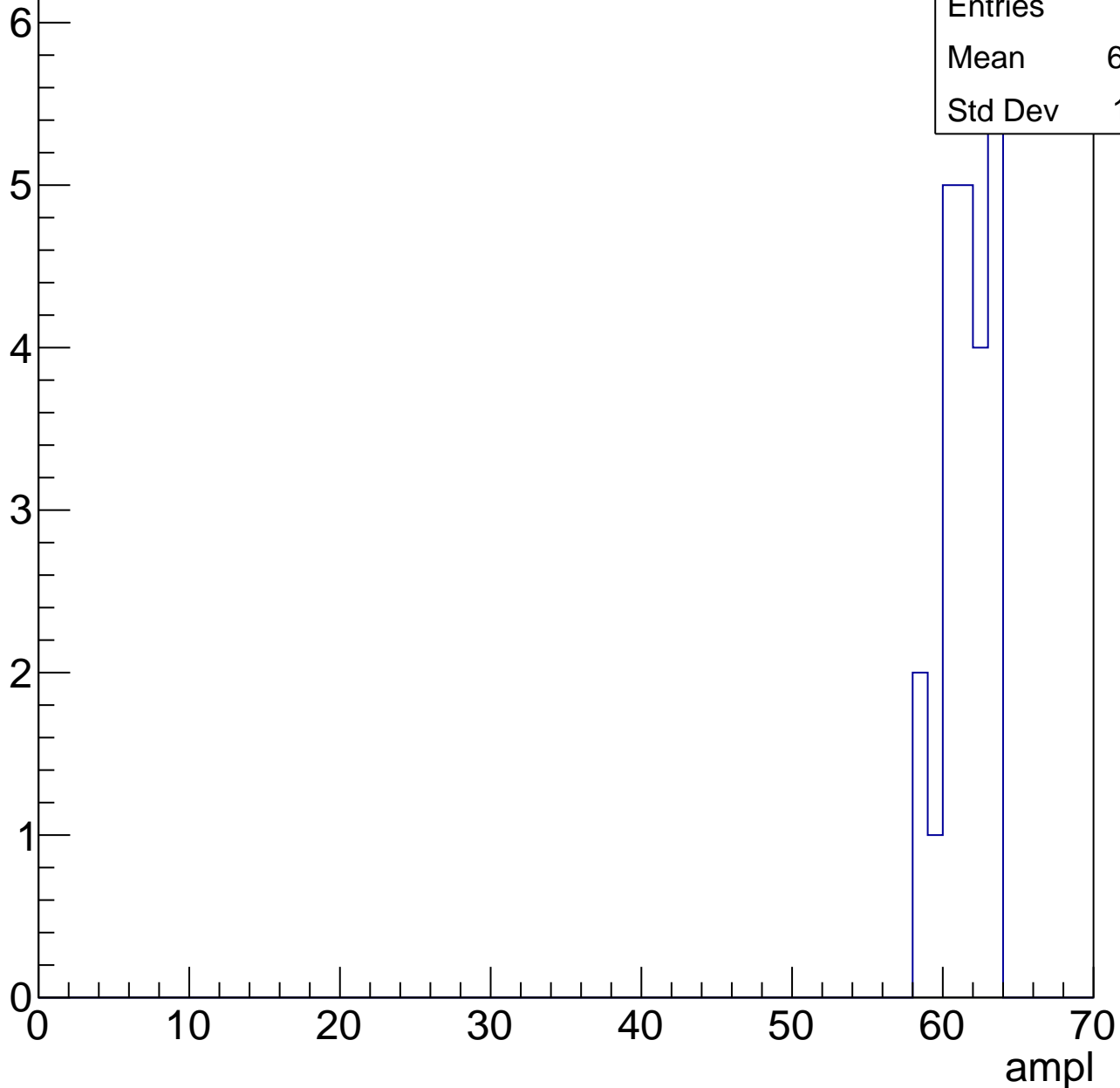
Entries	59
Mean	57.32
Std Dev	7.992



# B1L002S, U3-ch3, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch3, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	0
Std Dev	0

# B1L002S, U3-ch4, adc0

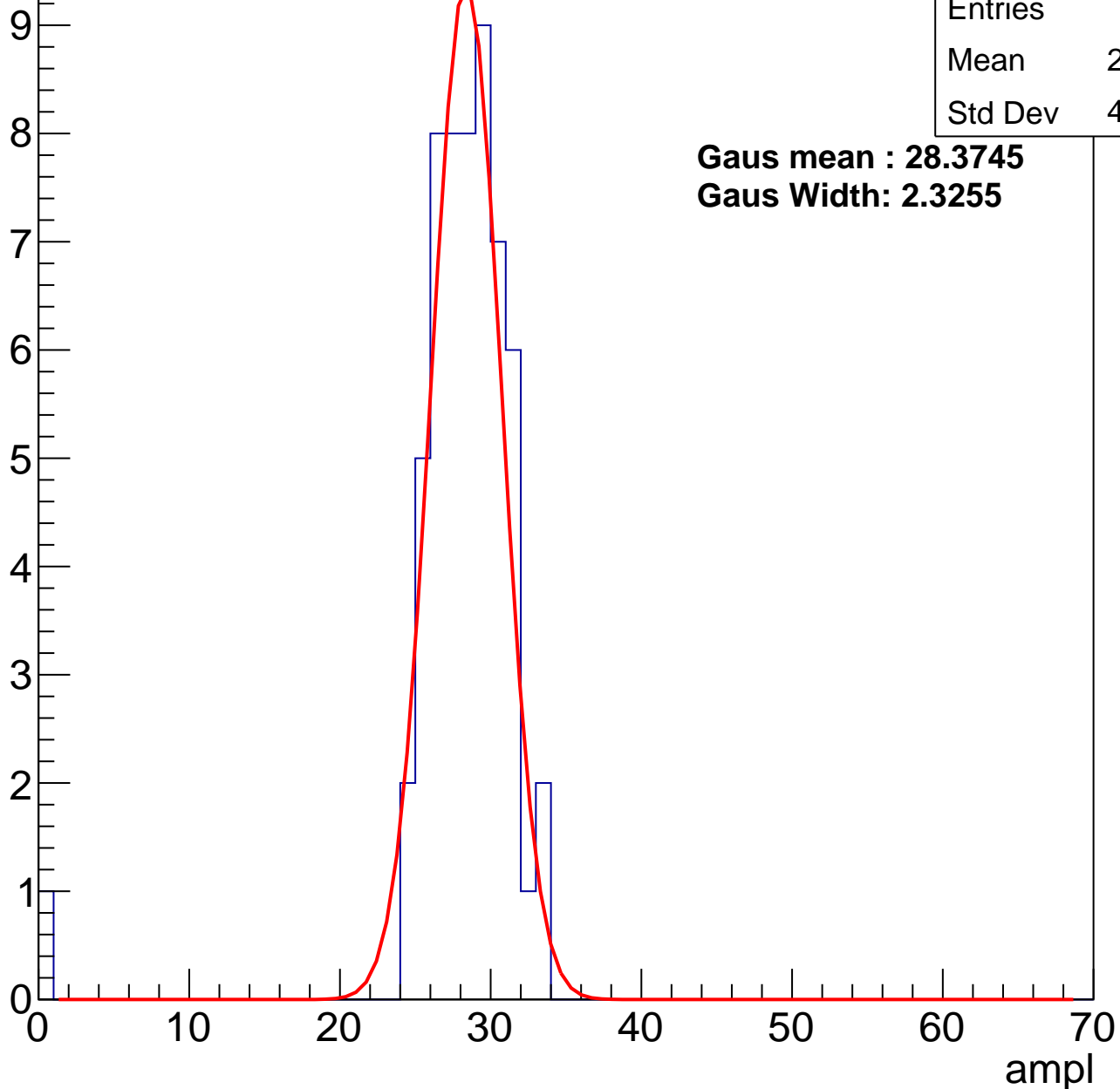
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	27.65
Std Dev	4.294

**Gaus mean : 28.3745**

**Gaus Width: 2.3255**



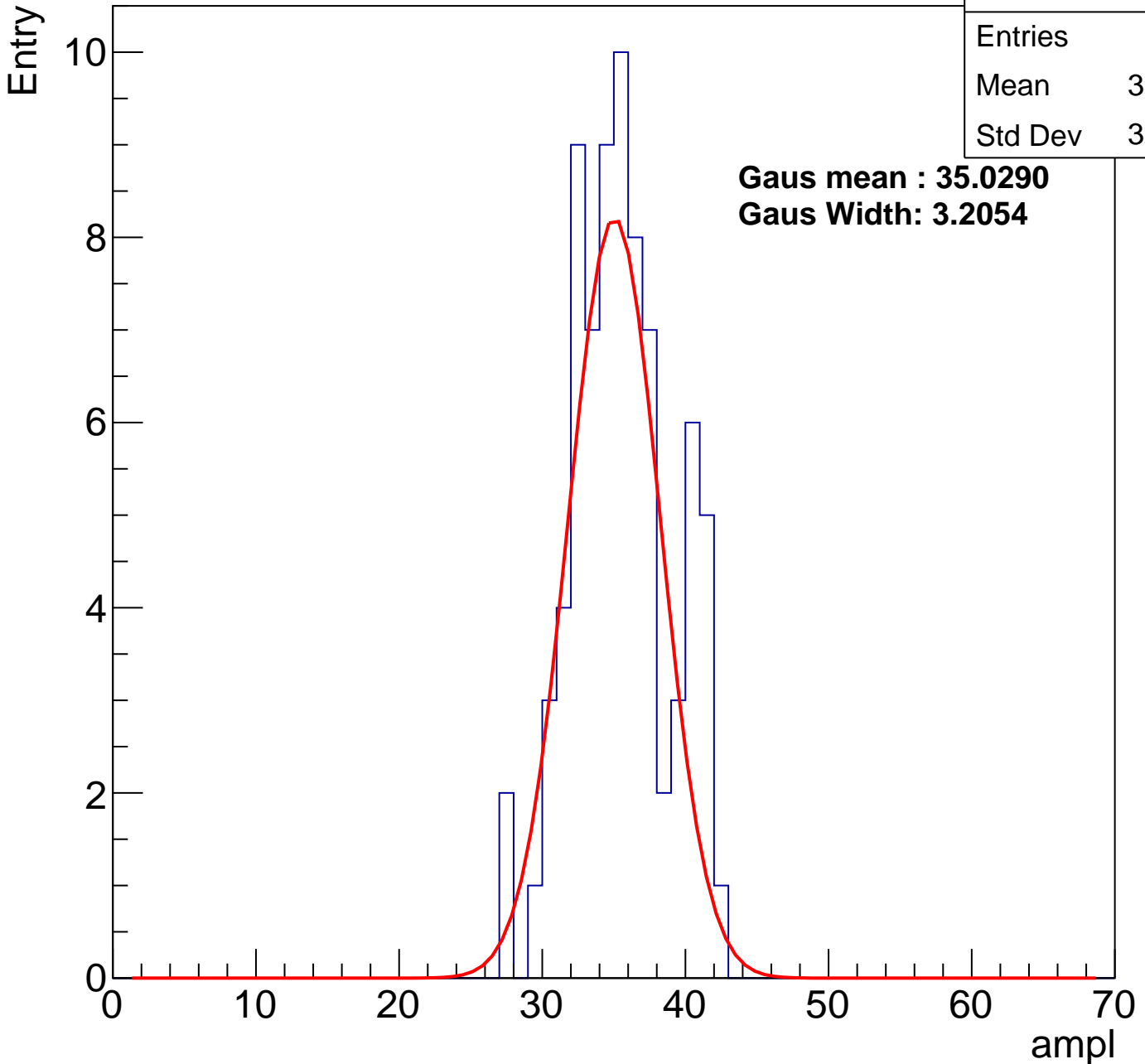
# B1L002S, U3-ch4, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	35.05
Std Dev	3.422

**Gaus mean : 35.0290**

**Gaus Width: 3.2054**



# B1L002S, U3-ch4, adc2

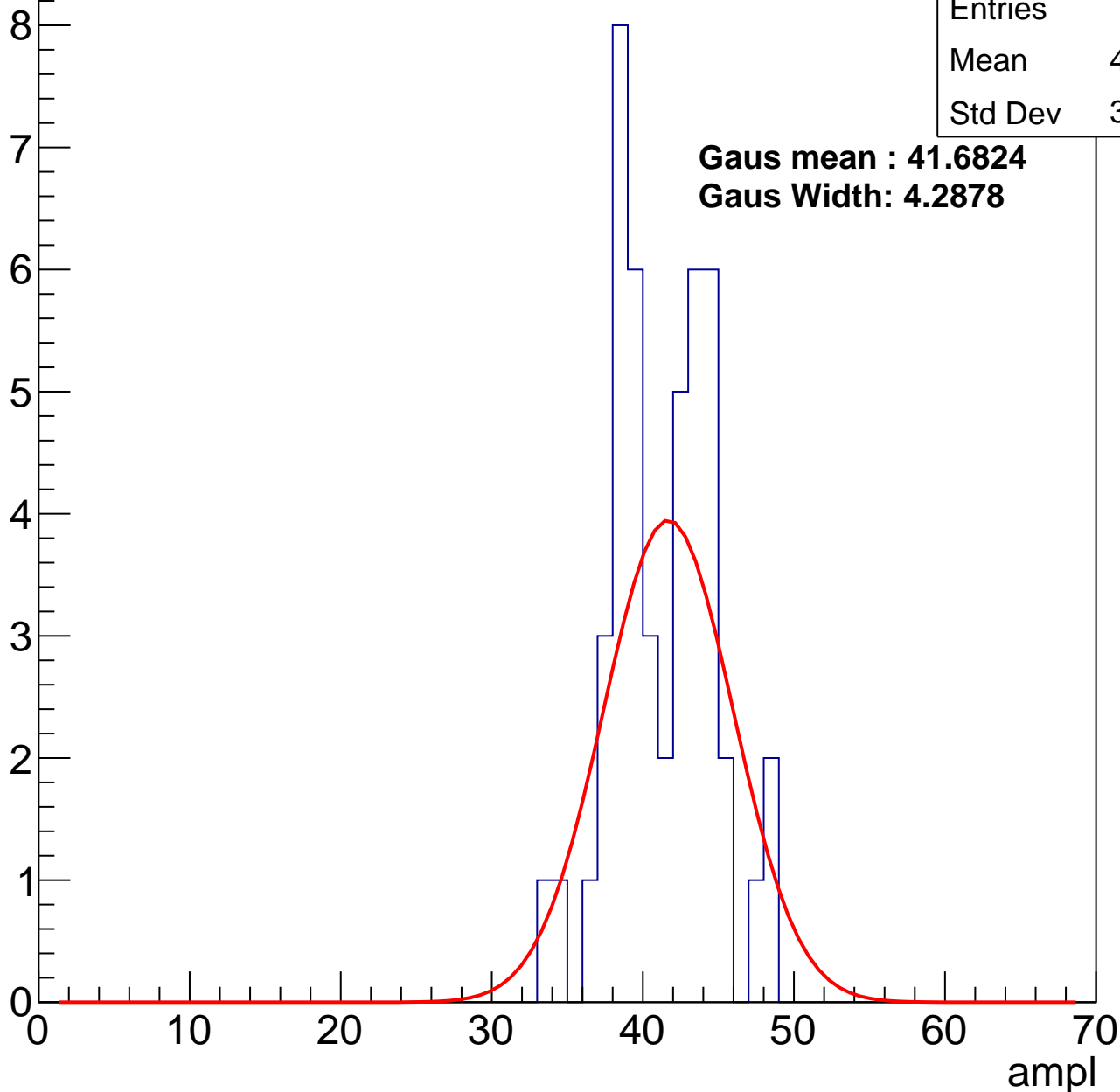
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	40.83
Std Dev	3.366

**Gaus mean : 41.6824**

**Gaus Width: 4.2878**

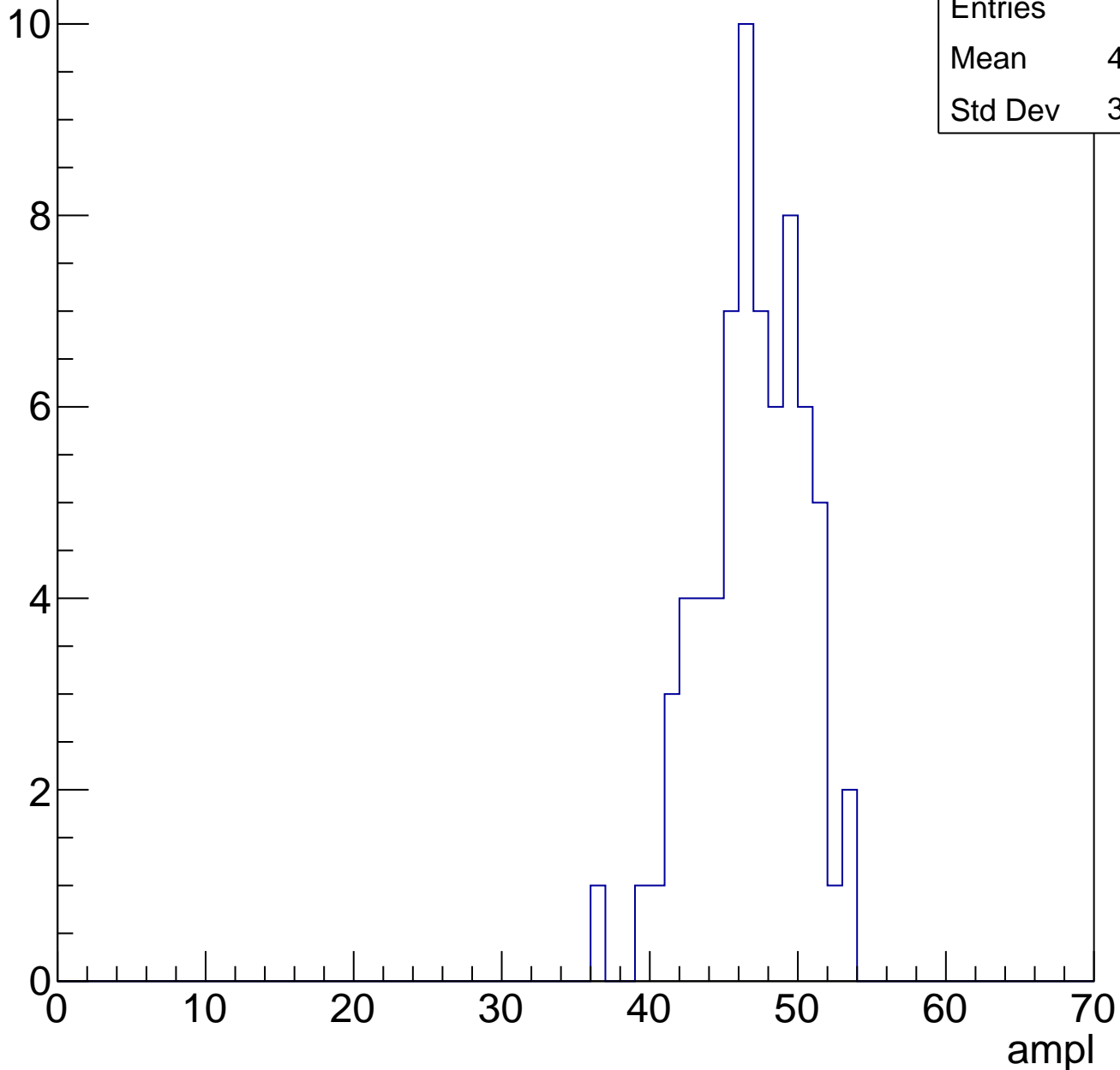


# B1L002S, U3-ch4, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	46.44
Std Dev	3.438

Entry

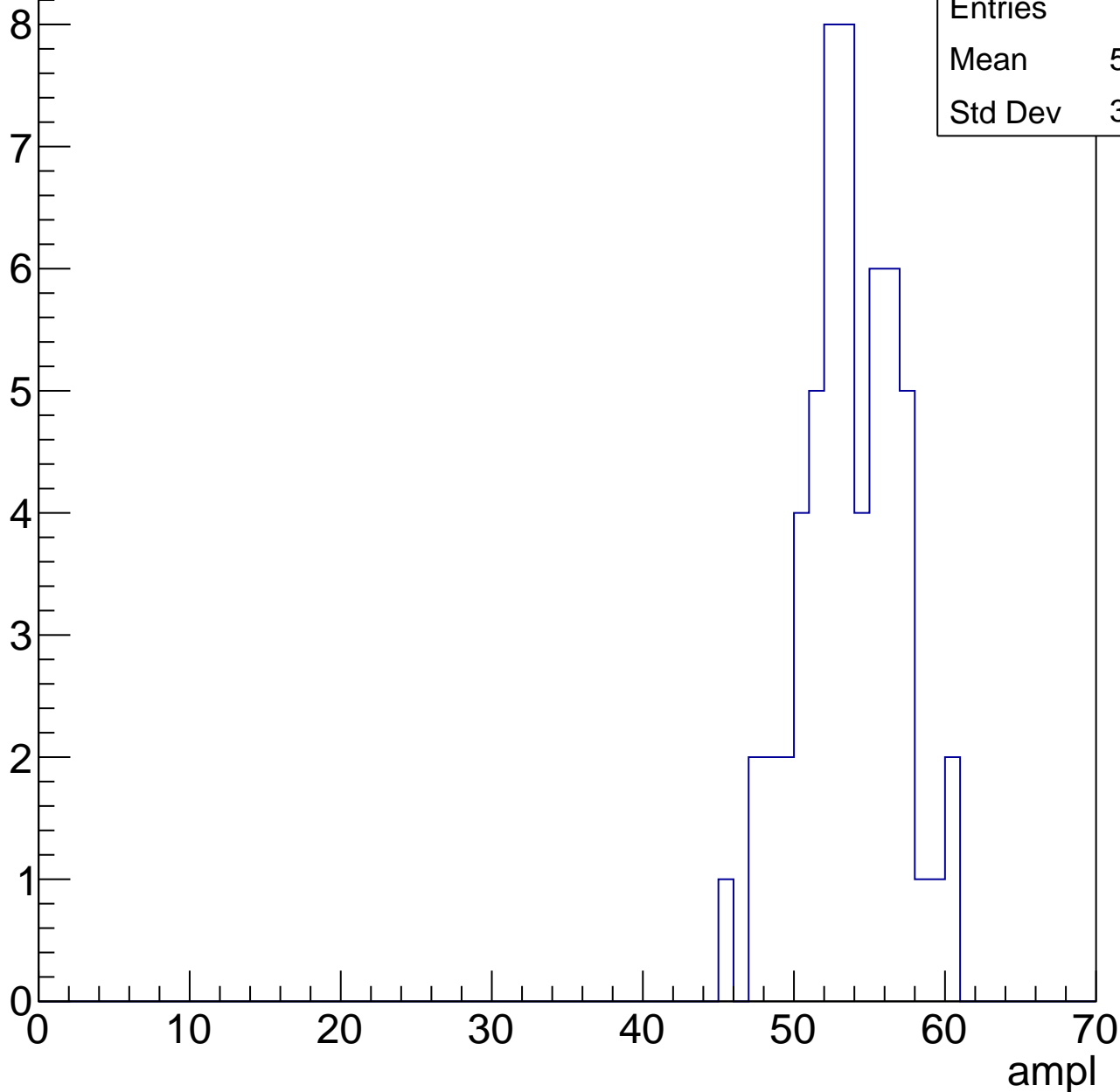


# B1L002S, U3-ch4, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.19
Std Dev	3.247

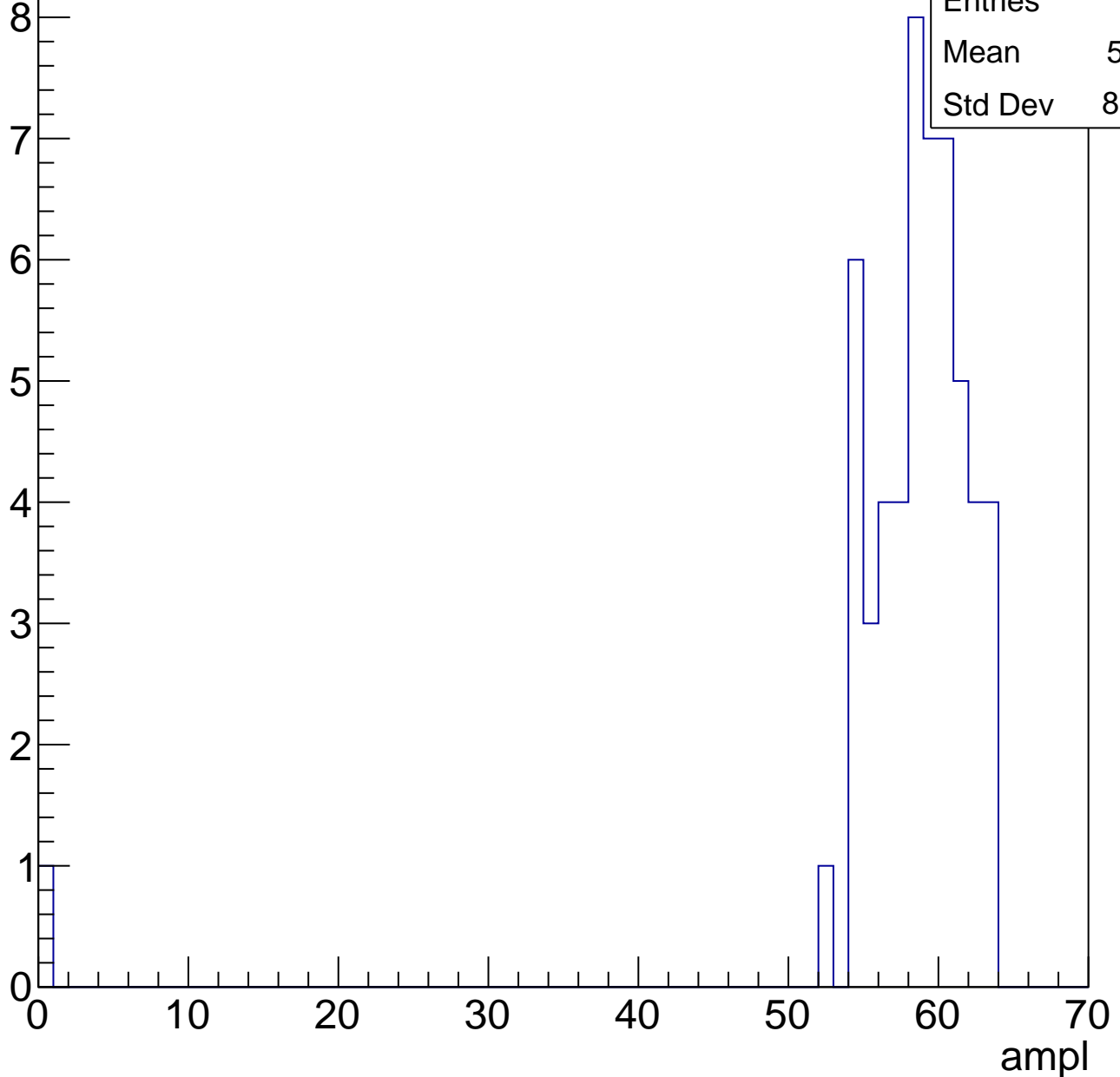


# B1L002S, U3-ch4, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.31
Std Dev	8.346

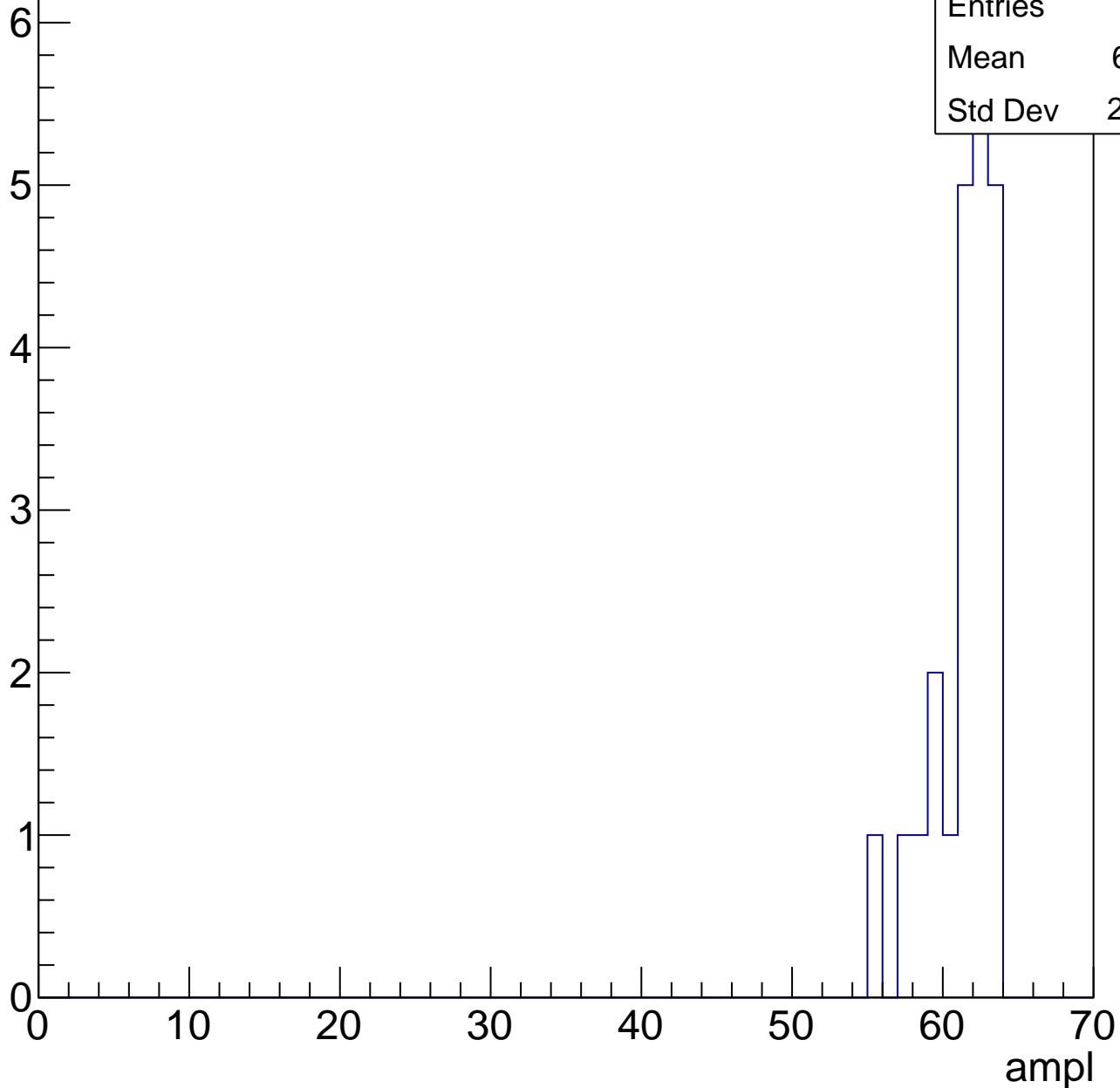


# B1L002S, U3-ch4, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	60.91
Std Dev	2.087

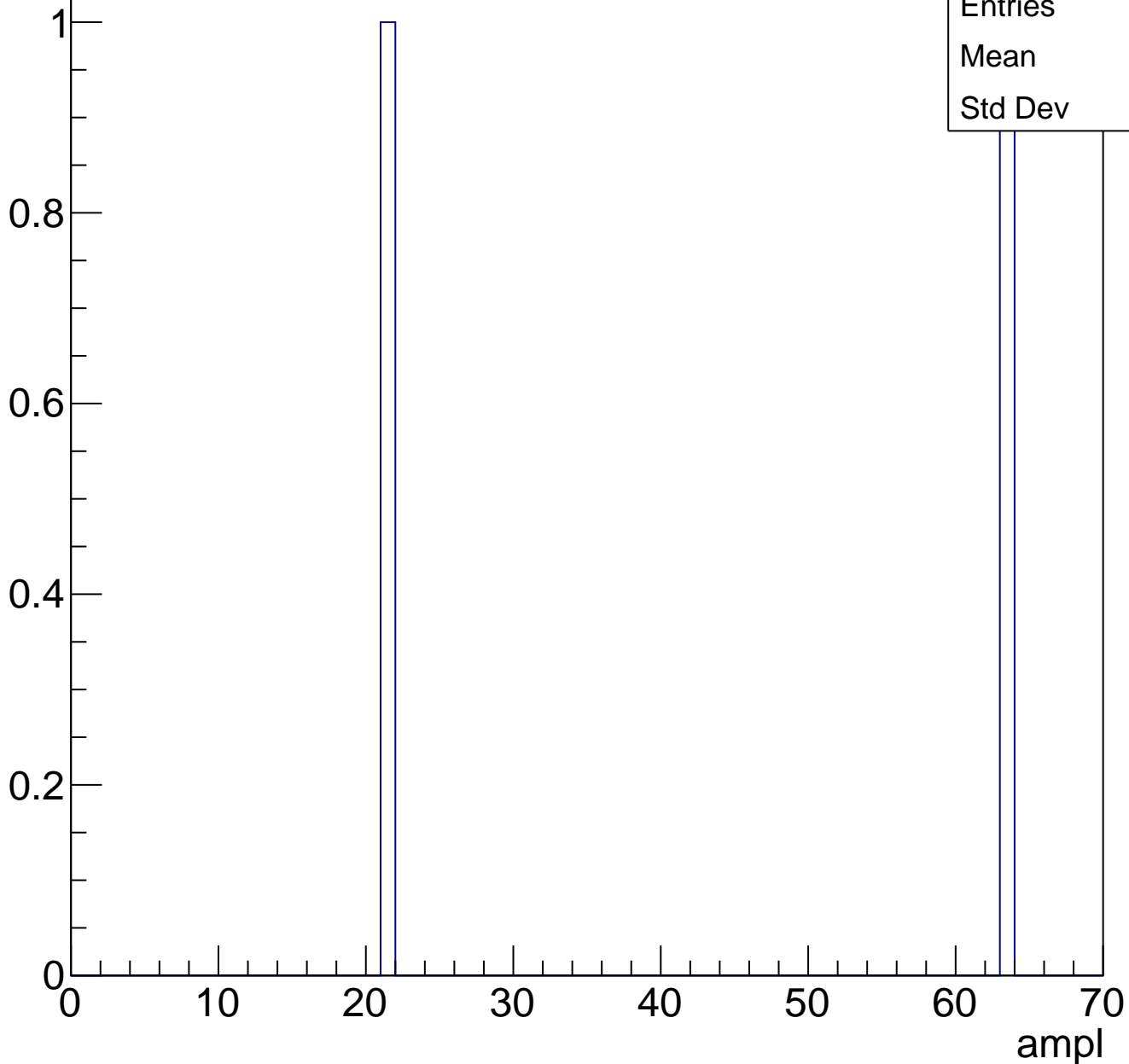




# B1L002S, U3-ch4, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch5, adc0

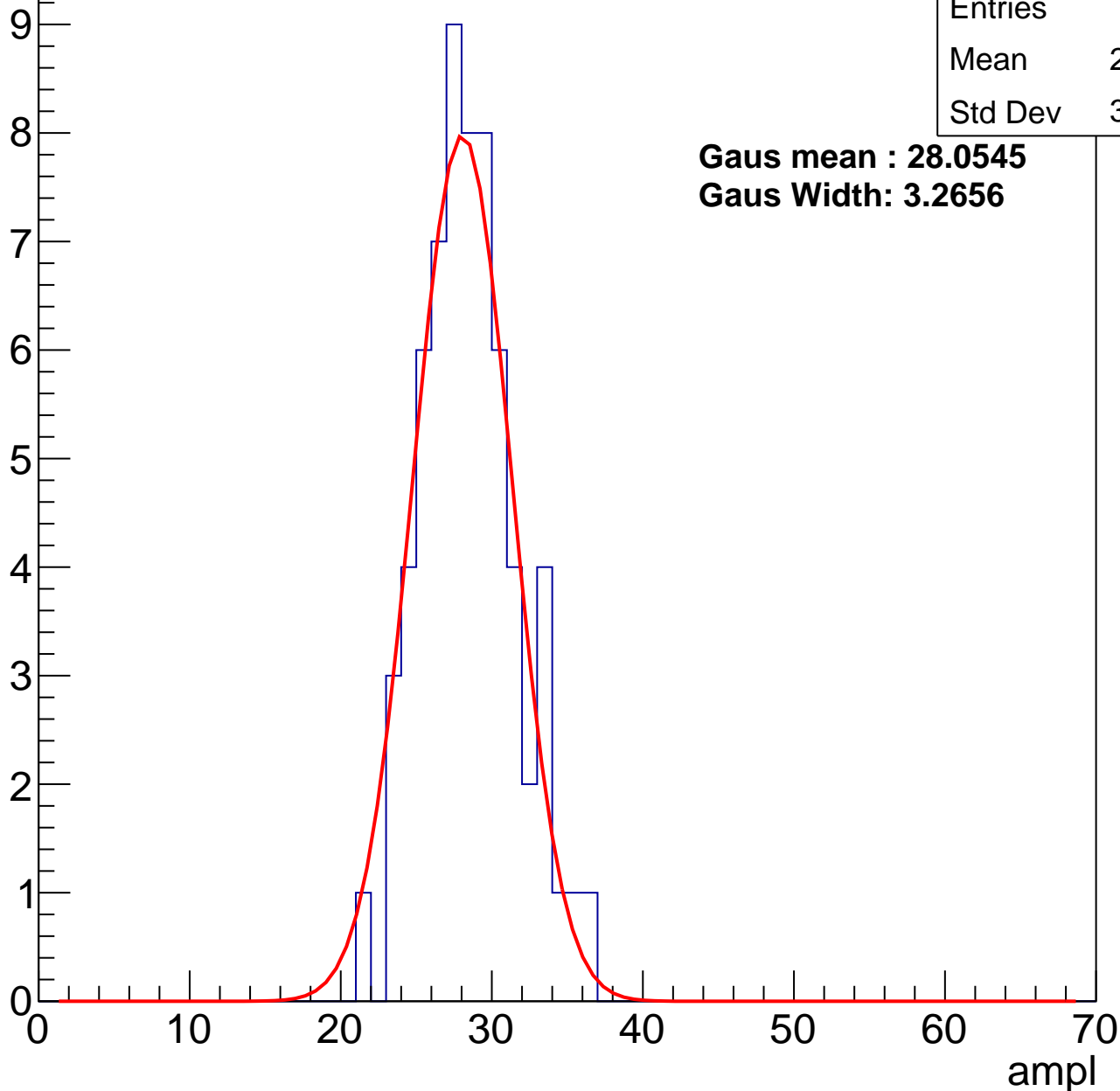
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.03
Std Dev	3.108

**Gaus mean : 28.0545**

**Gaus Width: 3.2656**



# B1L002S, U3-ch5, adc1

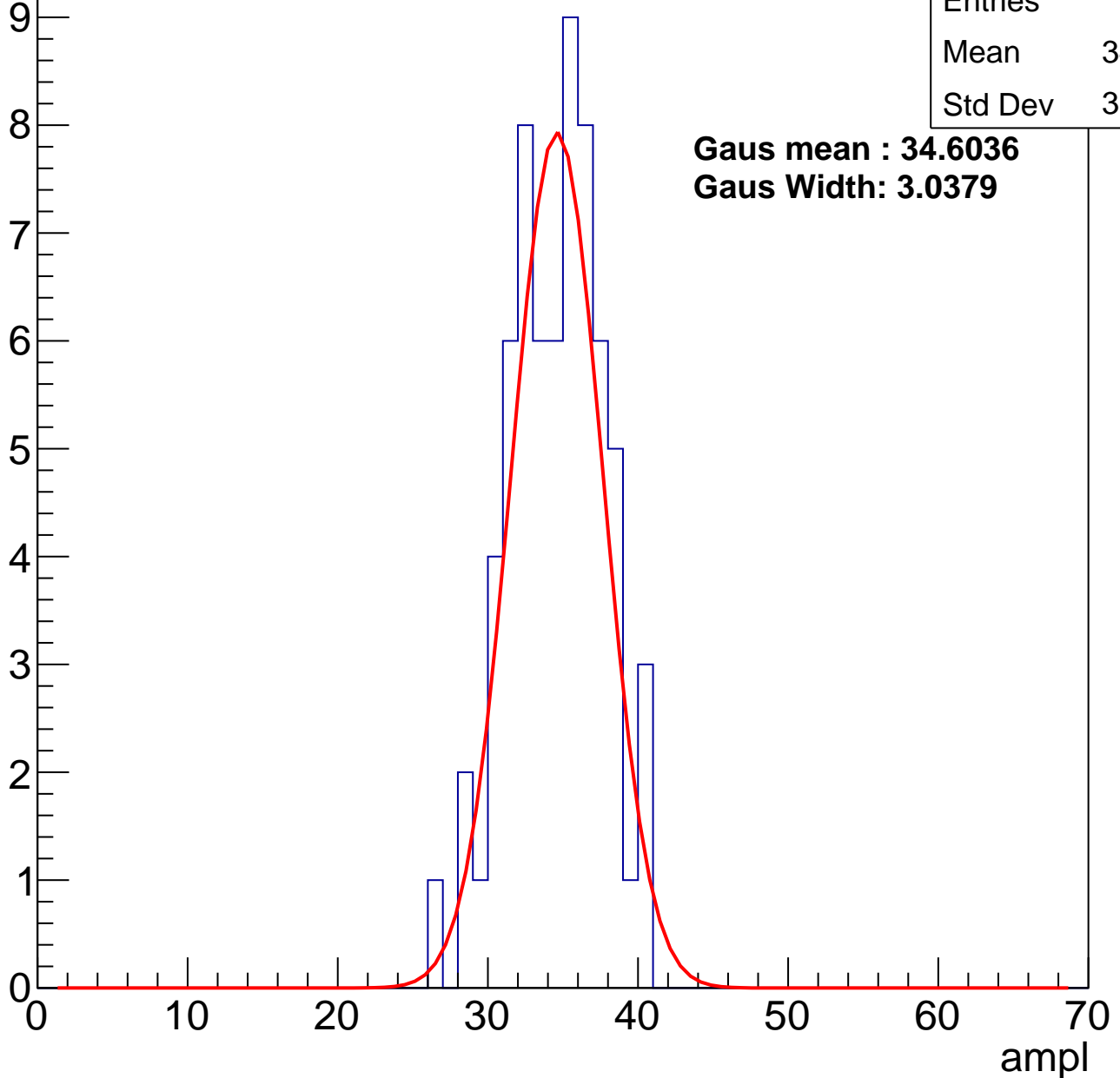
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.08
Std Dev	3.076

**Gaus mean : 34.6036**

**Gaus Width: 3.0379**



# B1L002S, U3-ch5, adc2

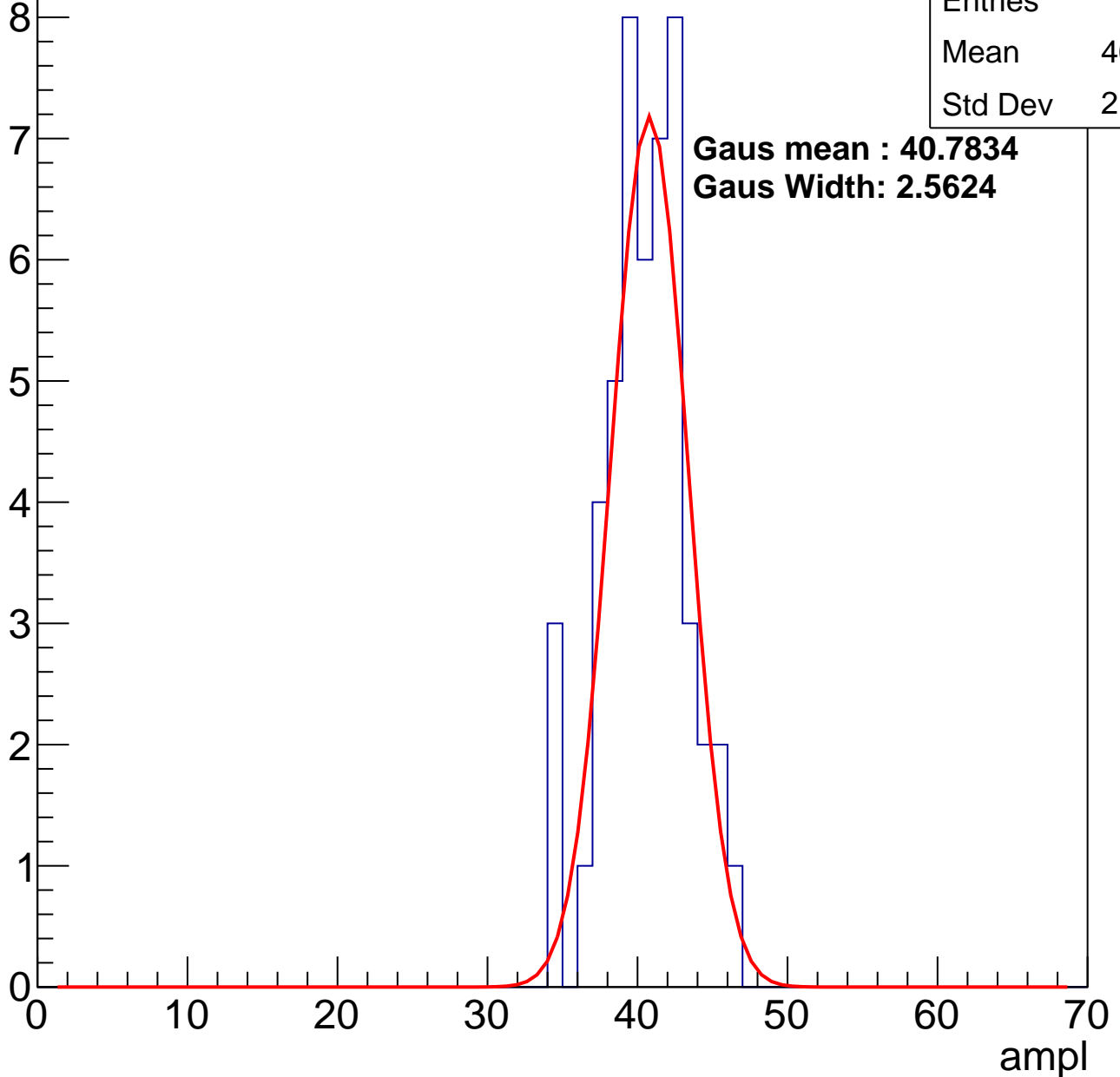
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	40.08
Std Dev	2.726

**Gaus mean : 40.7834**

**Gaus Width: 2.5624**

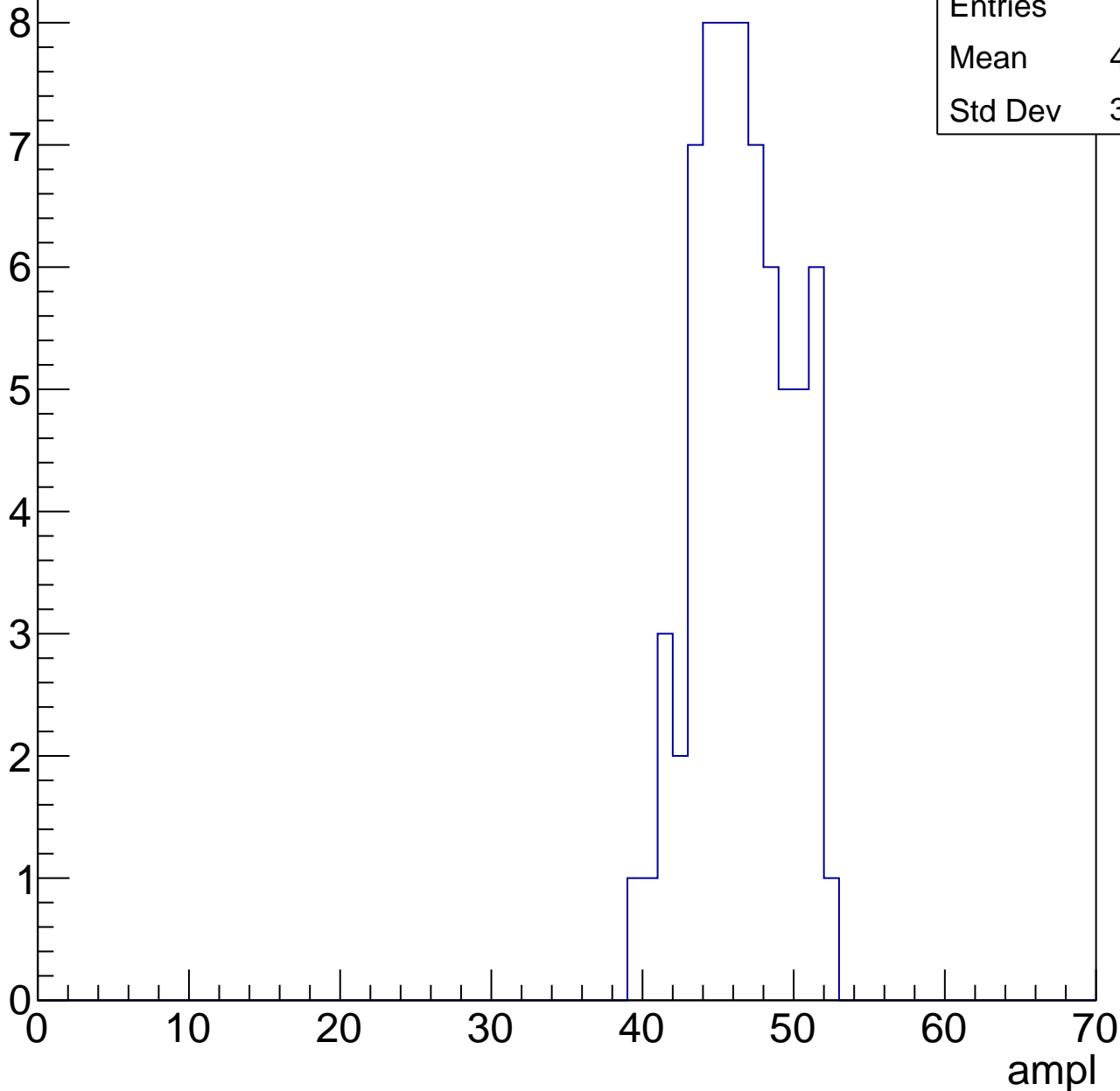


# B1L002S, U3-ch5, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

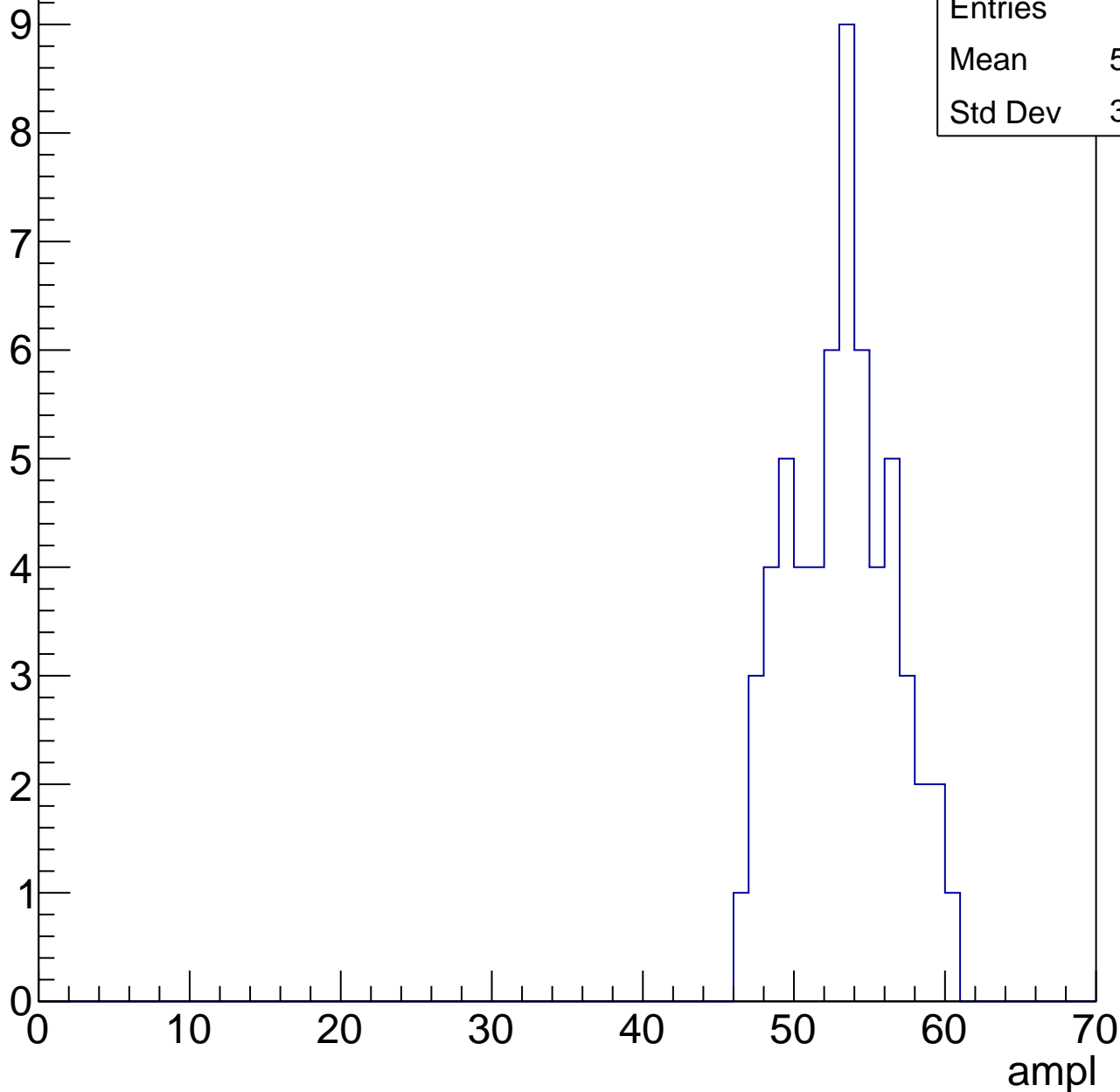
Entries	68
Mean	46.13
Std Dev	3.058



# B1L002S, U3-ch5, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

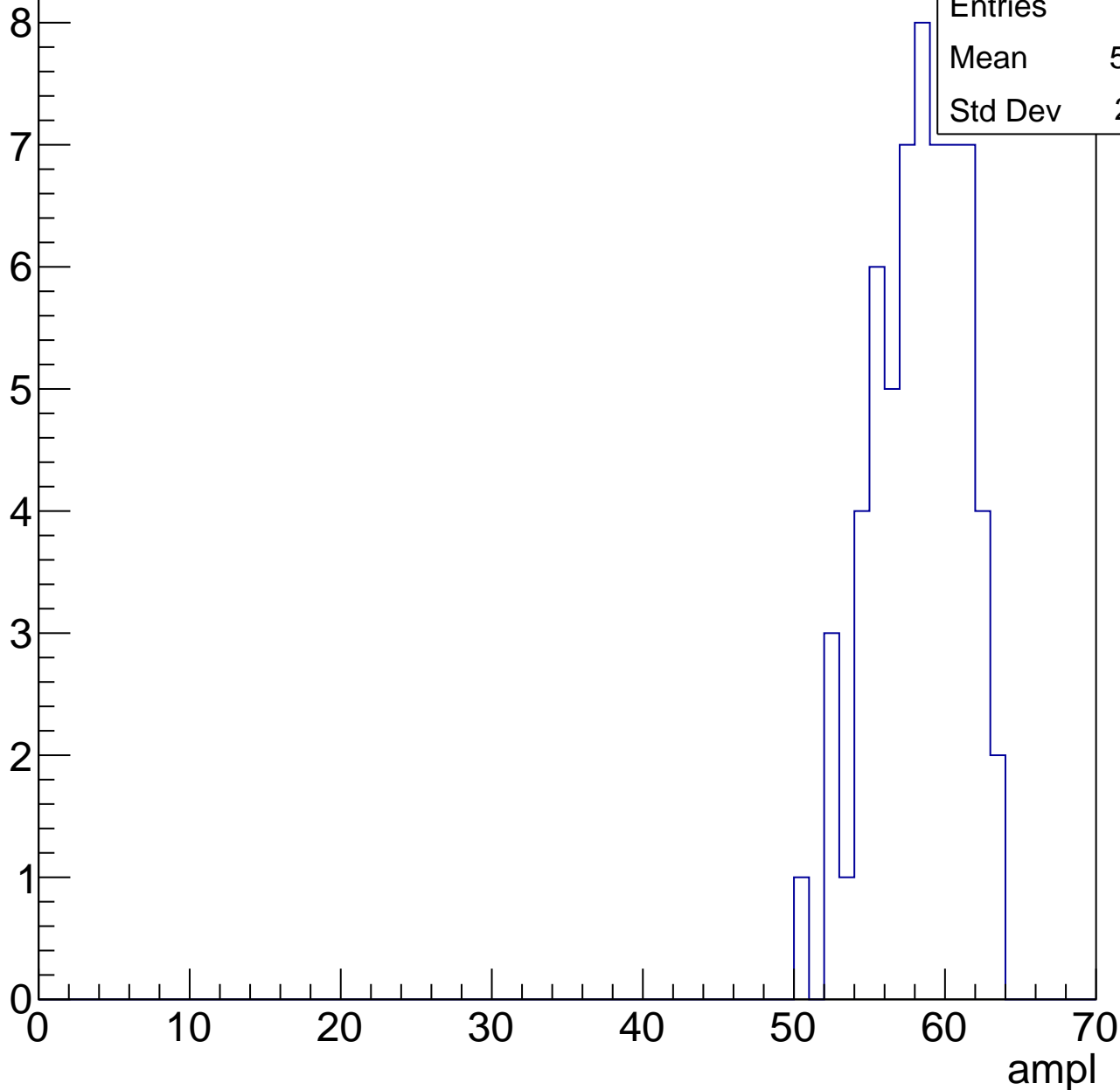
Entry



# B1L002S, U3-ch5, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

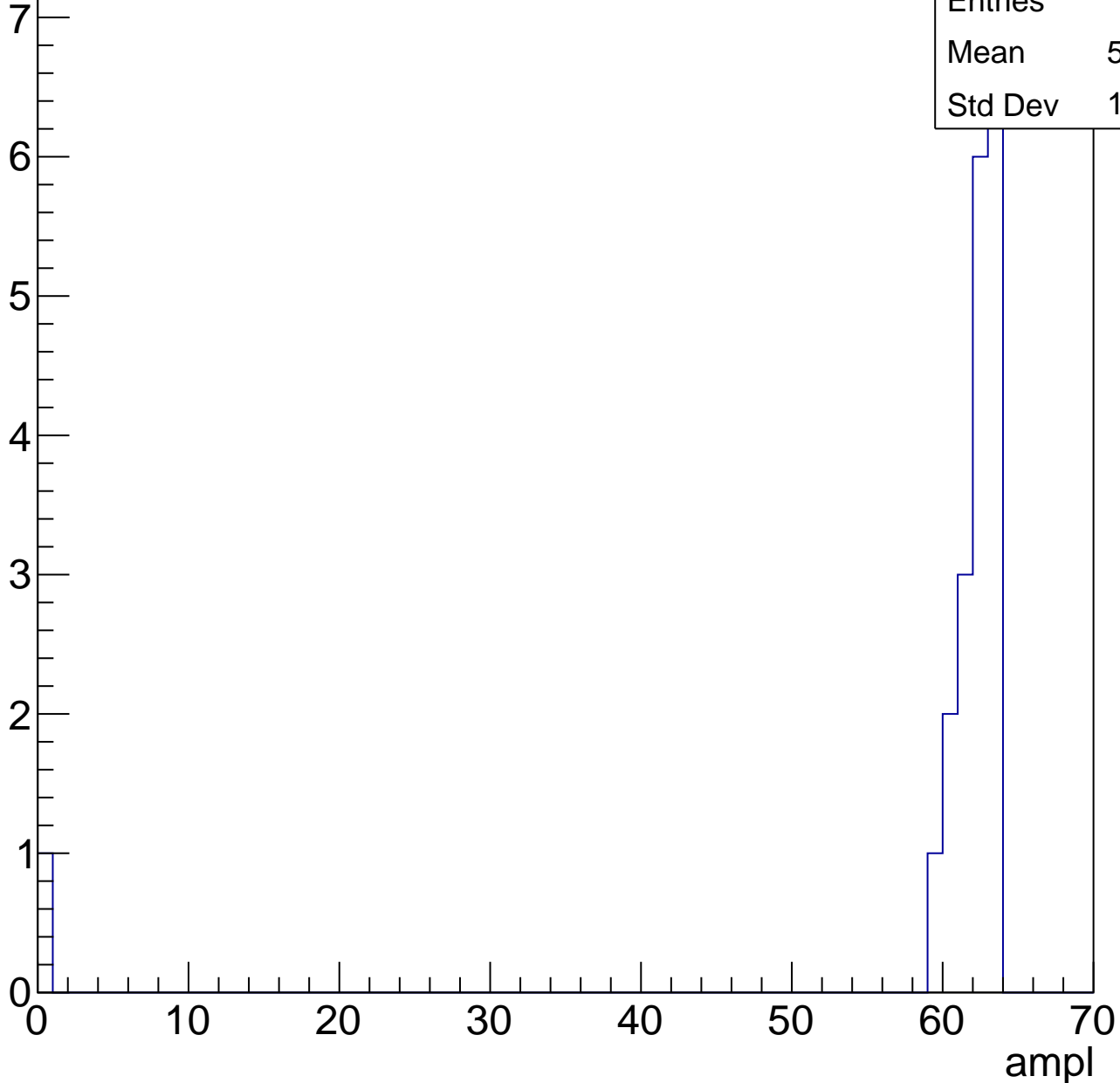


# B1L002S, U3-ch5, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.75
Std Dev	13.53

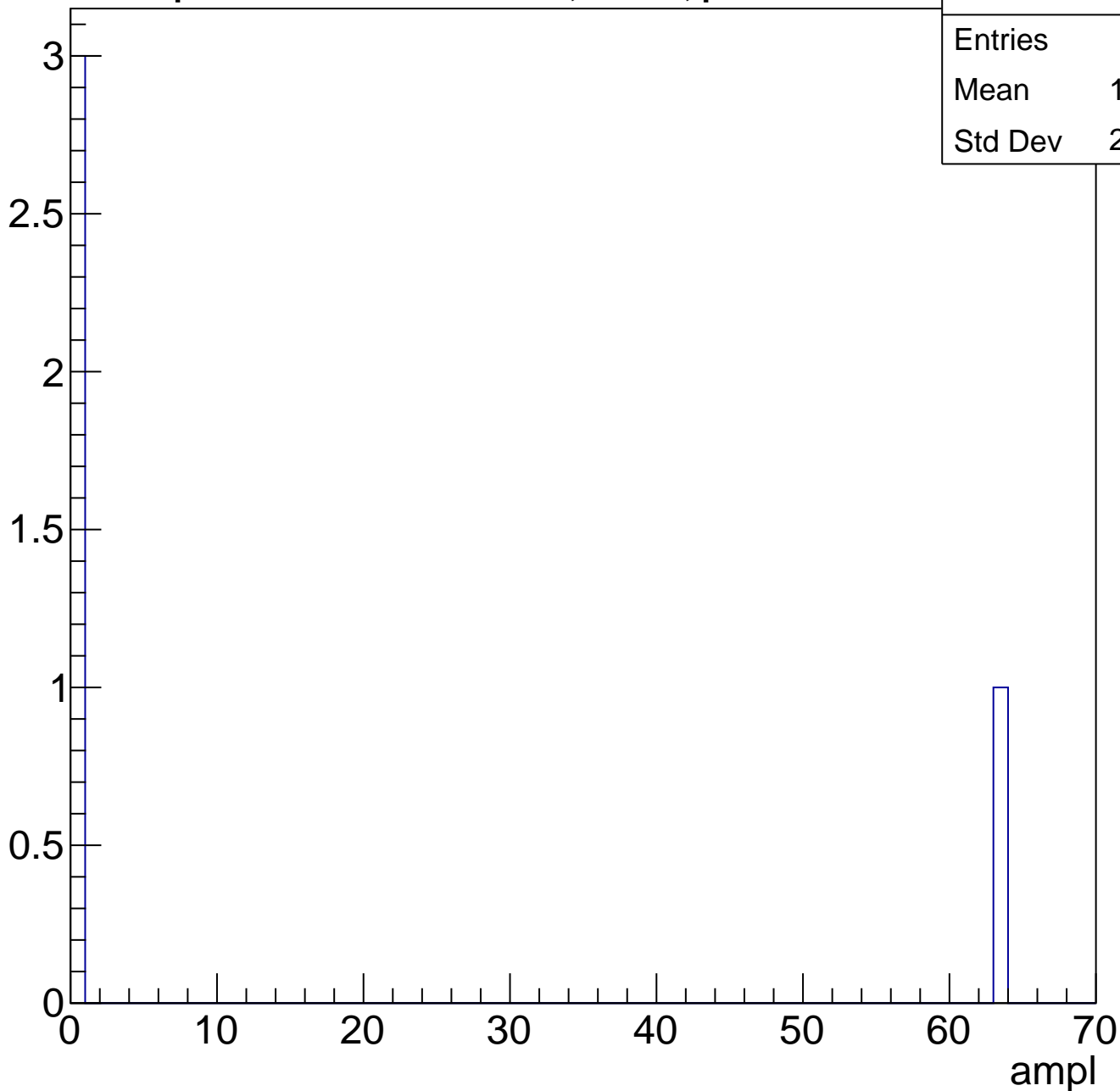




# B1L002S, U3-ch5, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch6, adc0

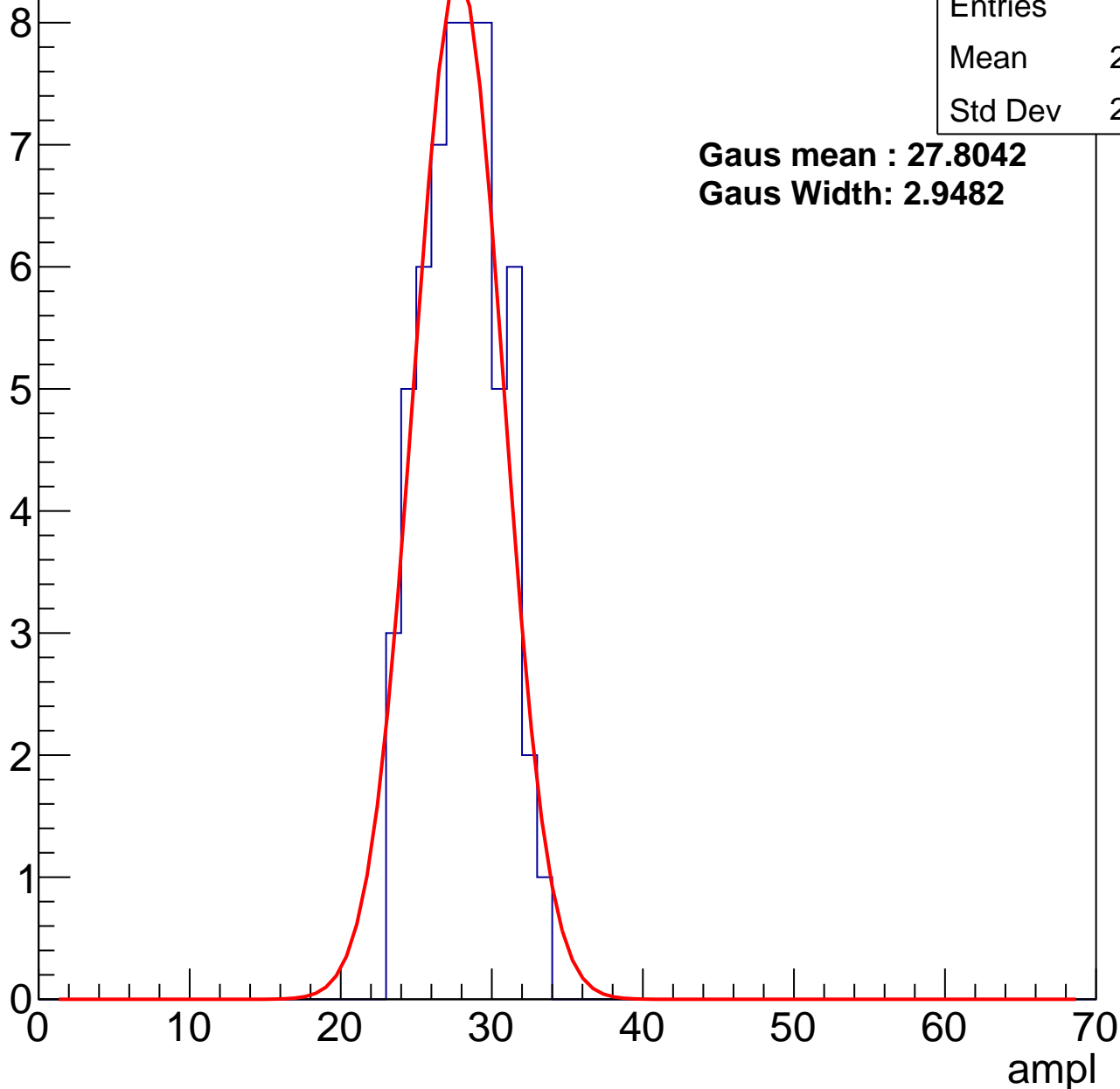
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.56
Std Dev	2.513

**Gaus mean : 27.8042**

**Gaus Width: 2.9482**



# B1L002S, U3-ch6, adc1

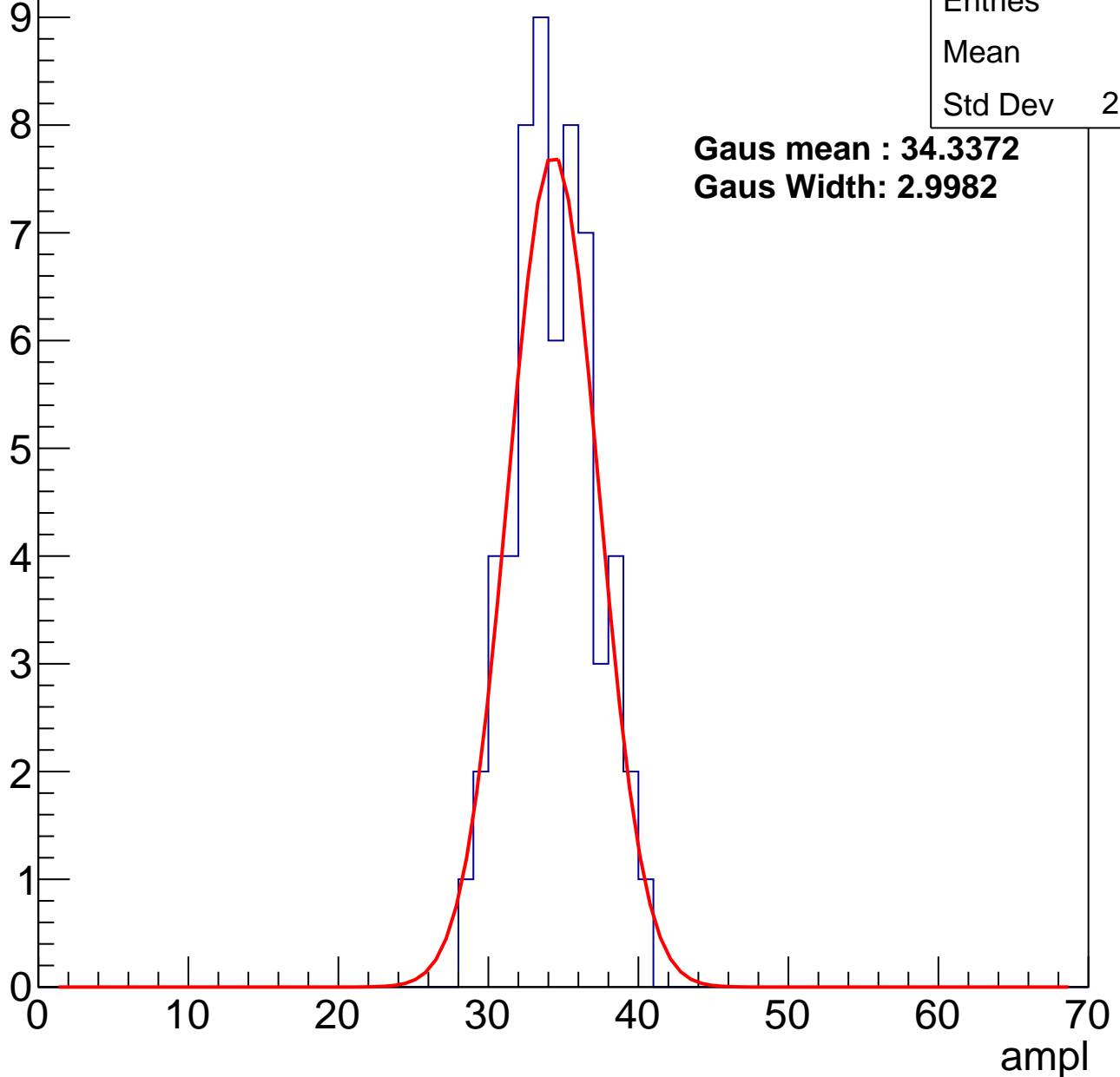
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	33.9
Std Dev	2.729

**Gaus mean : 34.3372**

**Gaus Width: 2.9982**



# B1L002S, U3-ch6, adc2

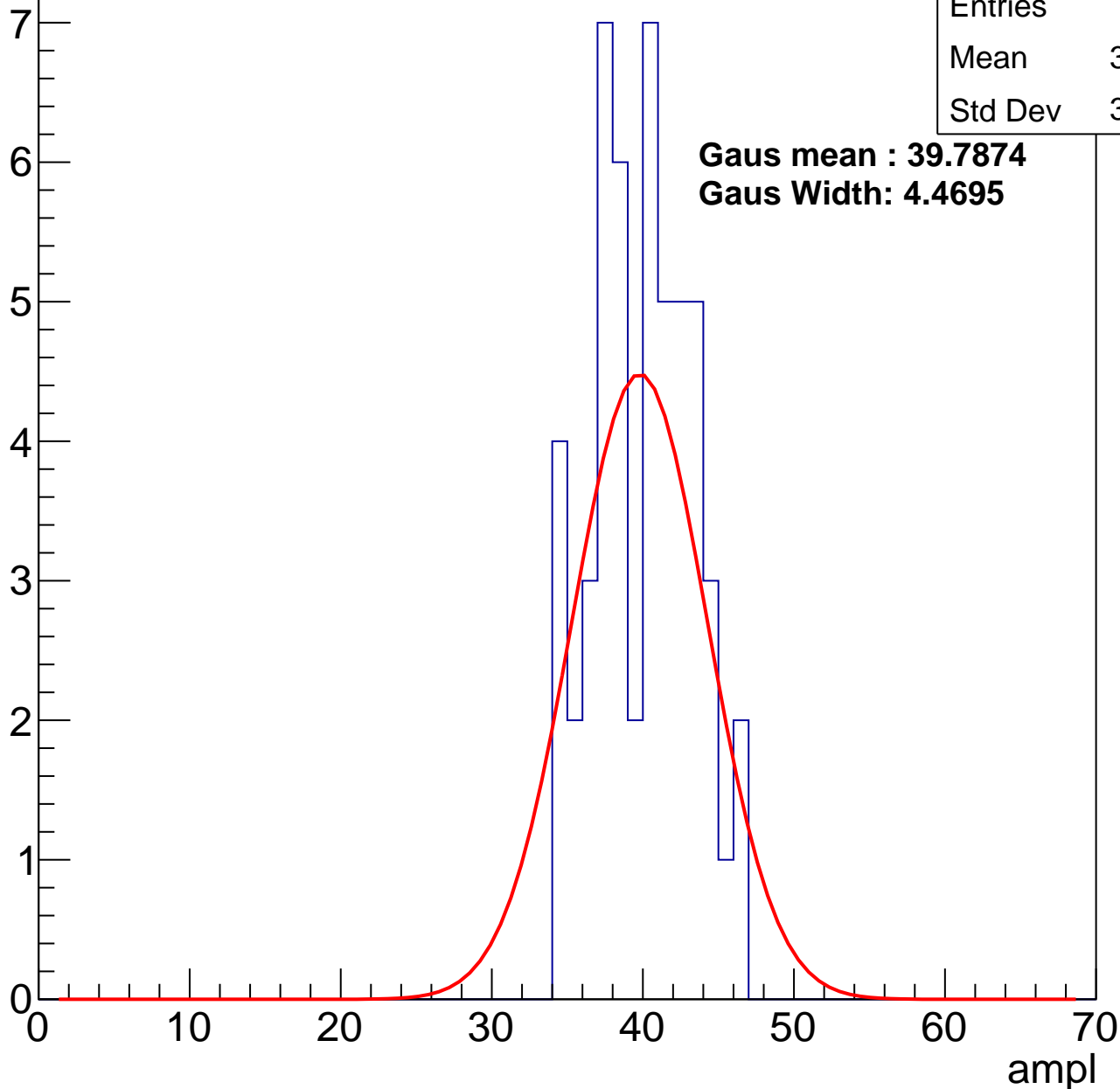
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	39.58
Std Dev	3.213

**Gaus mean : 39.7874**

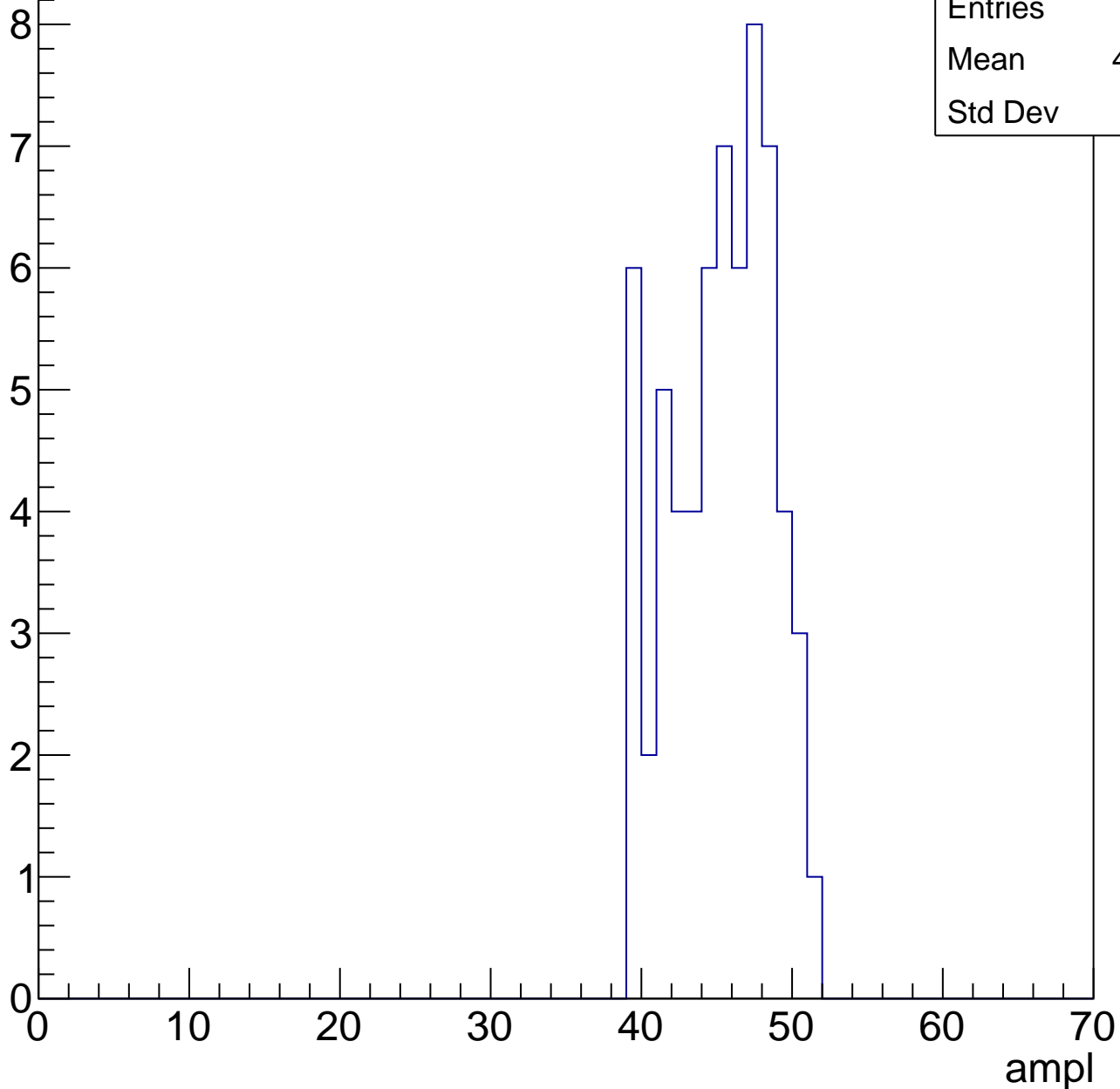
**Gaus Width: 4.4695**



# B1L002S, U3-ch6, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

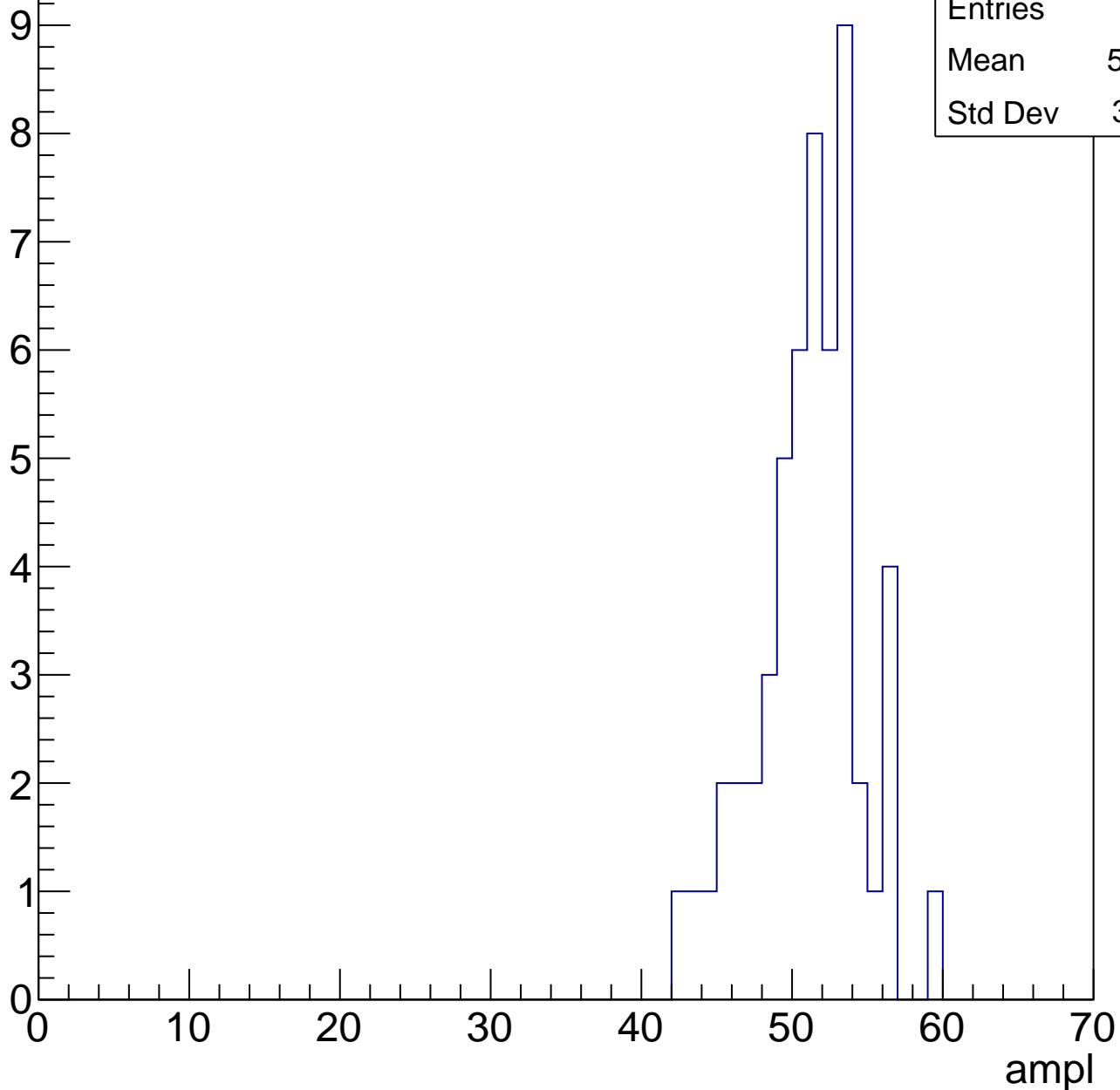
Entry



# B1L002S, U3-ch6, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

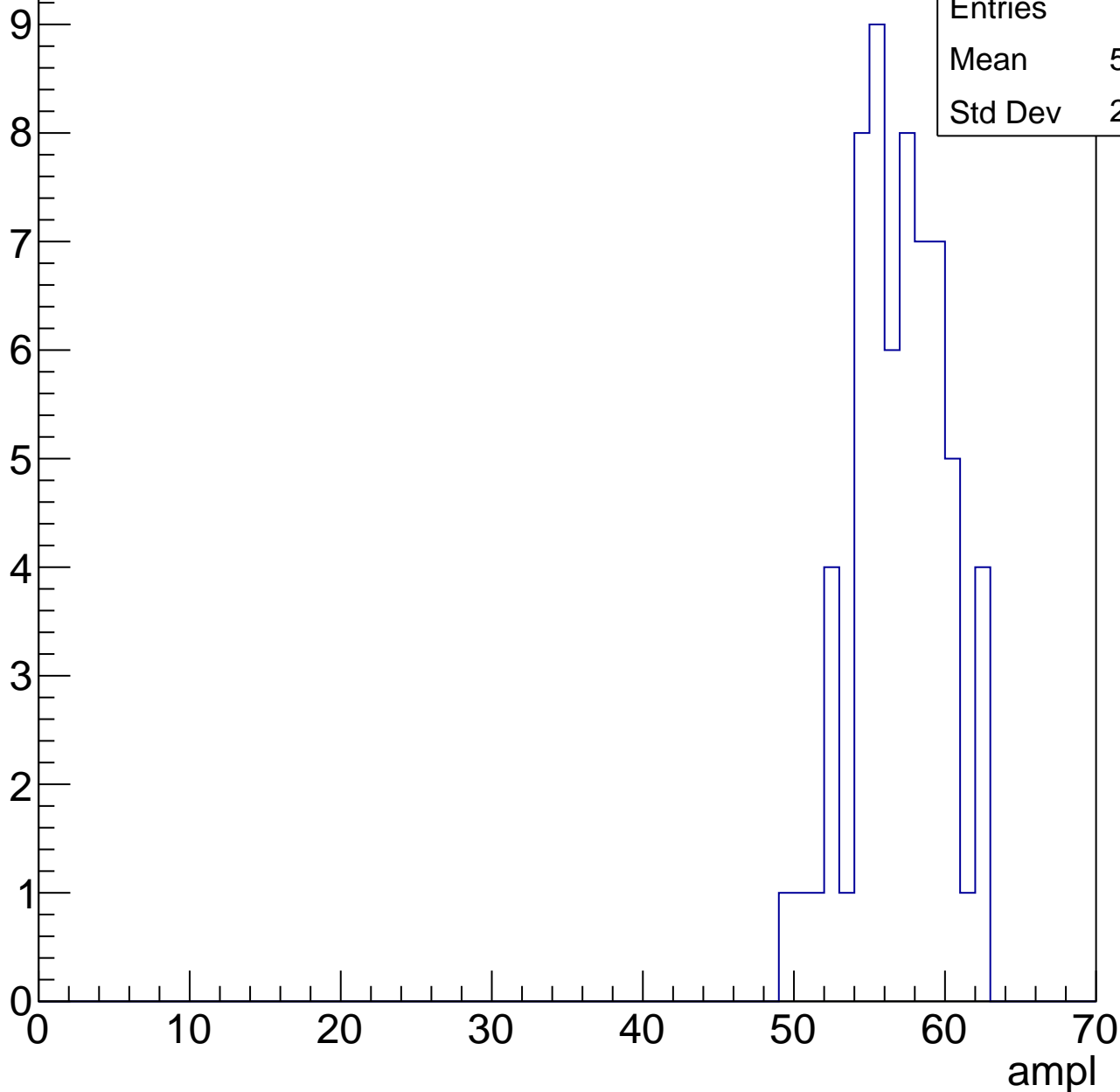
Entry



# B1L002S, U3-ch6, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

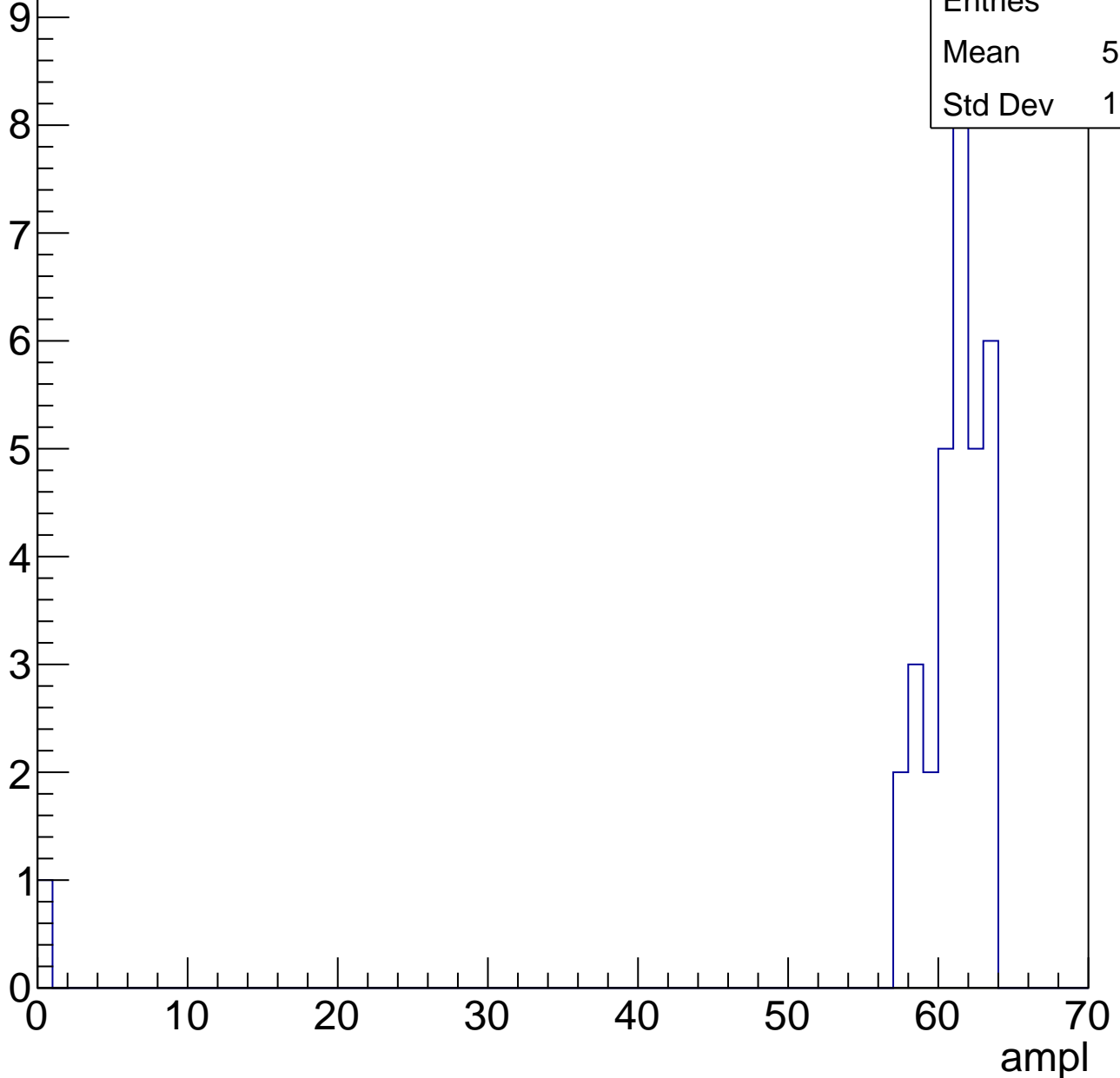


# B1L002S, U3-ch6, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	58.88
Std Dev	10.55

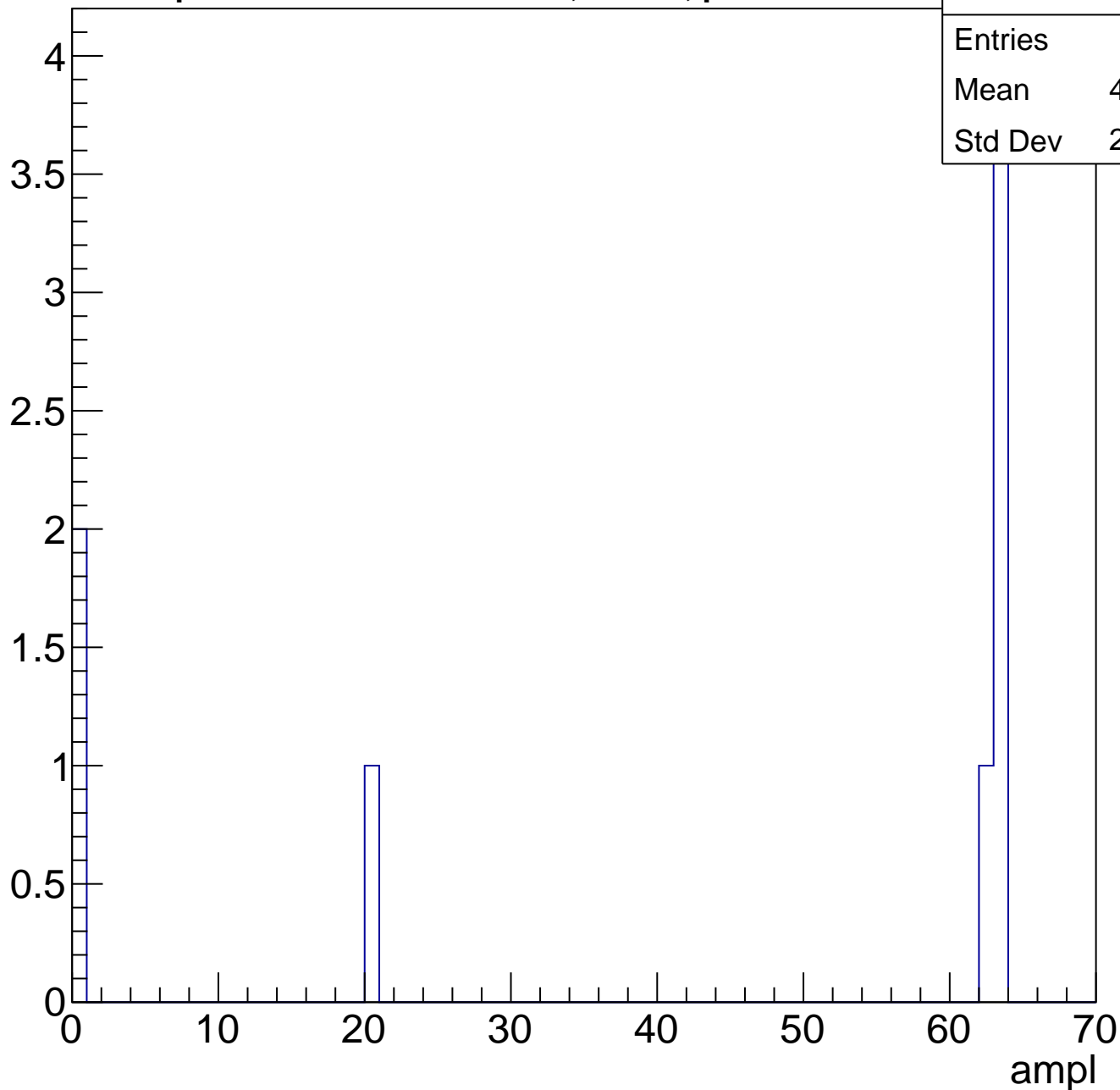




# B1L002S, U3-ch6, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch7, adc0

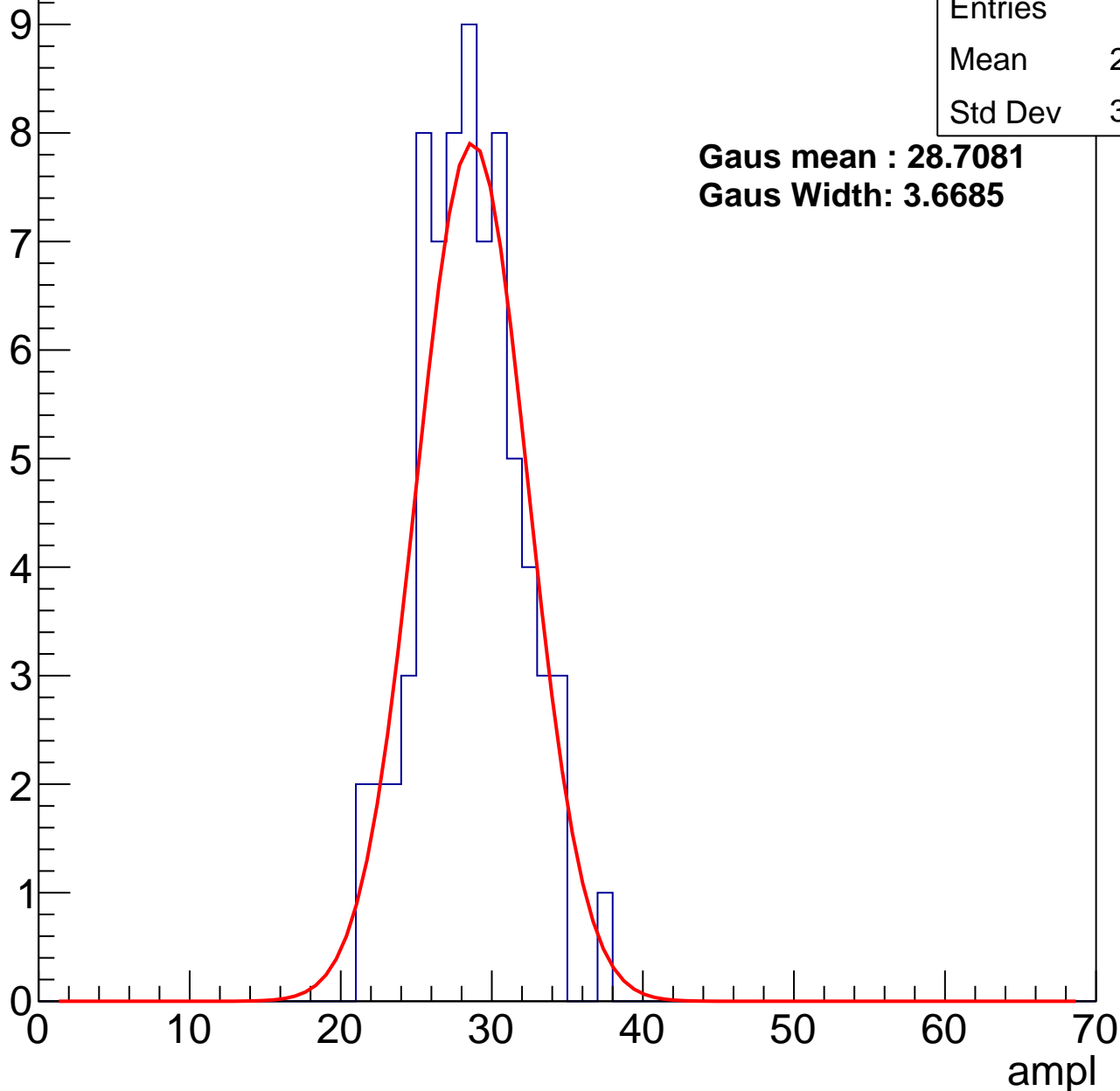
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	28.03
Std Dev	3.308

**Gaus mean : 28.7081**

**Gaus Width: 3.6685**



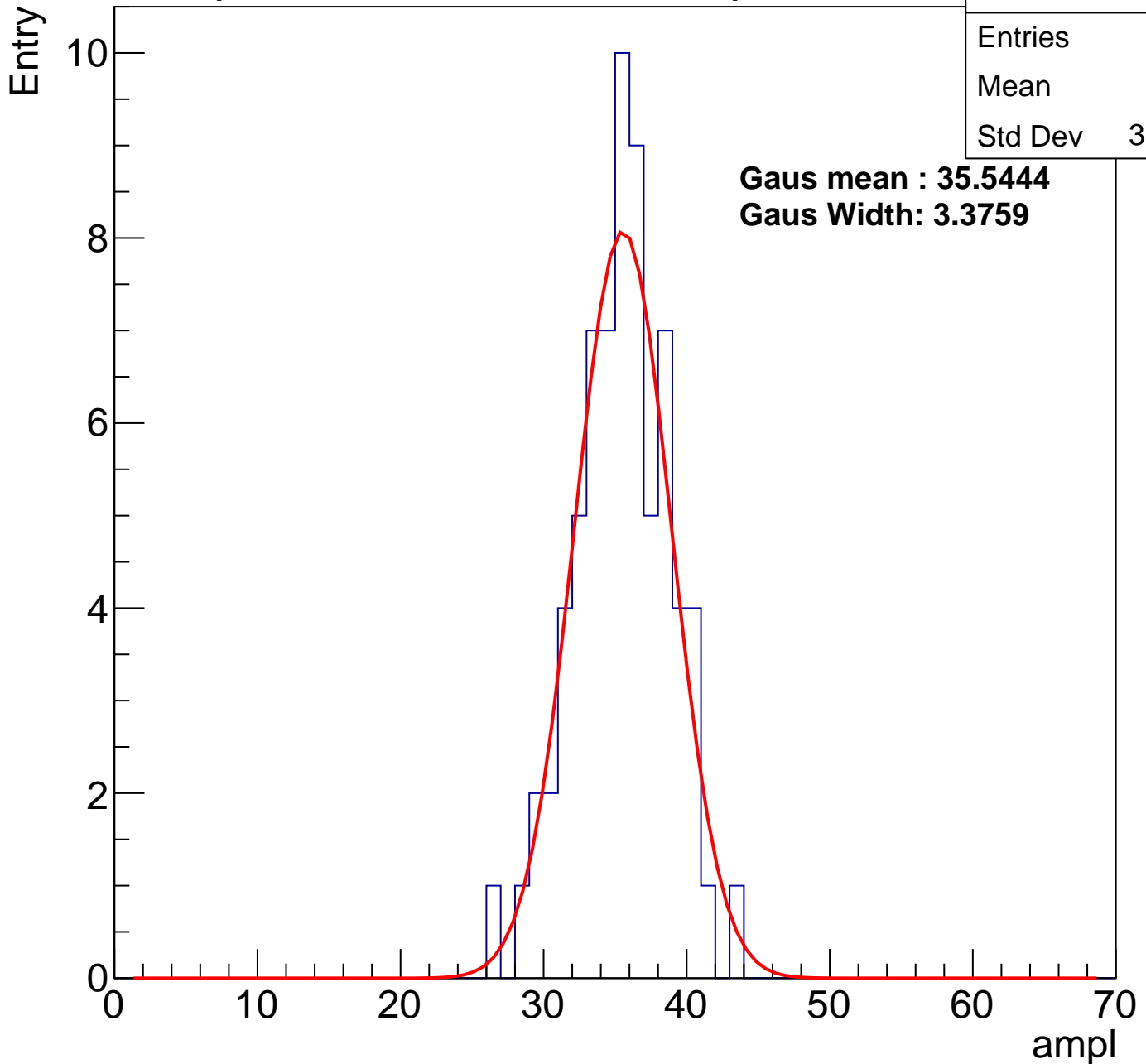
# B1L002S, U3-ch7, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	35
Std Dev	3.278

**Gaus mean : 35.5444**

**Gaus Width: 3.3759**



# B1L002S, U3-ch7, adc2

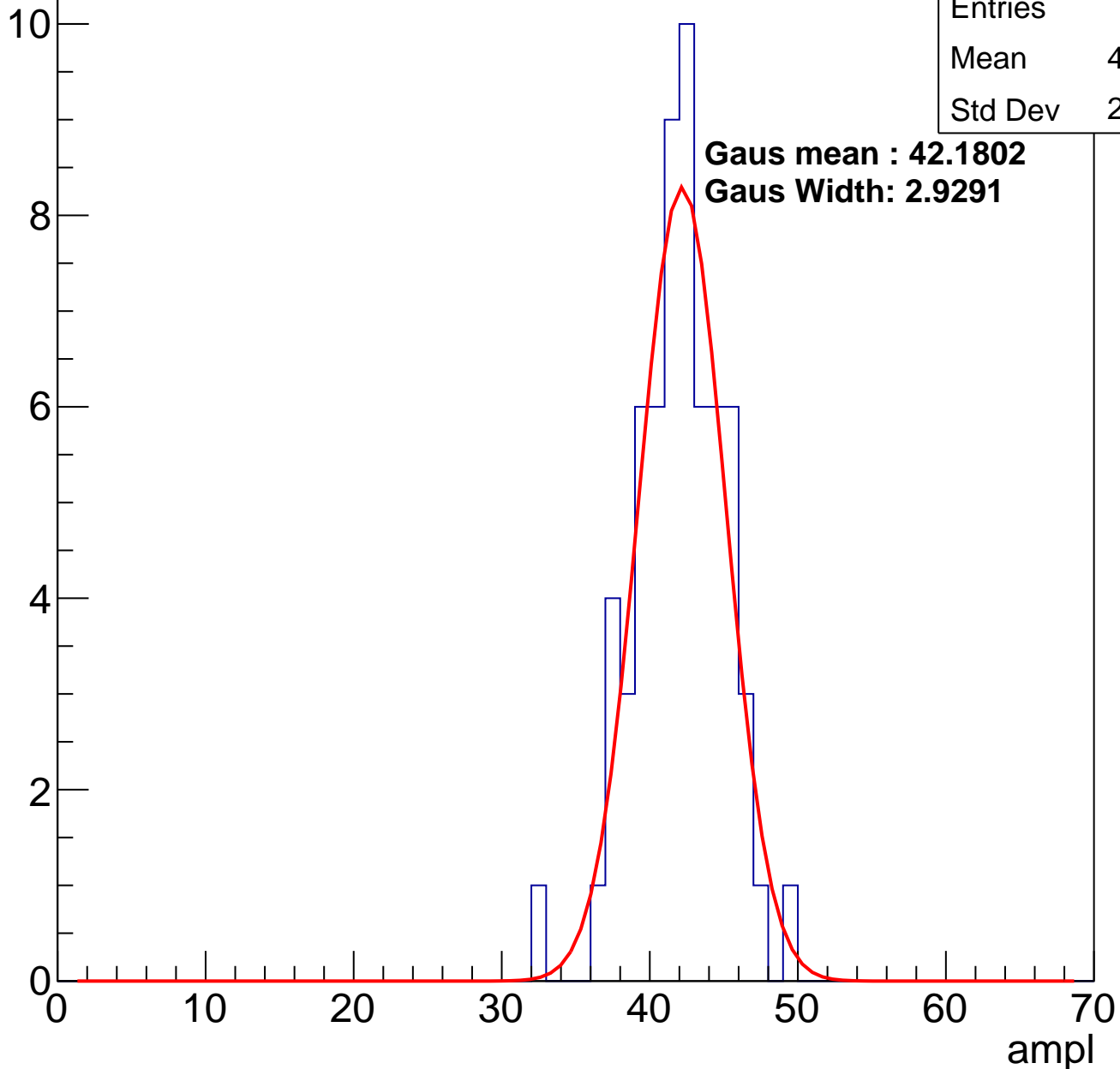
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	41.57
Std Dev	2.996

**Gaus mean : 42.1802**

**Gaus Width: 2.9291**

Entry

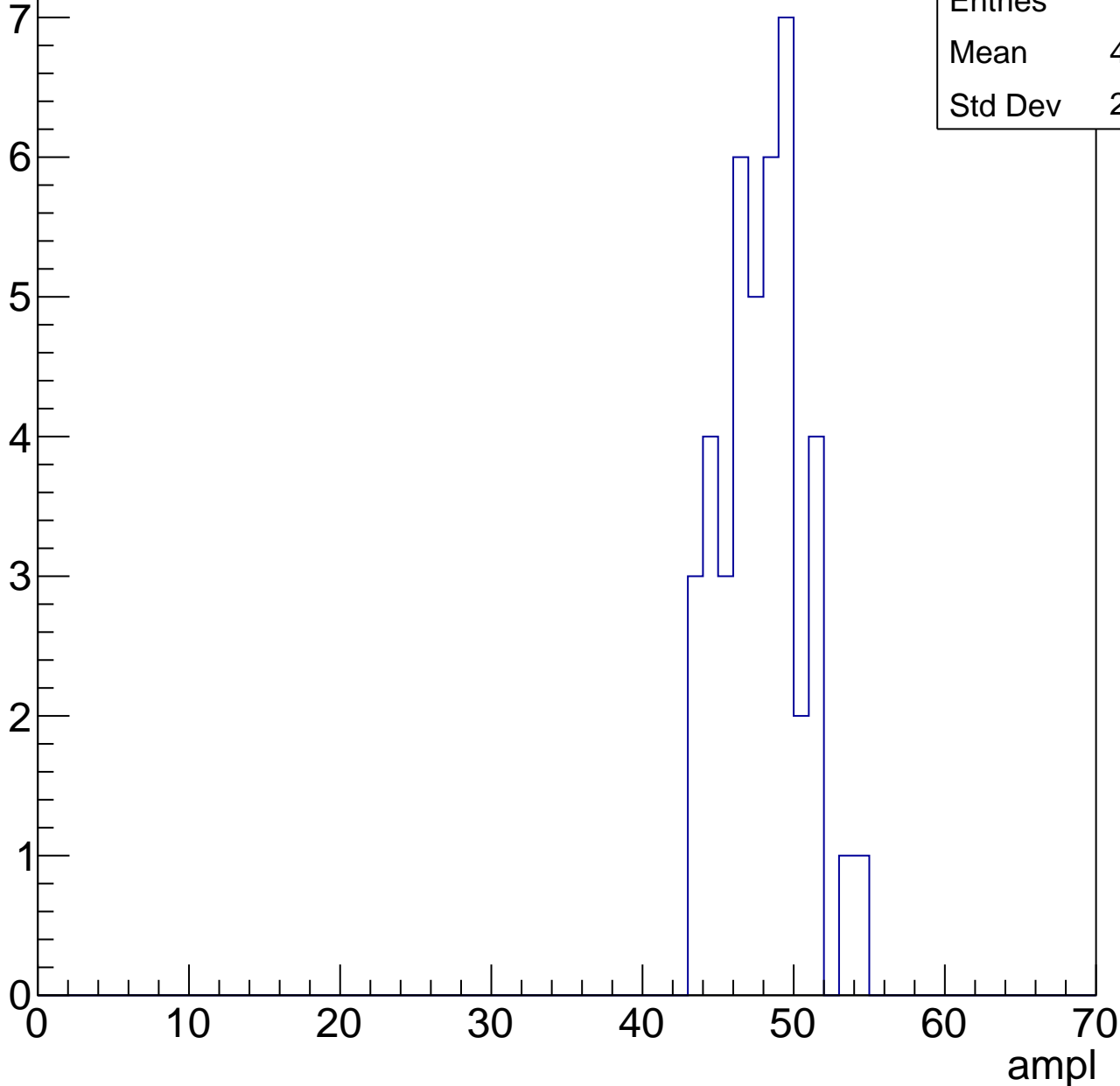


# B1L002S, U3-ch7, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

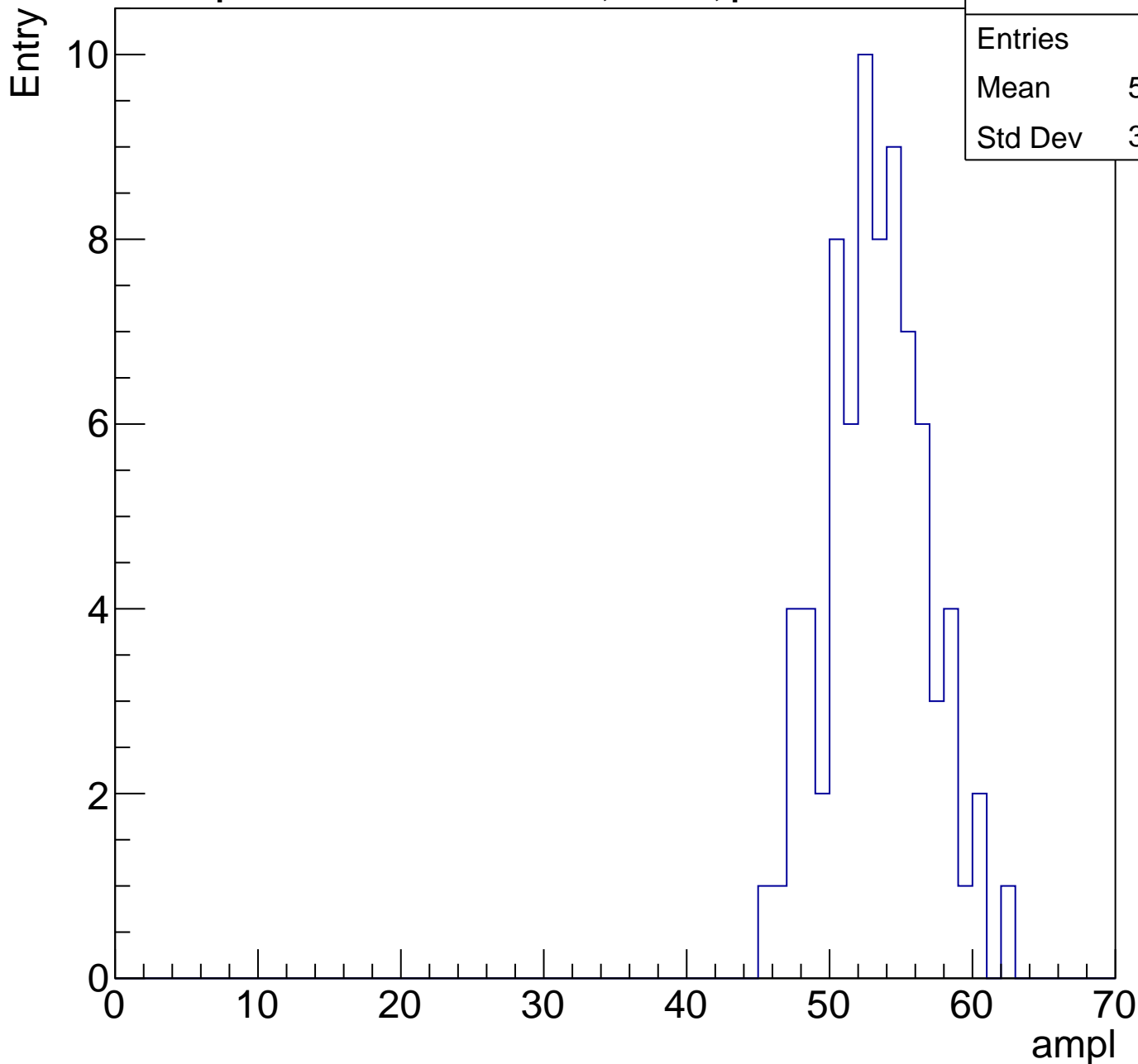
Entries	42
Mean	47.45
Std Dev	2.648



# B1L002S, U3-ch7, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

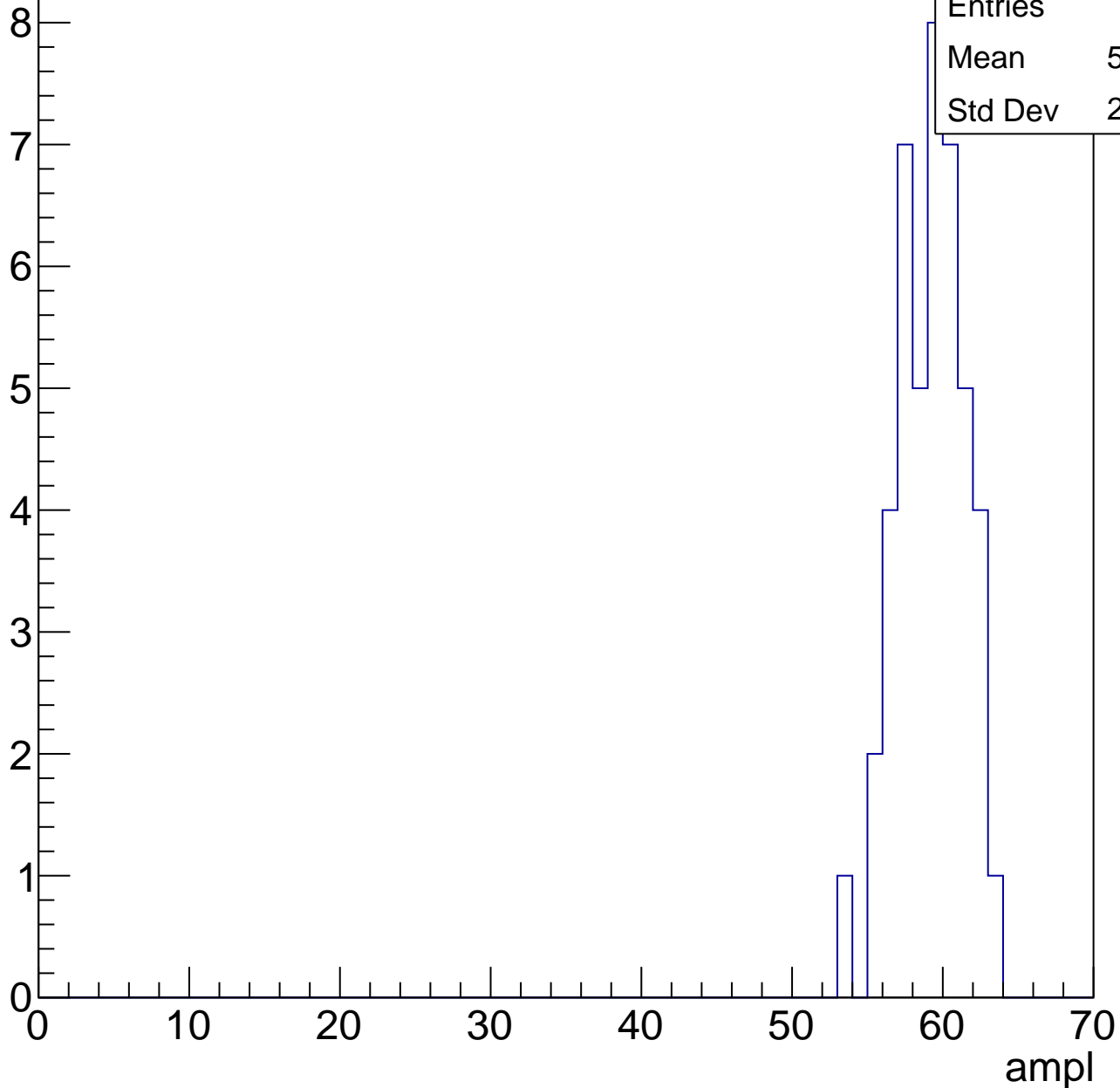
Entries	77
Mean	52.86
Std Dev	3.508



# B1L002S, U3-ch7, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

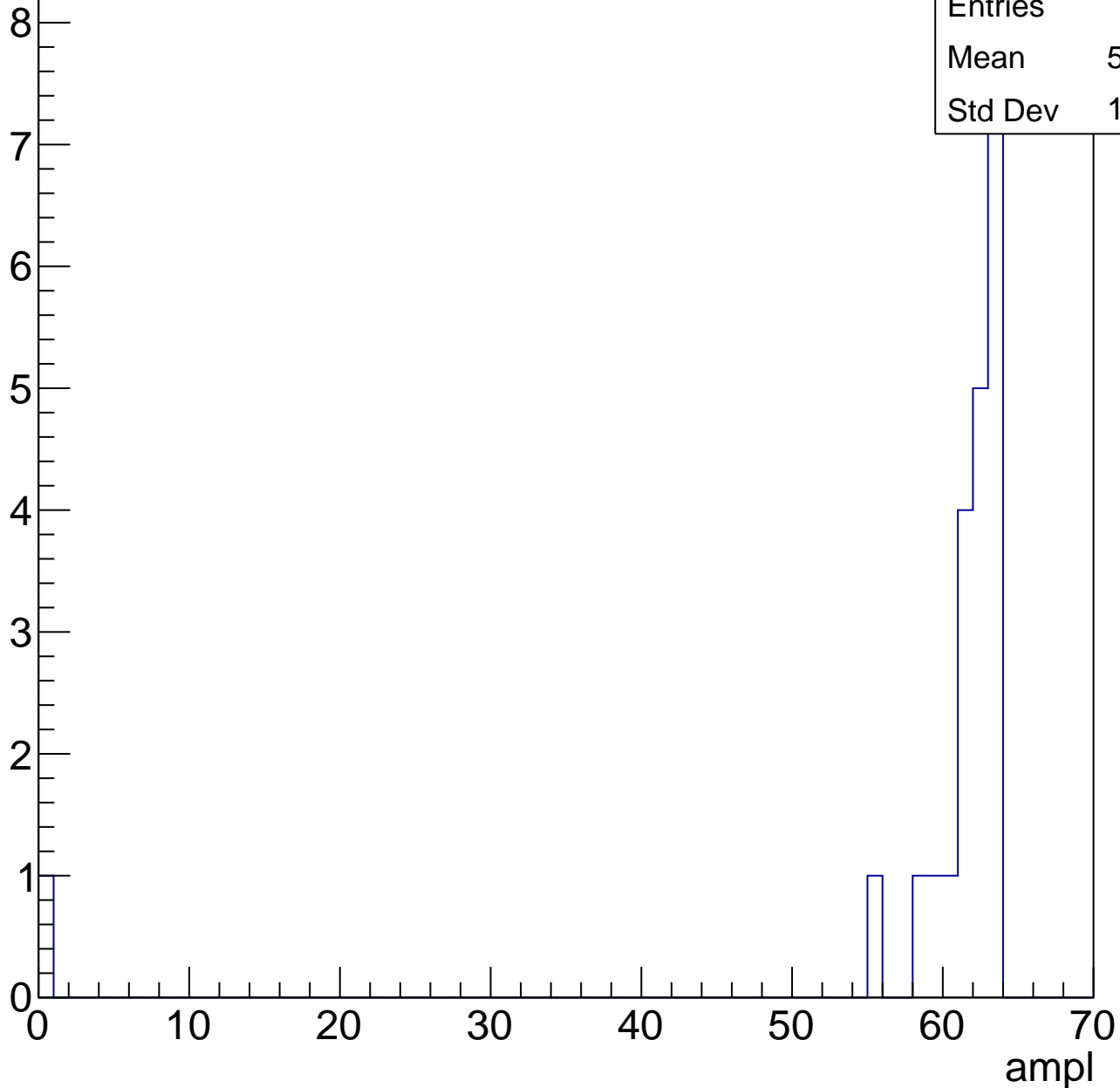


# B1L002S, U3-ch7, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.64
Std Dev	12.94





# B1L002S, U3-ch7, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch8, adc0

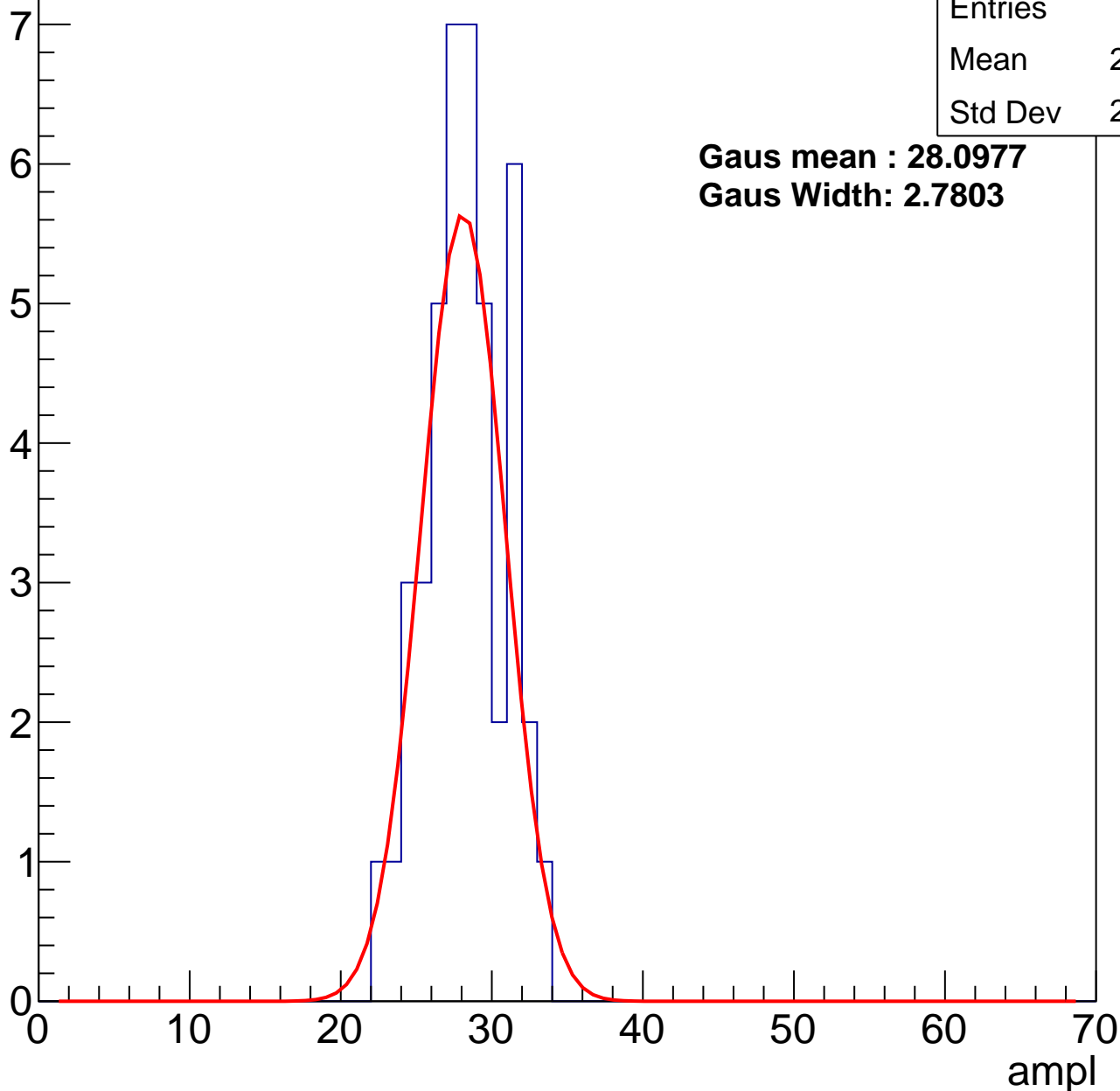
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	27.79
Std Dev	2.575

**Gaus mean : 28.0977**

**Gaus Width: 2.7803**



# B1L002S, U3-ch8, adc1

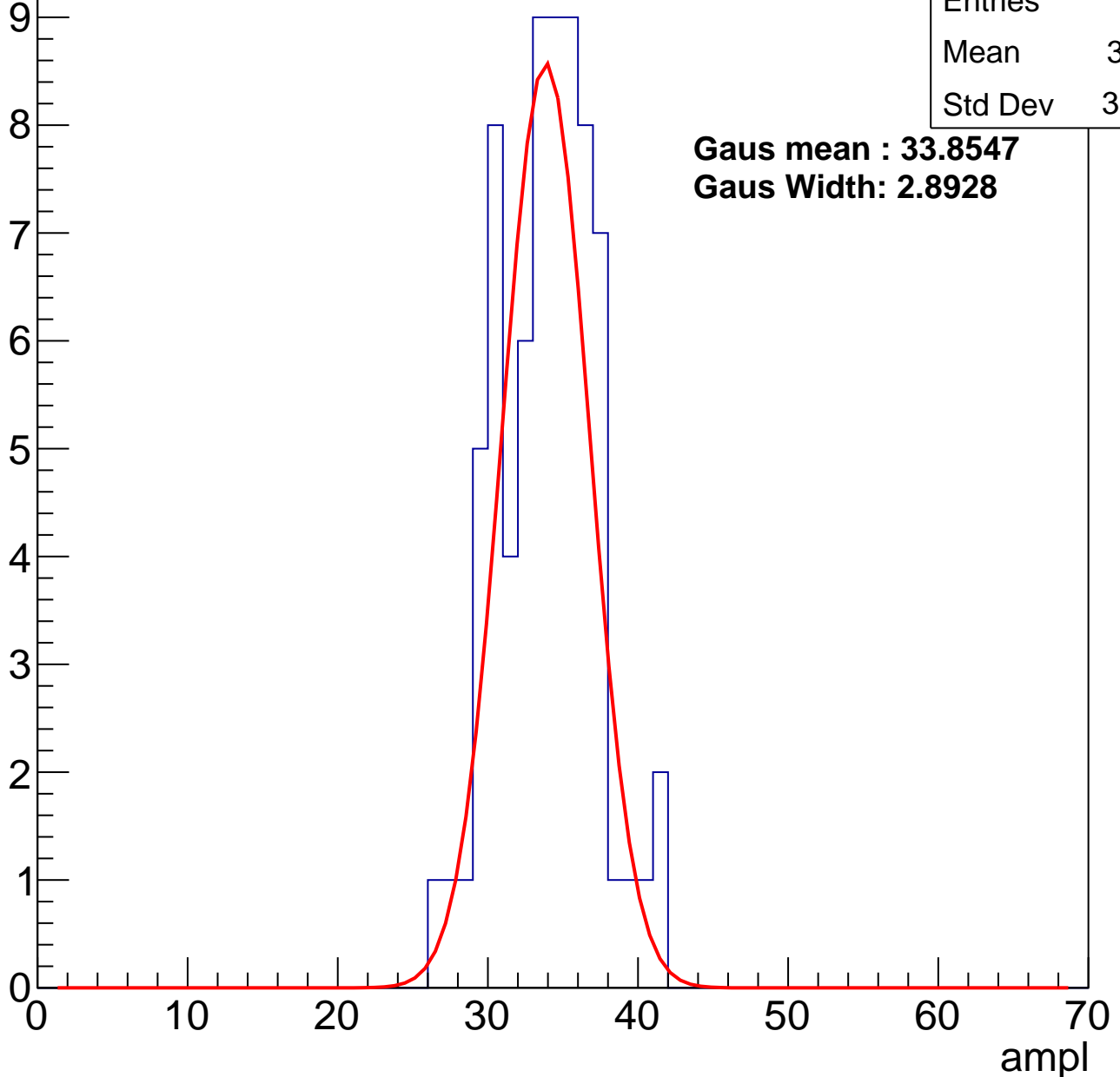
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	33.51
Std Dev	3.167

**Gaus mean : 33.8547**

**Gaus Width: 2.8928**



# B1L002S, U3-ch8, adc2

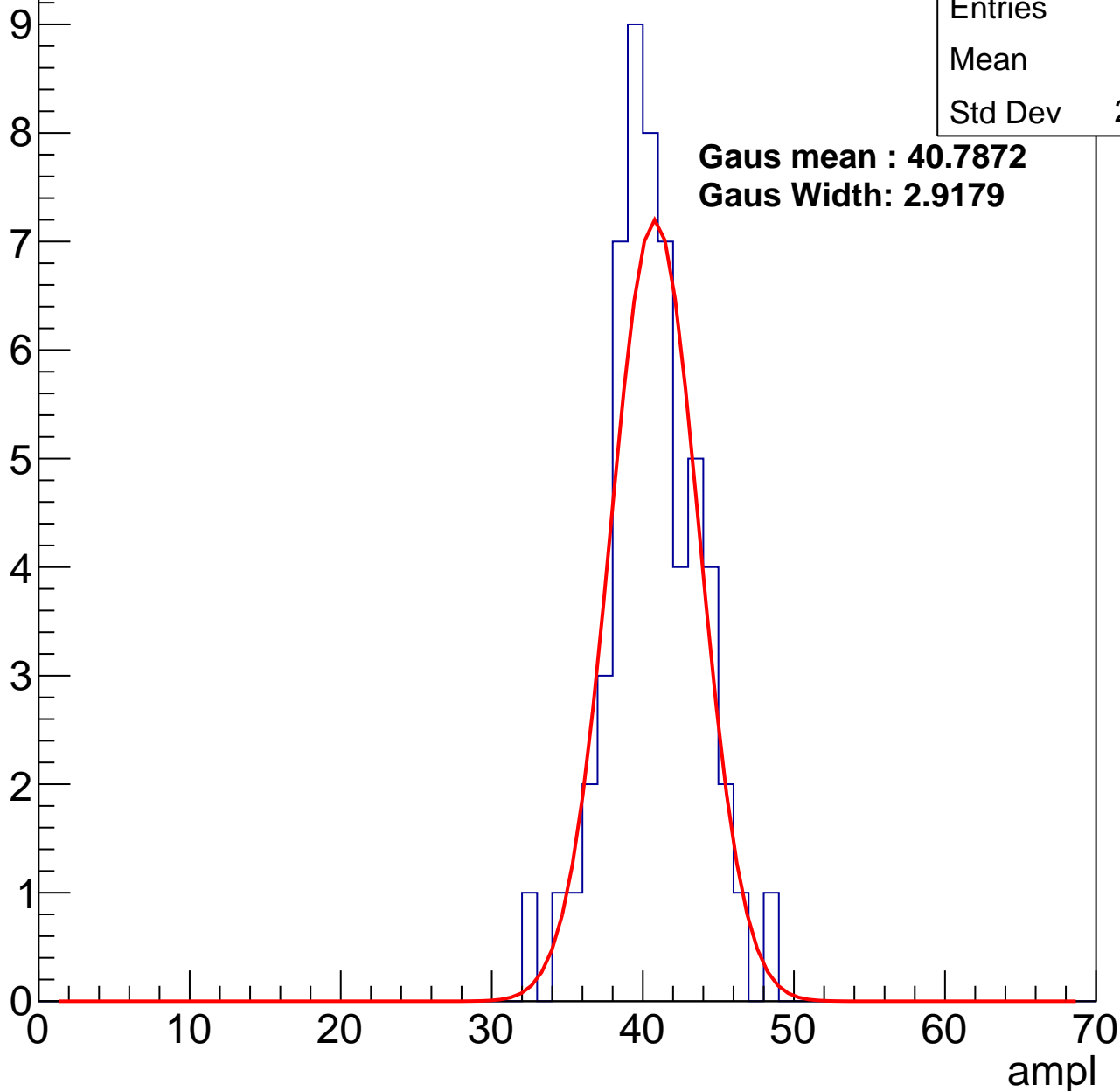
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.2
Std Dev	2.991

**Gaus mean : 40.7872**

**Gaus Width: 2.9179**

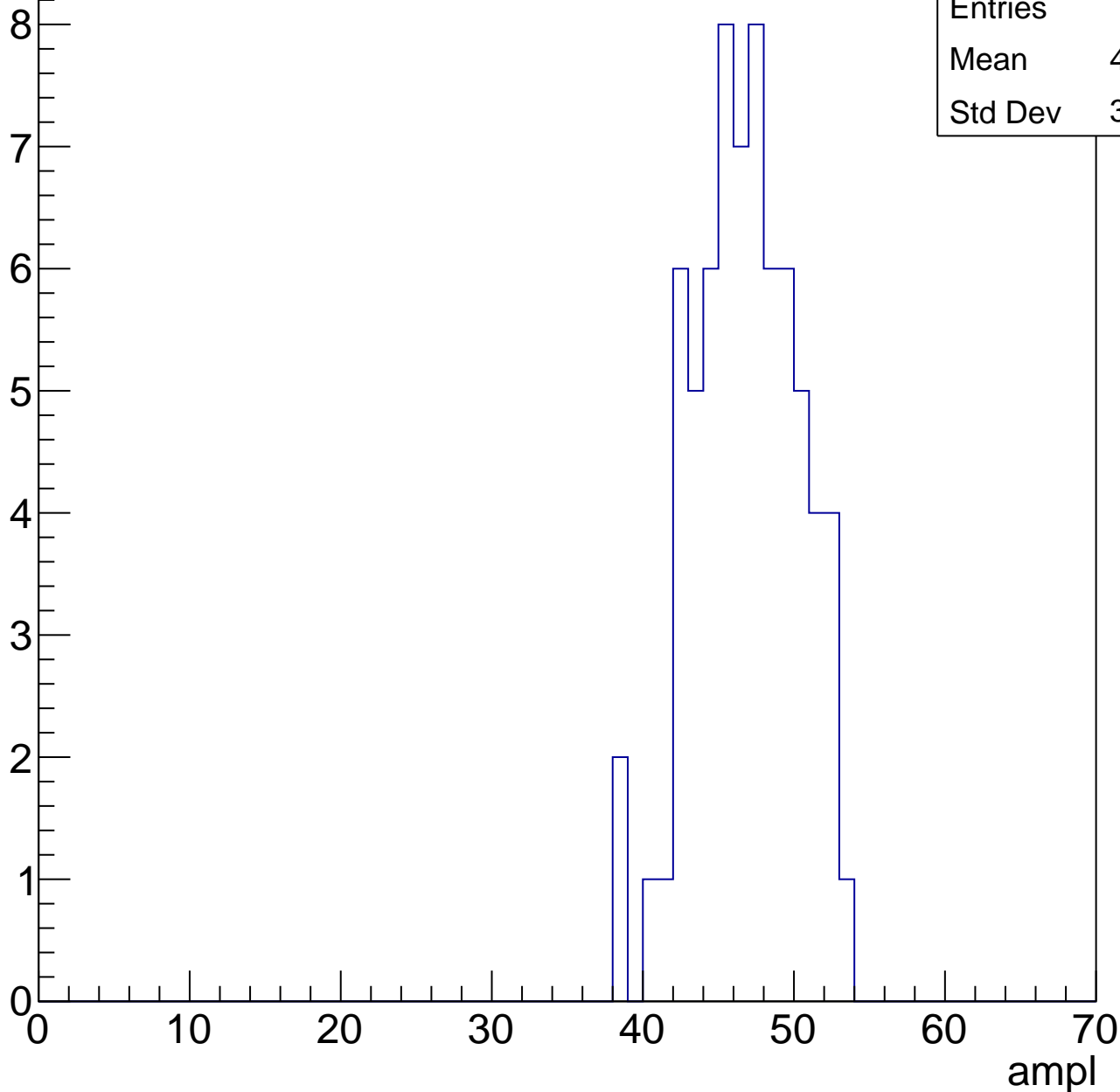


# B1L002S, U3-ch8, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	46.33
Std Dev	3.409

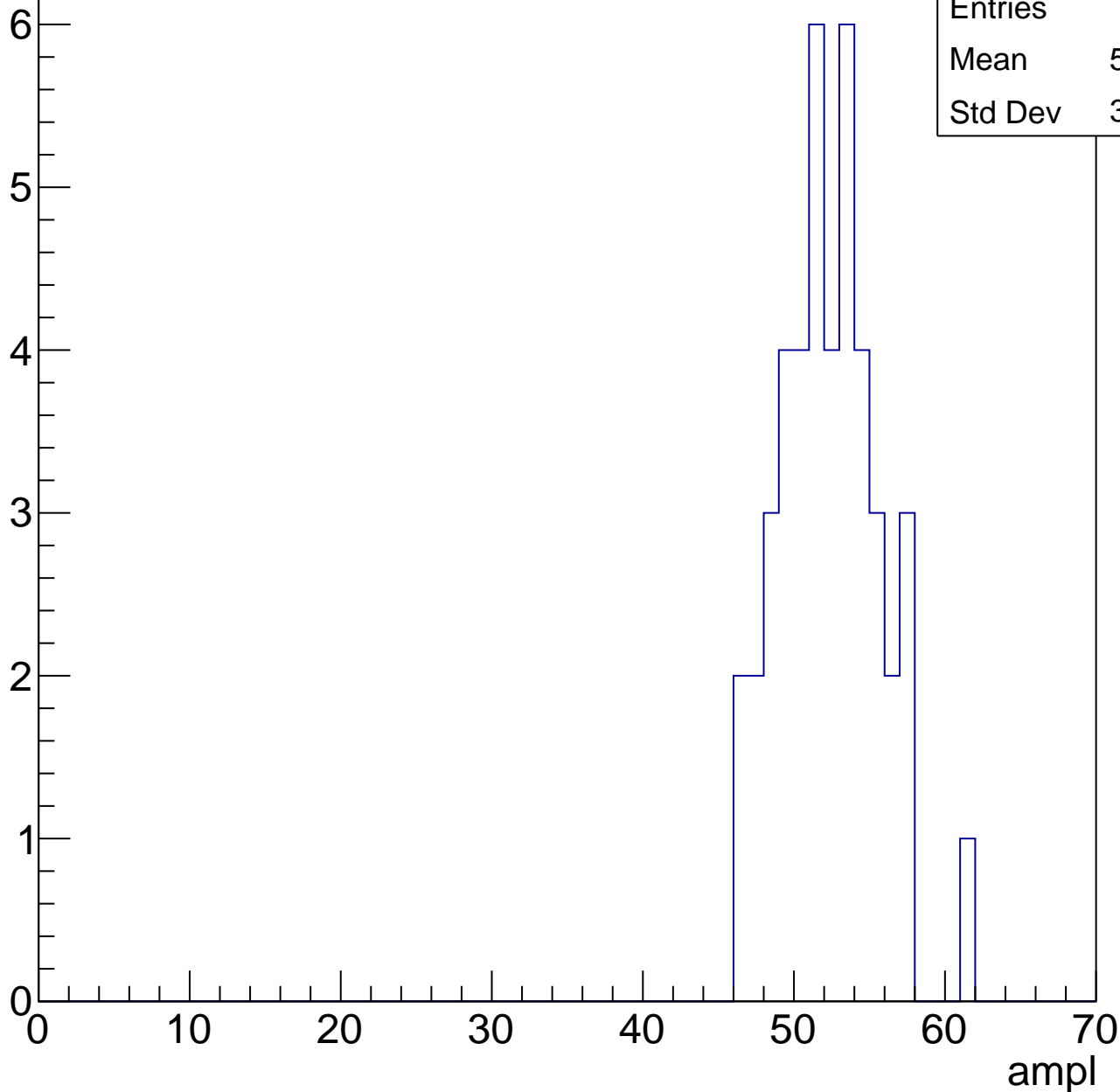


# B1L002S, U3-ch8, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.89
Std Dev	3.249

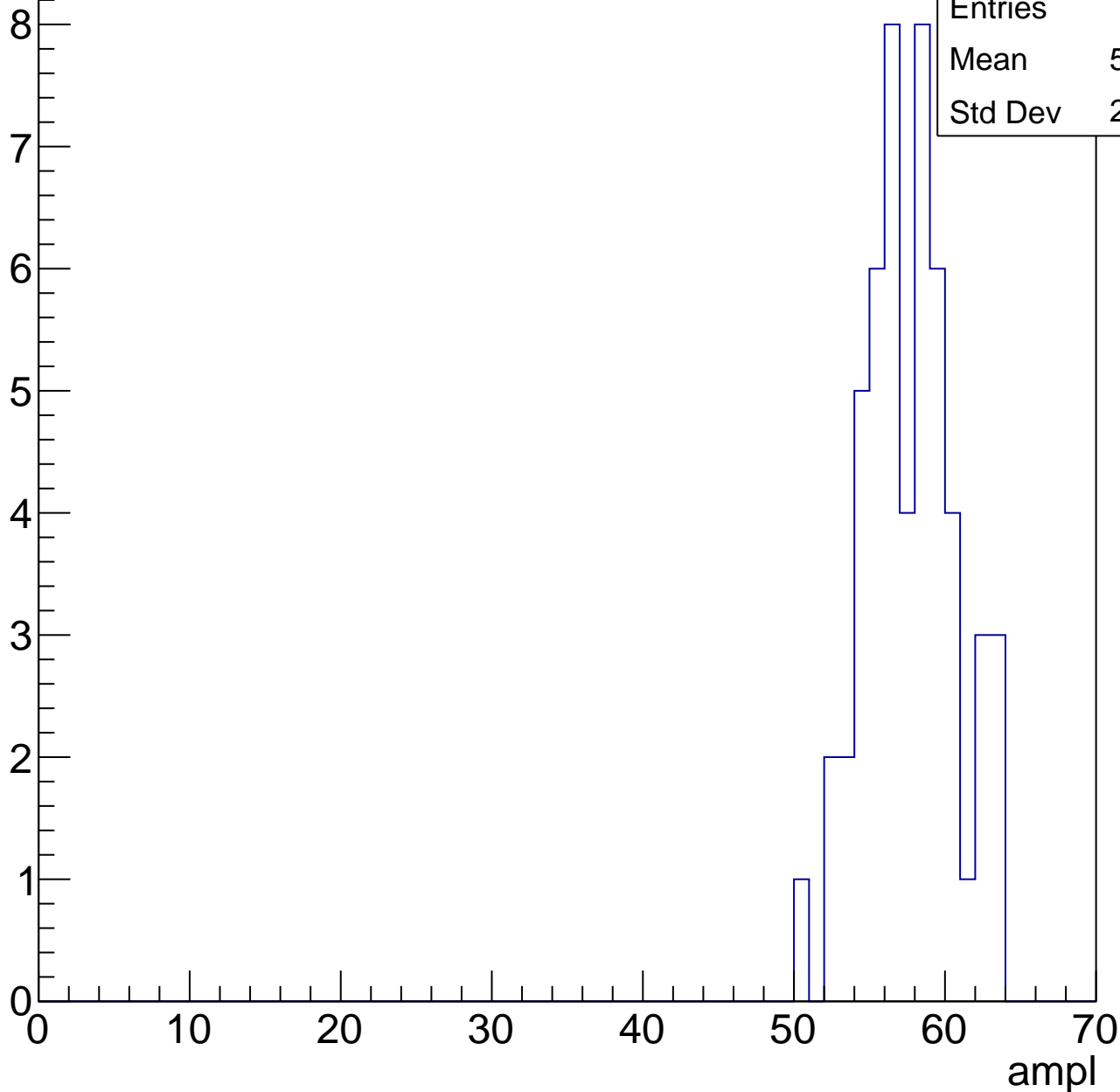


# B1L002S, U3-ch8, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.17
Std Dev	2.989

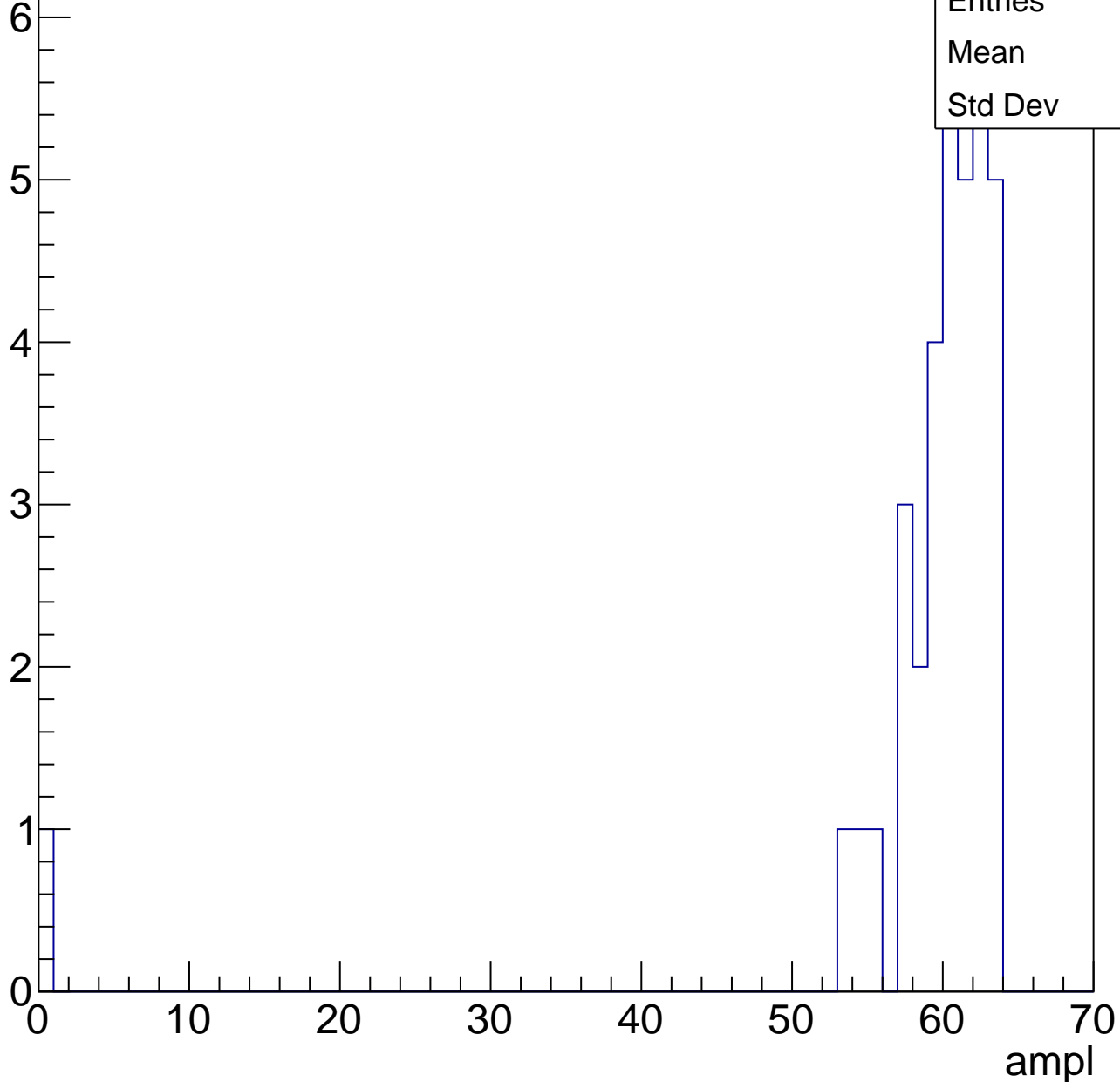


# B1L002S, U3-ch8, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	58.2
Std Dev	10.3

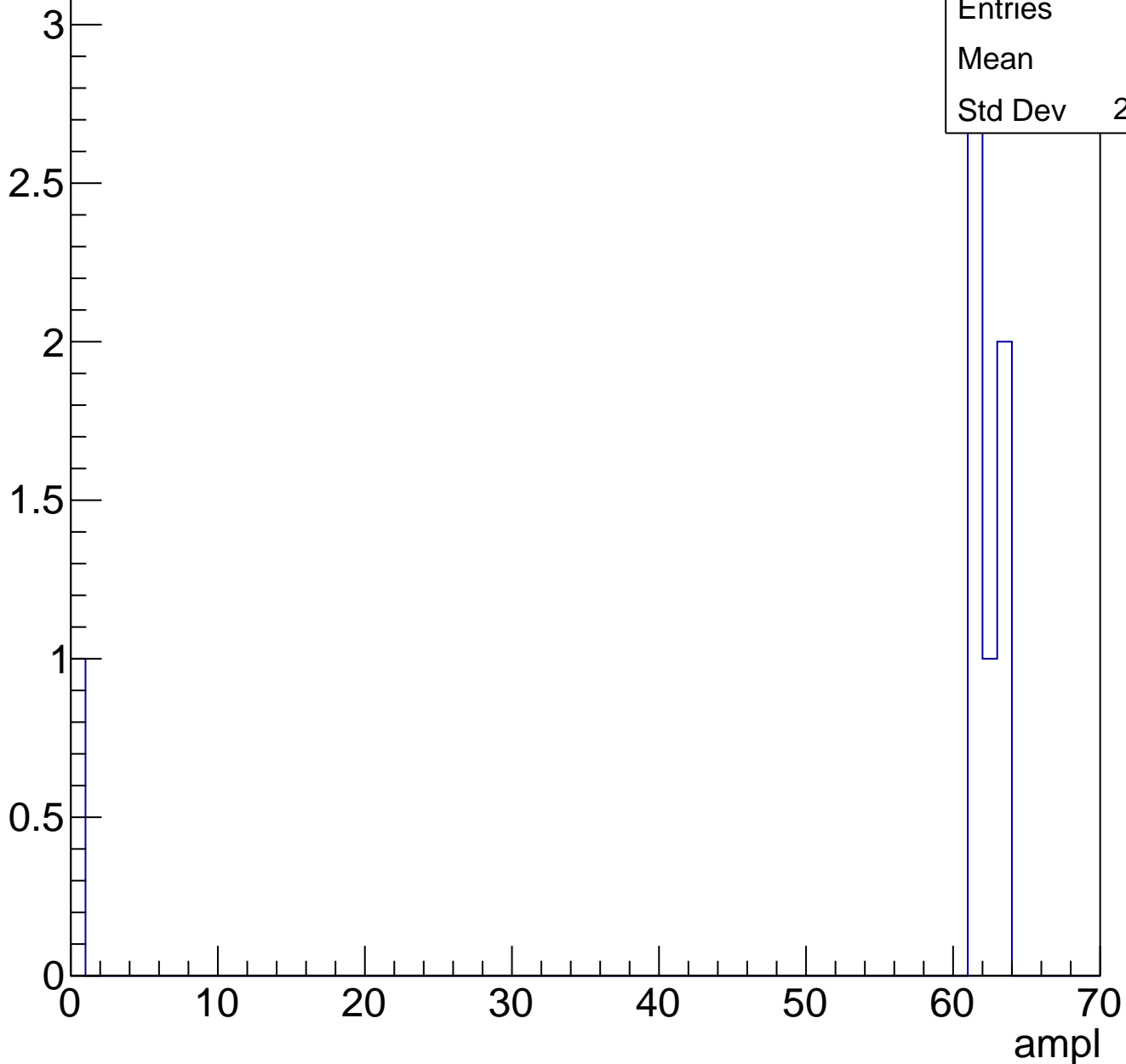




# B1L002S, U3-ch8, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch9, adc0

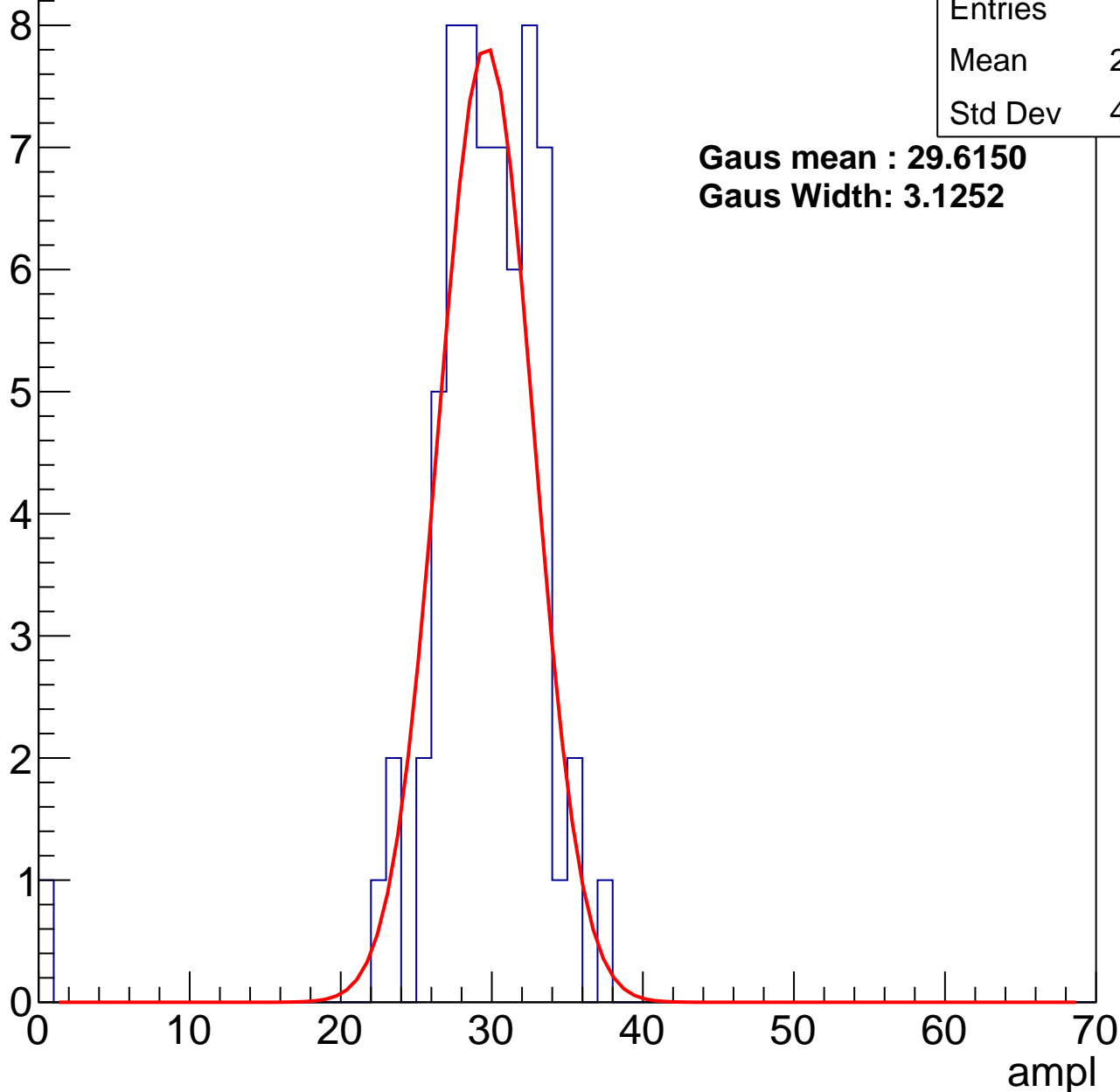
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	29.02
Std Dev	4.695

**Gaus mean : 29.6150**

**Gaus Width: 3.1252**

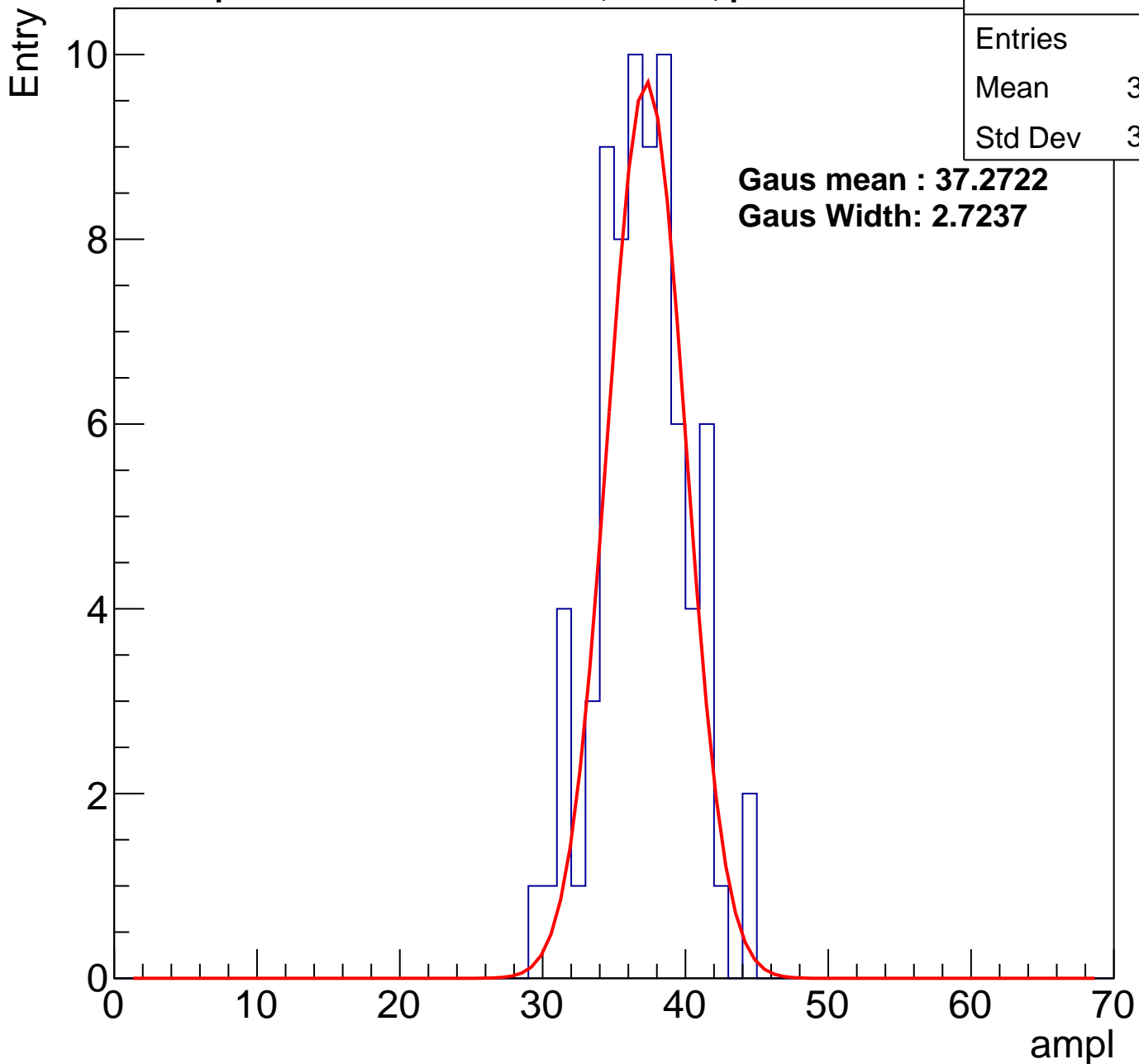


# B1L002S, U3-ch9, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	36.57
Std Dev	3.116

**Gaus mean : 37.2722**  
**Gaus Width: 2.7237**



# B1L002S, U3-ch9, adc2

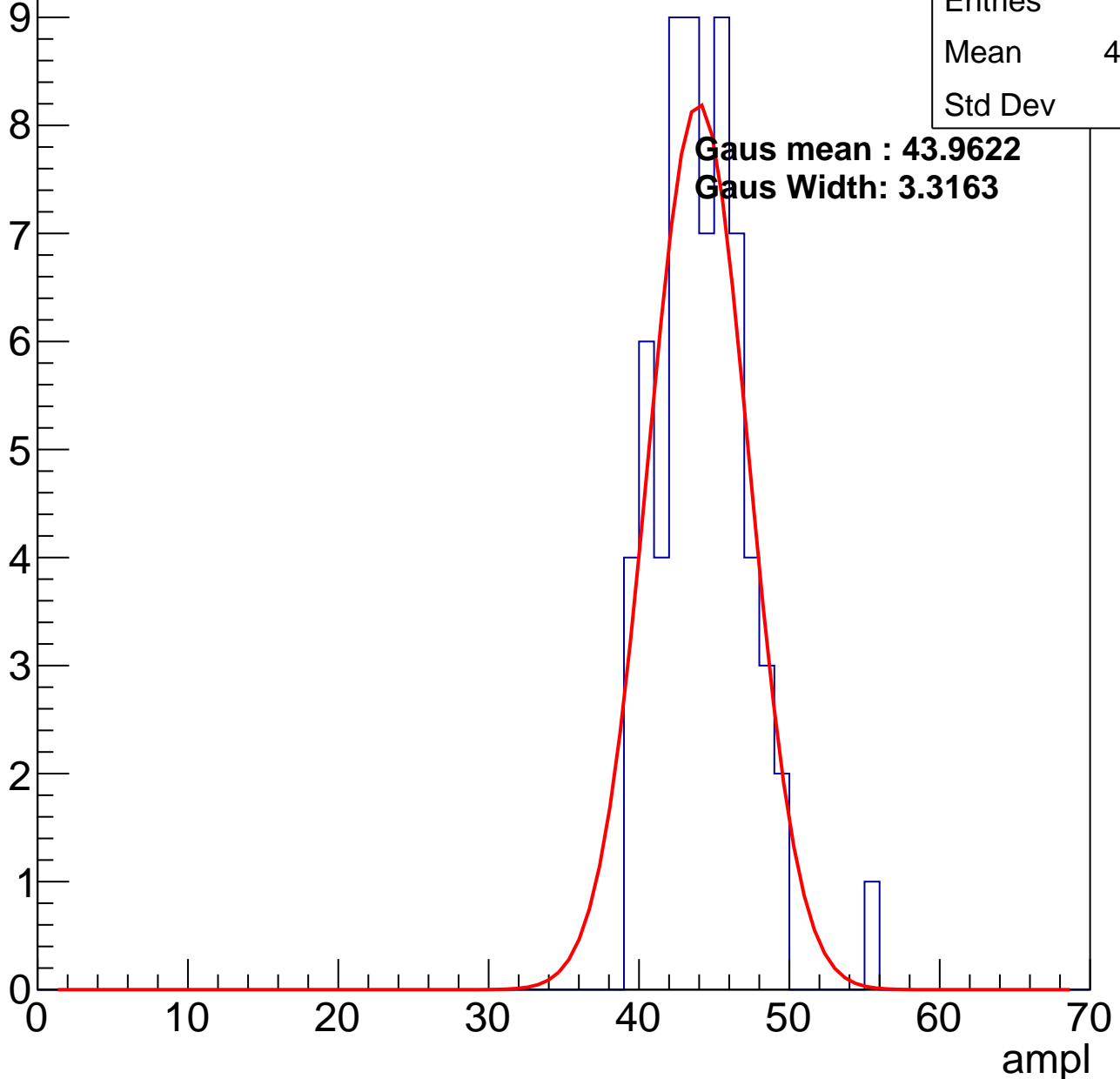
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	43.77
Std Dev	2.95

**Gaus mean : 43.9622**

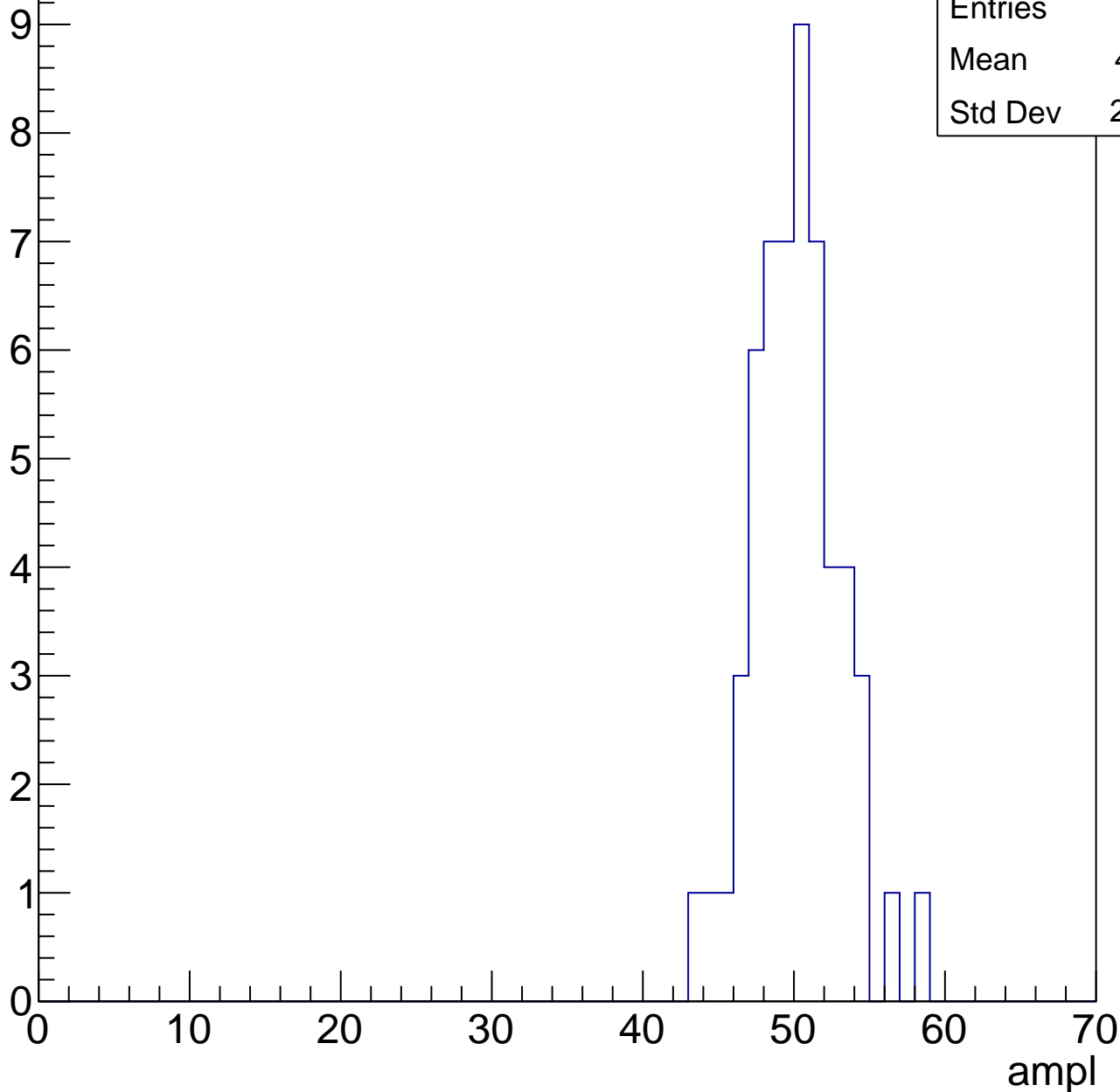
**Gaus Width: 3.3163**



# B1L002S, U3-ch9, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

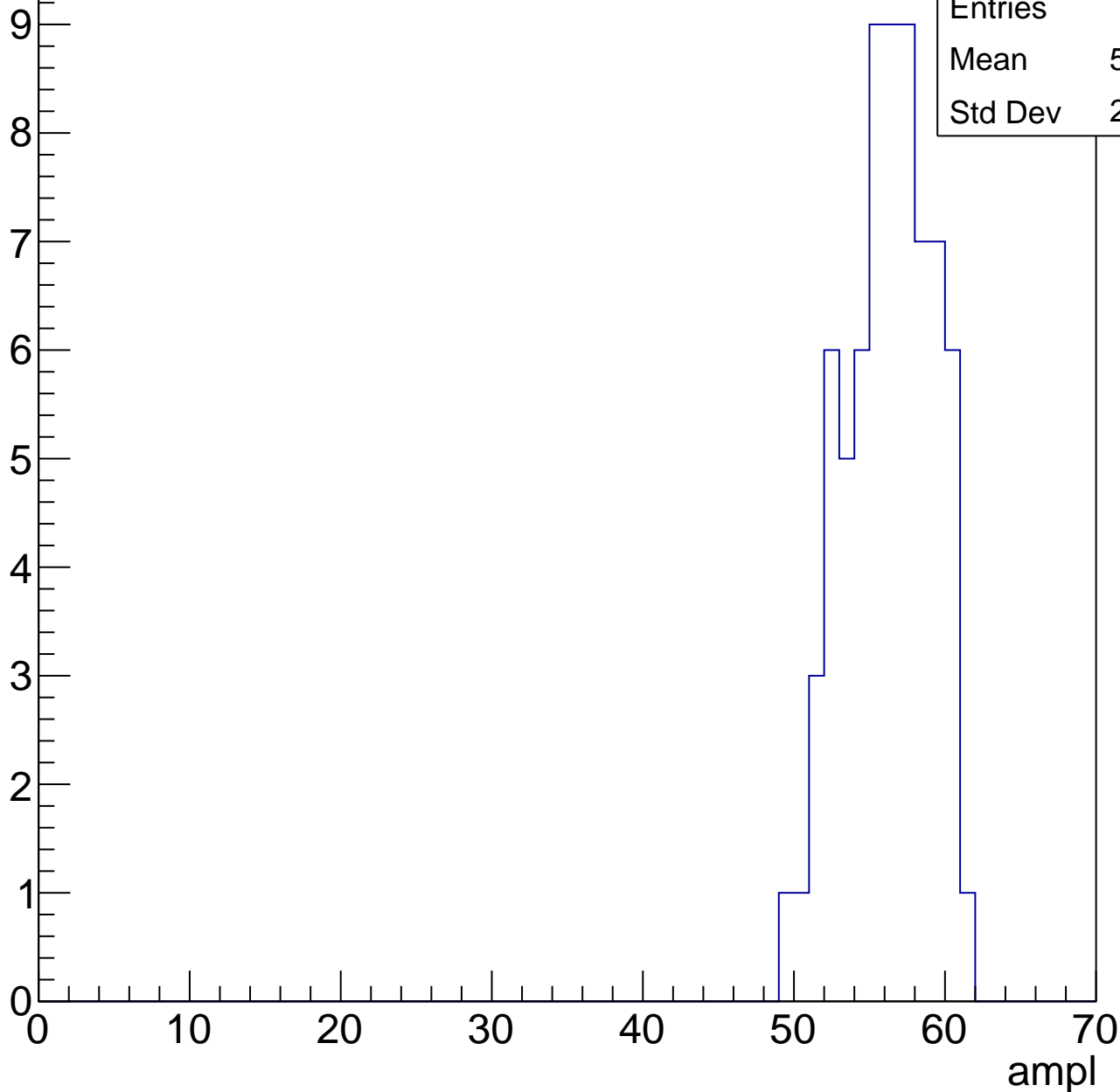


# B1L002S, U3-ch9, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	55.79
Std Dev	2.808

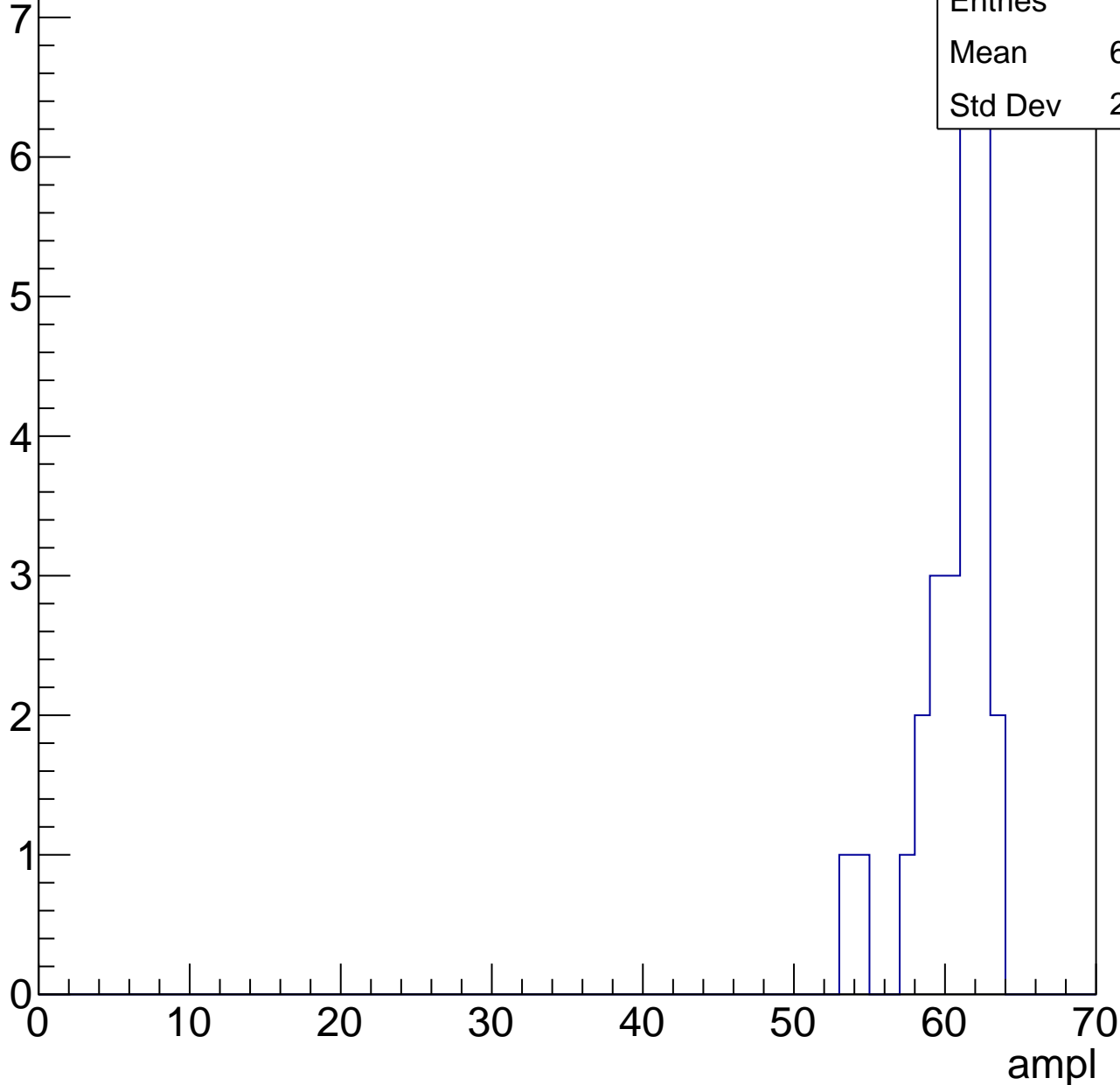


# B1L002S, U3-ch9, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	60.15
Std Dev	2.415

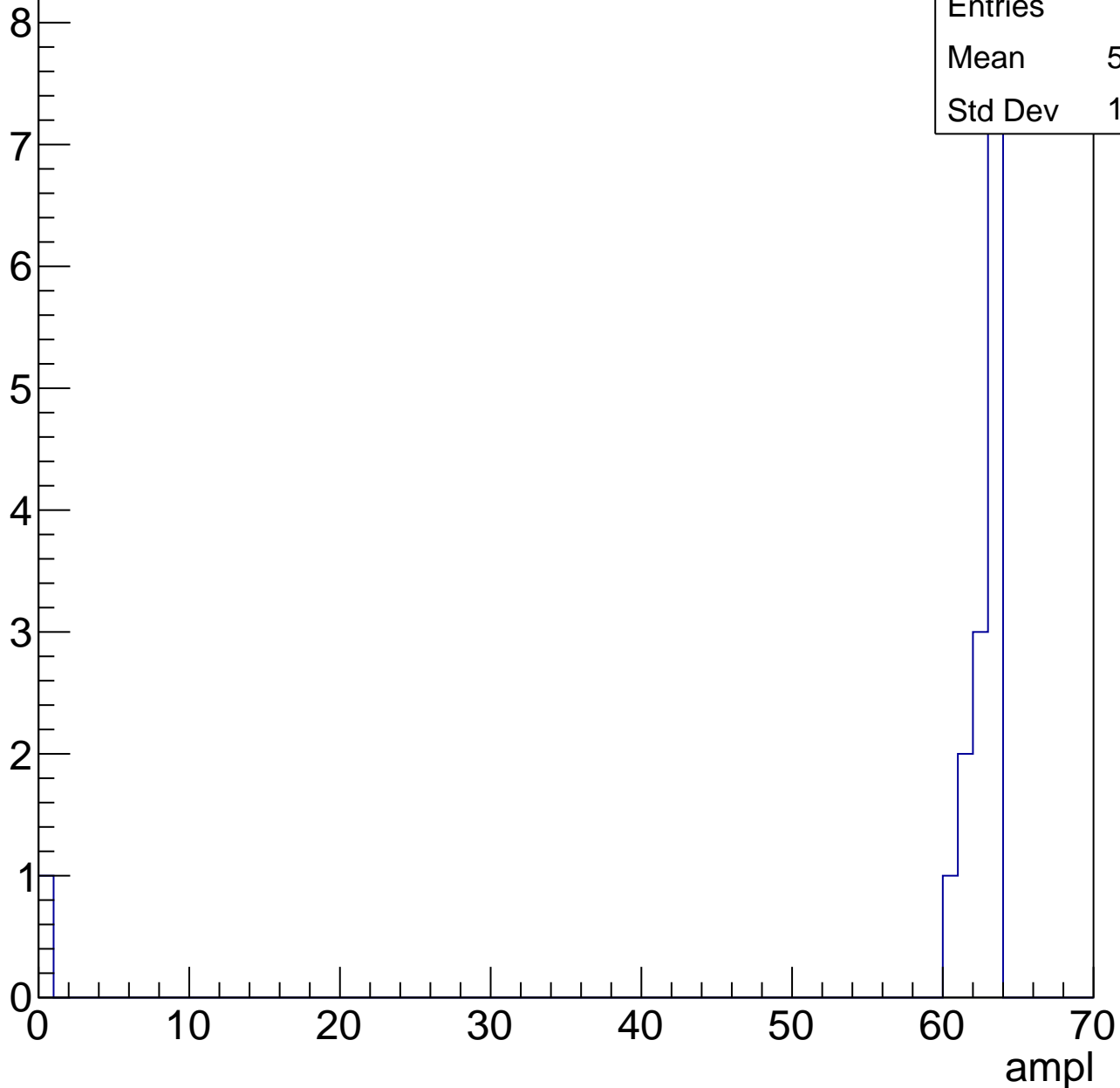


# B1L002S, U3-ch9, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	58.13
Std Dev	15.56





# B1L002S, U3-ch9, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch10, adc0

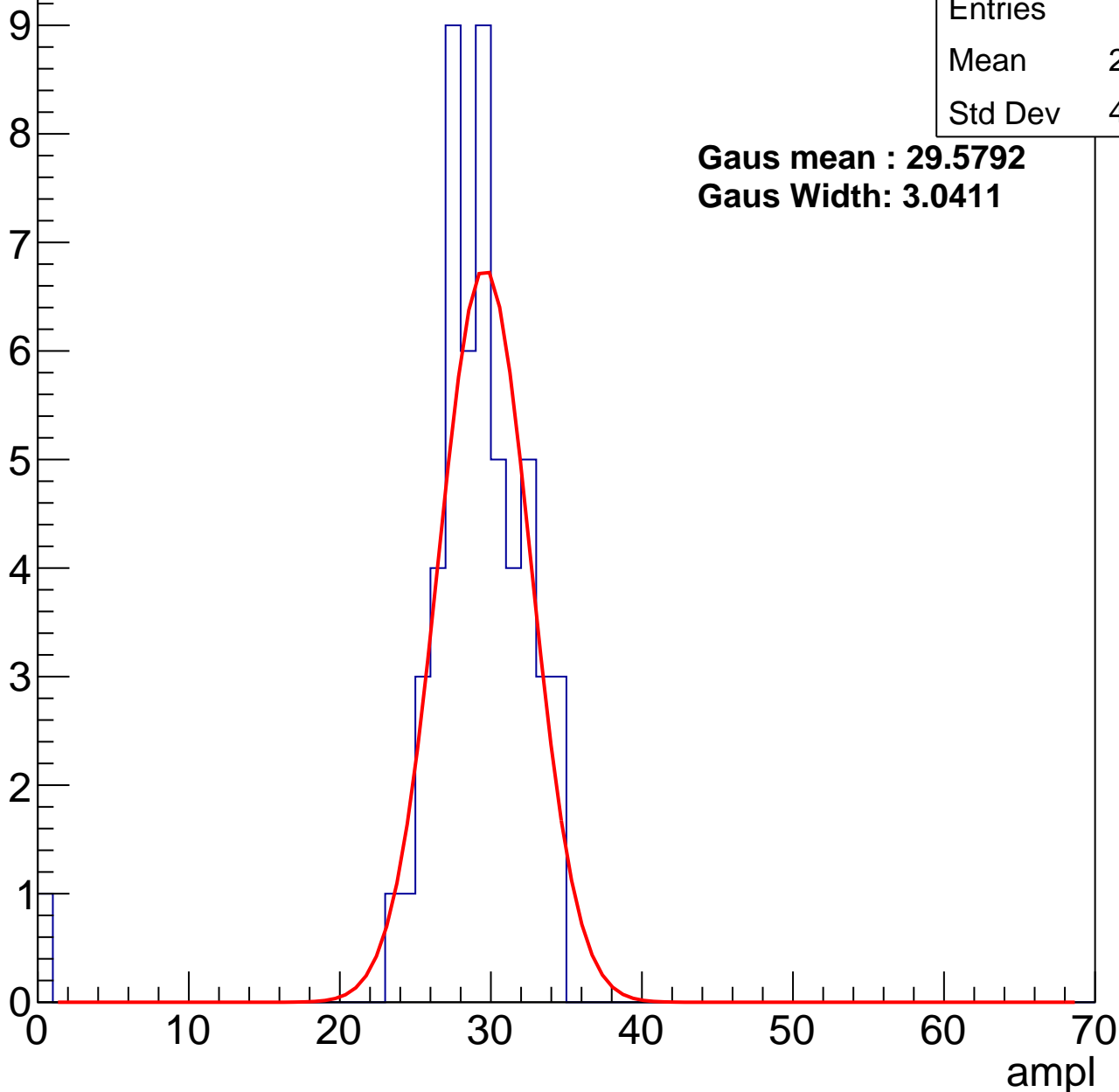
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.39
Std Dev	4.708

**Gaus mean : 29.5792**

**Gaus Width: 3.0411**



# B1L002S, U3-ch10, adc1

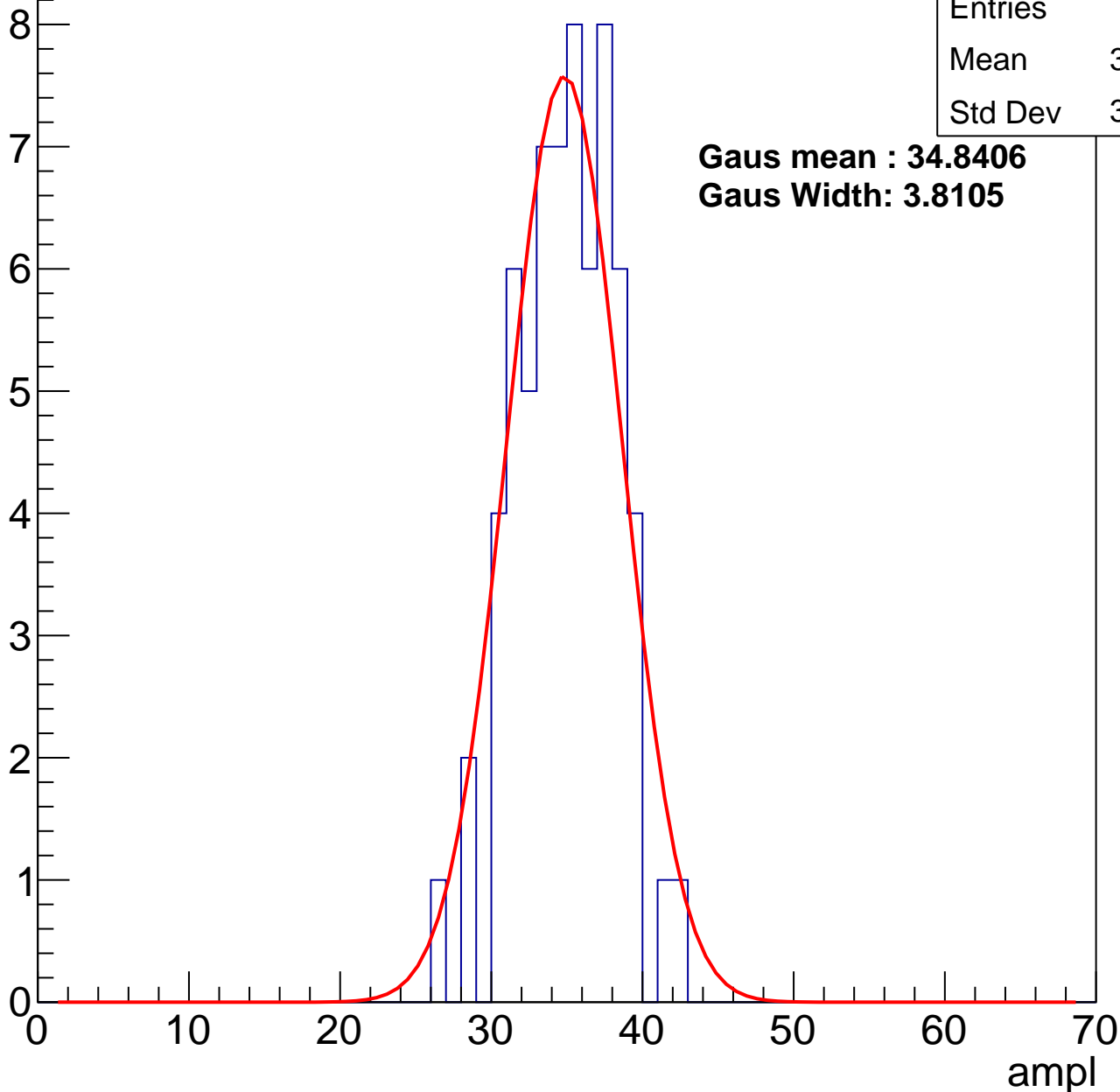
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.48
Std Dev	3.206

**Gaus mean : 34.8406**

**Gaus Width: 3.8105**



# B1L002S, U3-ch10, adc2

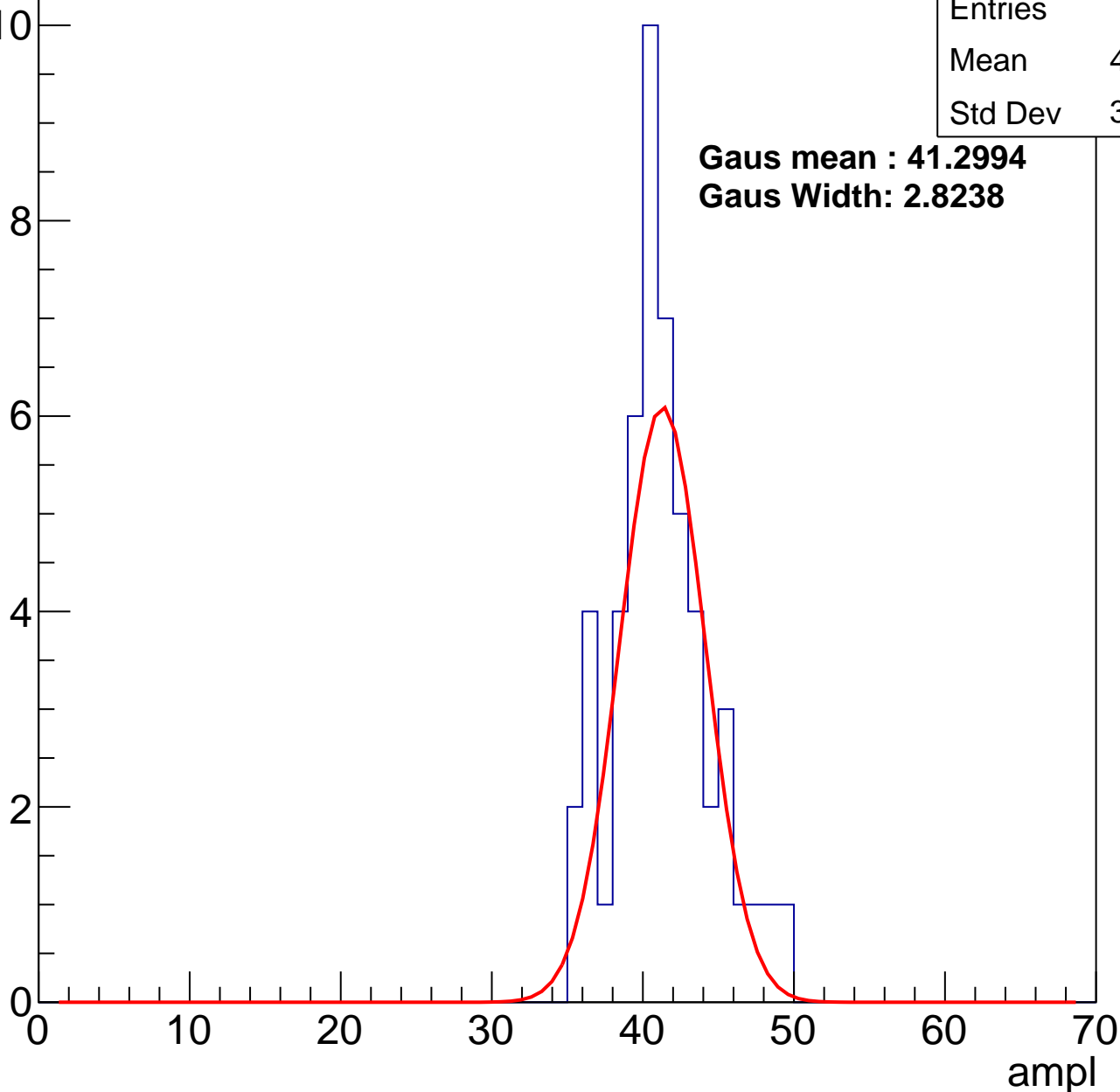
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.75
Std Dev	3.149

**Gaus mean : 41.2994**

**Gaus Width: 2.8238**

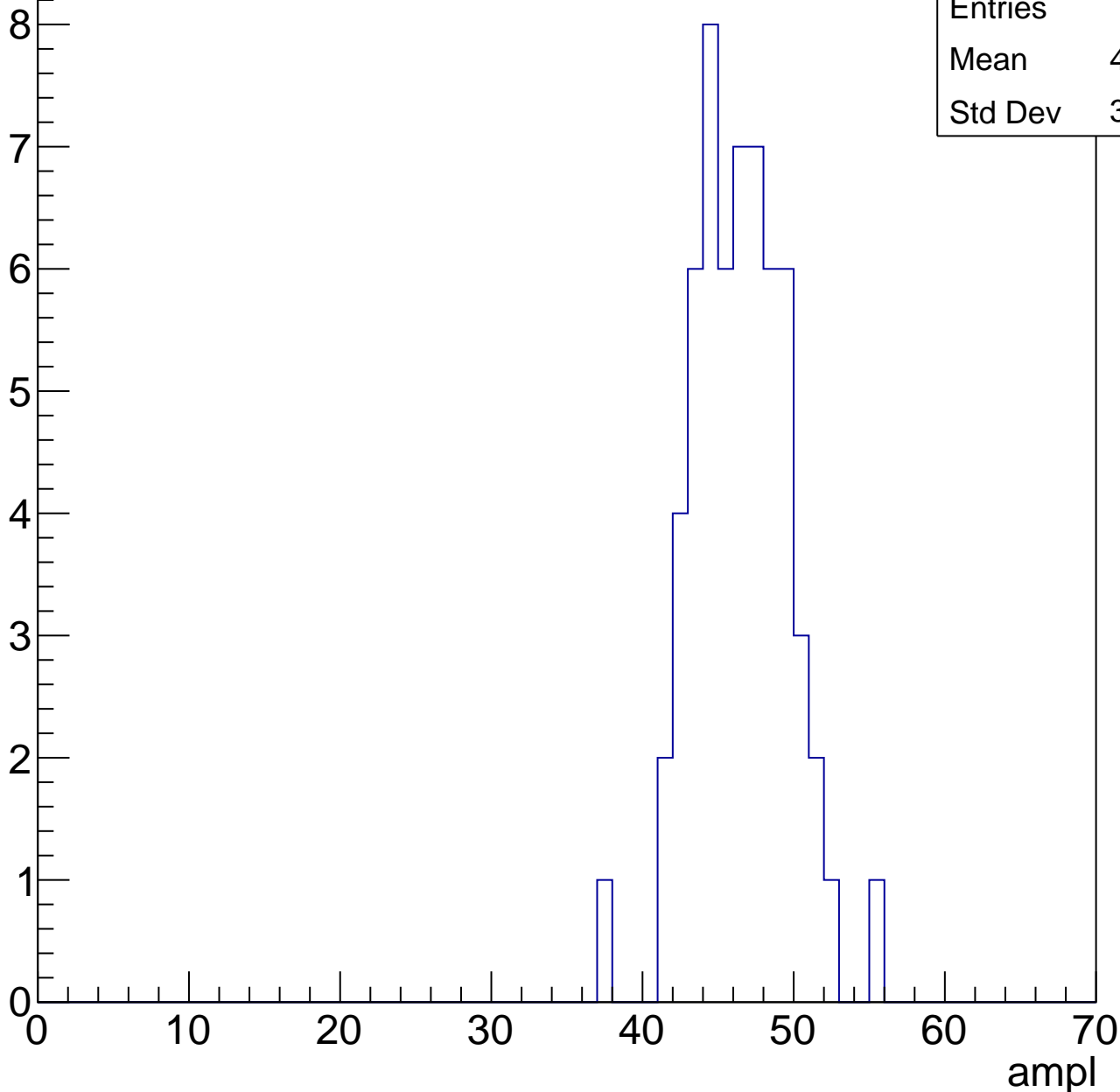


# B1L002S, U3-ch10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

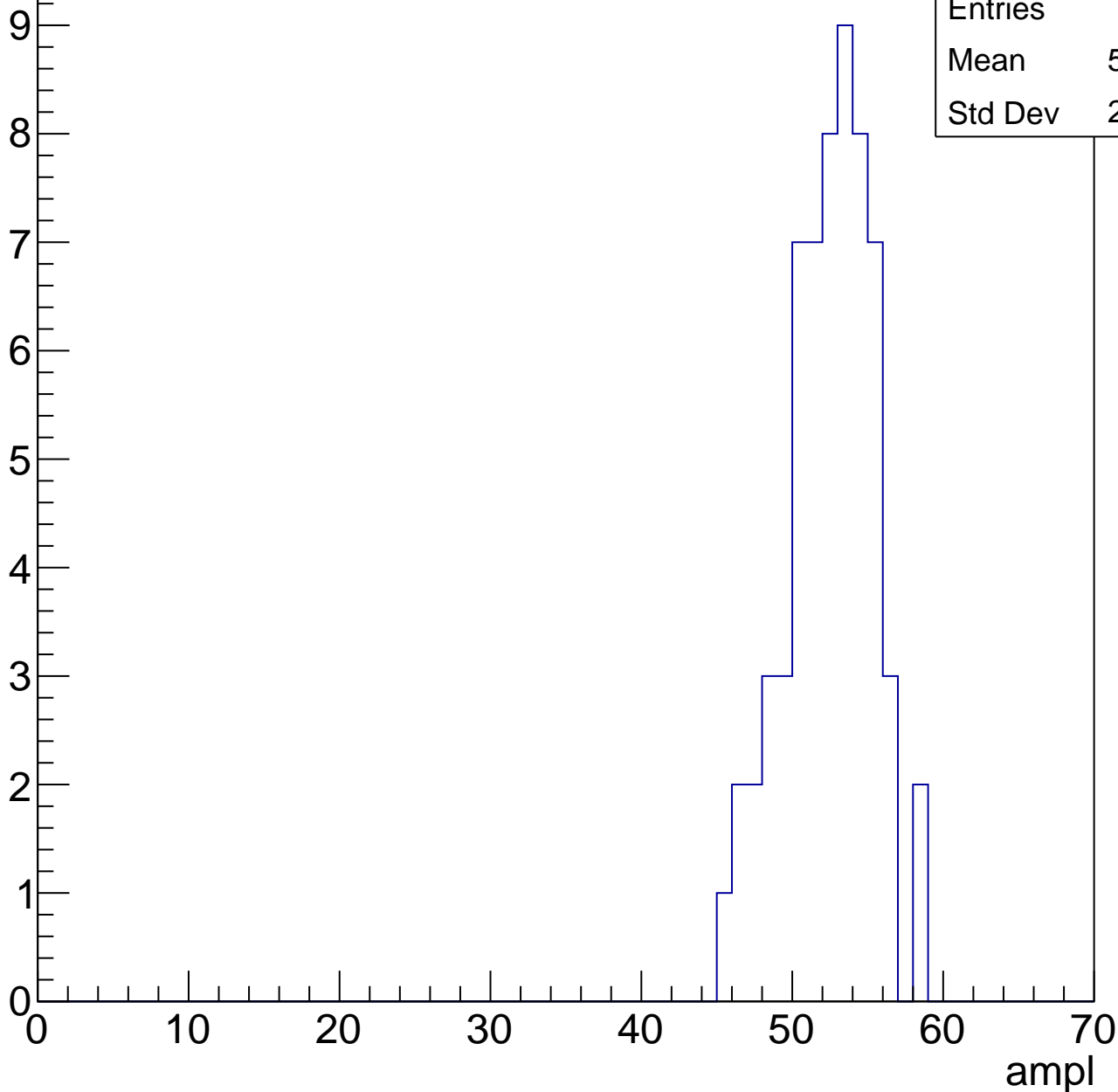
Entries	60
Mean	45.98
Std Dev	3.128



# B1L002S, U3-ch10, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

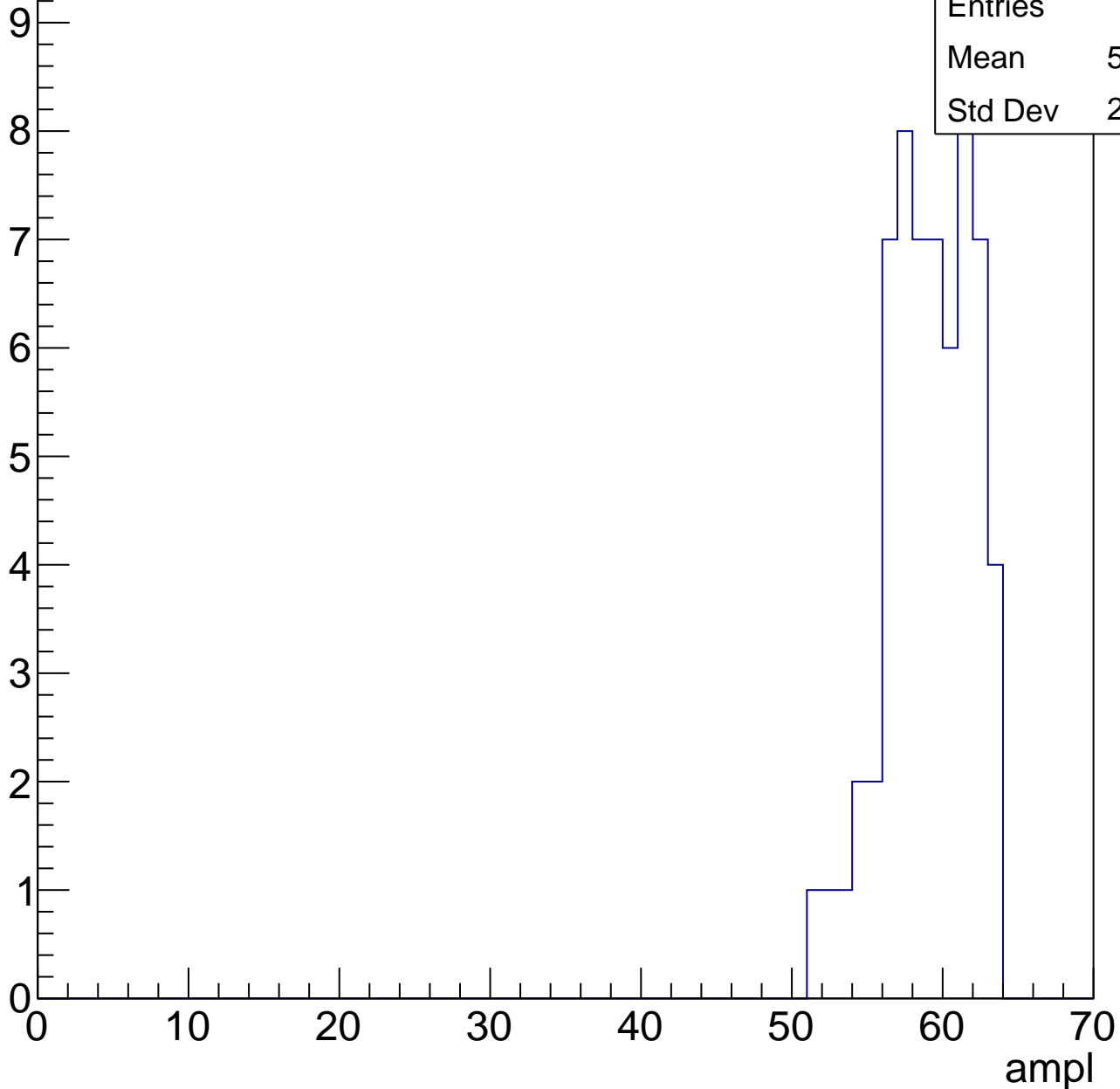


# B1L002S, U3-ch10, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.65
Std Dev	2.823

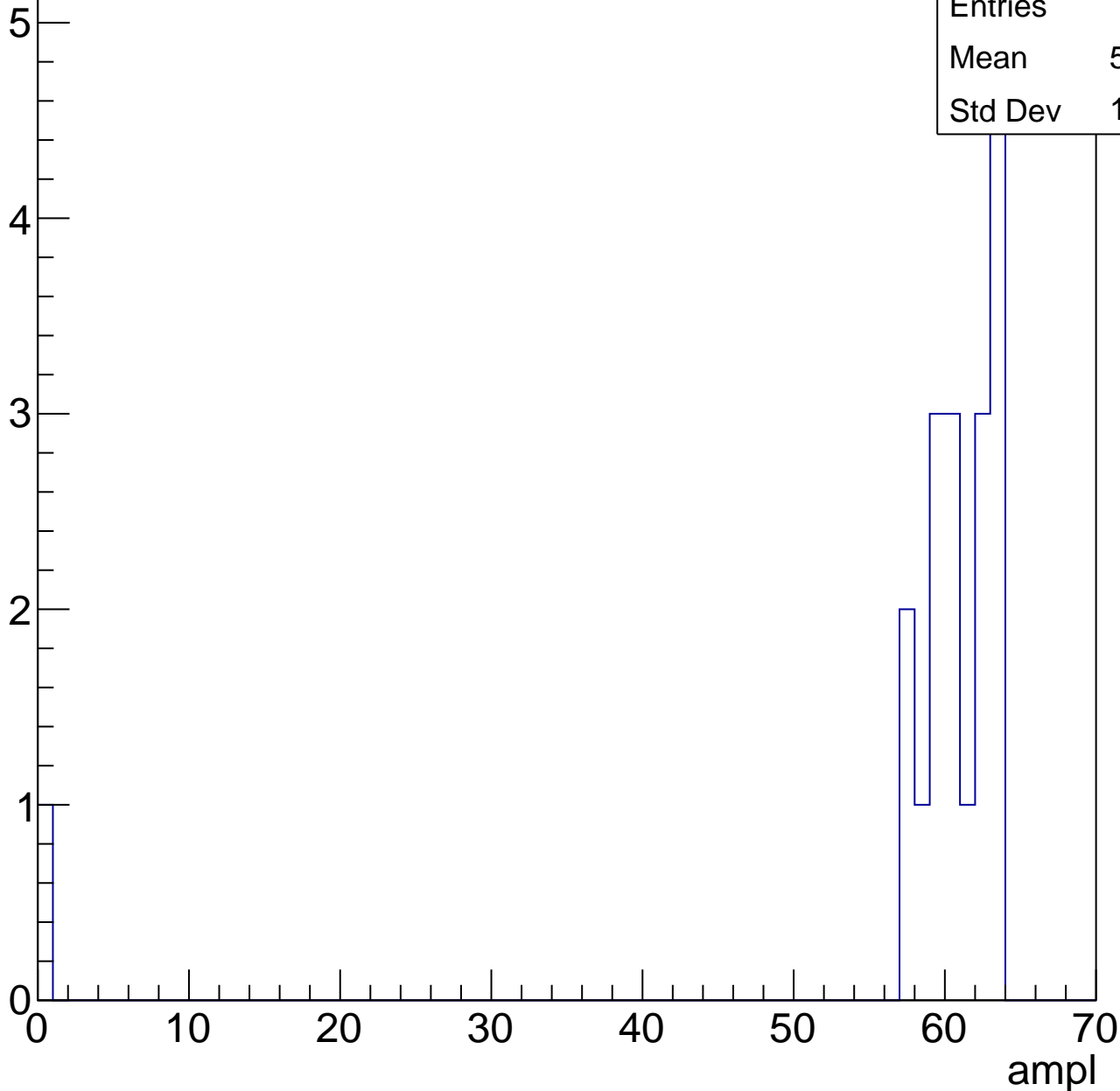


# B1L002S, U3-ch10, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	57.42
Std Dev	13.68

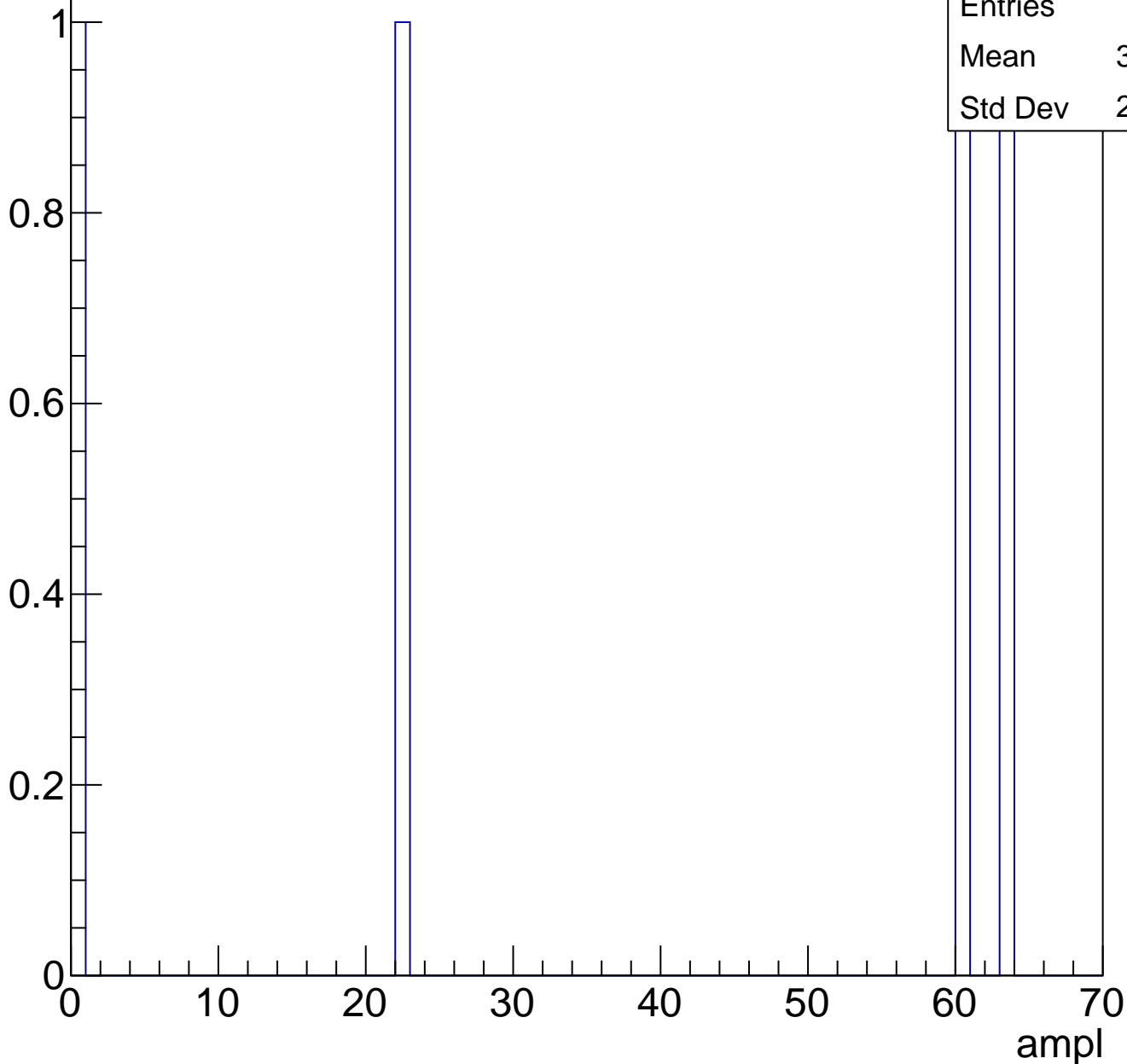




# B1L002S, U3-ch10, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch11, adc0

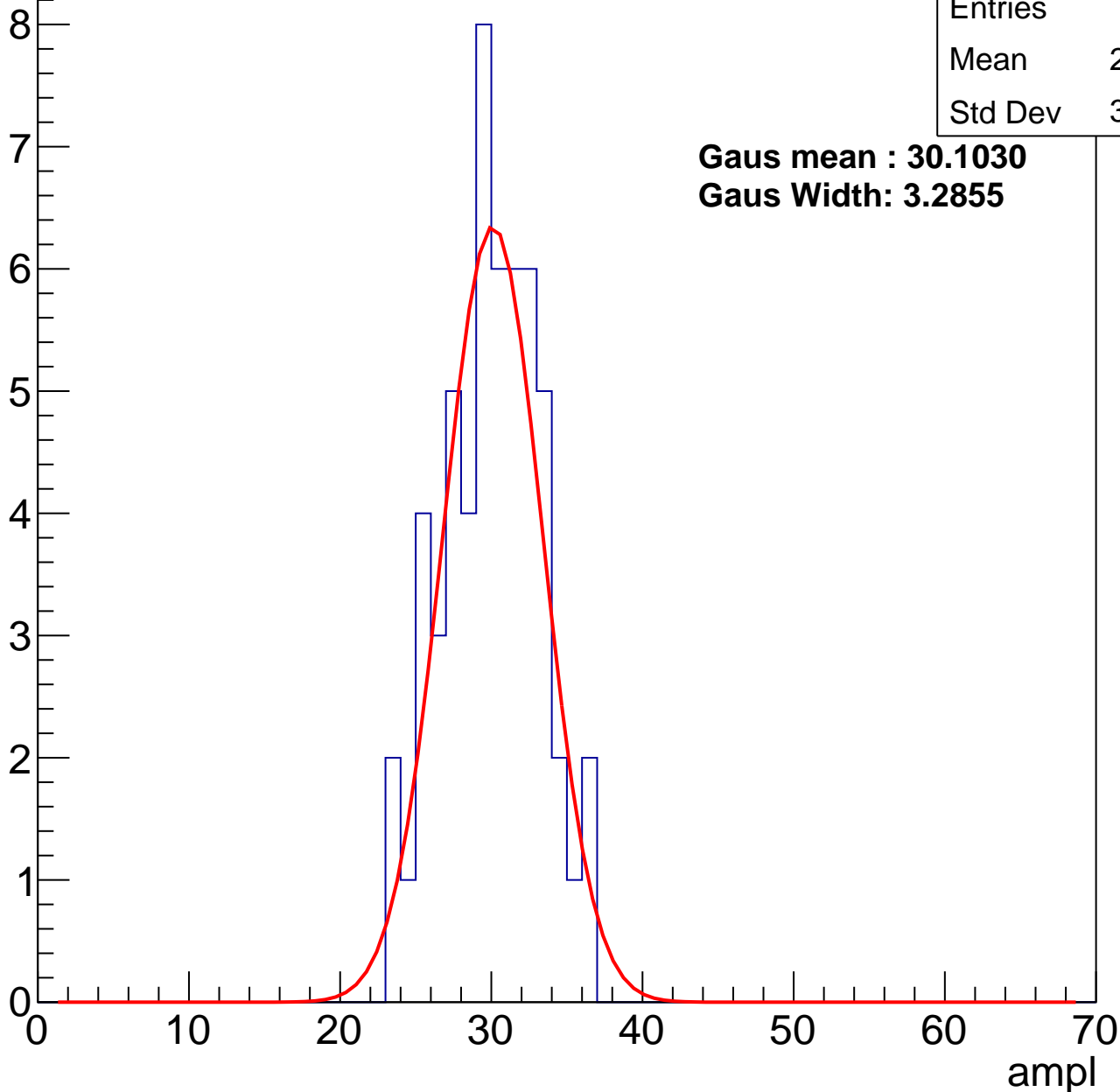
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	29.55
Std Dev	3.144

**Gaus mean : 30.1030**

**Gaus Width: 3.2855**



# B1L002S, U3-ch11, adc1

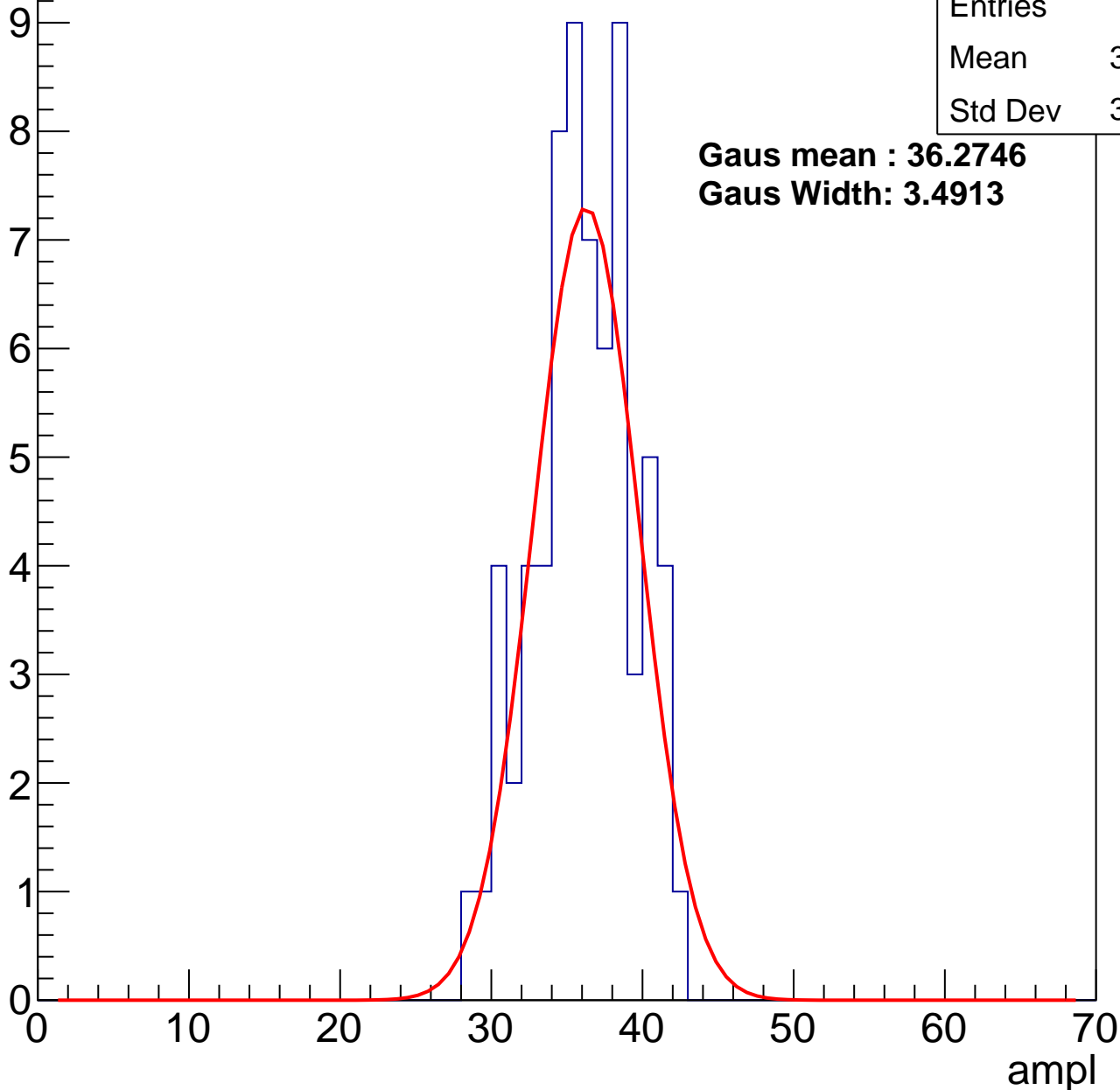
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.66
Std Dev	3.266

**Gaus mean : 36.2746**

**Gaus Width: 3.4913**



# B1L002S, U3-ch11, adc2

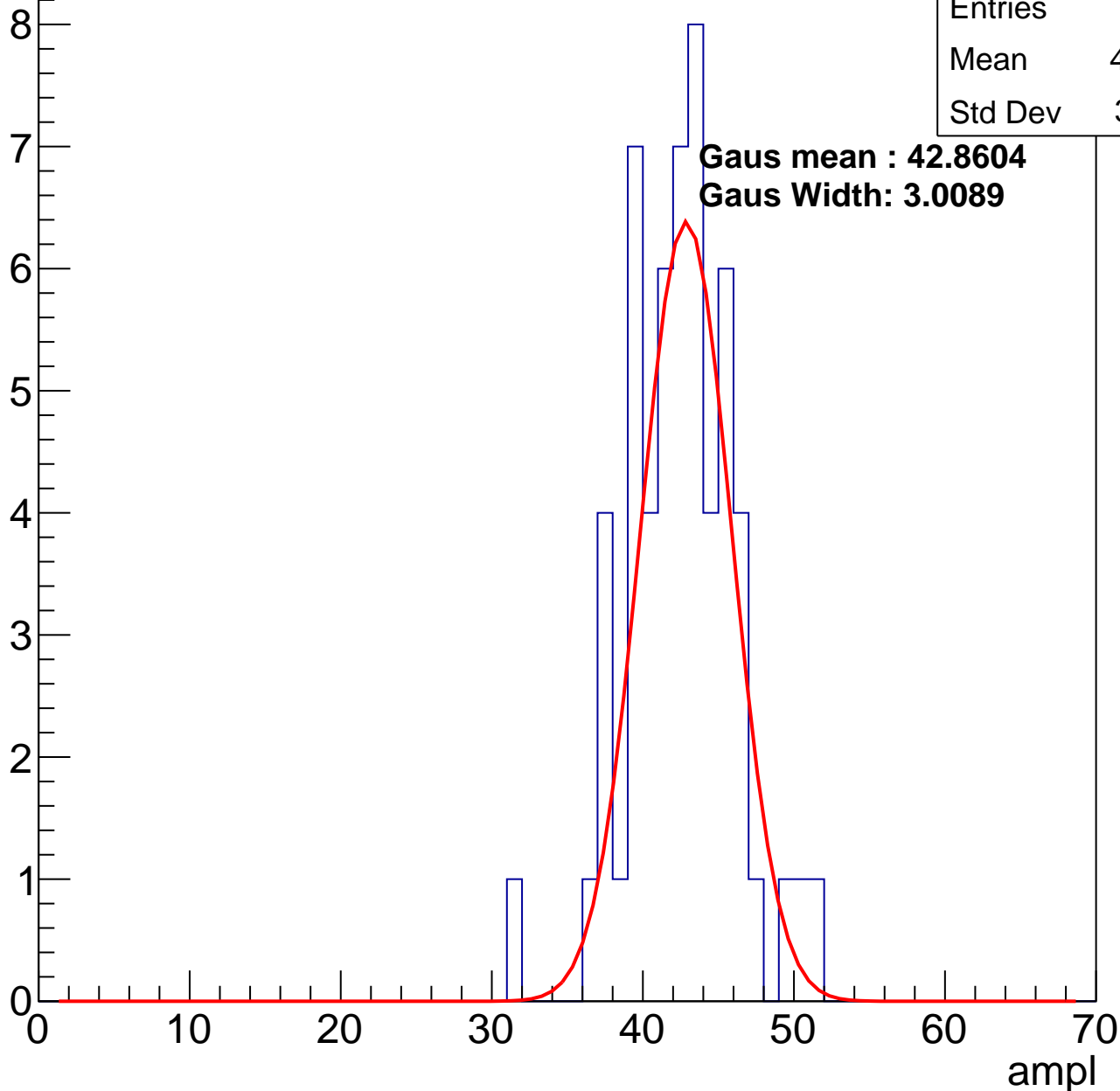
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	42.05
Std Dev	3.551

**Gaus mean : 42.8604**

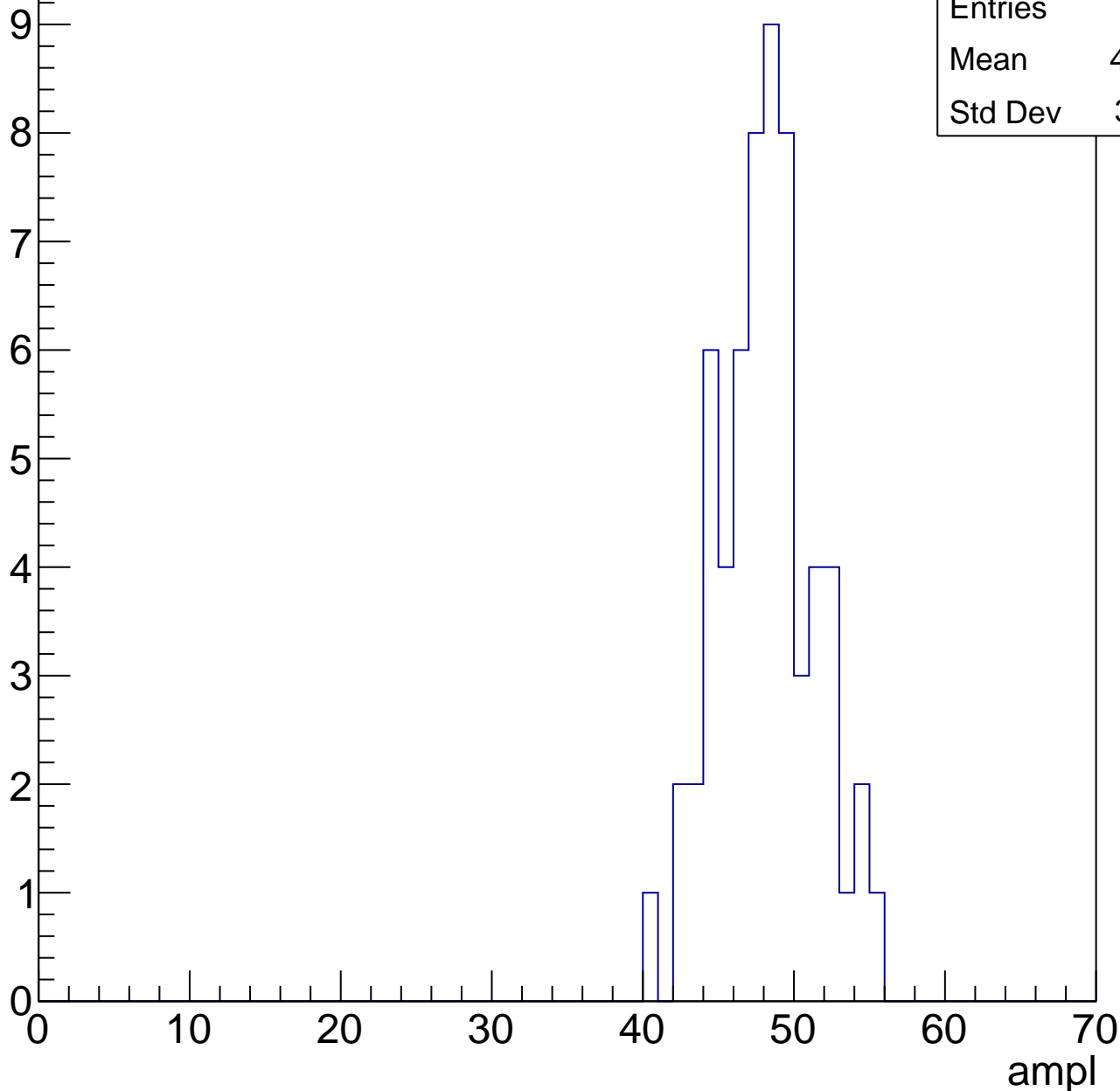
**Gaus Width: 3.0089**



# B1L002S, U3-ch11, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



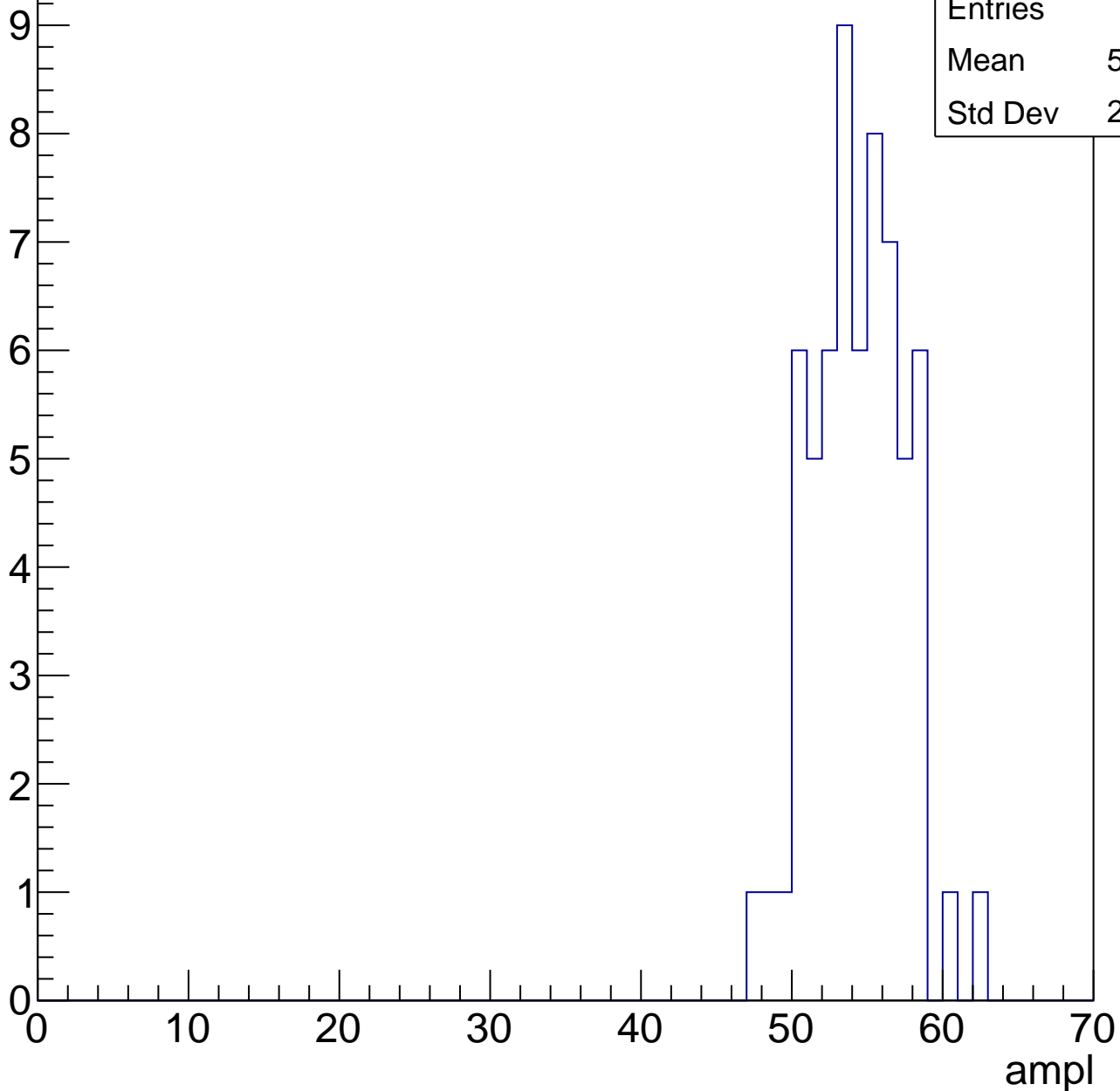
Entries	61
Mean	47.67
Std Dev	3.161

# B1L002S, U3-ch11, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

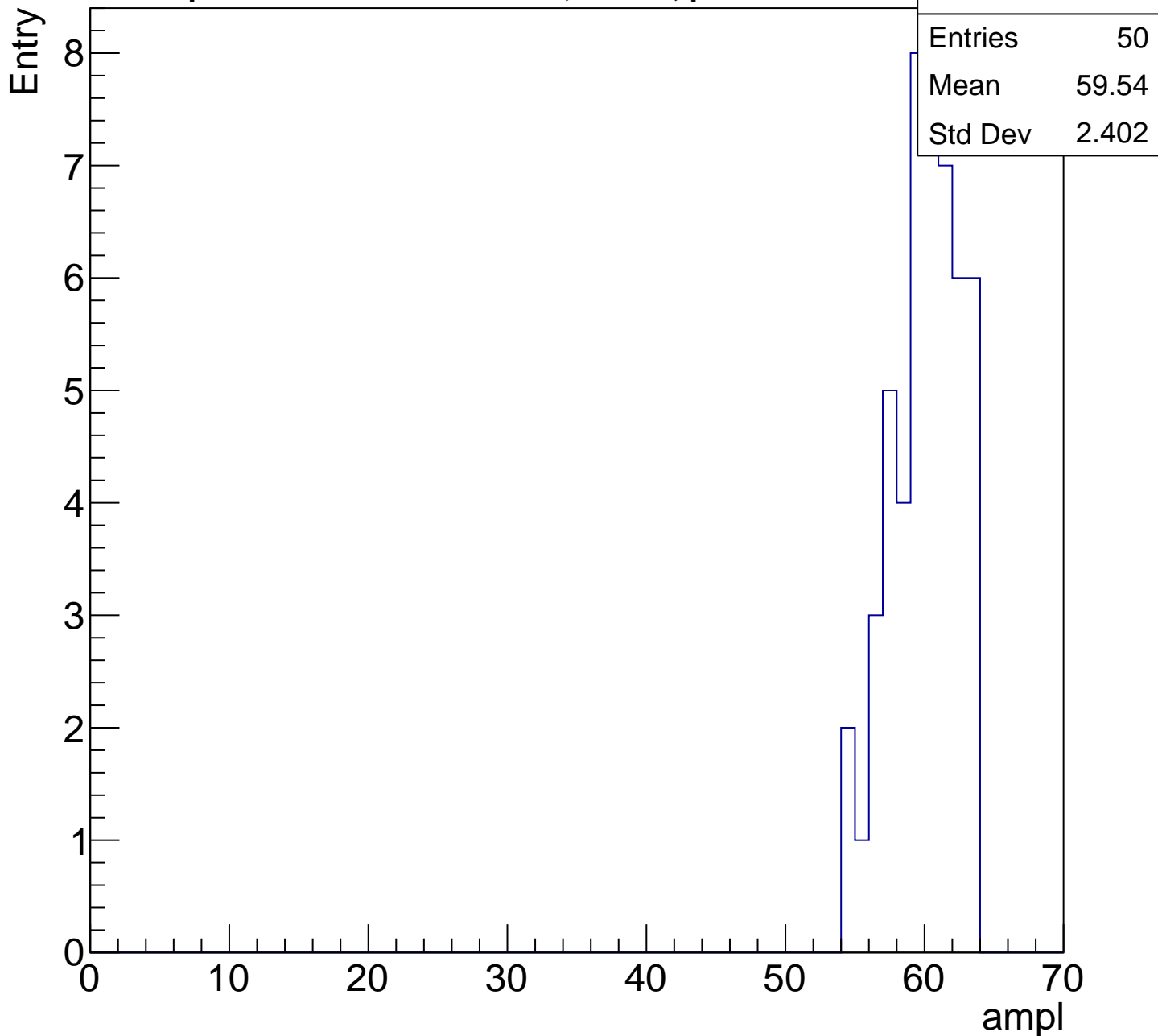
Entry

Entries	63
Mean	53.95
Std Dev	2.984



# B1L002S, U3-ch11, adc5

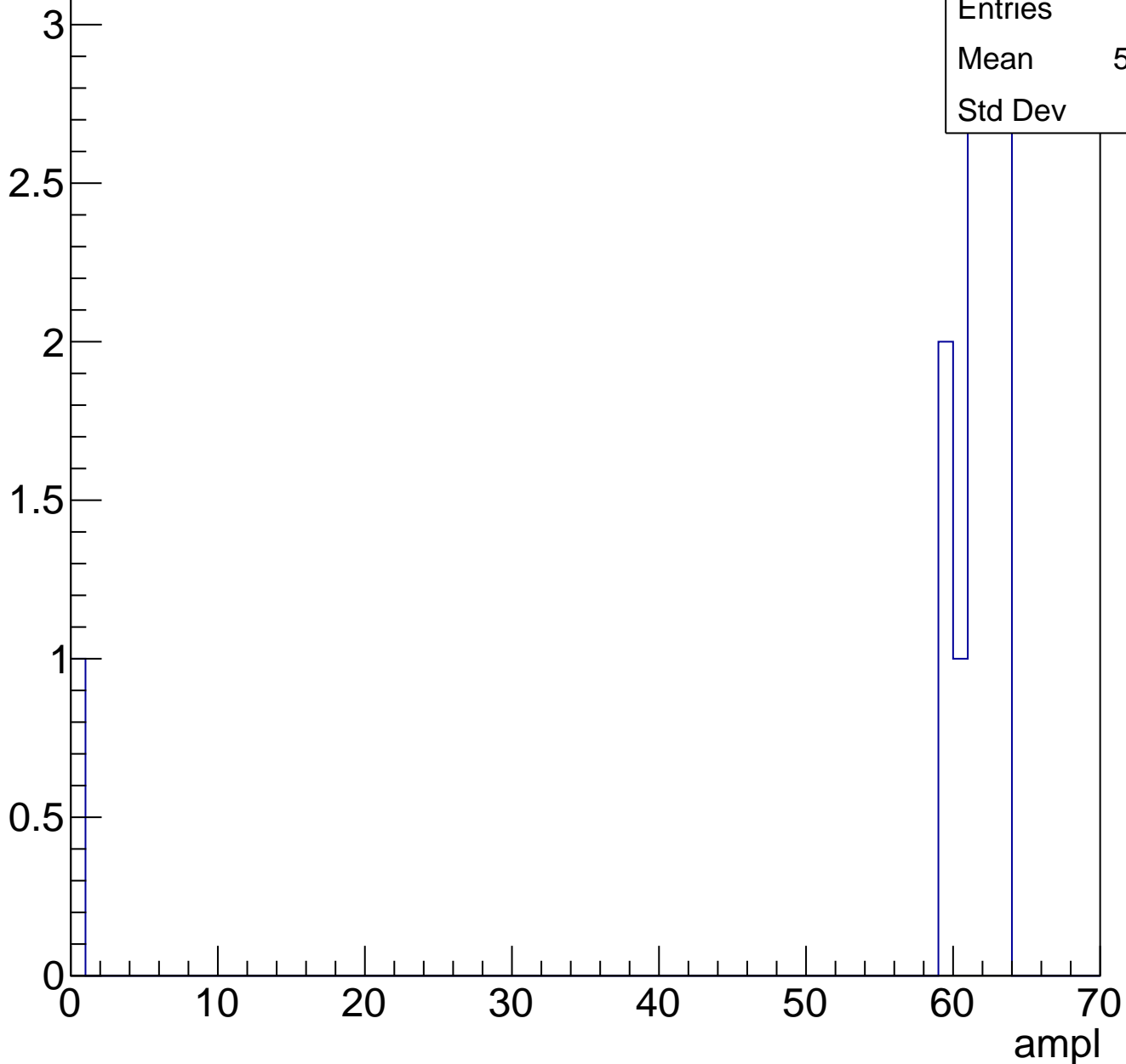
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch11, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch11, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch12, adc0

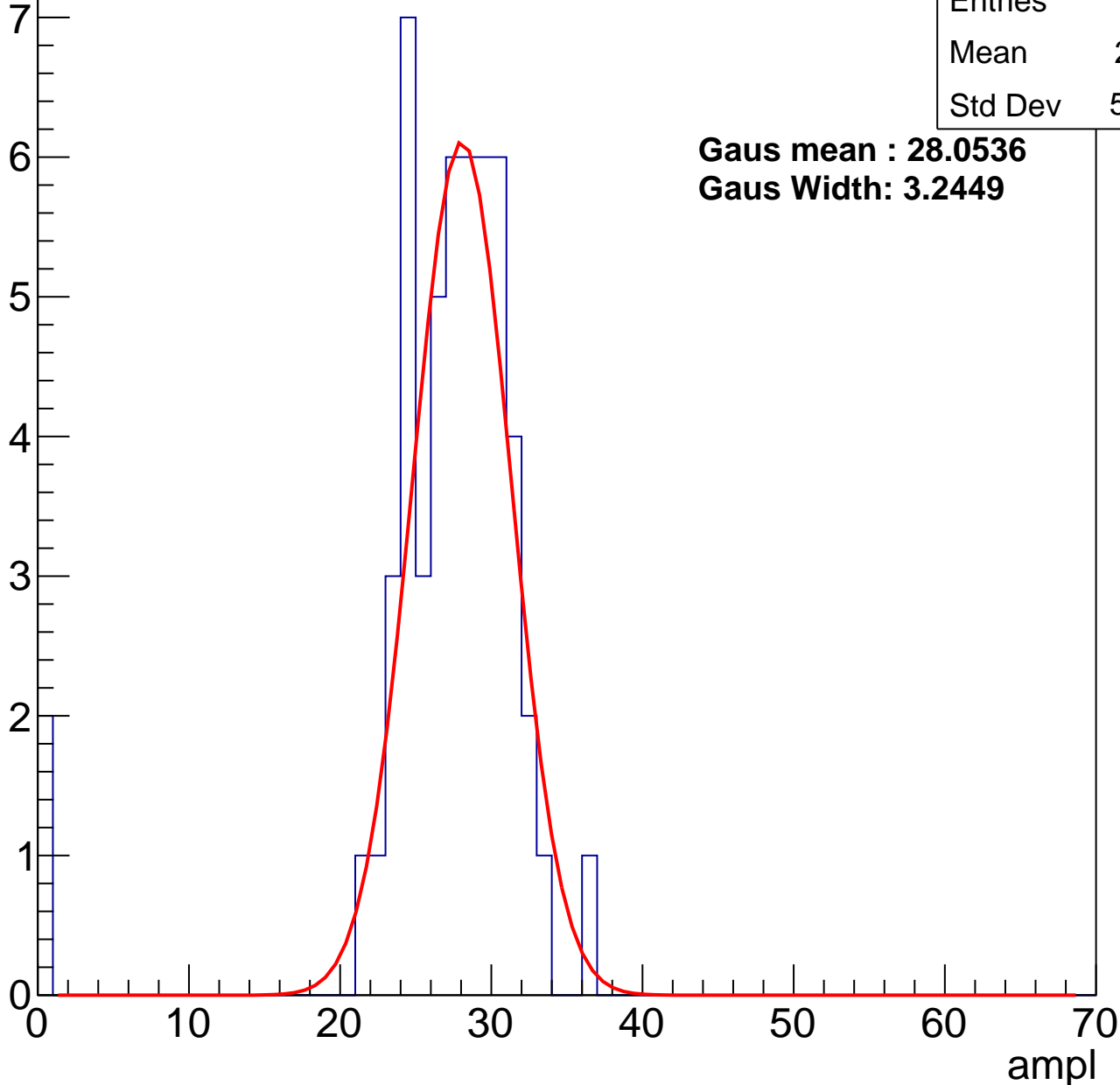
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	26.41
Std Dev	5.999

**Gaus mean : 28.0536**

**Gaus Width: 3.2449**



# B1L002S, U3-ch12, adc1

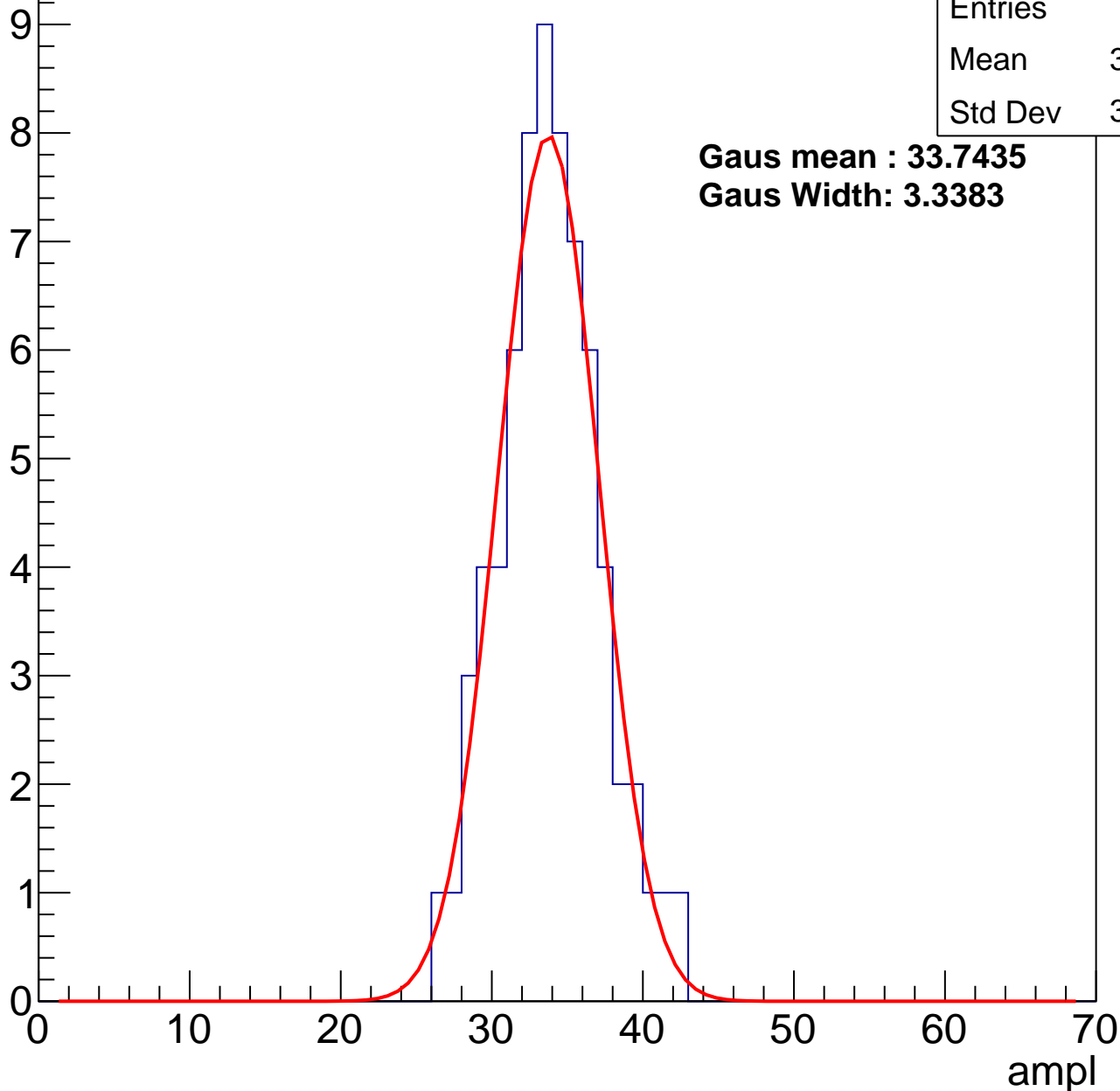
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.38
Std Dev	3.326

**Gaus mean : 33.7435**

**Gaus Width: 3.3383**



# B1L002S, U3-ch12, adc2

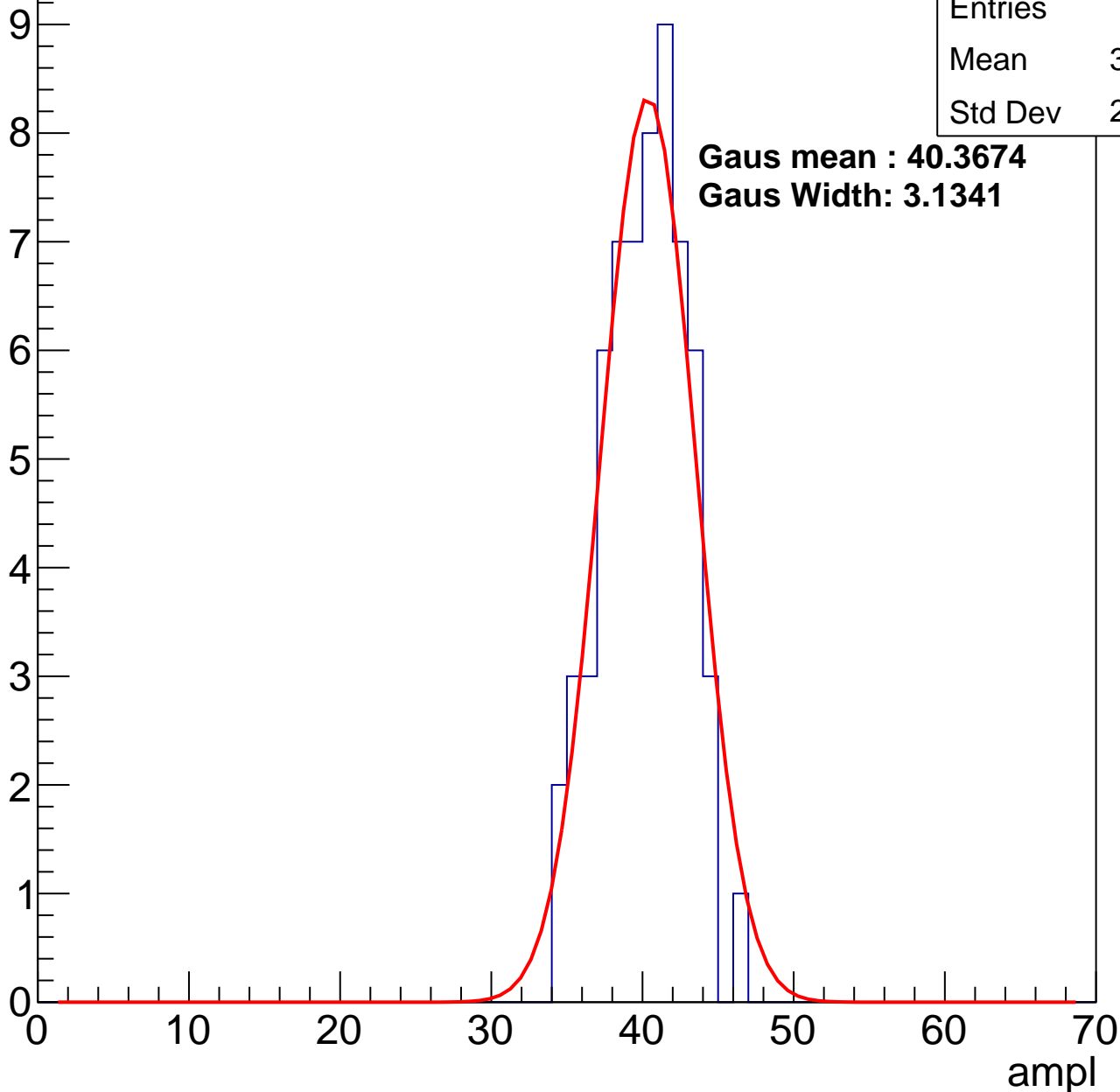
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	39.69
Std Dev	2.704

**Gaus mean : 40.3674**

**Gaus Width: 3.1341**

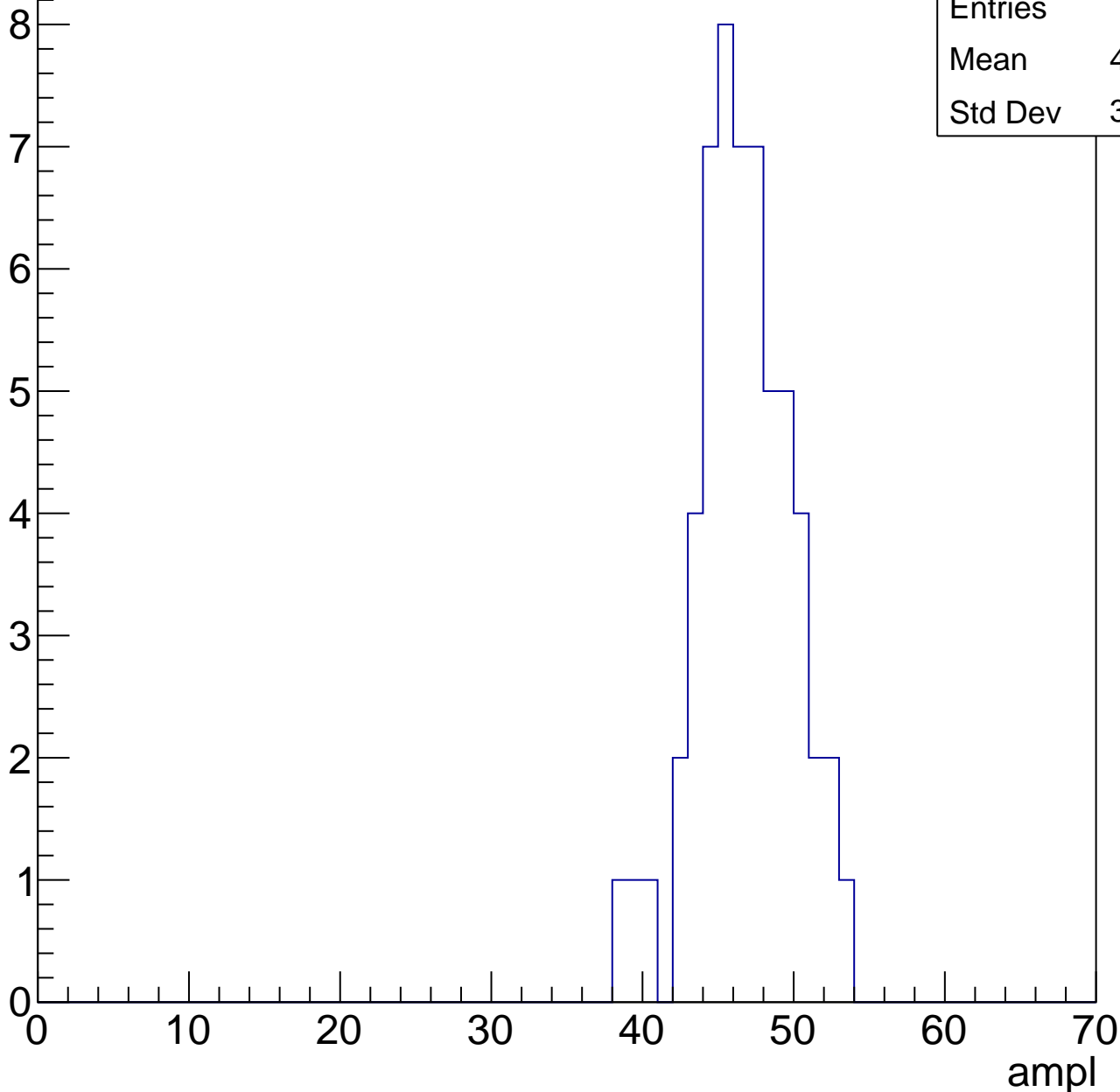


# B1L002S, U3-ch12, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.25
Std Dev	3.125

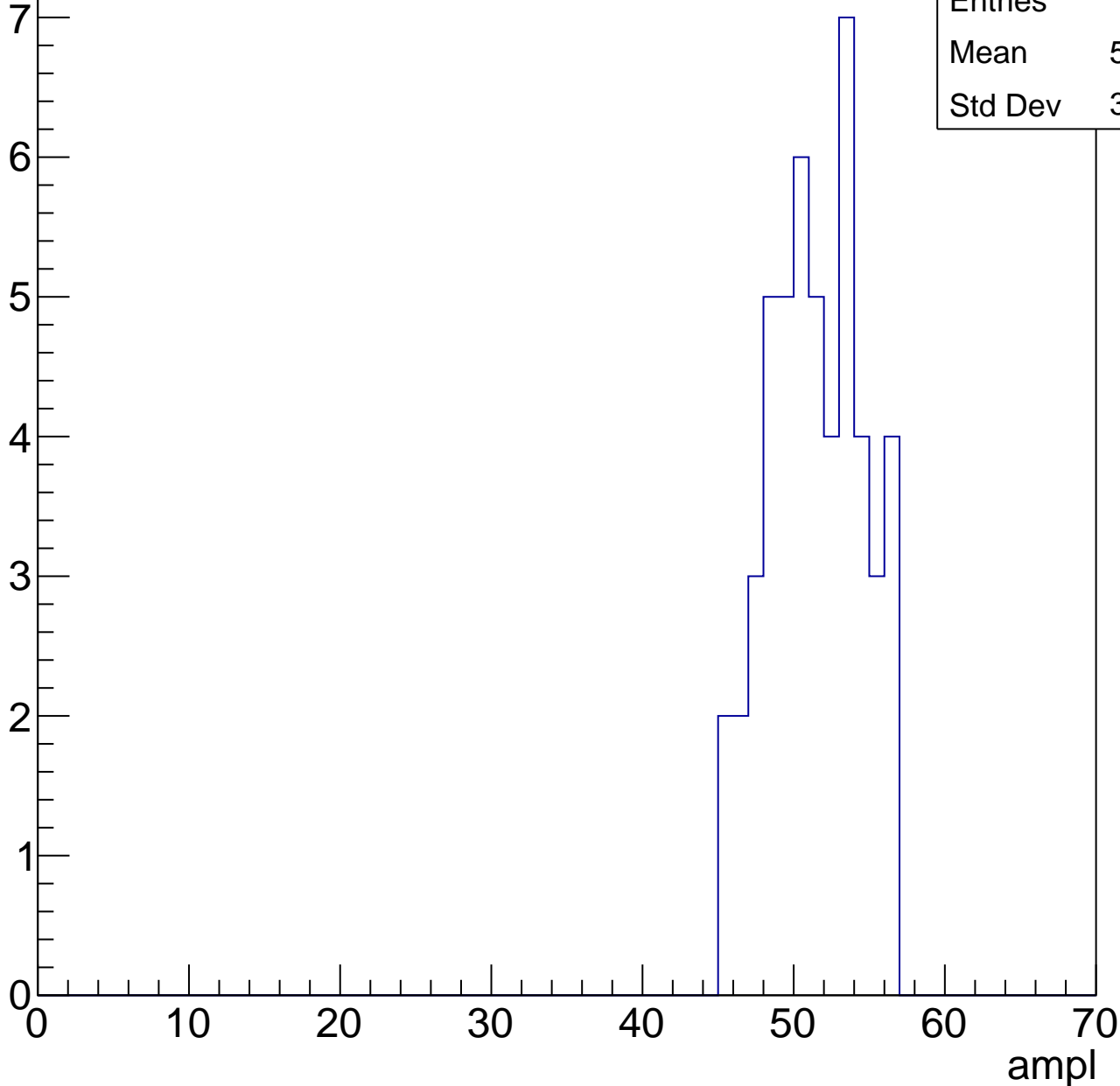


# B1L002S, U3-ch12, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	50.94
Std Dev	3.023



# B1L002S, U3-ch12, adc5

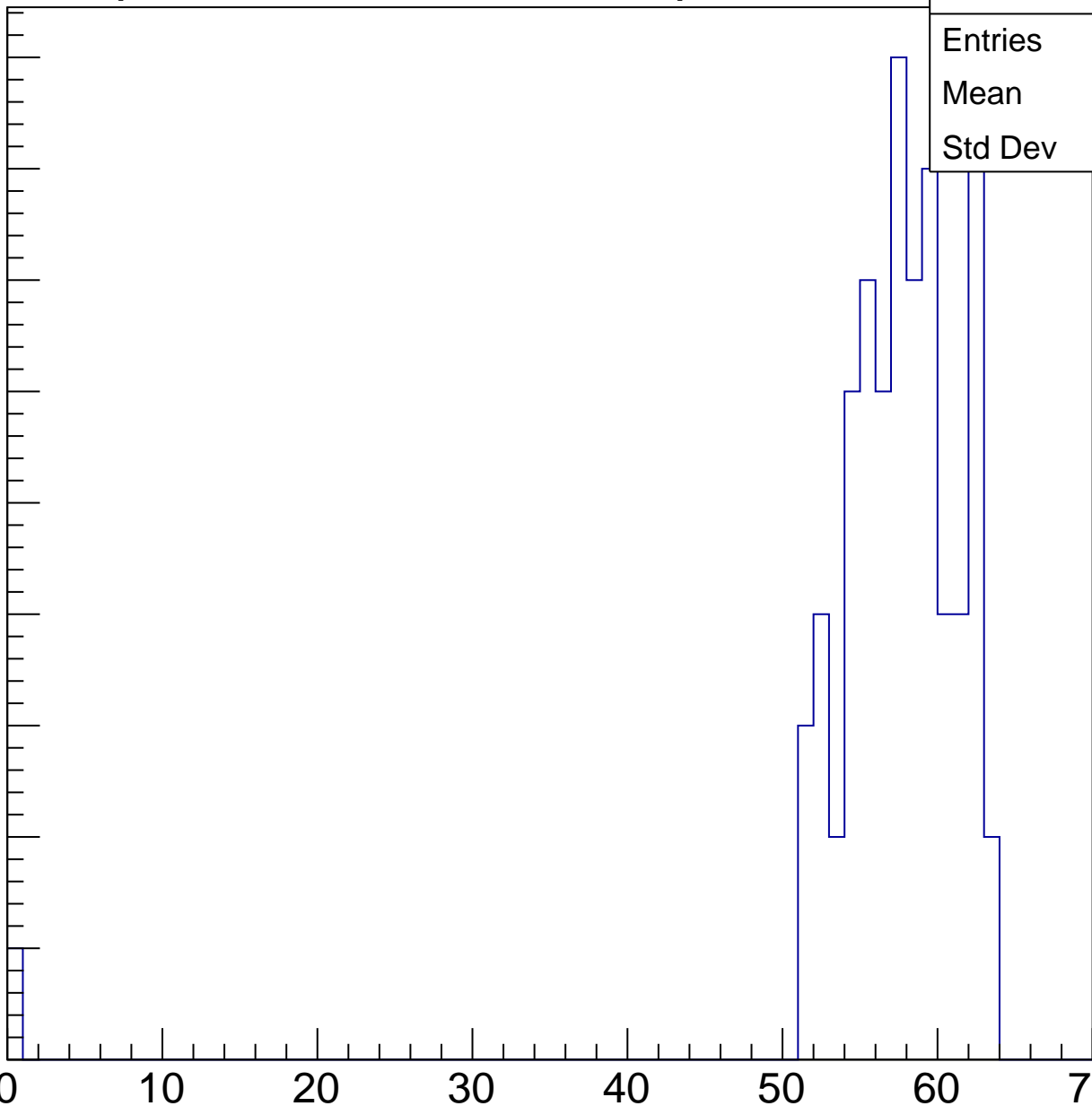
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	71
Mean	56.46
Std Dev	7.475

ampl

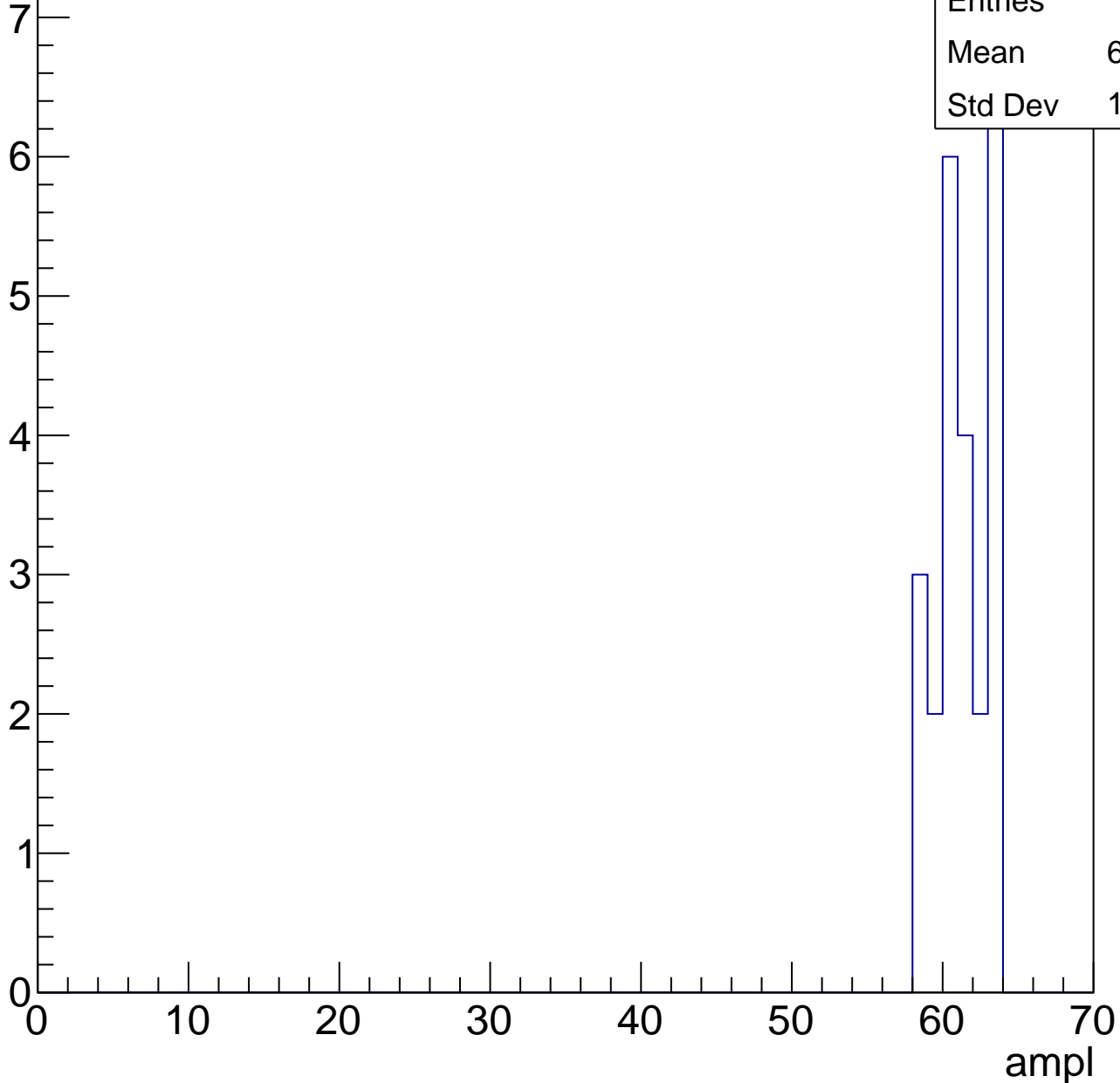


# B1L002S, U3-ch12, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	60.88
Std Dev	1.715

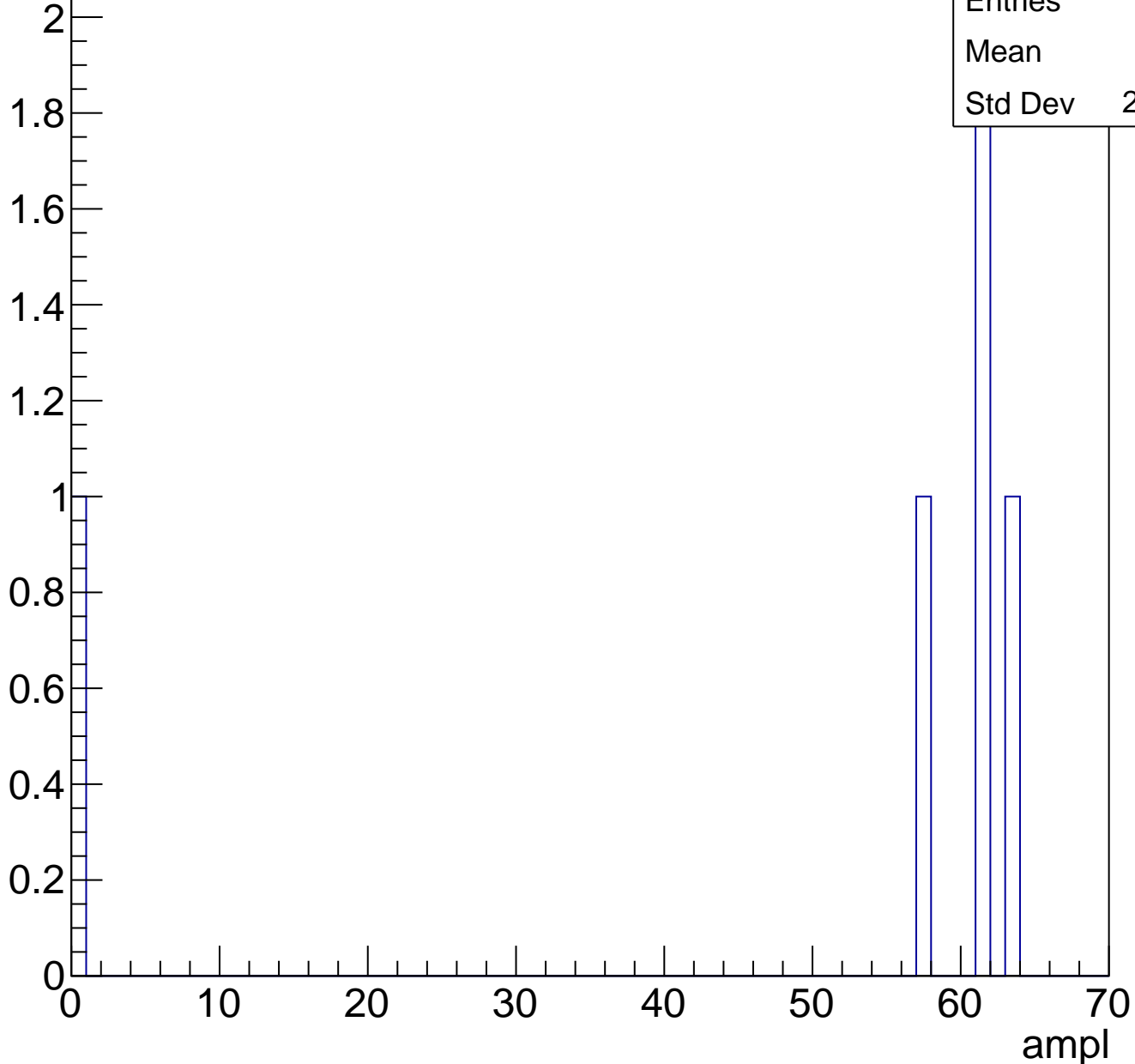




# B1L002S, U3-ch12, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	48.4
Std Dev	24.28

# B1L002S, U3-ch13, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	27.33
Std Dev	2.749

**Gaus mean : 27.9513**

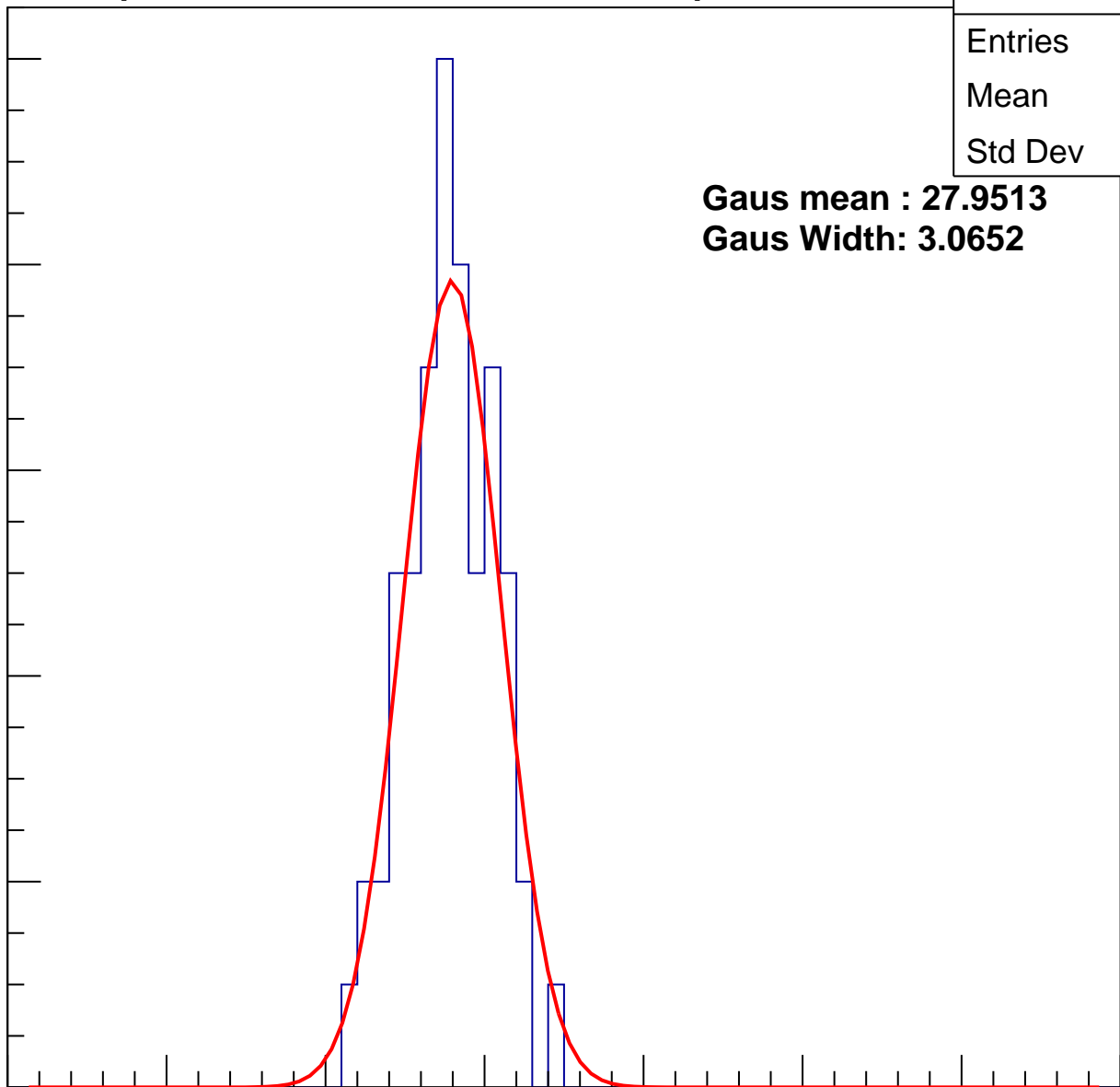
**Gaus Width: 3.0652**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch13, adc1

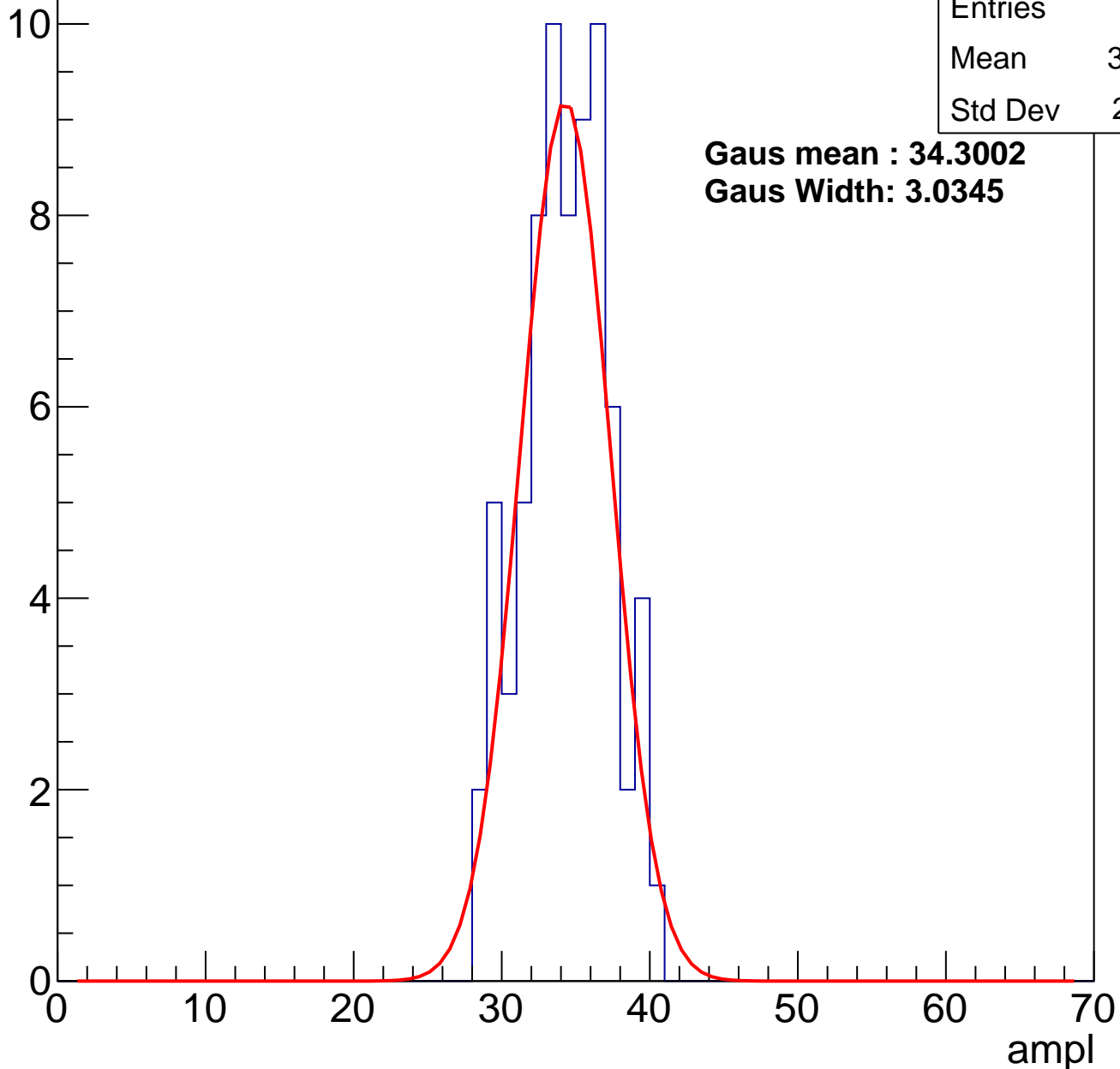
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	33.88
Std Dev	2.871

**Gaus mean : 34.3002**

**Gaus Width: 3.0345**

Entry



# B1L002S, U3-ch13, adc2

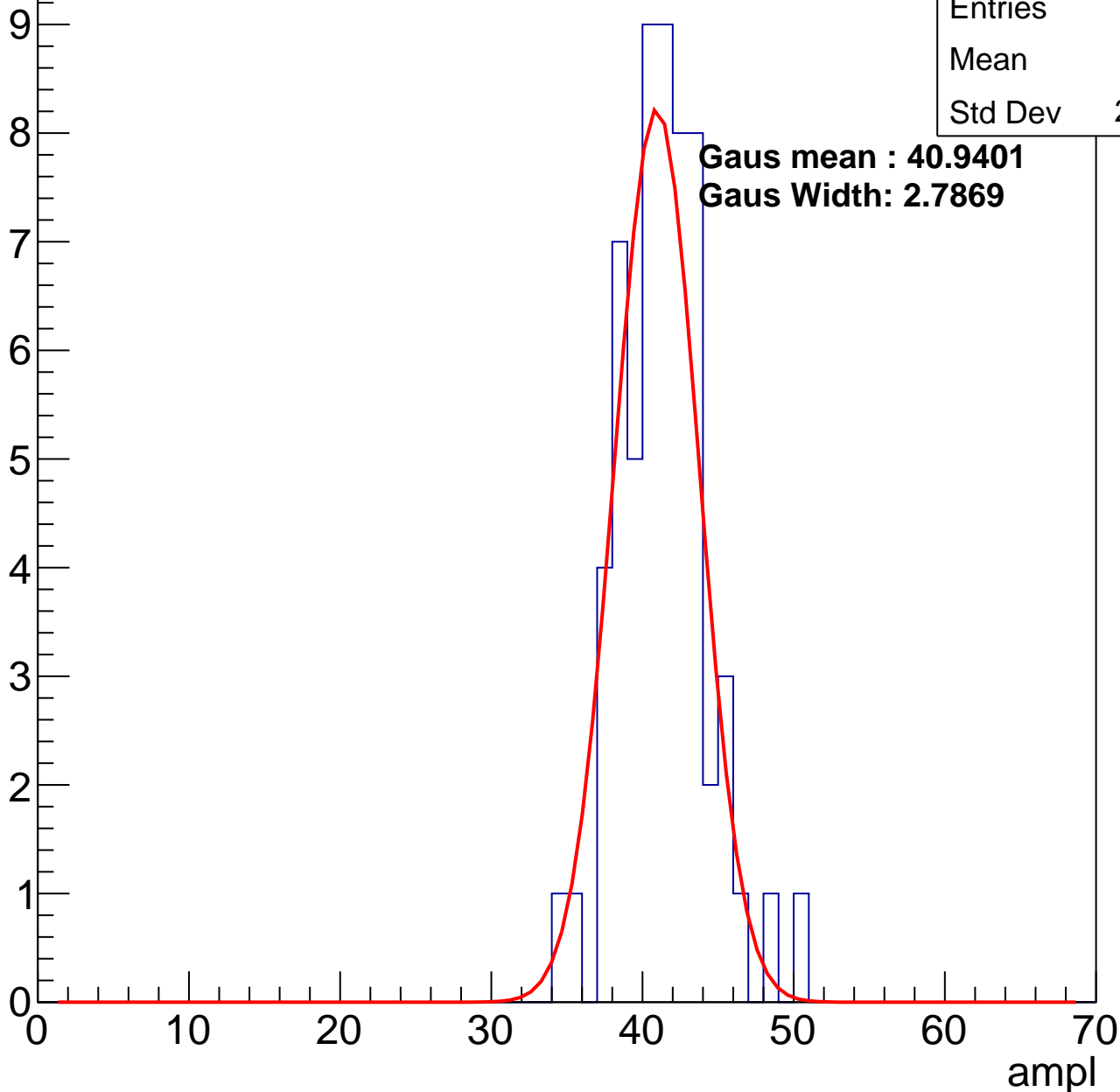
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	40.9
Std Dev	2.891

**Gaus mean : 40.9401**

**Gaus Width: 2.7869**

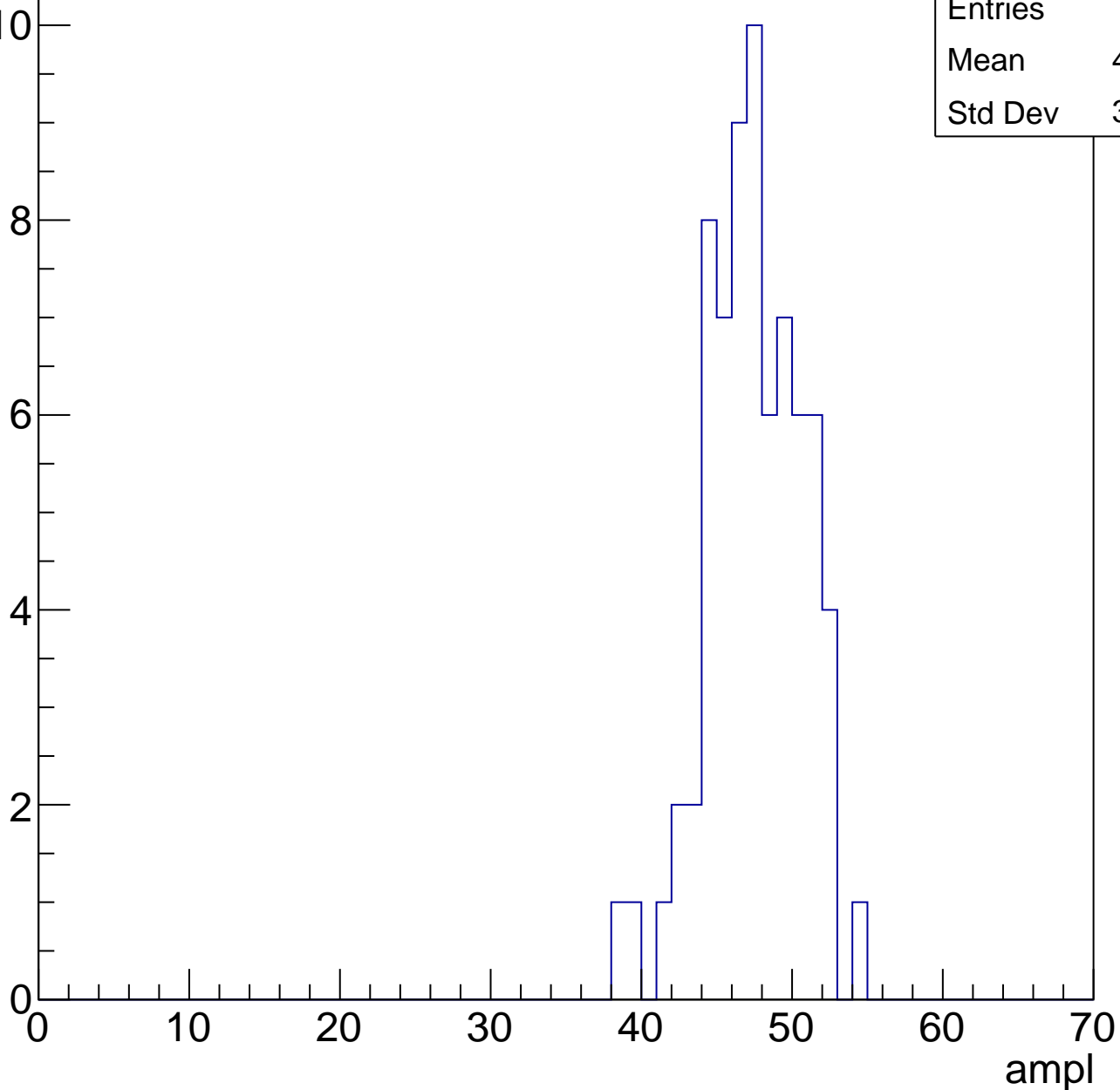


# B1L002S, U3-ch13, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	47.01
Std Dev	3.151

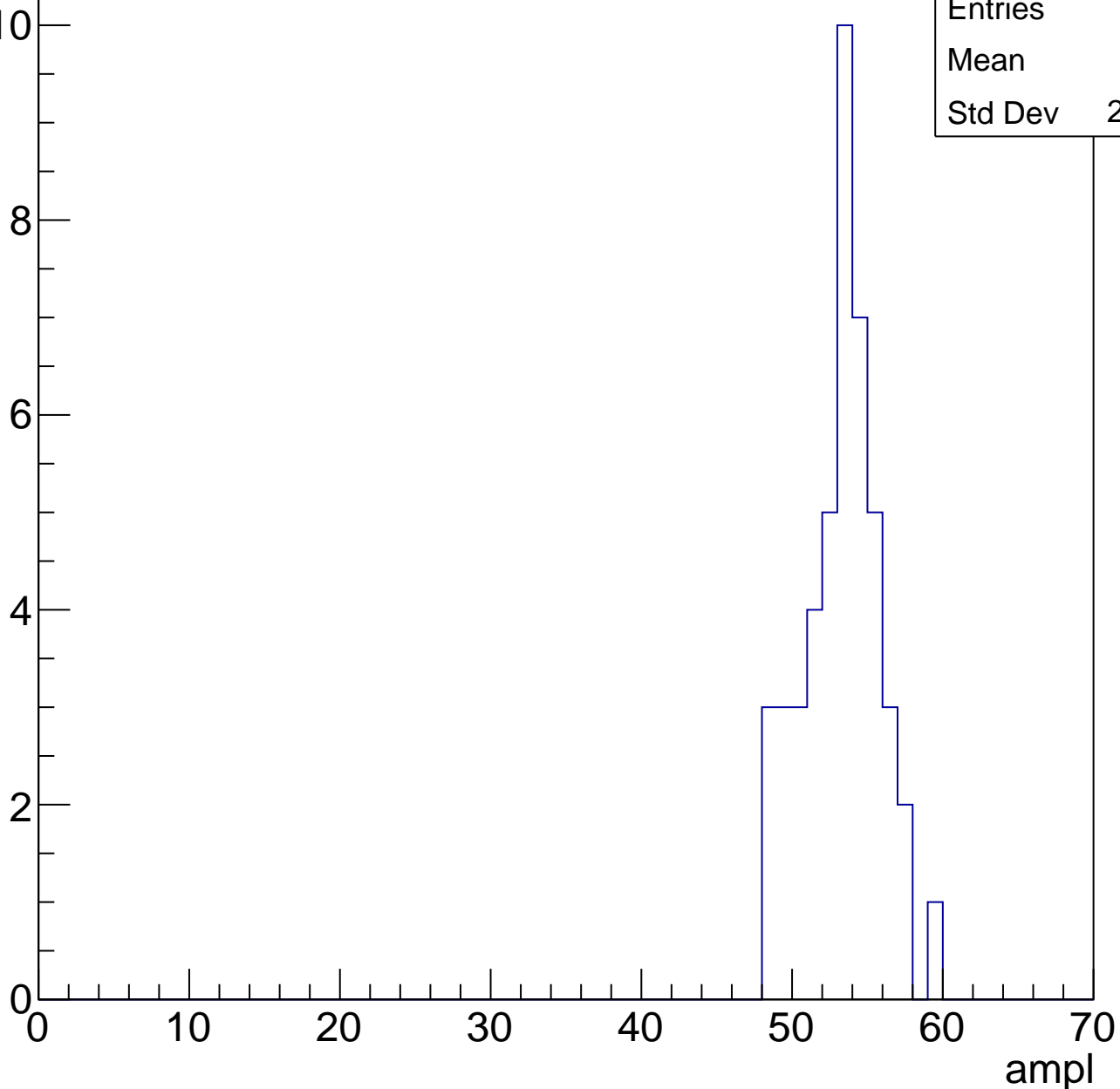


# B1L002S, U3-ch13, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	52.8
Std Dev	2.516

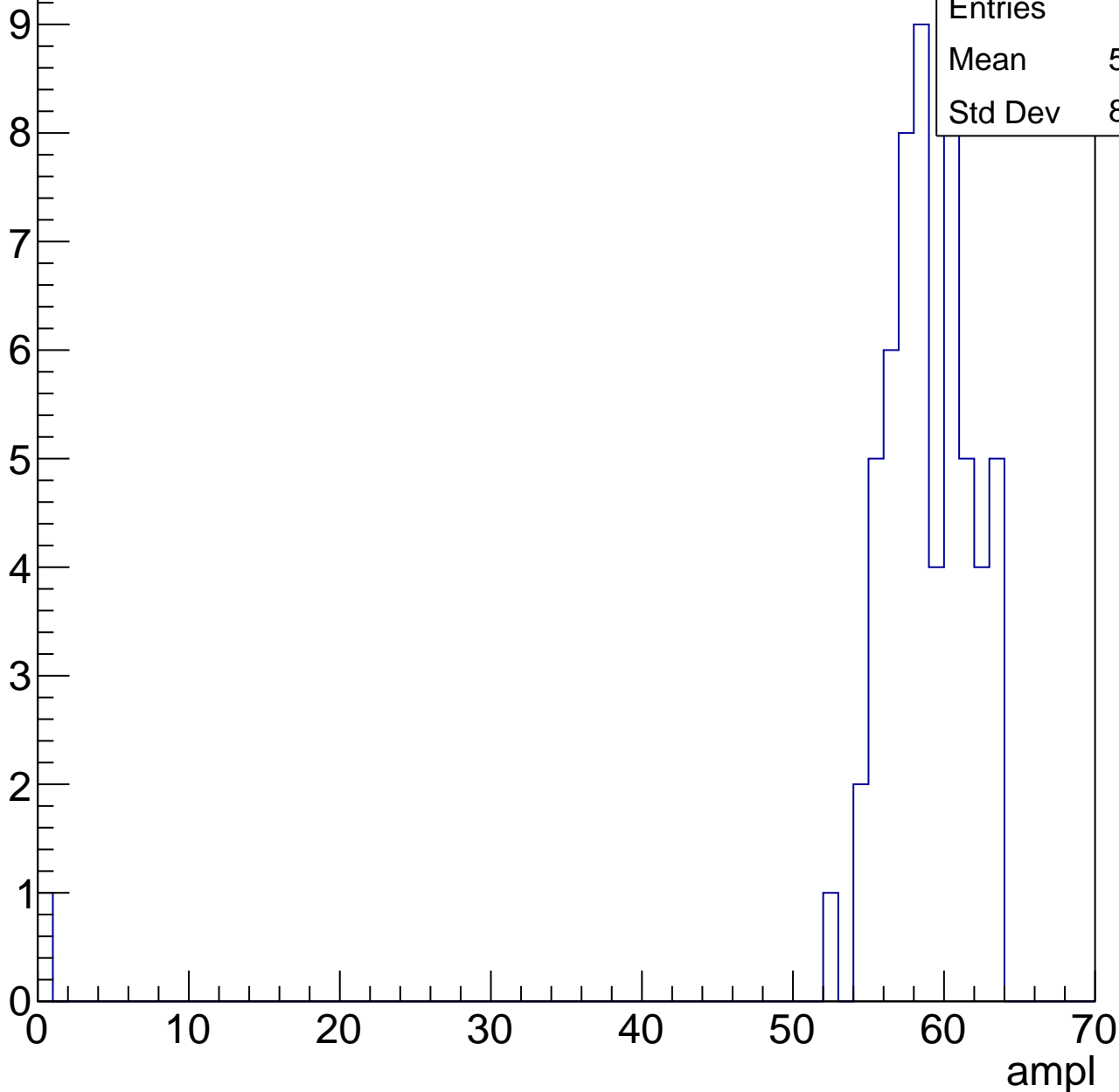


# B1L002S, U3-ch13, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.47
Std Dev	8.054

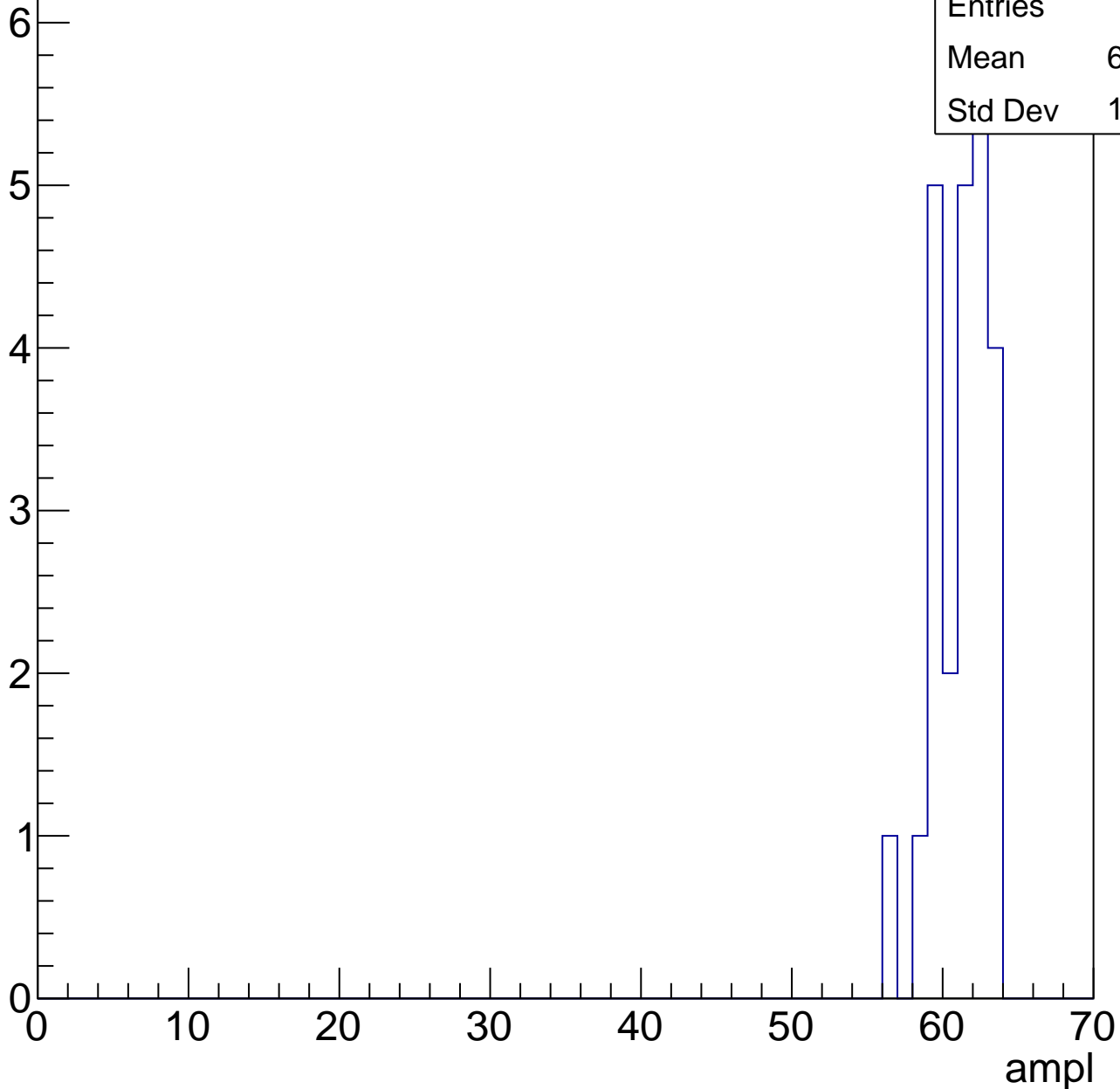


# B1L002S, U3-ch13, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	60.75
Std Dev	1.785





# B1L002S, U3-ch13, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	1
Mean	63
Std Dev	0

ampl

# B1L002S, U3-ch14, adc0

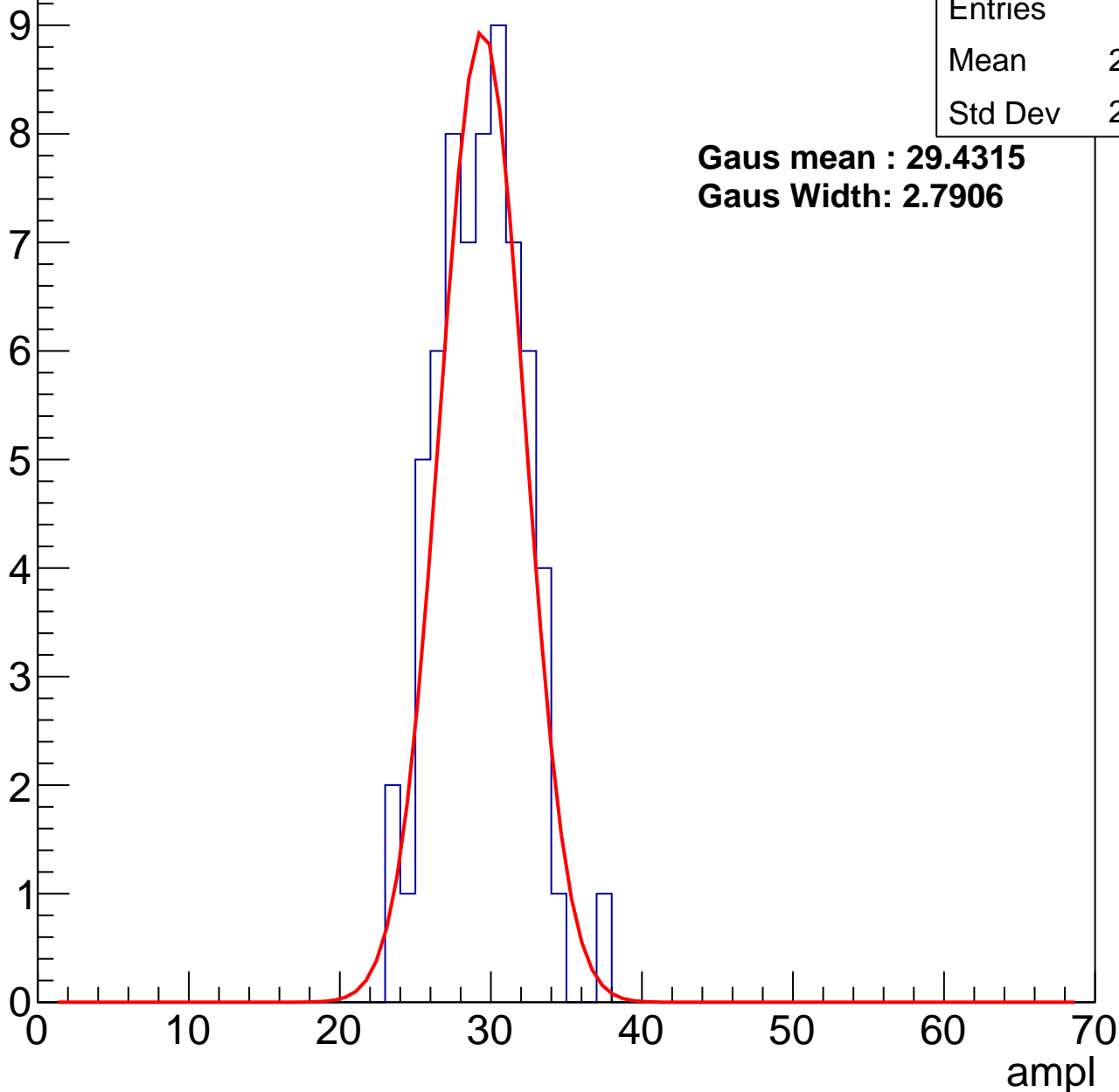
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	28.88
Std Dev	2.809

**Gaus mean : 29.4315**

**Gaus Width: 2.7906**



# B1L002S, U3-ch14, adc1

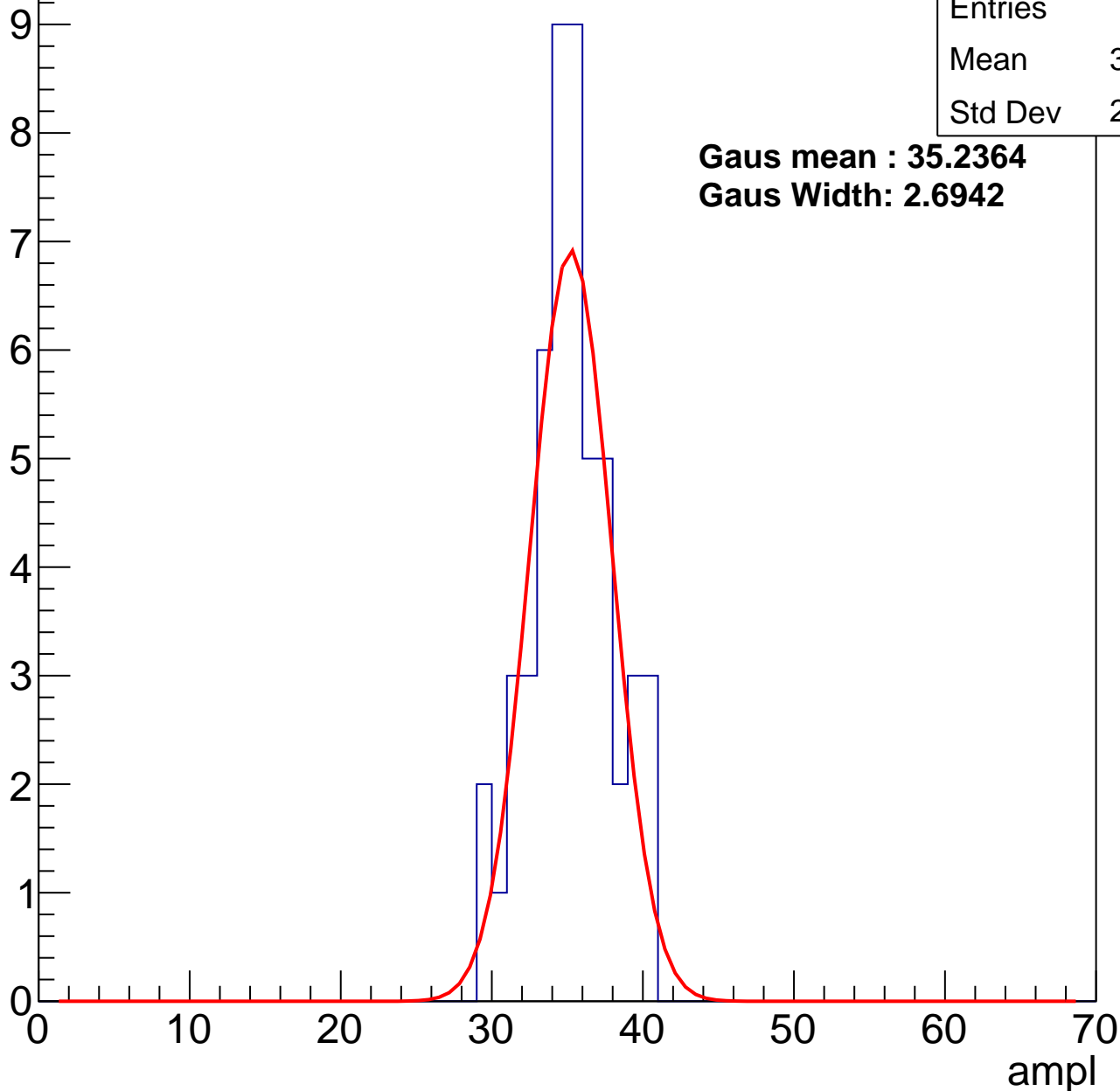
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	34.78
Std Dev	2.688

**Gaus mean : 35.2364**

**Gaus Width: 2.6942**



# B1L002S, U3-ch14, adc2

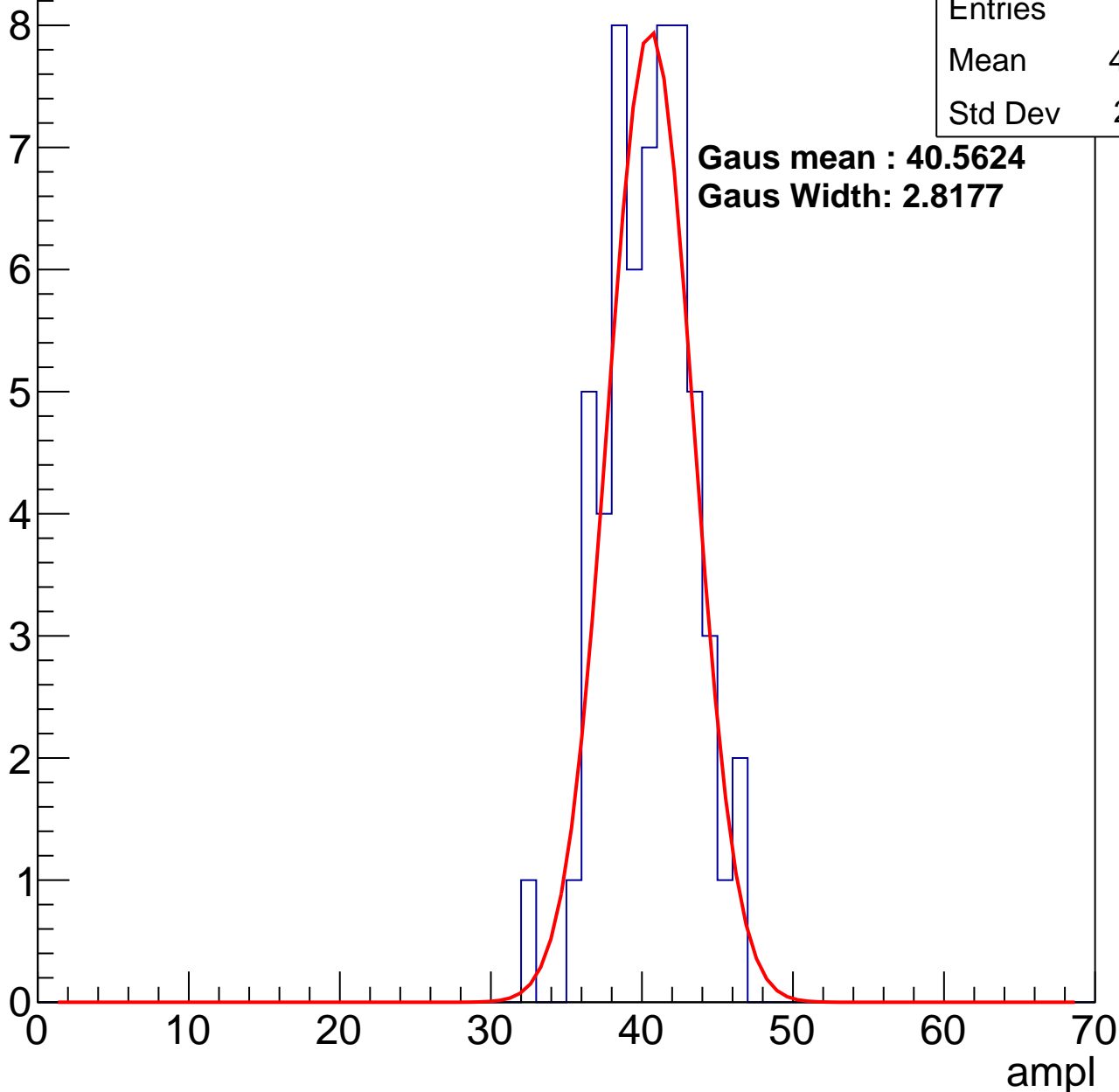
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.02
Std Dev	2.831

**Gaus mean : 40.5624**

**Gaus Width: 2.8177**

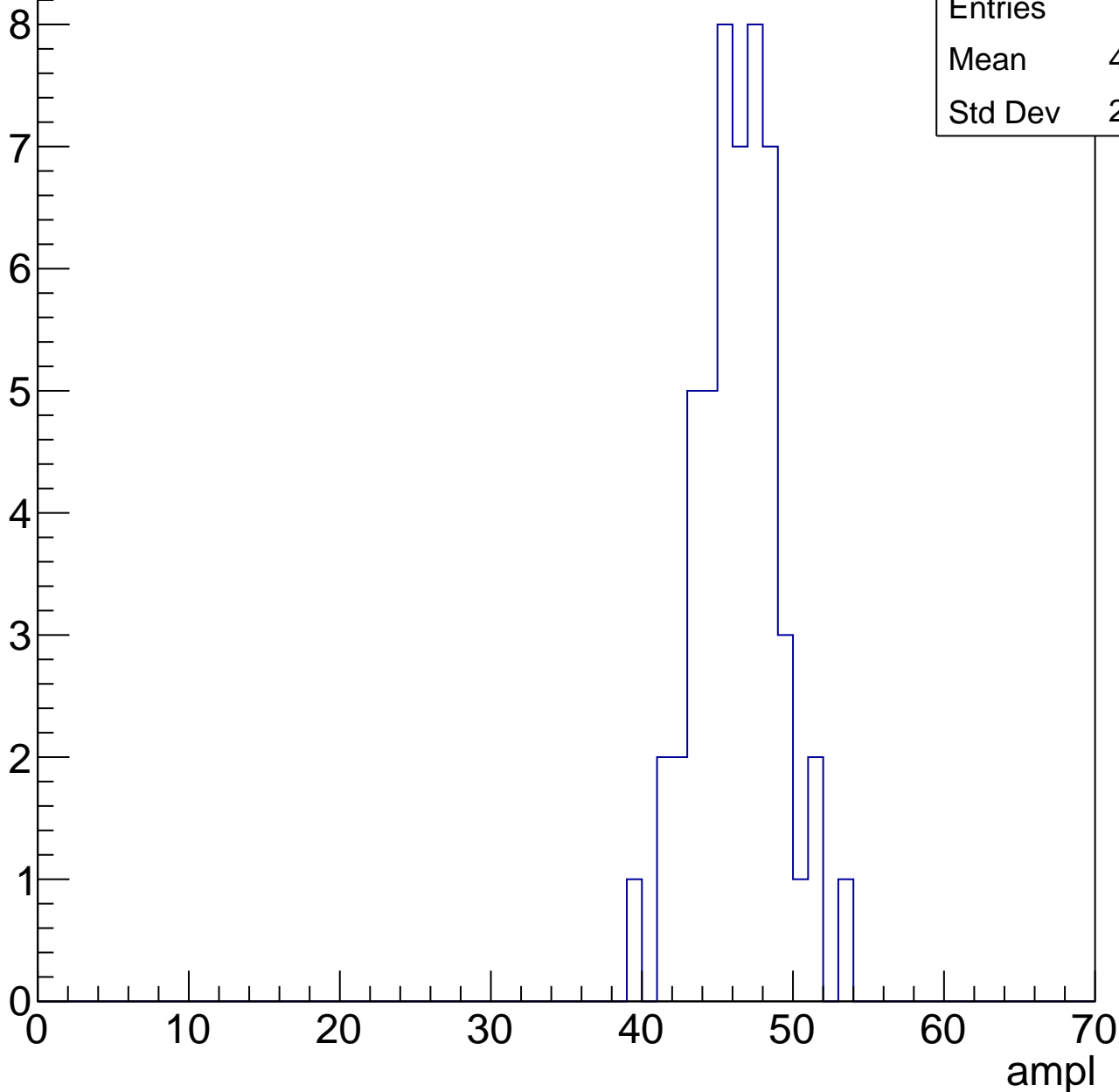


# B1L002S, U3-ch14, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	45.88
Std Dev	2.708

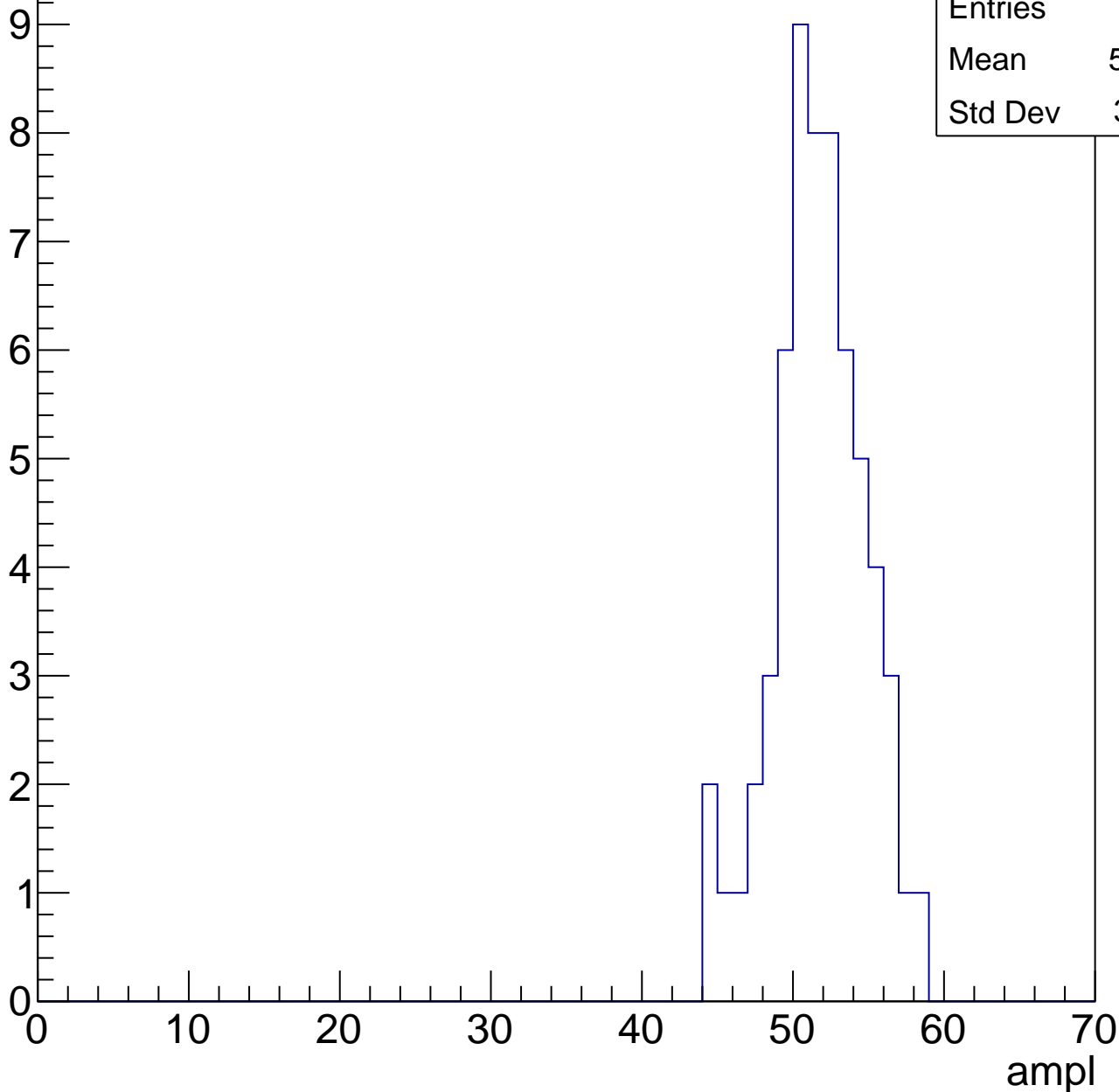


# B1L002S, U3-ch14, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	51.27
Std Dev	3.021

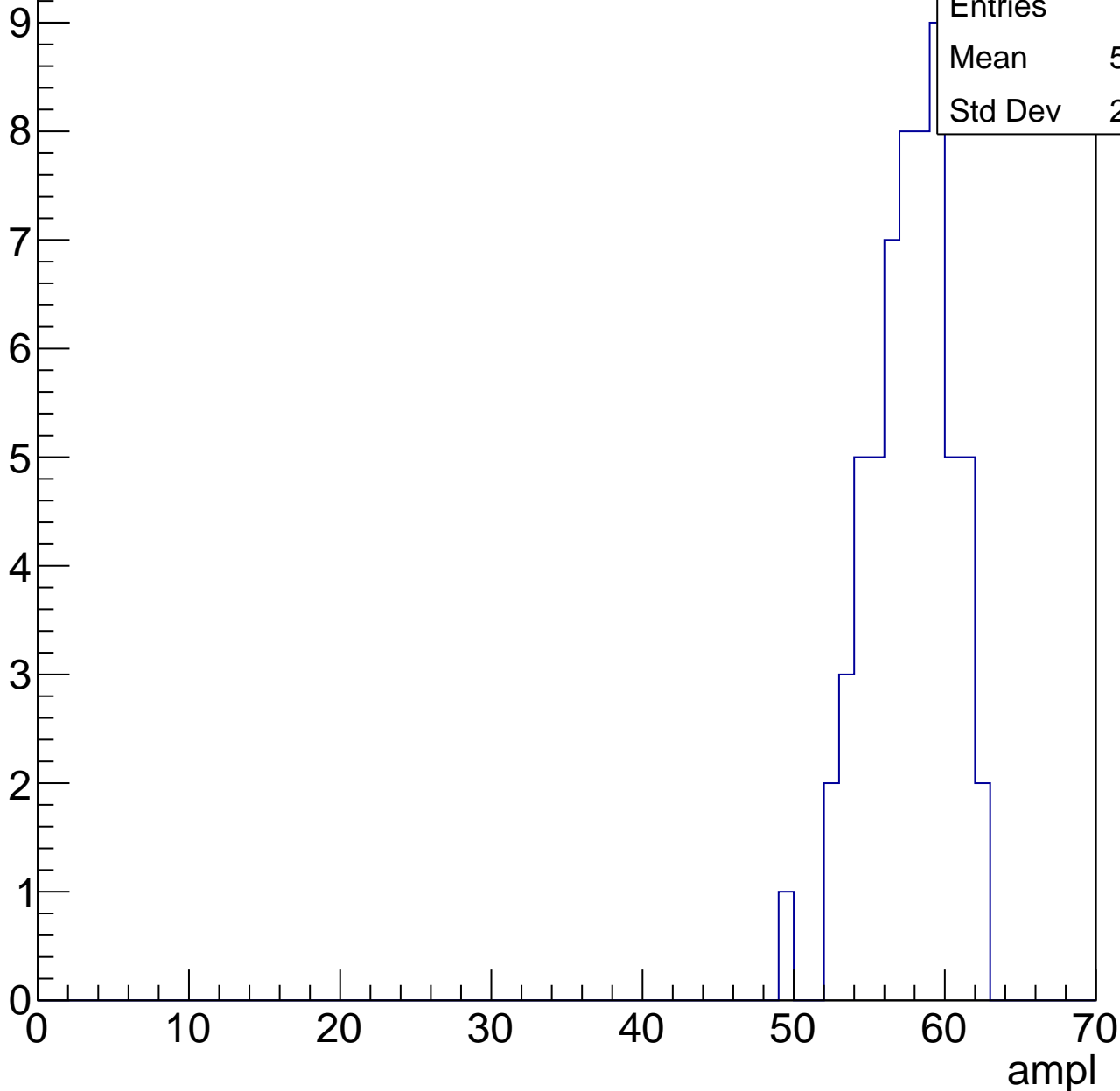


# B1L002S, U3-ch14, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	57.15
Std Dev	2.744

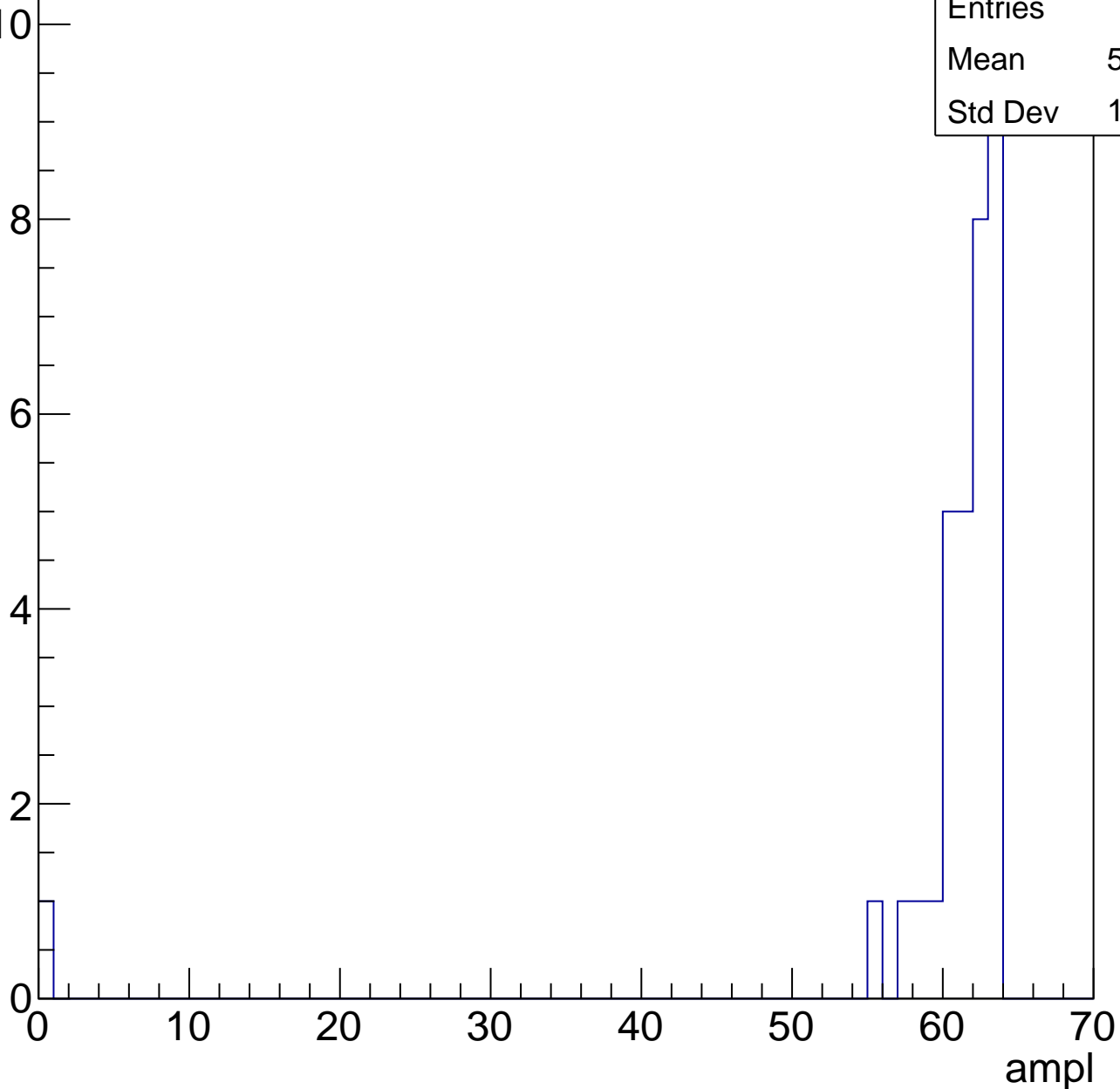


# B1L002S, U3-ch14, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	59.39
Std Dev	10.67





# B1L002S, U3-ch14, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch15, adc0

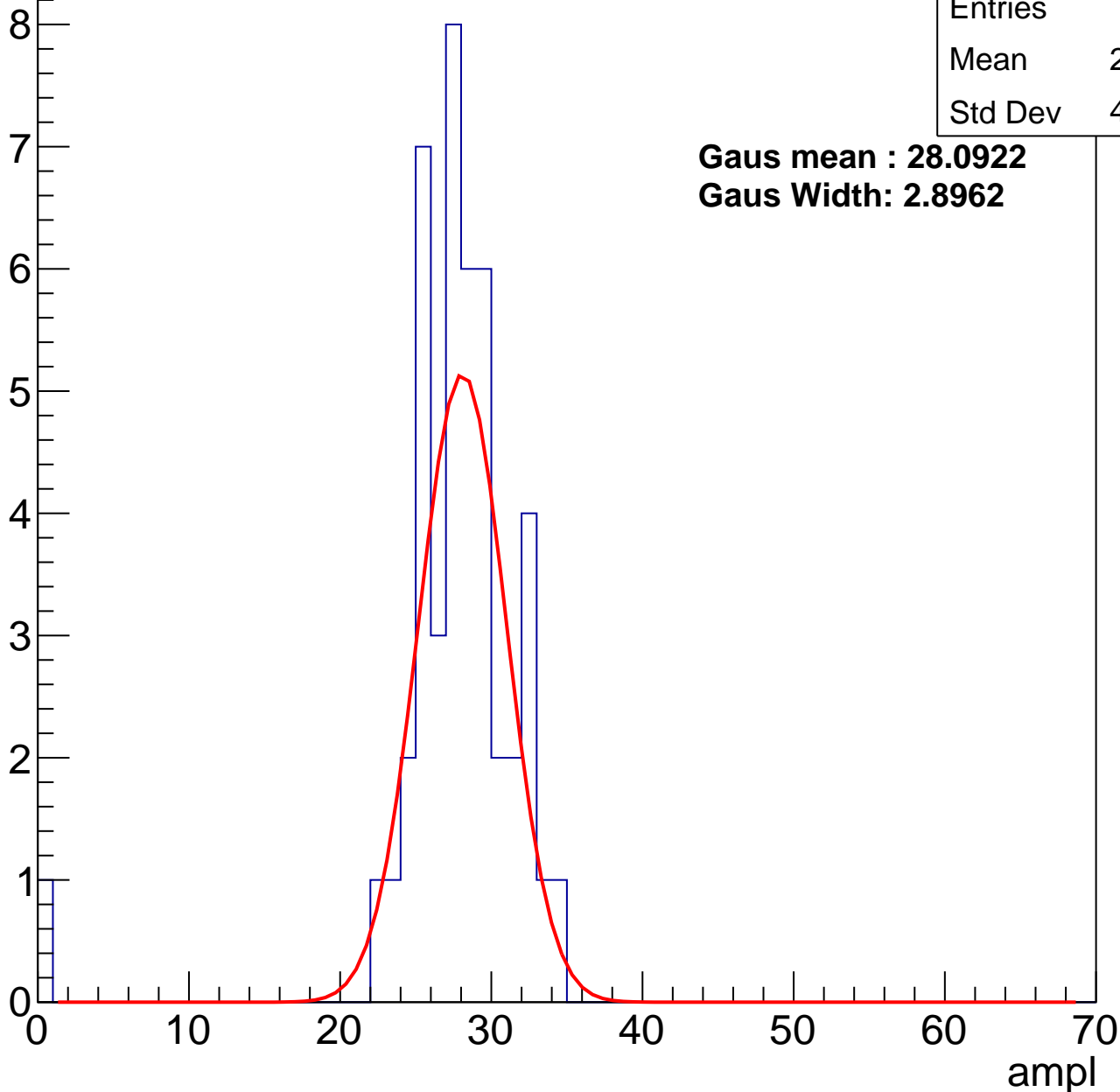
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	27.13
Std Dev	4.906

**Gaus mean : 28.0922**

**Gaus Width: 2.8962**



# B1L002S, U3-ch15, adc1

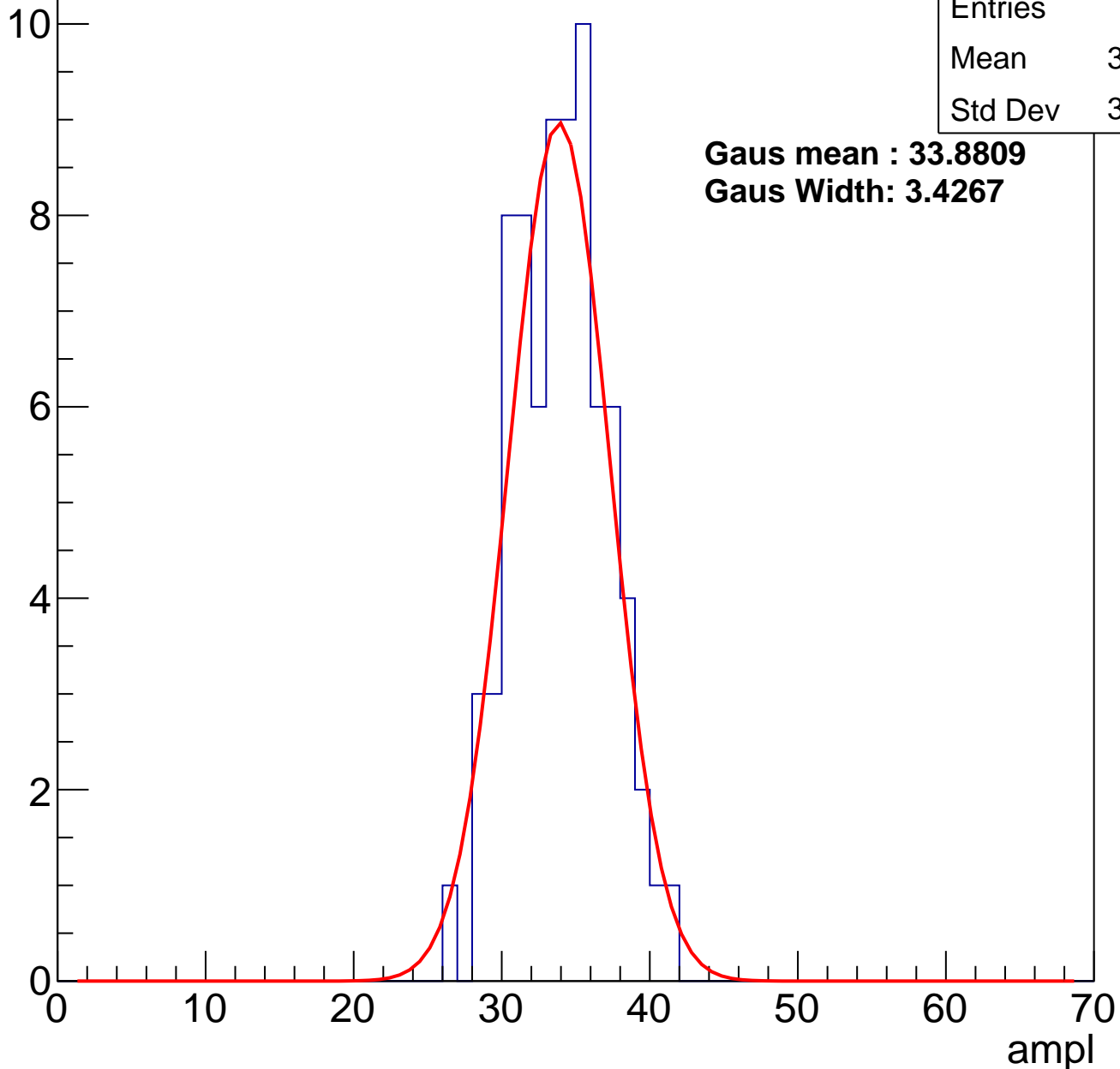
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	33.49
Std Dev	3.115

**Gaus mean : 33.8809**

**Gaus Width: 3.4267**

Entry



# B1L002S, U3-ch15, adc2

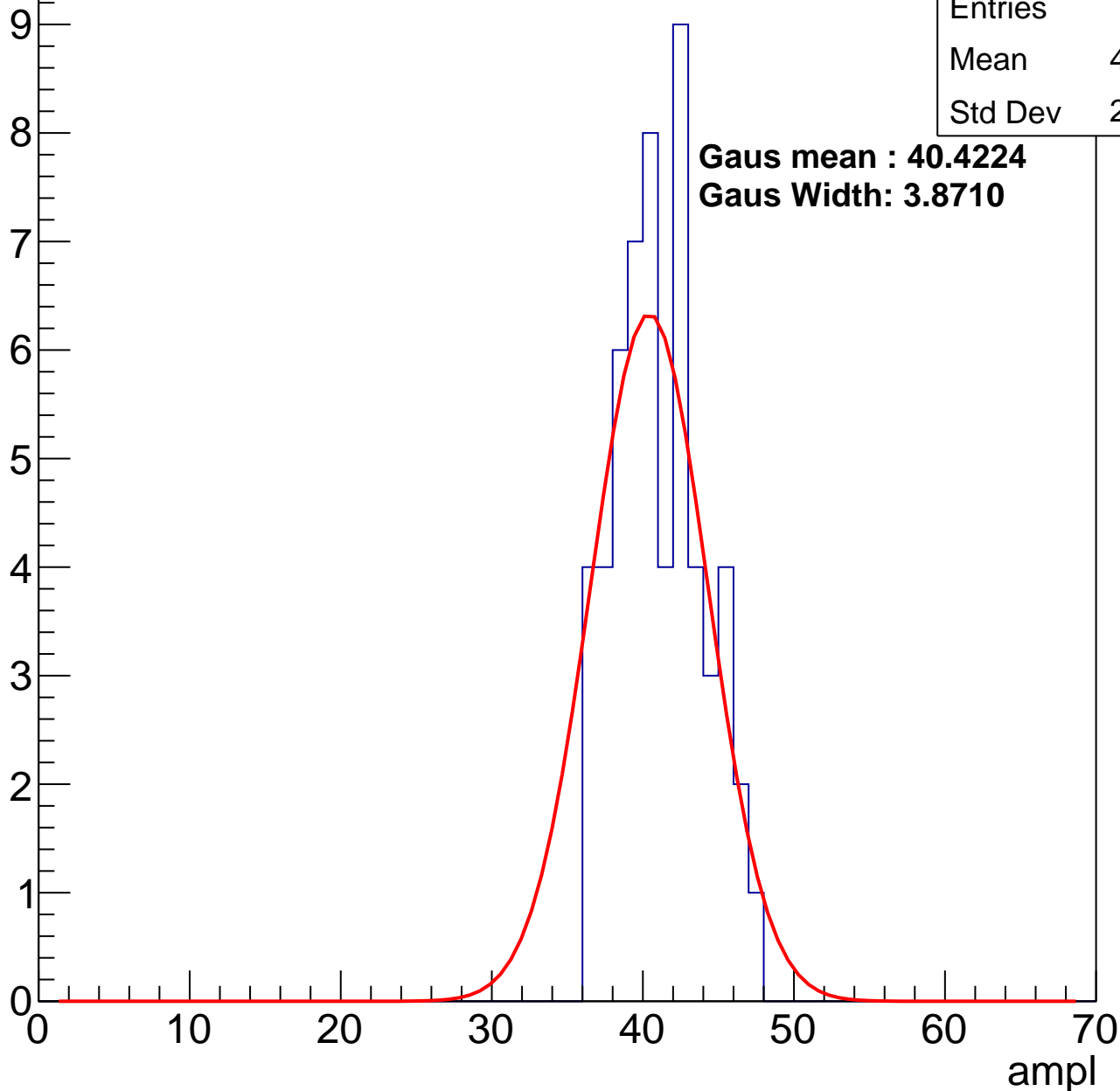
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.68
Std Dev	2.835

**Gaus mean : 40.4224**

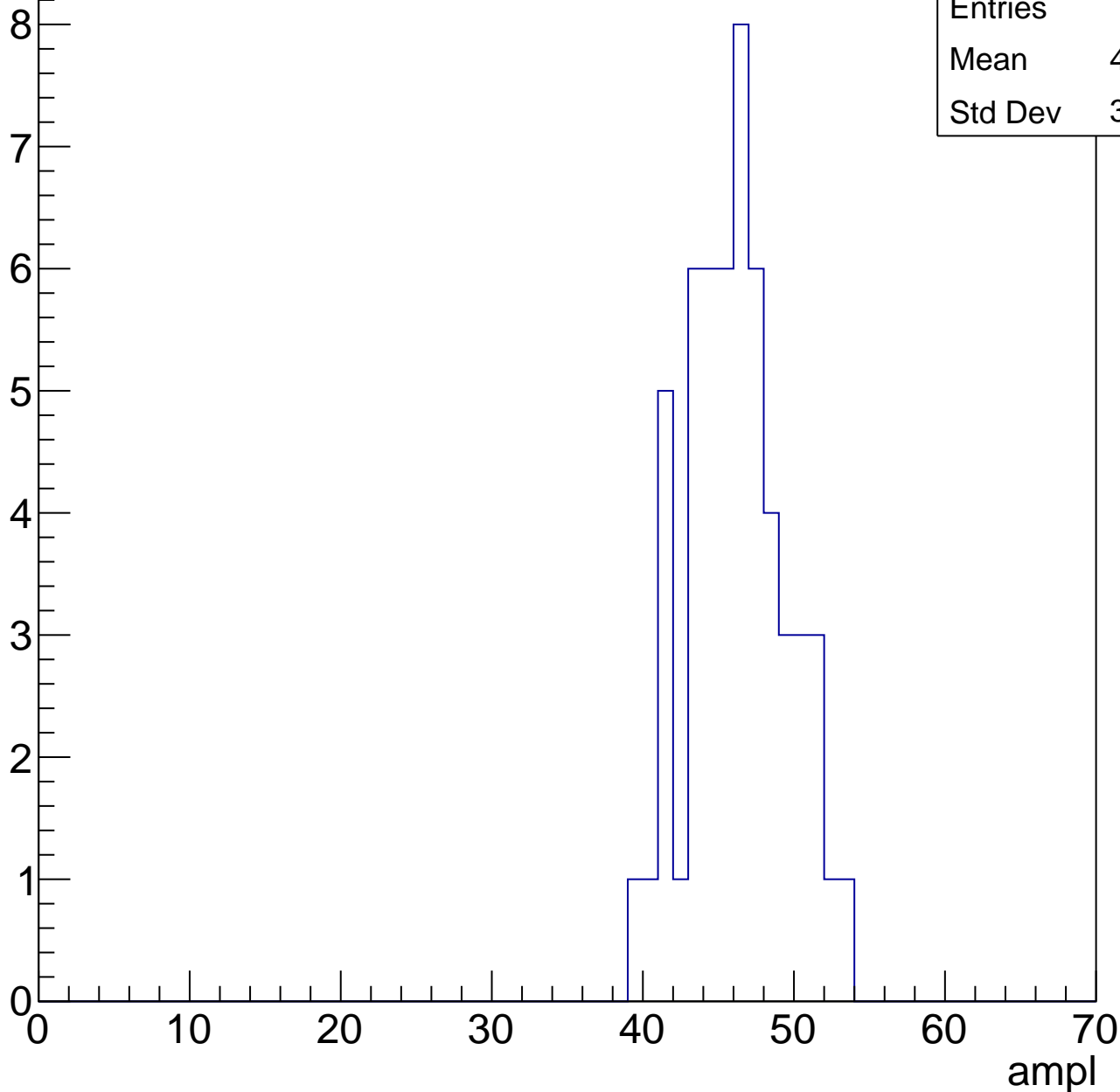
**Gaus Width: 3.8710**



# B1L002S, U3-ch15, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



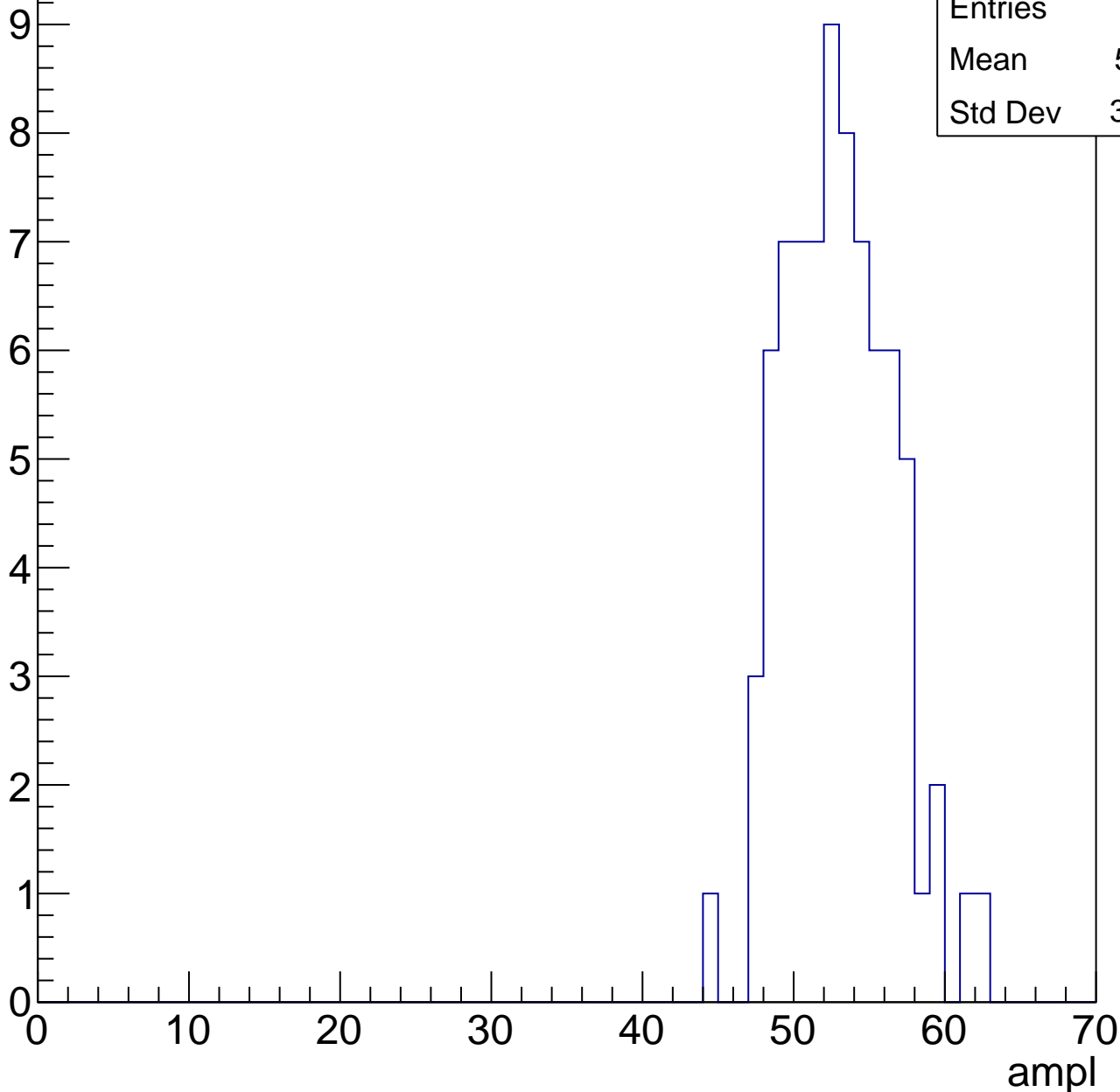
Entries	55
Mean	45.73
Std Dev	3.199

# B1L002S, U3-ch15, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	52.51
Std Dev	3.493

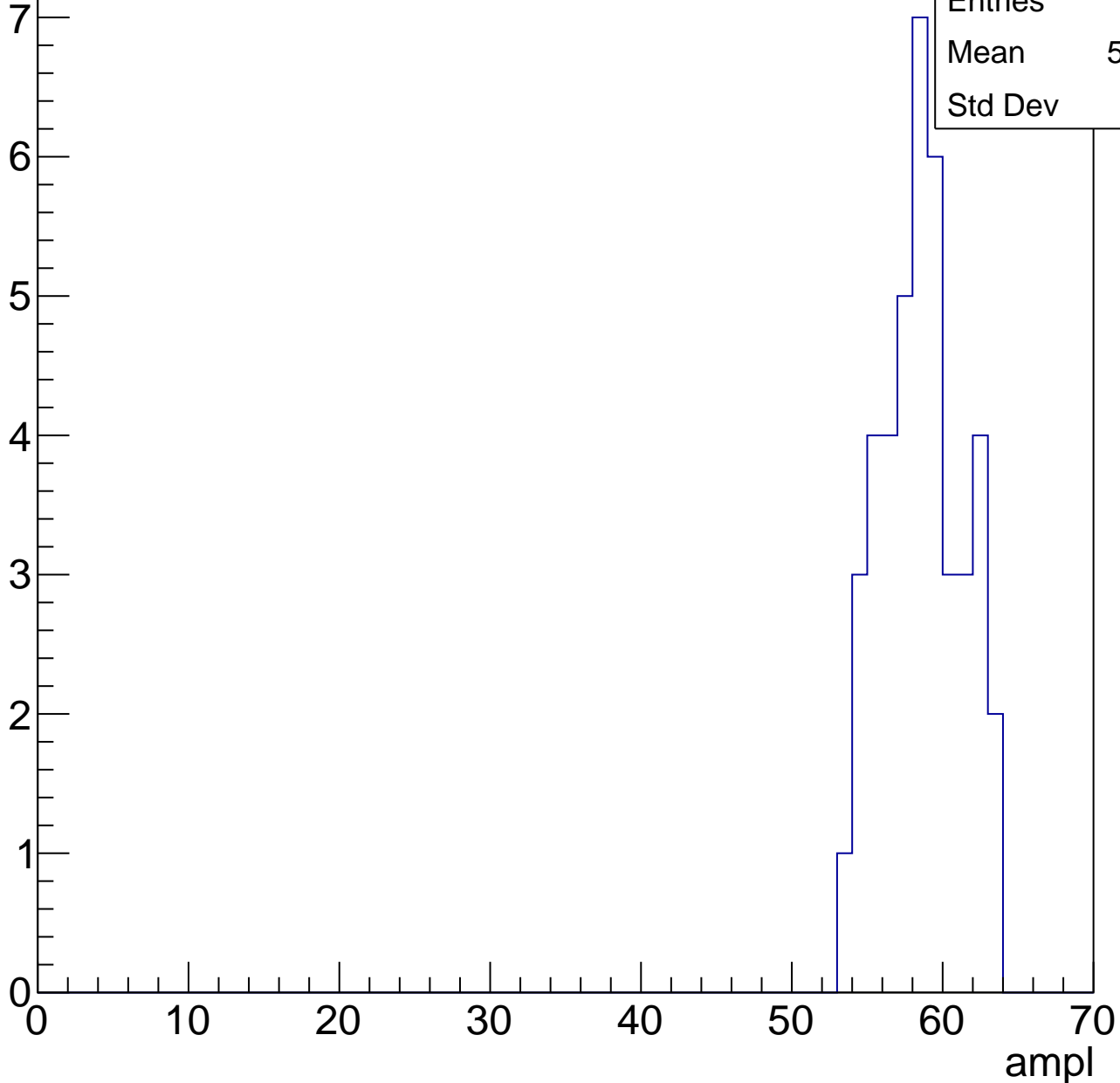


# B1L002S, U3-ch15, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

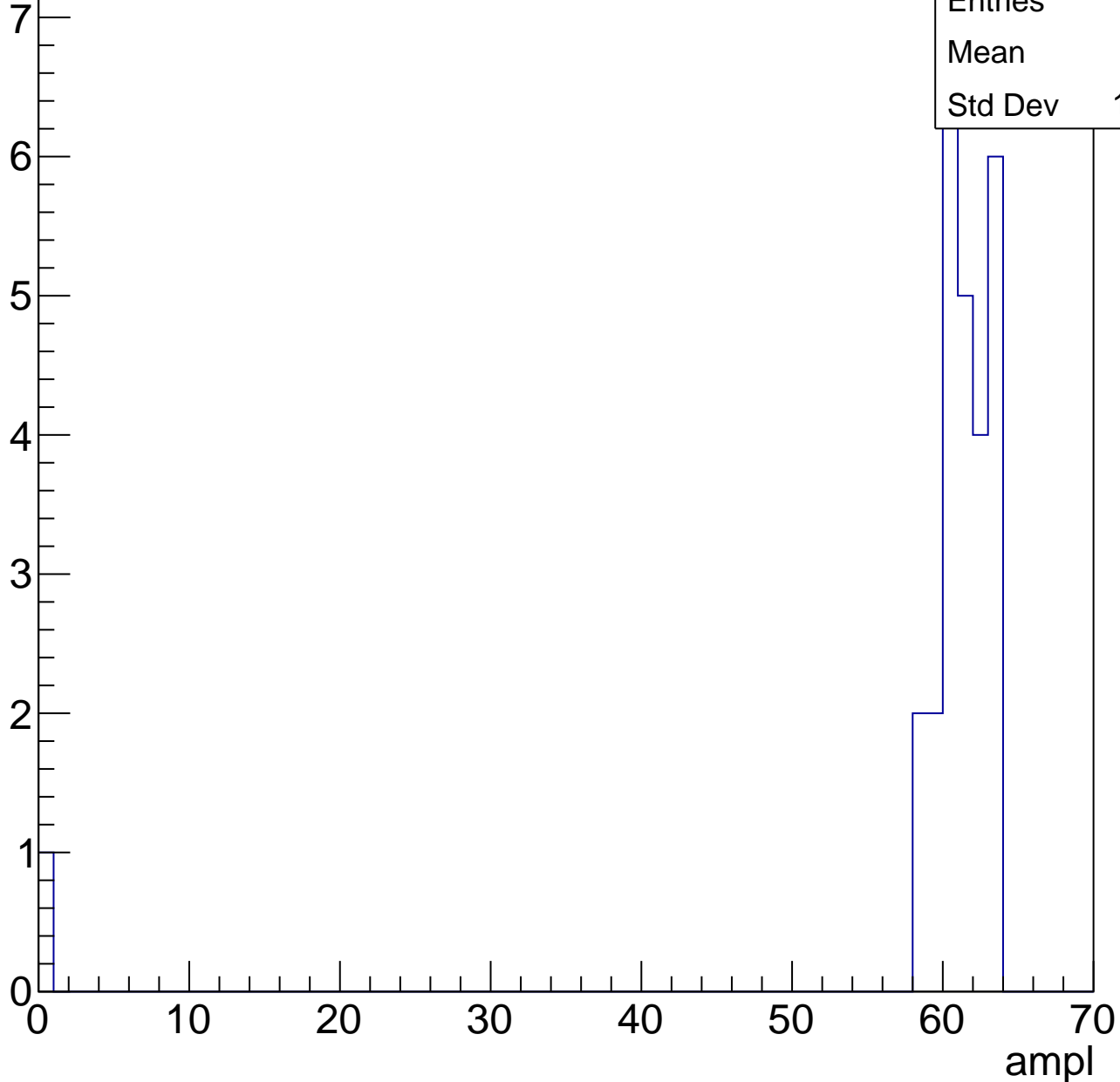
Entries	42
Mean	58.12
Std Dev	2.62



# B1L002S, U3-ch15, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

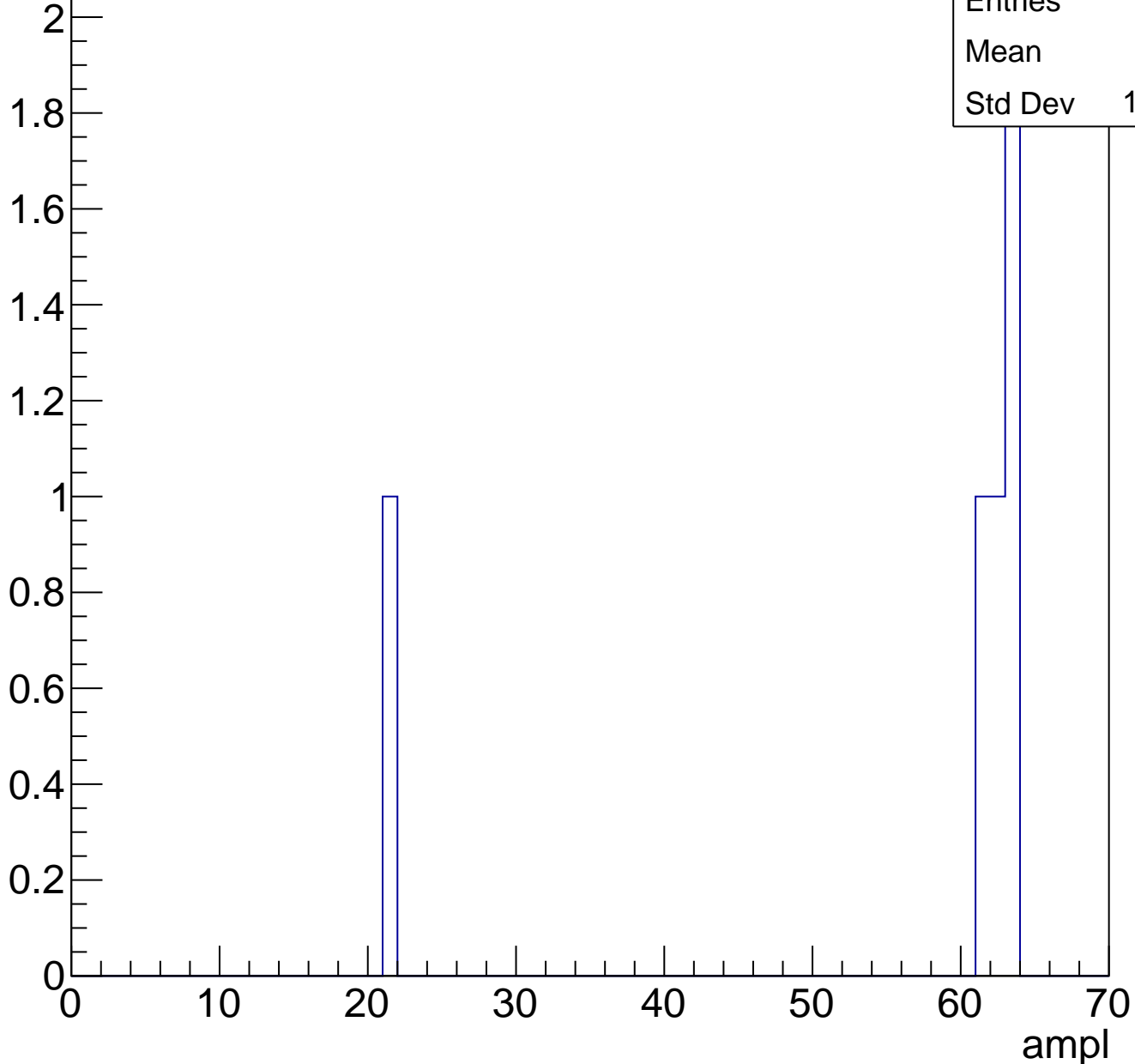




# B1L002S, U3-ch15, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	54
Std Dev	16.52

# B1L002S, U3-ch16, adc0

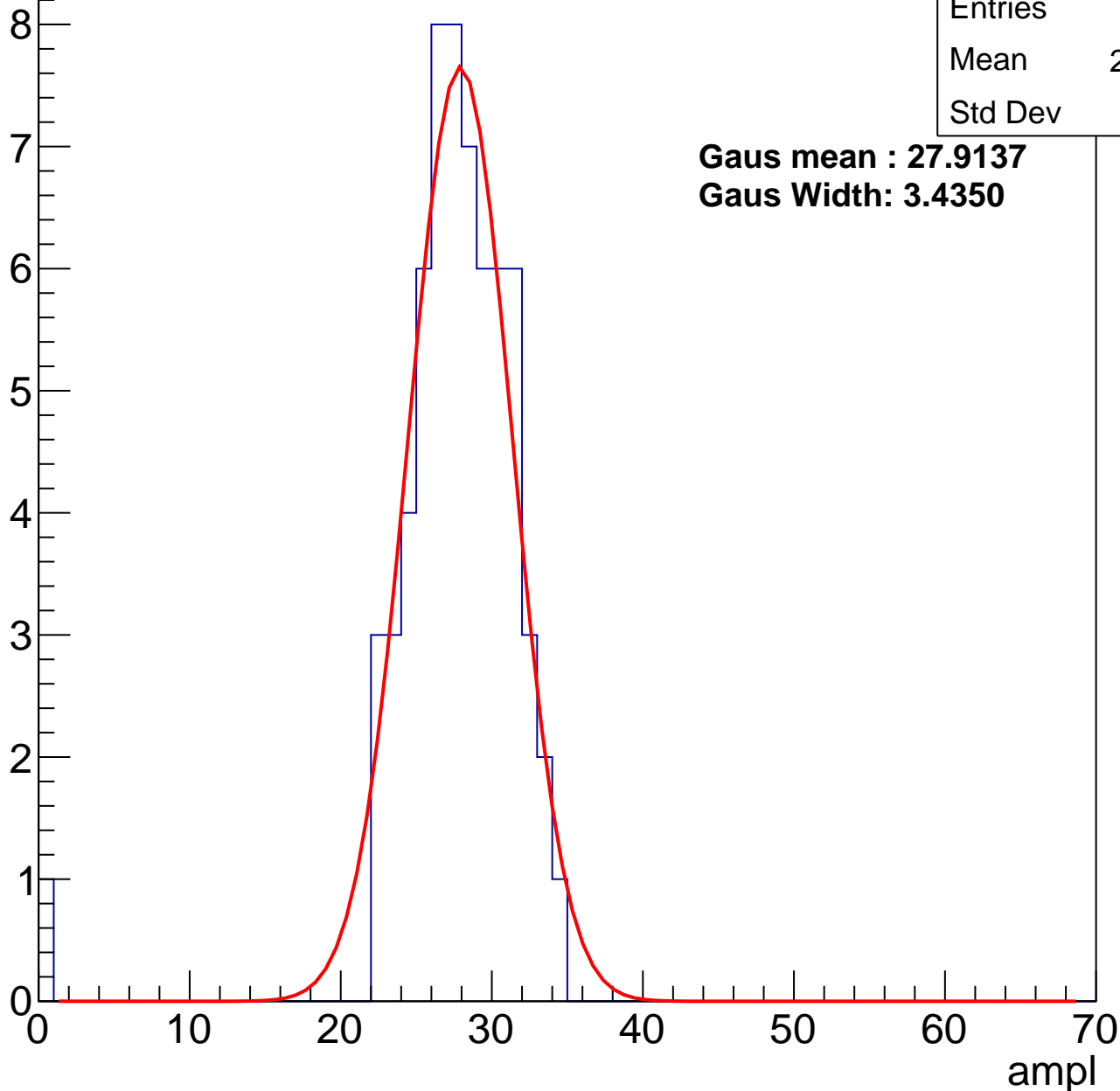
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.14
Std Dev	4.5

**Gaus mean : 27.9137**

**Gaus Width: 3.4350**



# B1L002S, U3-ch16, adc1

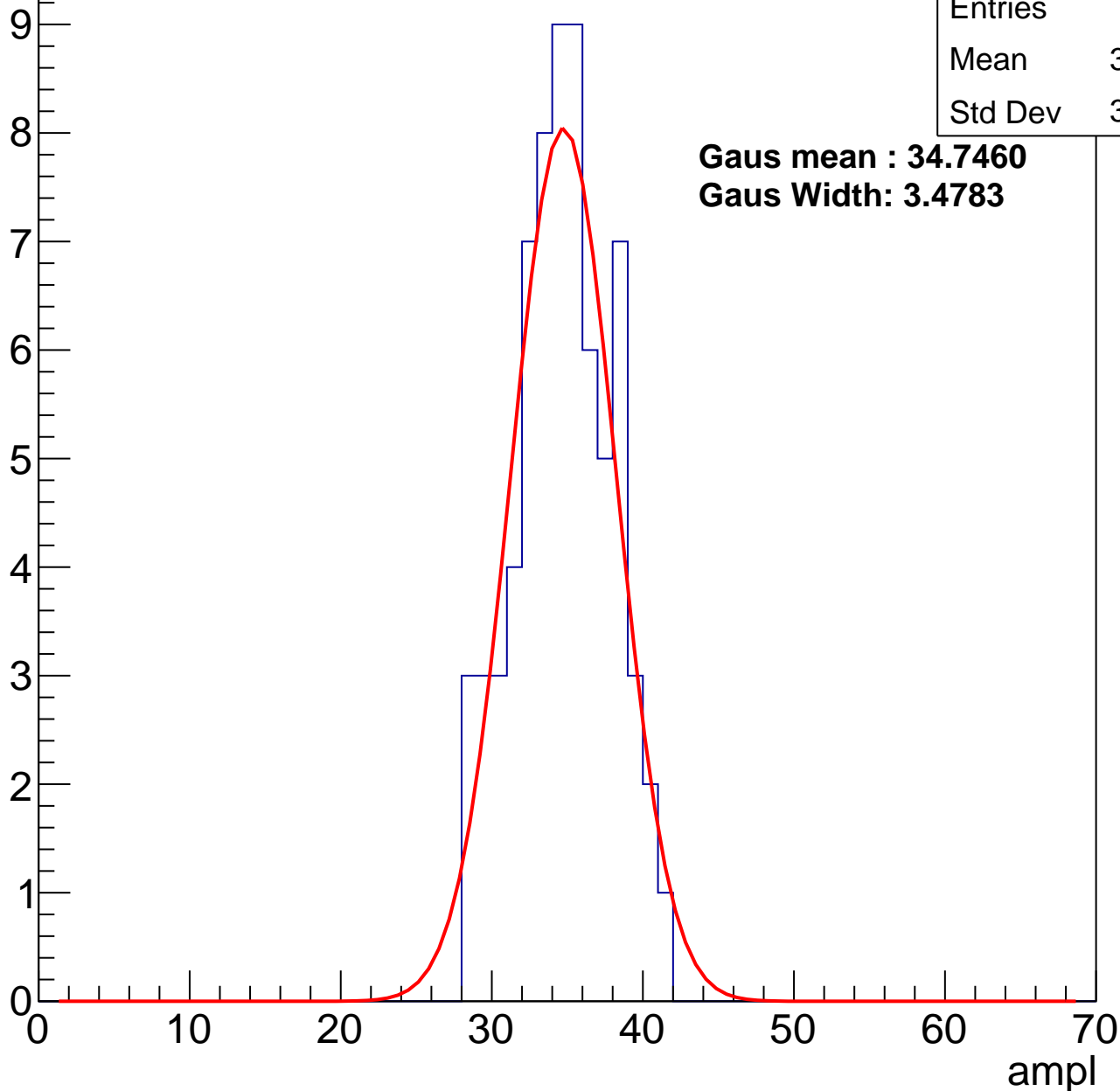
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	34.27
Std Dev	3.126

**Gaus mean : 34.7460**

**Gaus Width: 3.4783**



# B1L002S, U3-ch16, adc2

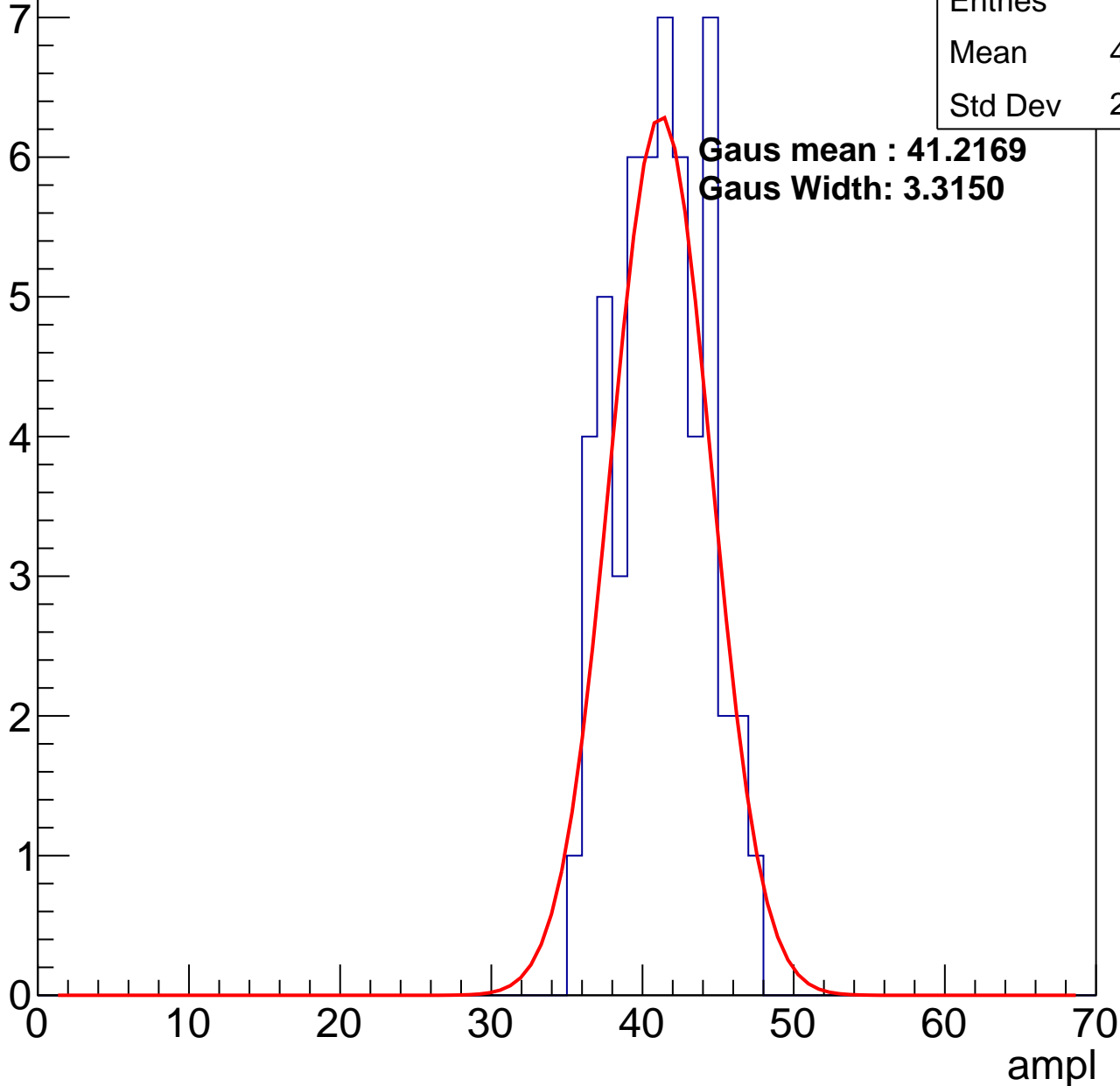
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.74
Std Dev	2.958

**Gaus mean : 41.2169**

**Gaus Width: 3.3150**

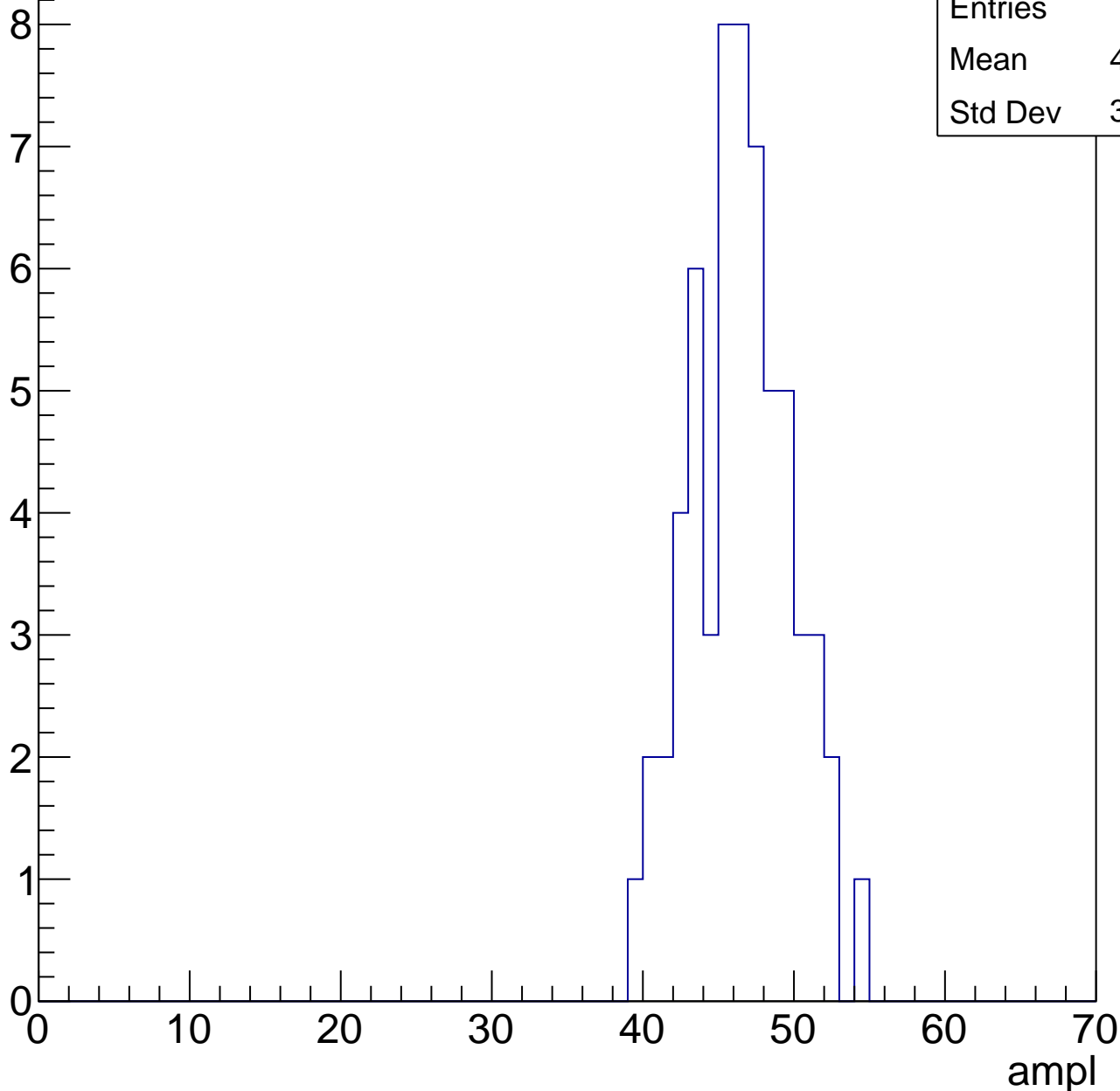


# B1L002S, U3-ch16, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.03
Std Dev	3.266

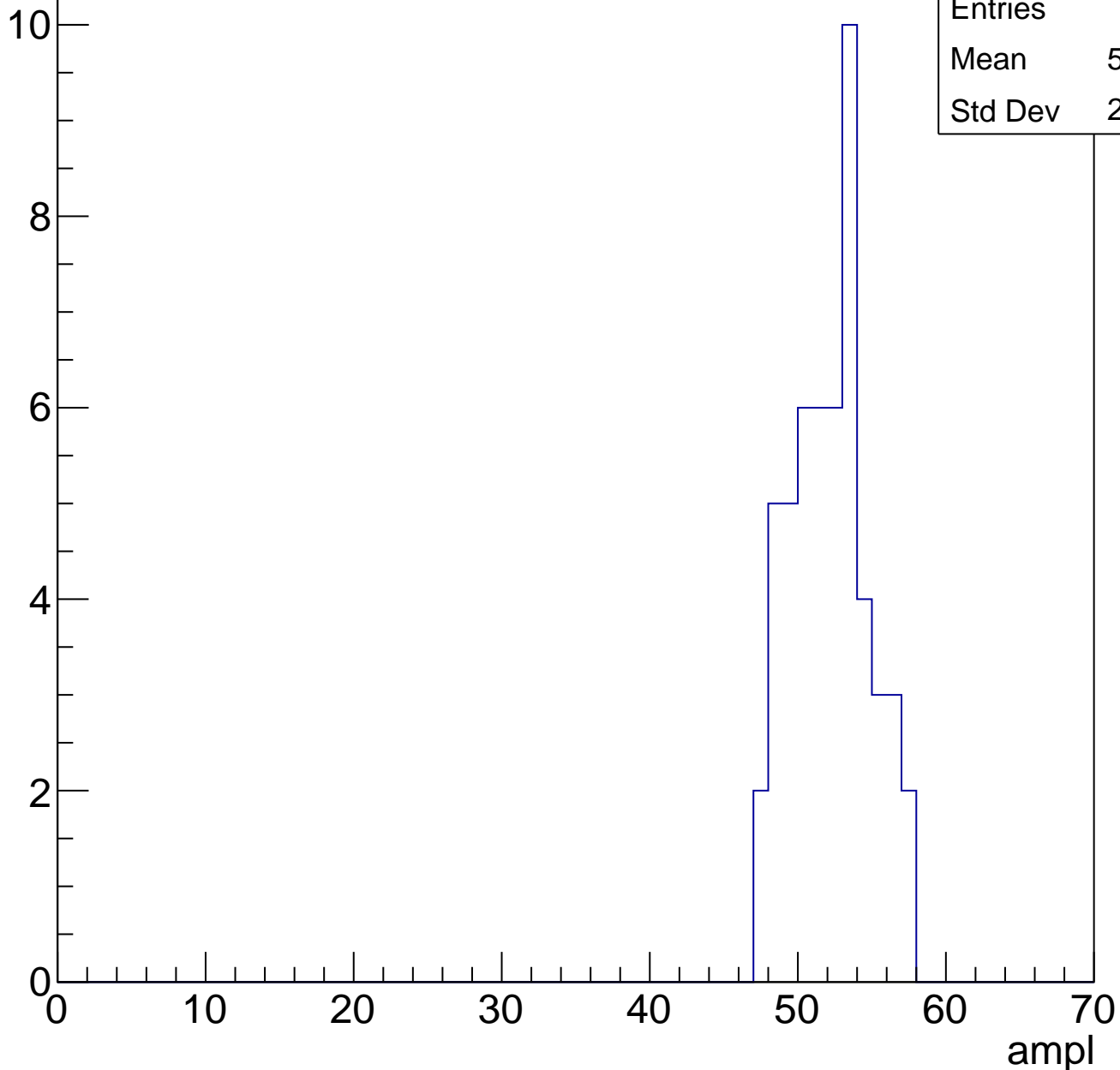


# B1L002S, U3-ch16, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

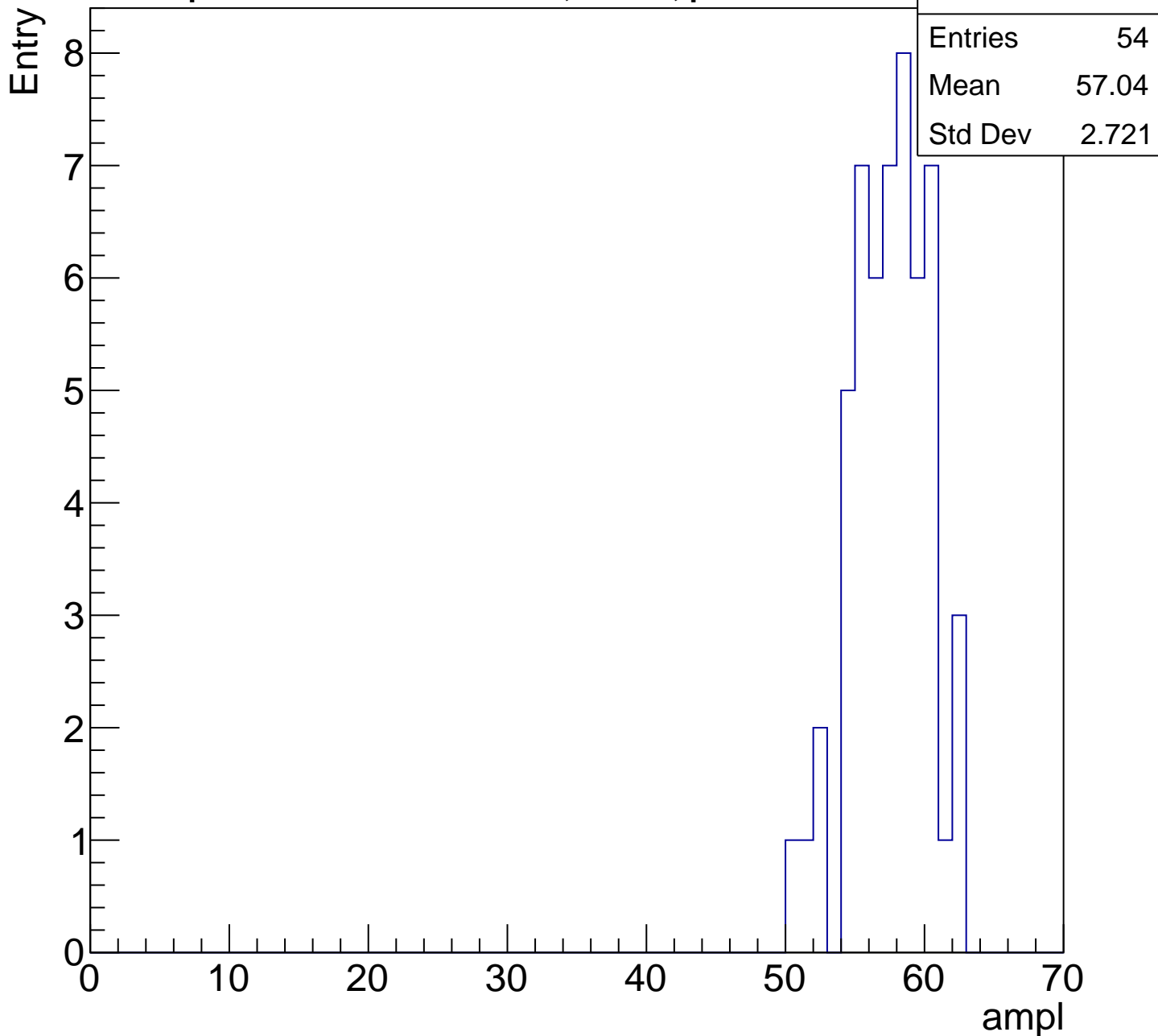
Entries	52
Mean	51.73
Std Dev	2.603

Entry



# B1L002S, U3-ch16, adc5

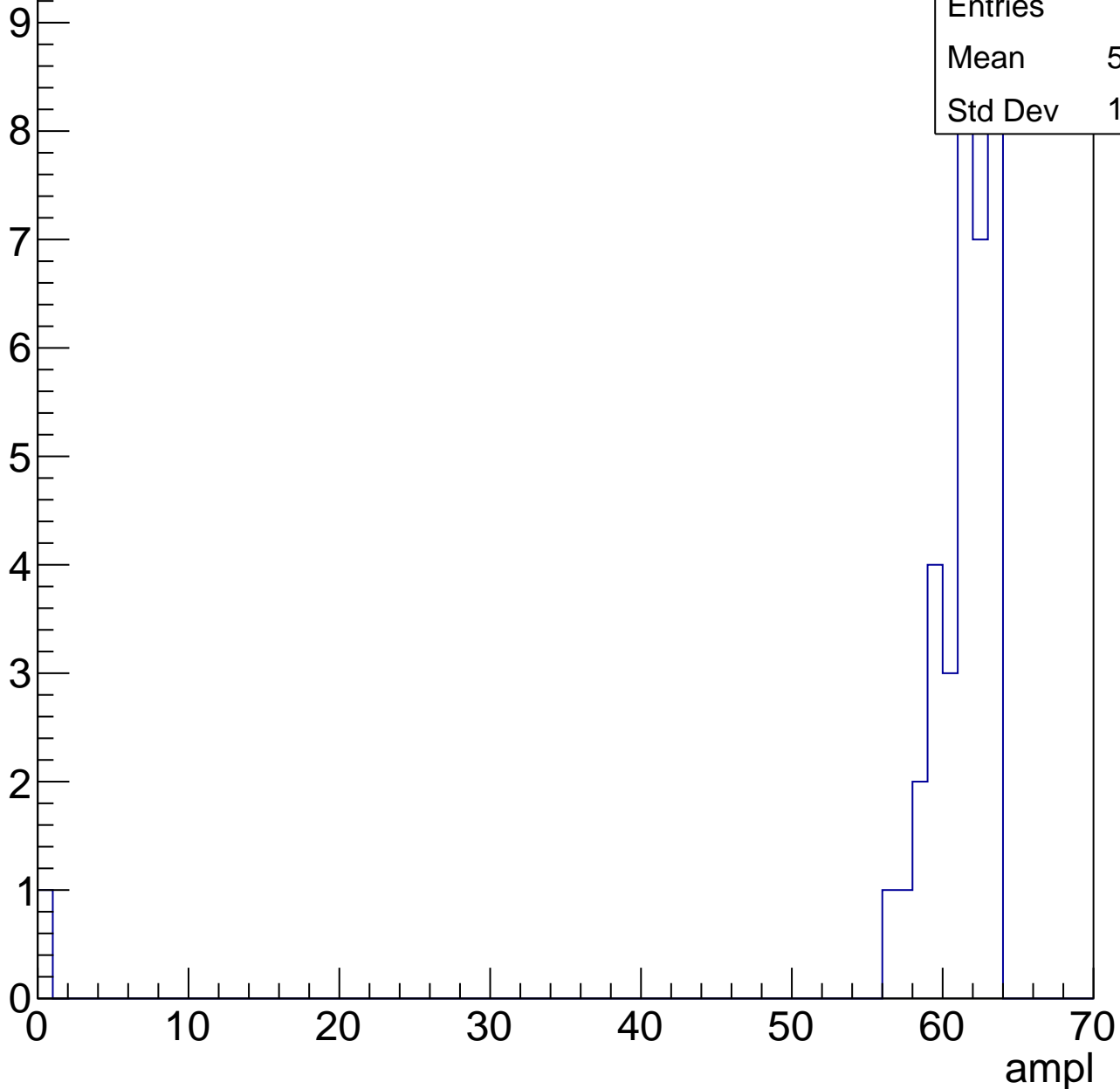
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch16, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	37
Mean	59.32
Std Dev	10.05



# B1L002S, U3-ch16, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch17, adc0

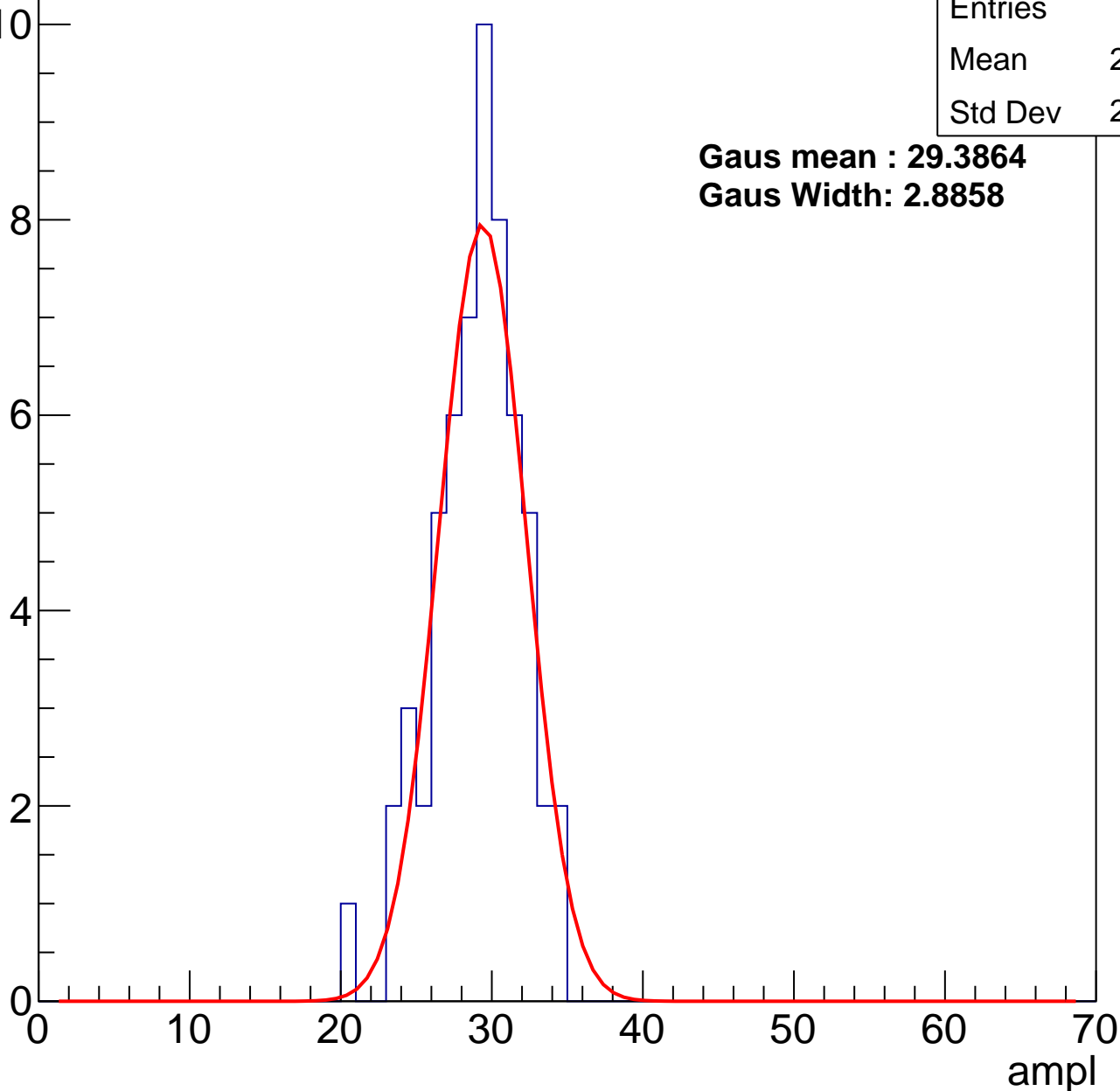
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	28.58
Std Dev	2.865

**Gaus mean : 29.3864**

**Gaus Width: 2.8858**



# B1L002S, U3-ch17, adc1

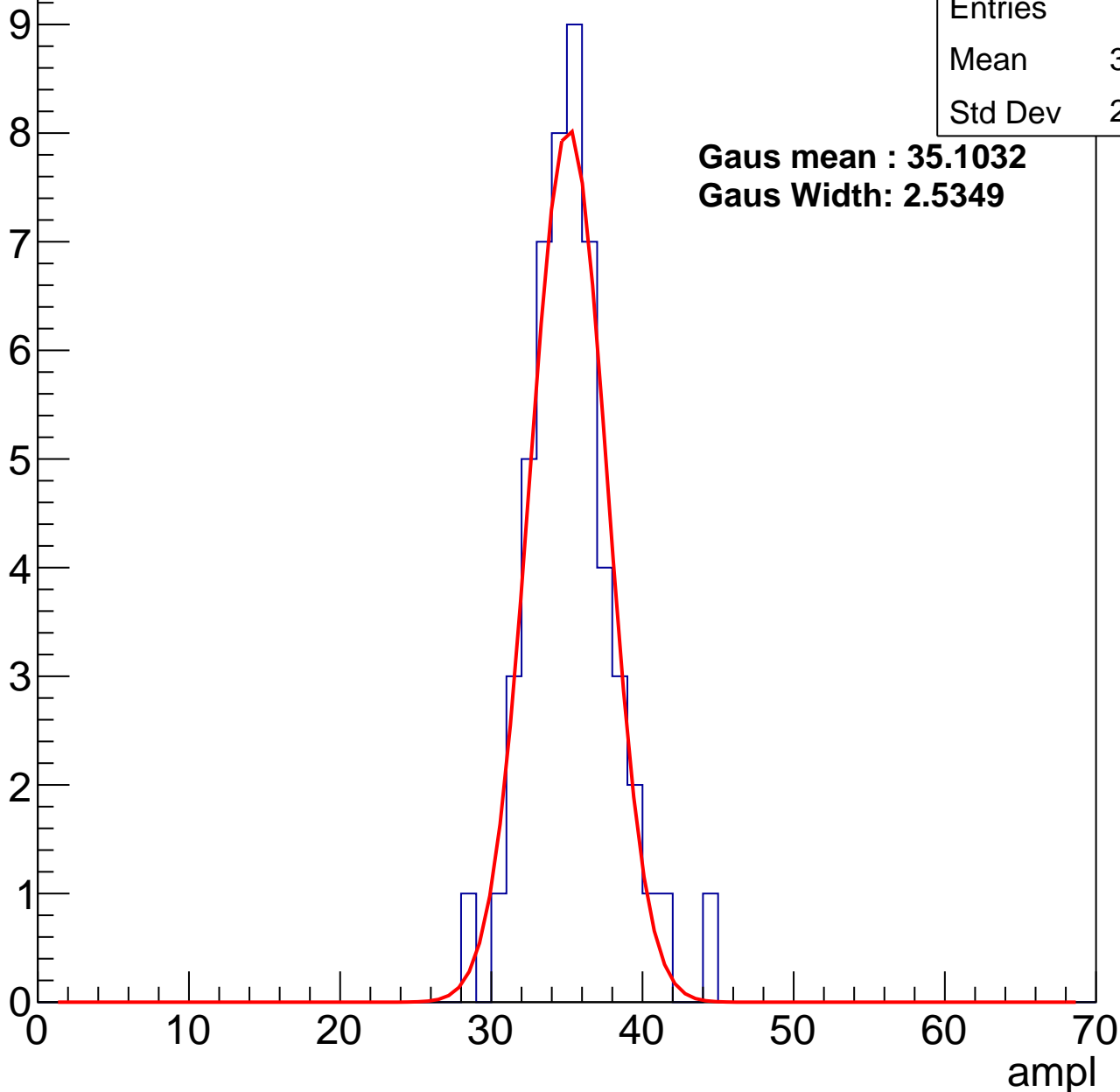
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	34.83
Std Dev	2.833

**Gaus mean : 35.1032**

**Gaus Width: 2.5349**



# B1L002S, U3-ch17, adc2

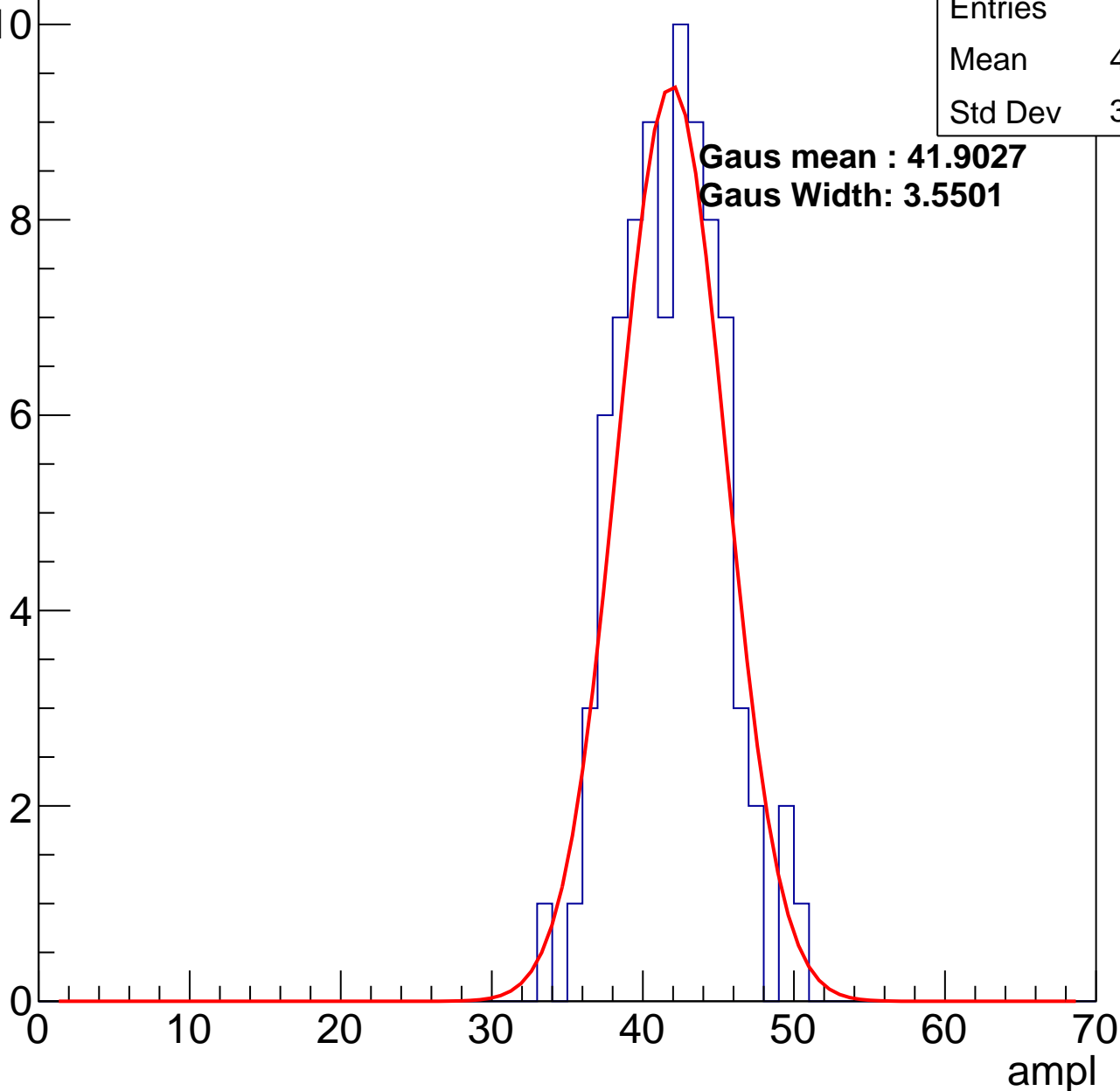
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	84
Mean	41.39
Std Dev	3.359

**Gaus mean : 41.9027**

**Gaus Width: 3.5501**



# B1L002S, U3-ch17, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	48.73
Std Dev	2.968

Entry

10

8

6

4

2

0

0

10

20

30

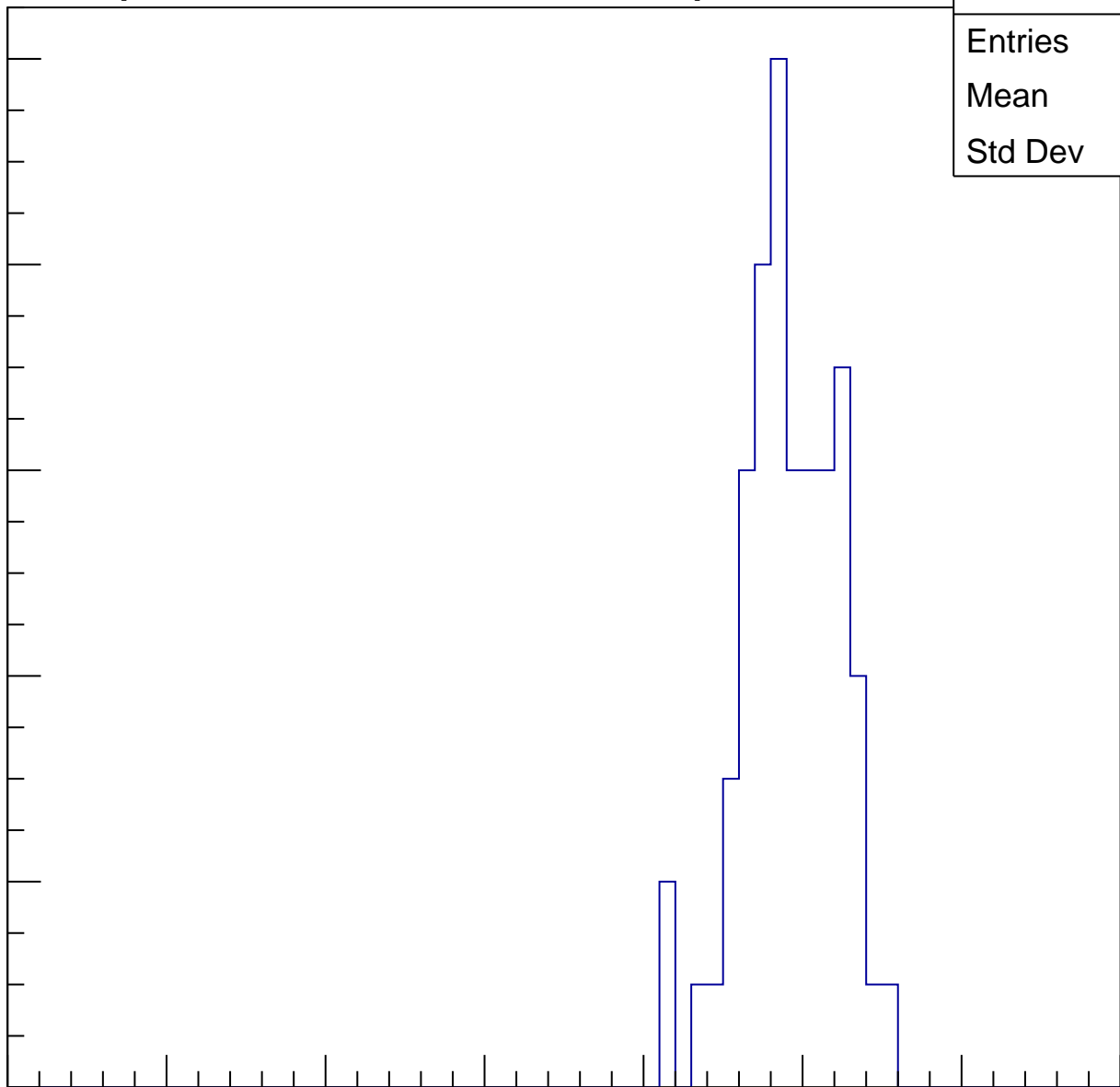
40

50

60

70

ampl

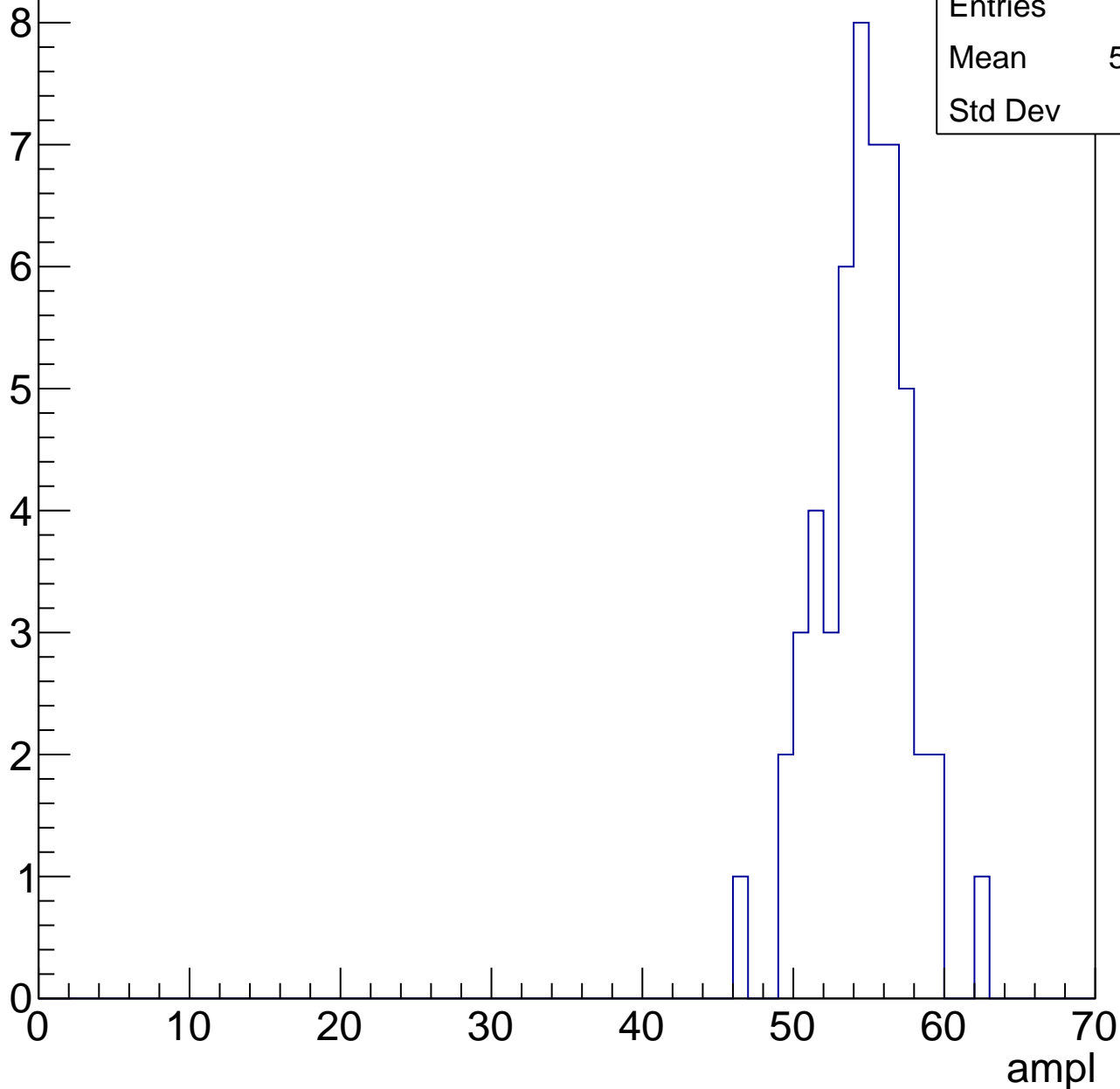


# B1L002S, U3-ch17, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	54.16
Std Dev	2.94

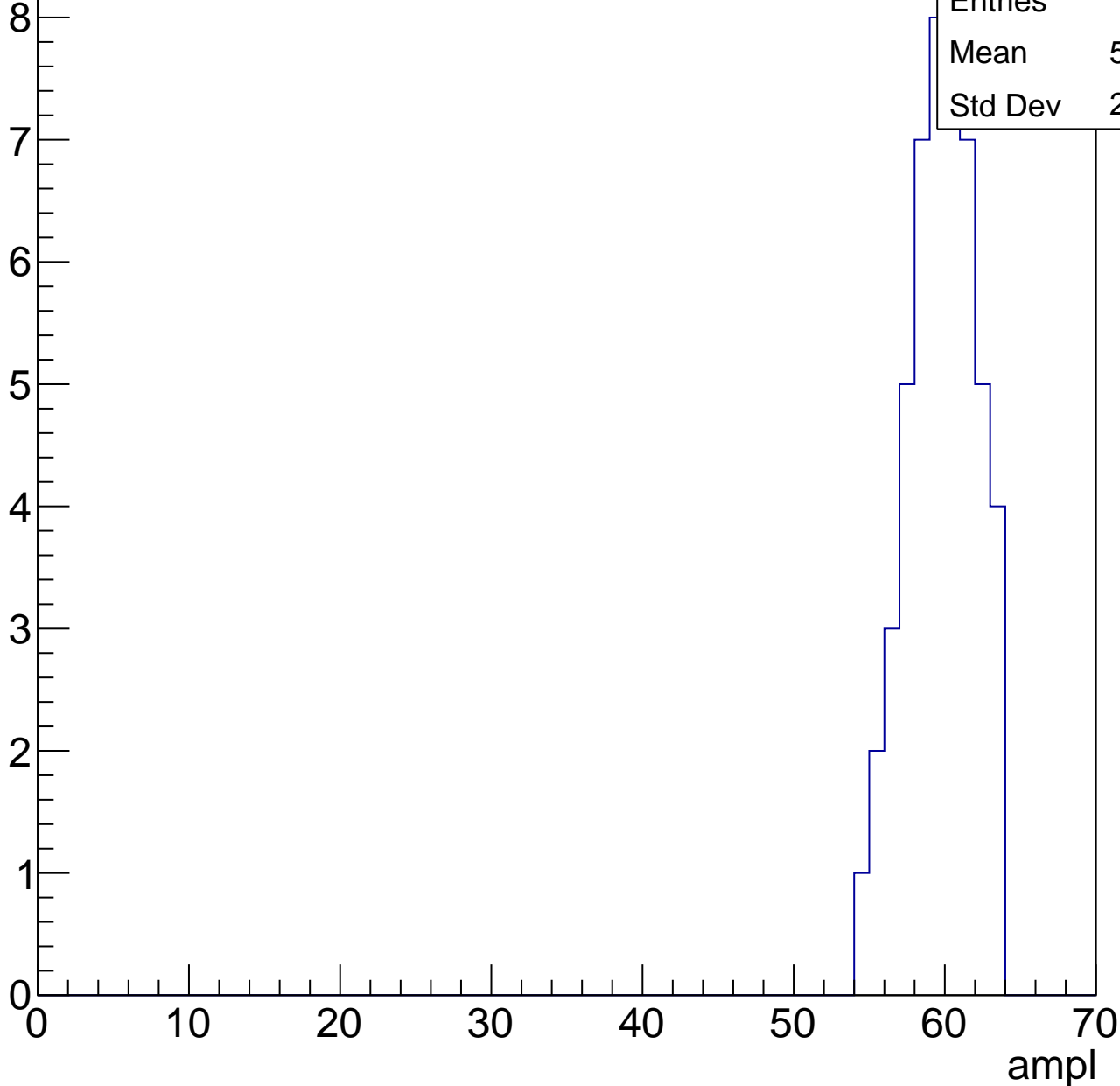


# B1L002S, U3-ch17, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	59.28
Std Dev	2.245

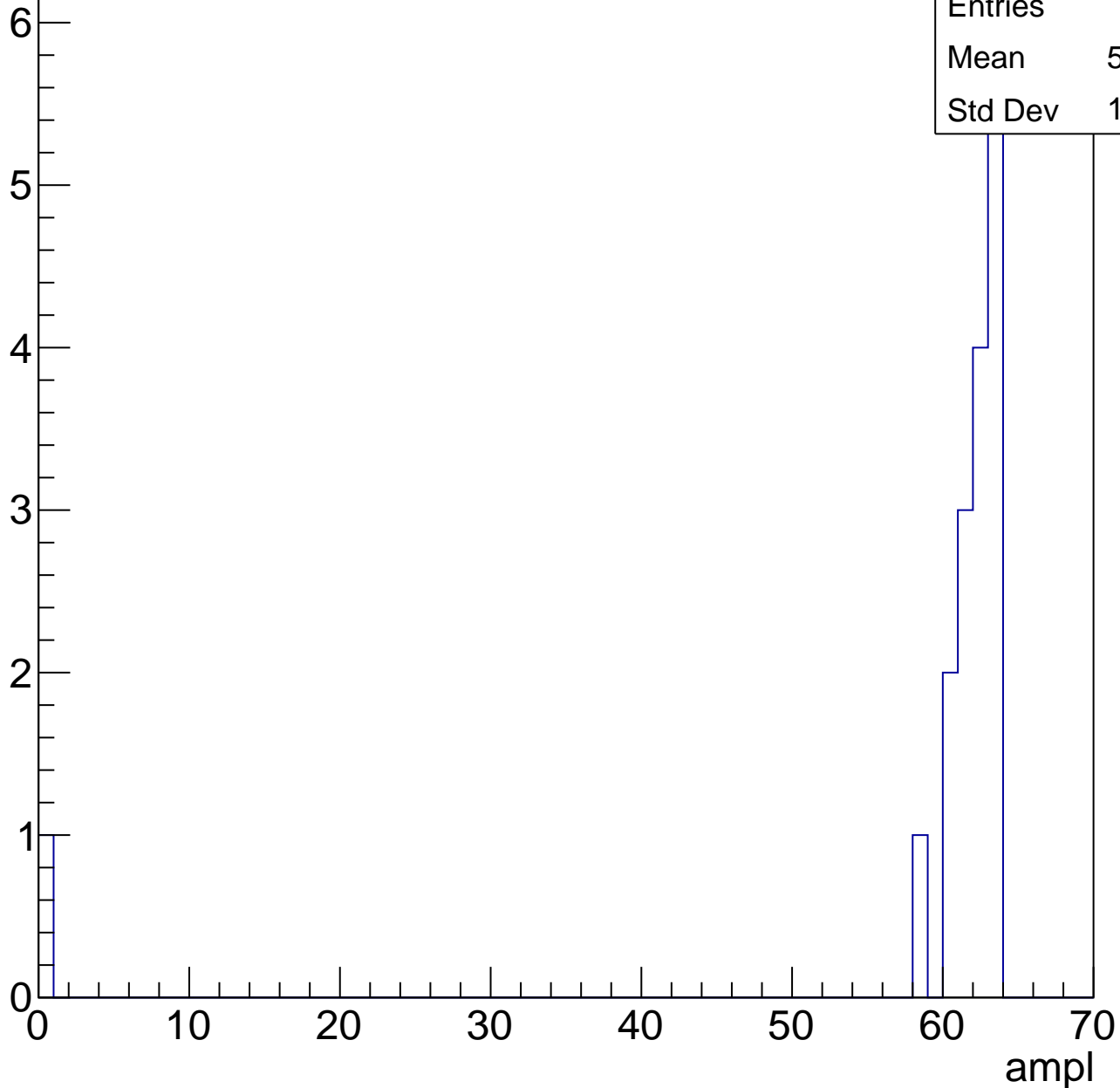


# B1L002S, U3-ch17, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	58.06
Std Dev	14.58





# B1L002S, U3-ch17, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	0
Std Dev	0

# B1L002S, U3-ch18, adc0

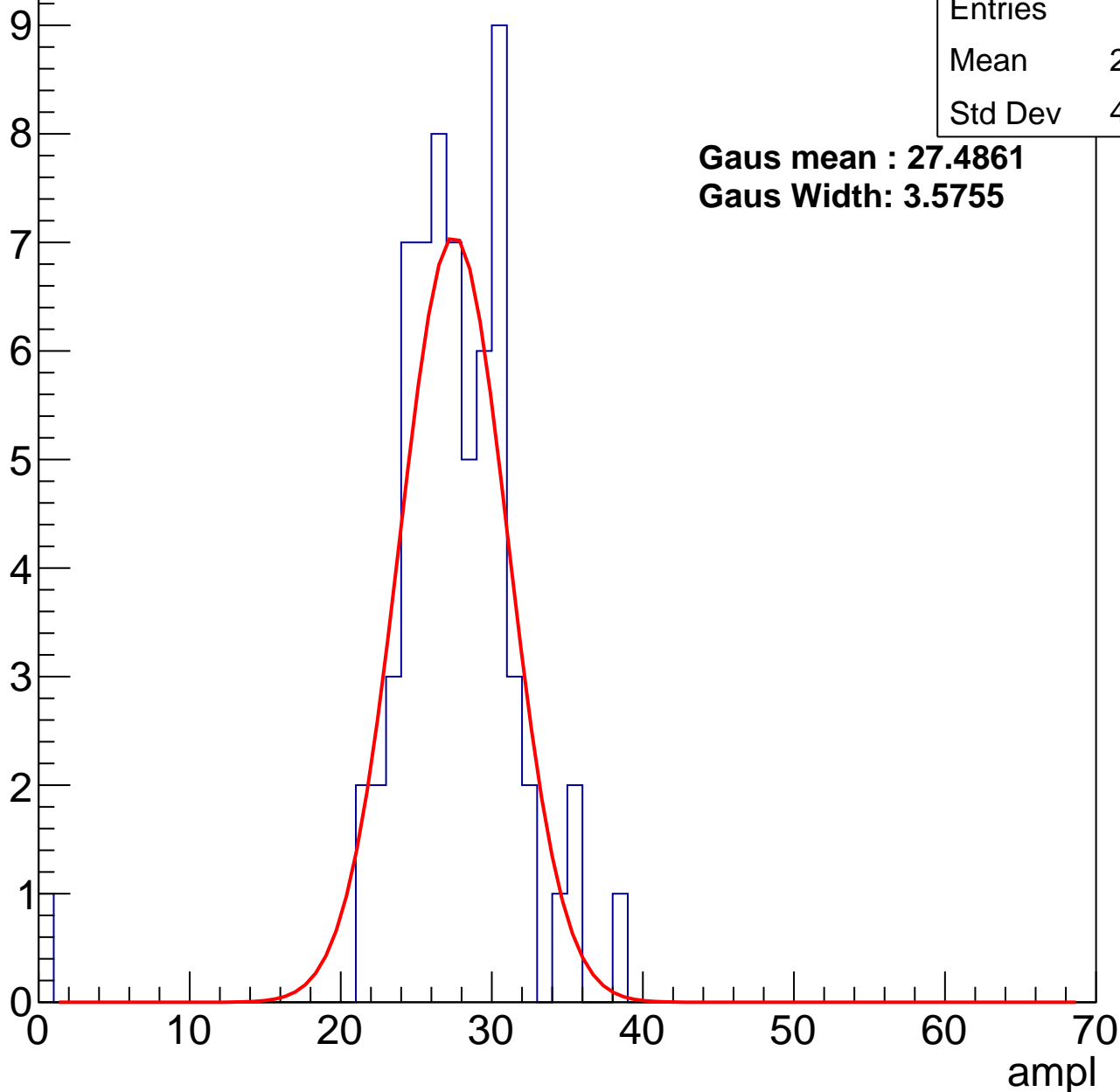
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	26.94
Std Dev	4.773

**Gaus mean : 27.4861**

**Gaus Width: 3.5755**



# B1L002S, U3-ch18, adc1

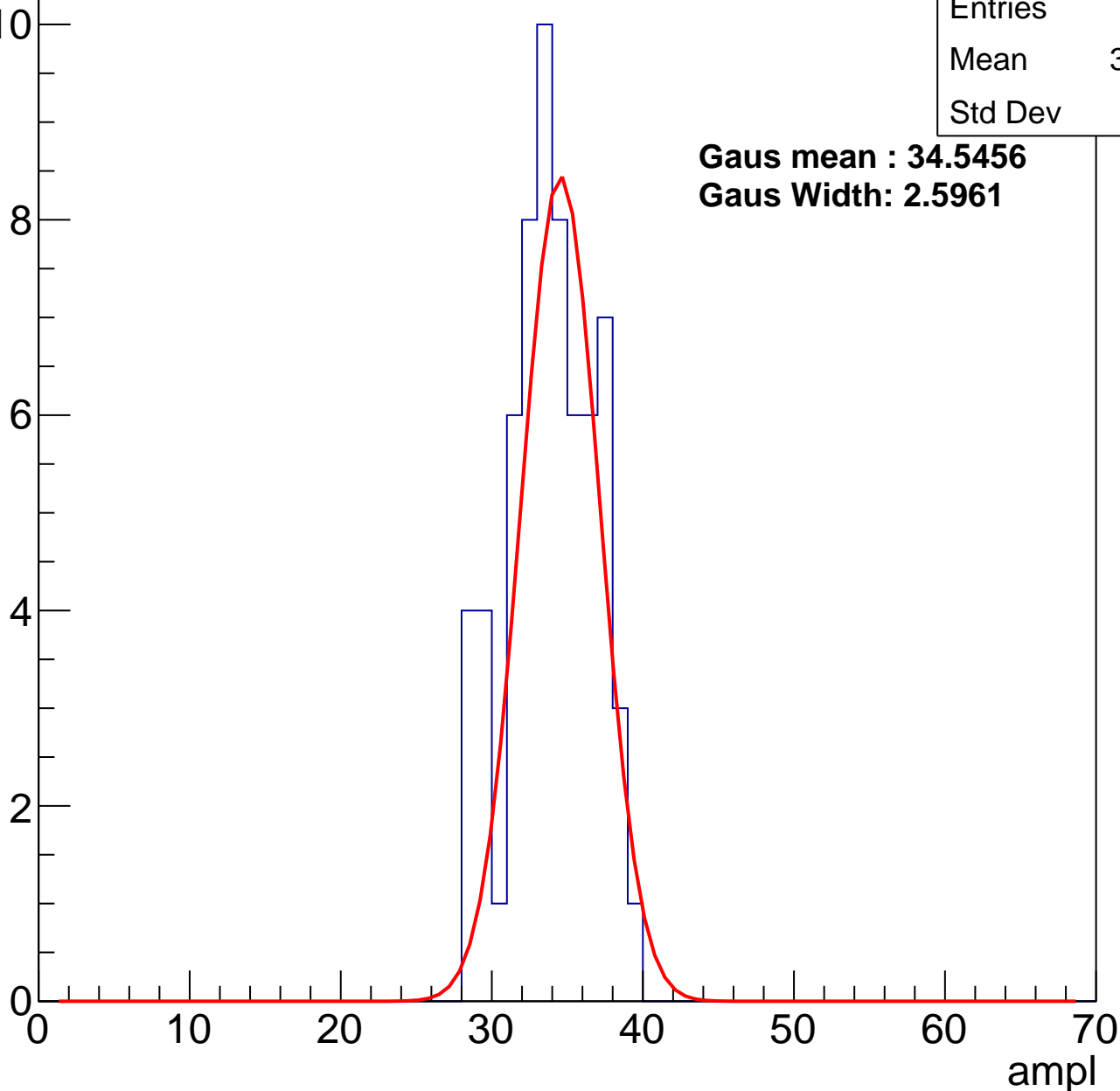
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.44
Std Dev	2.8

**Gaus mean : 34.5456**

**Gaus Width: 2.5961**



# B1L002S, U3-ch18, adc2

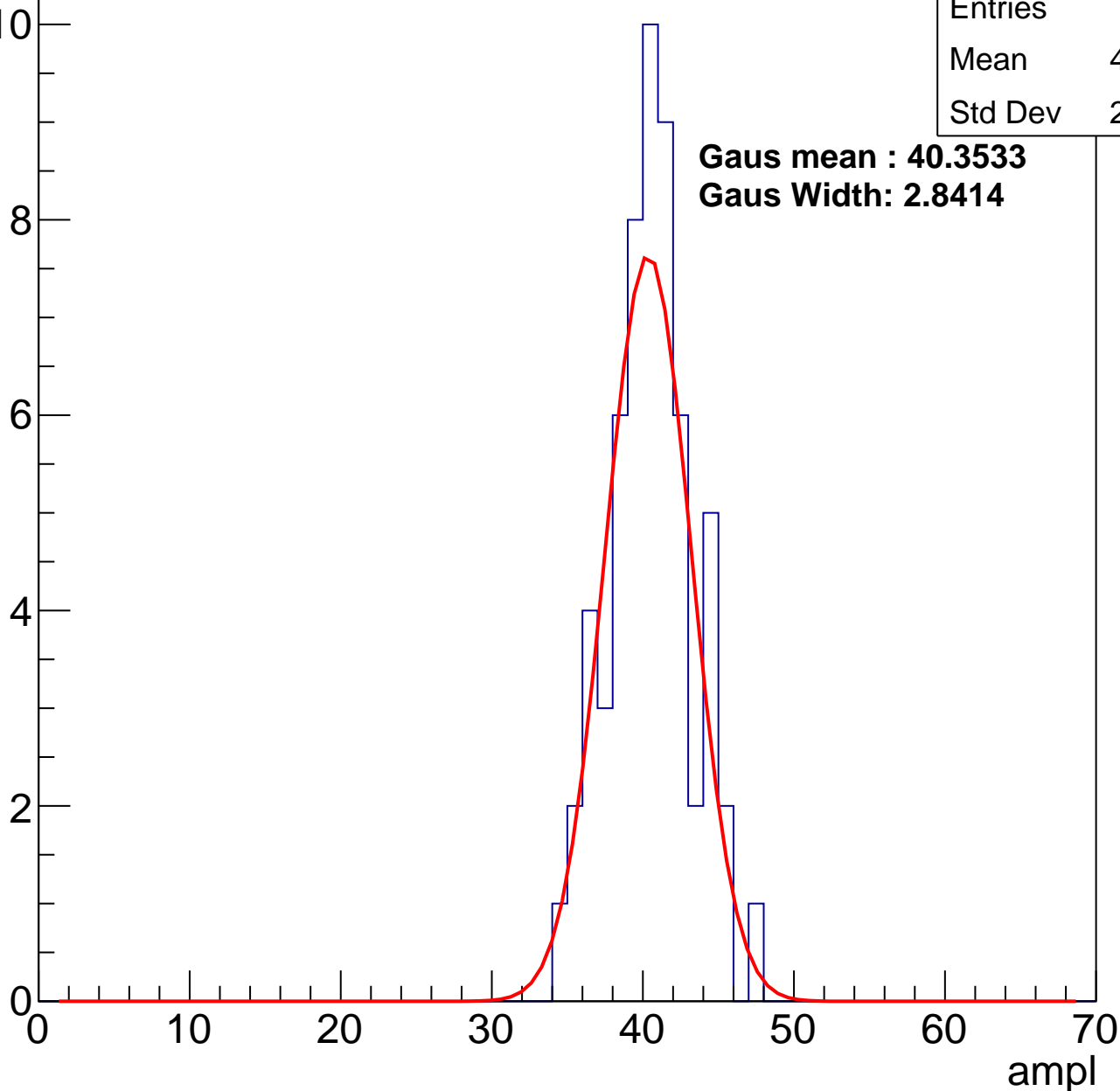
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.05
Std Dev	2.727

**Gaus mean : 40.3533**

**Gaus Width: 2.8414**

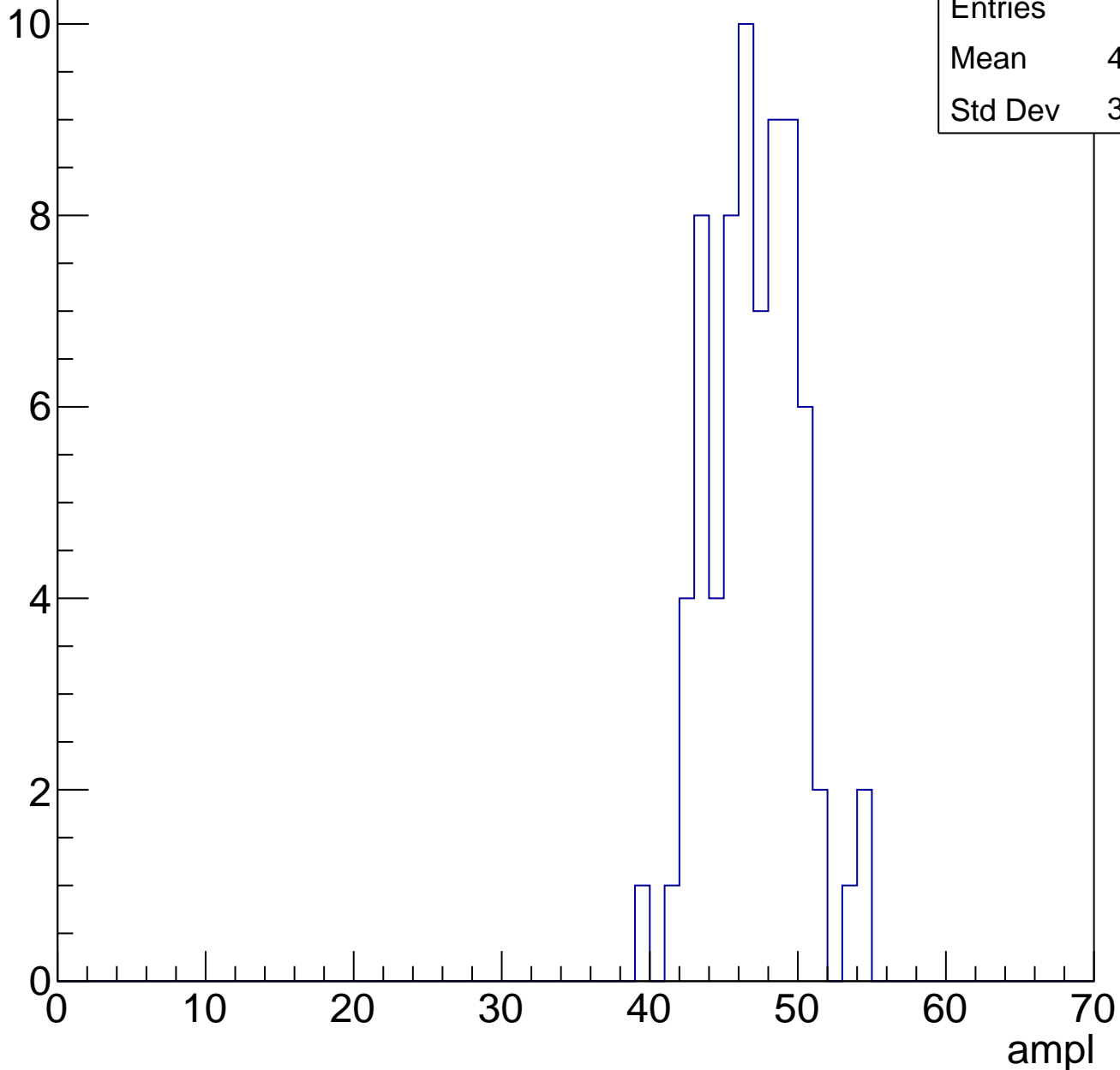


# B1L002S, U3-ch18, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	72
Mean	46.57
Std Dev	3.022

Entry

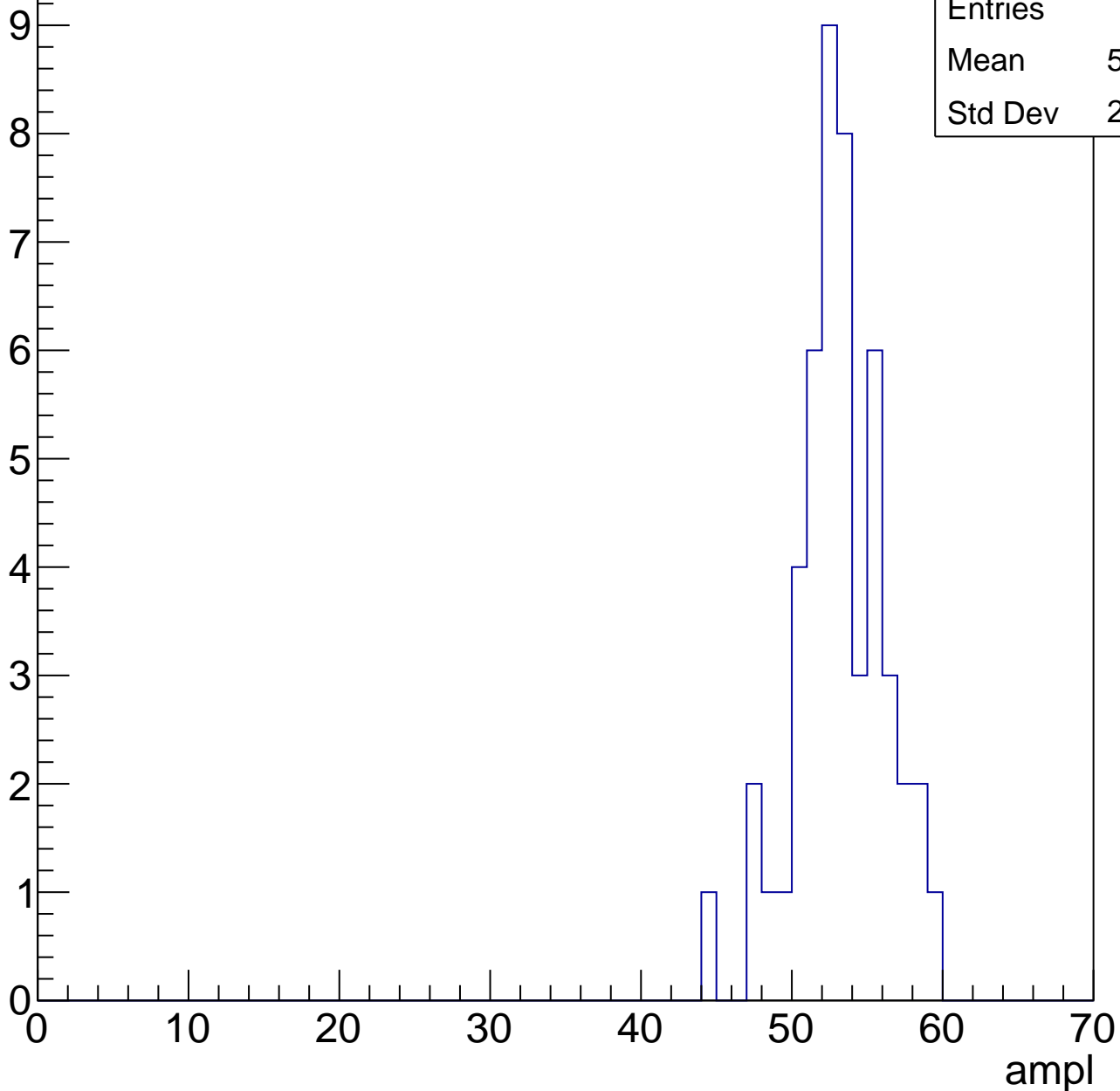


# B1L002S, U3-ch18, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.69
Std Dev	2.964

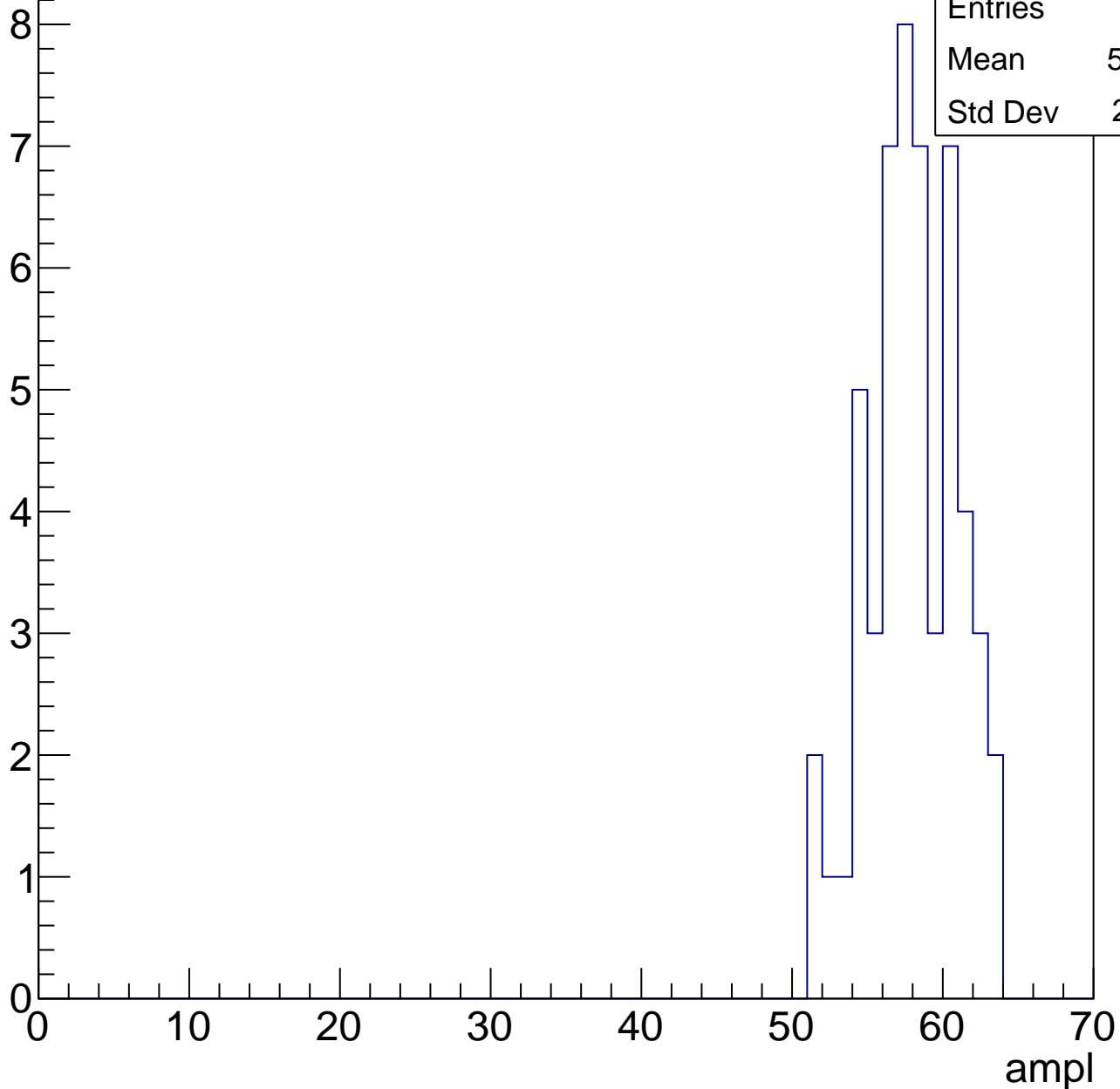


# B1L002S, U3-ch18, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

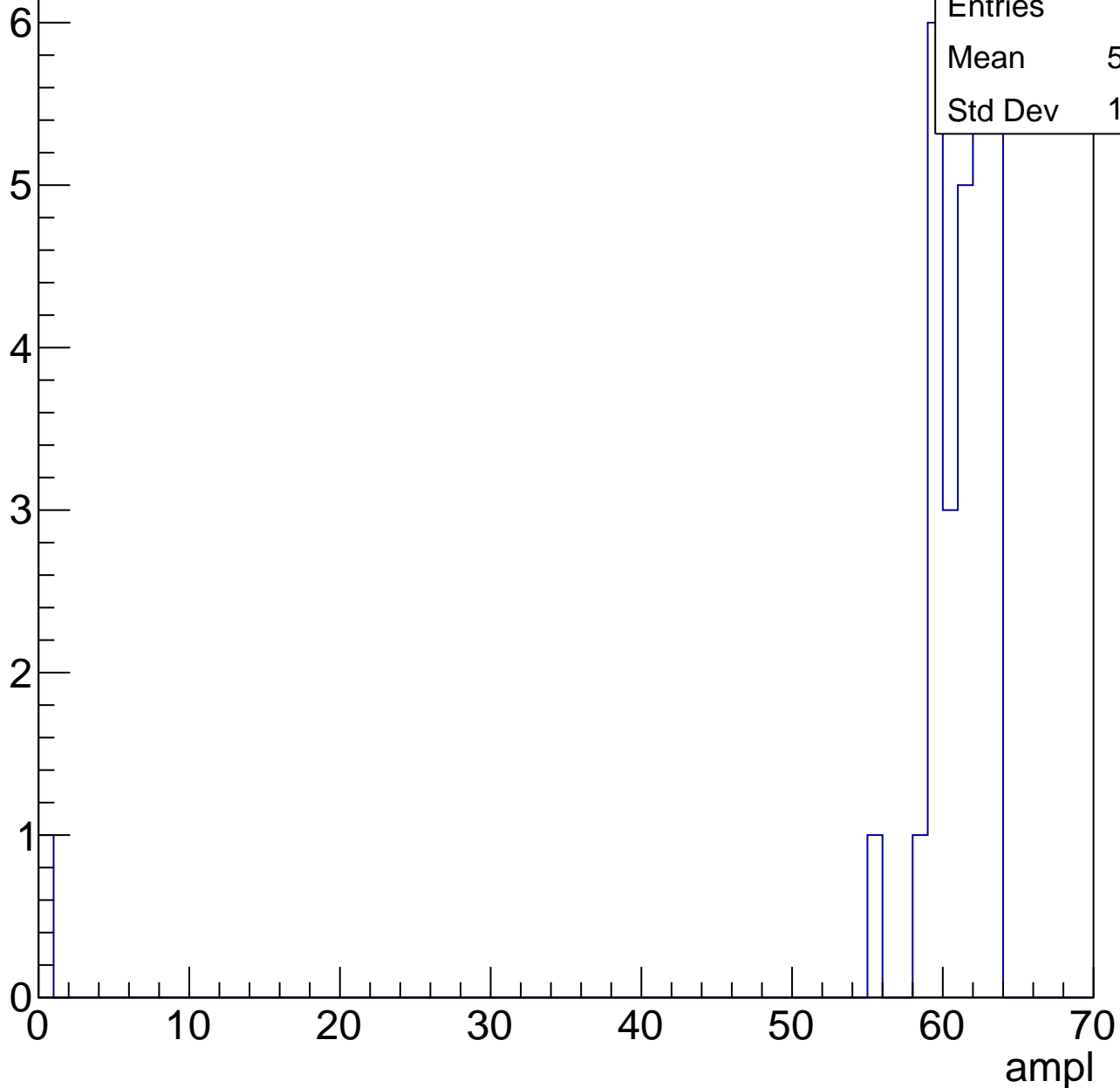
Entries	53
Mean	57.53
Std Dev	2.931



# B1L002S, U3-ch18, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

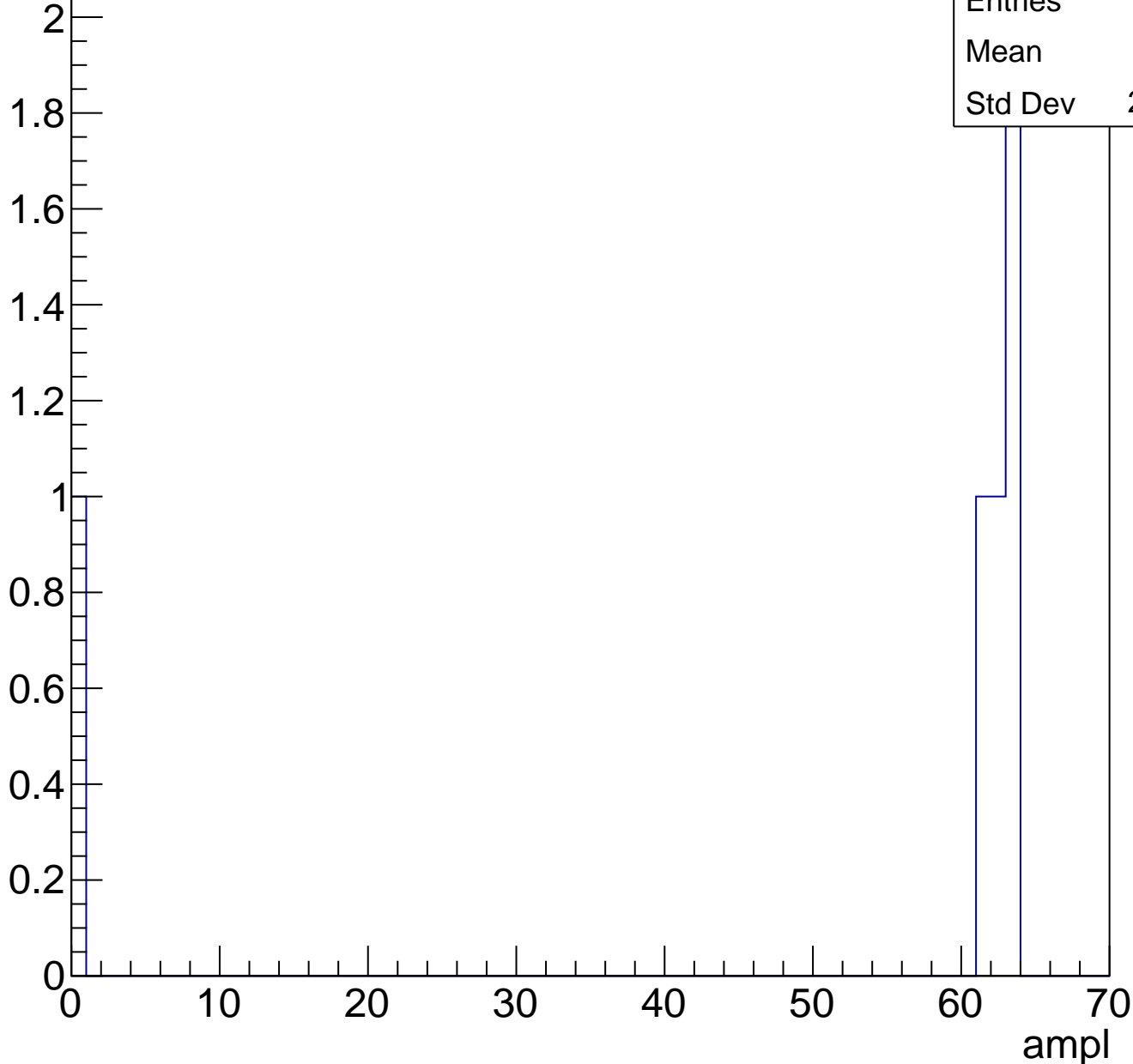




# B1L002S, U3-ch18, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch19, adc0

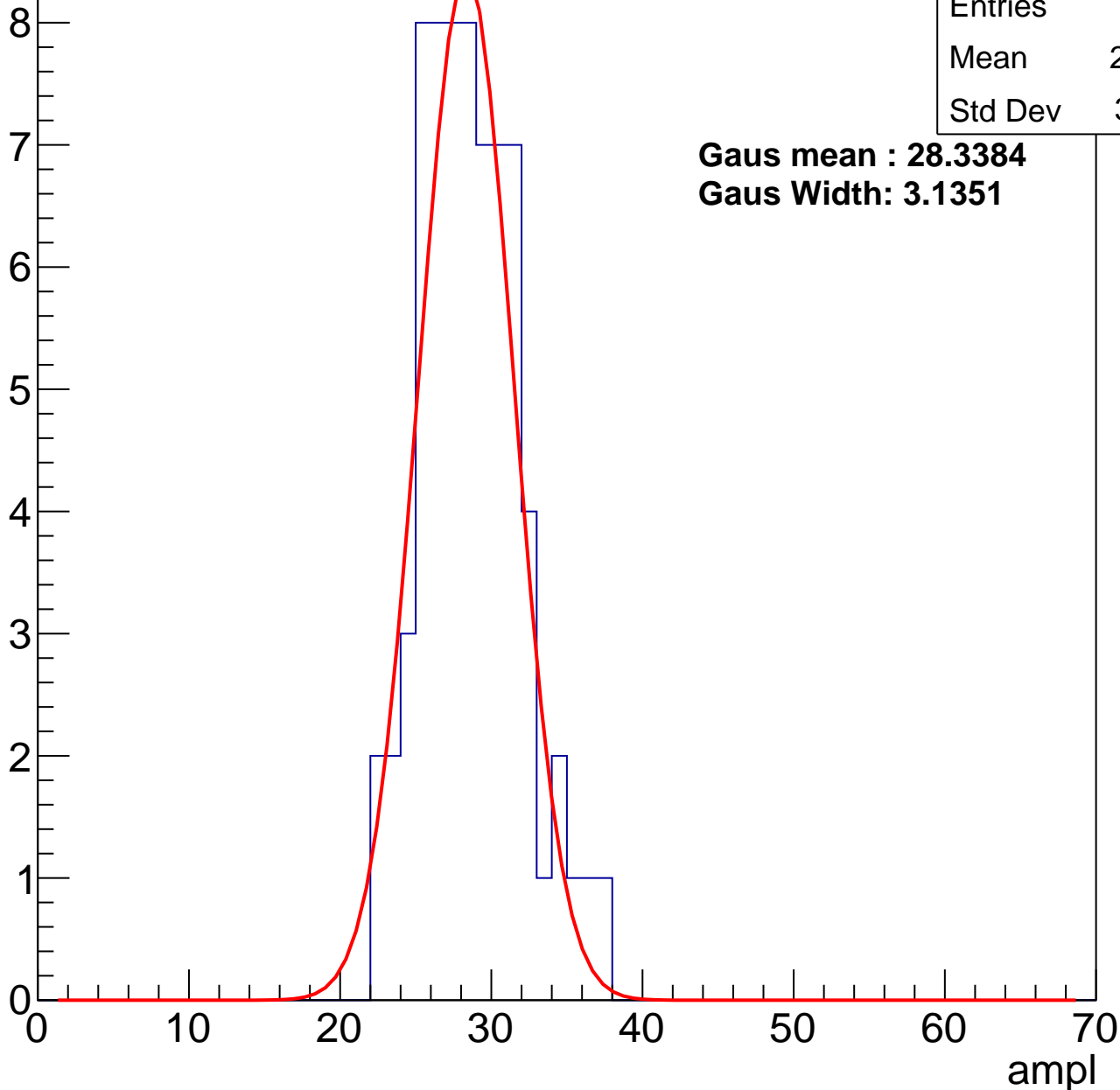
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.24
Std Dev	3.231

**Gaus mean : 28.3384**

**Gaus Width: 3.1351**



# B1L002S, U3-ch19, adc1

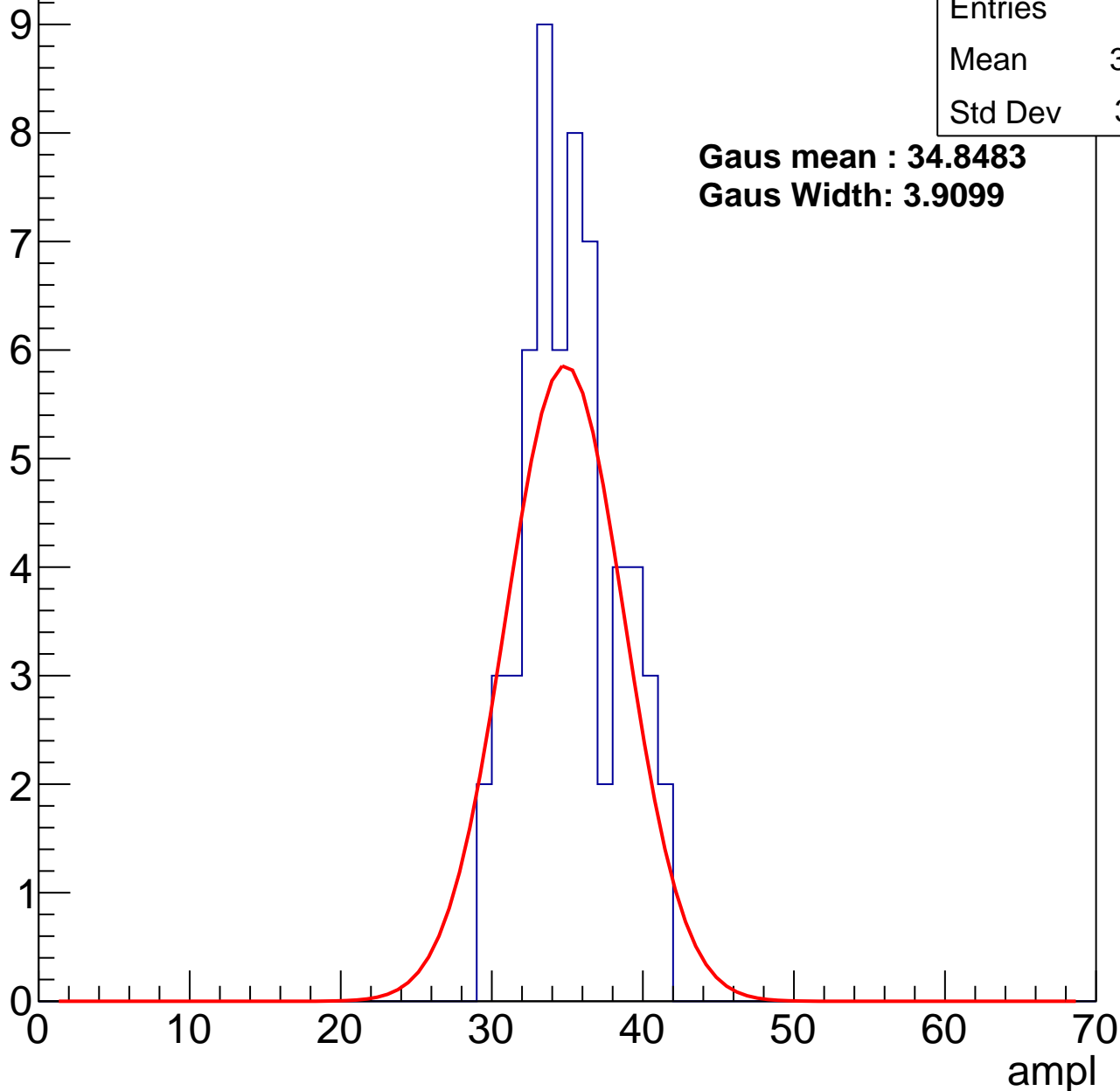
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.75
Std Dev	3.051

**Gaus mean : 34.8483**

**Gaus Width: 3.9099**



# B1L002S, U3-ch19, adc2

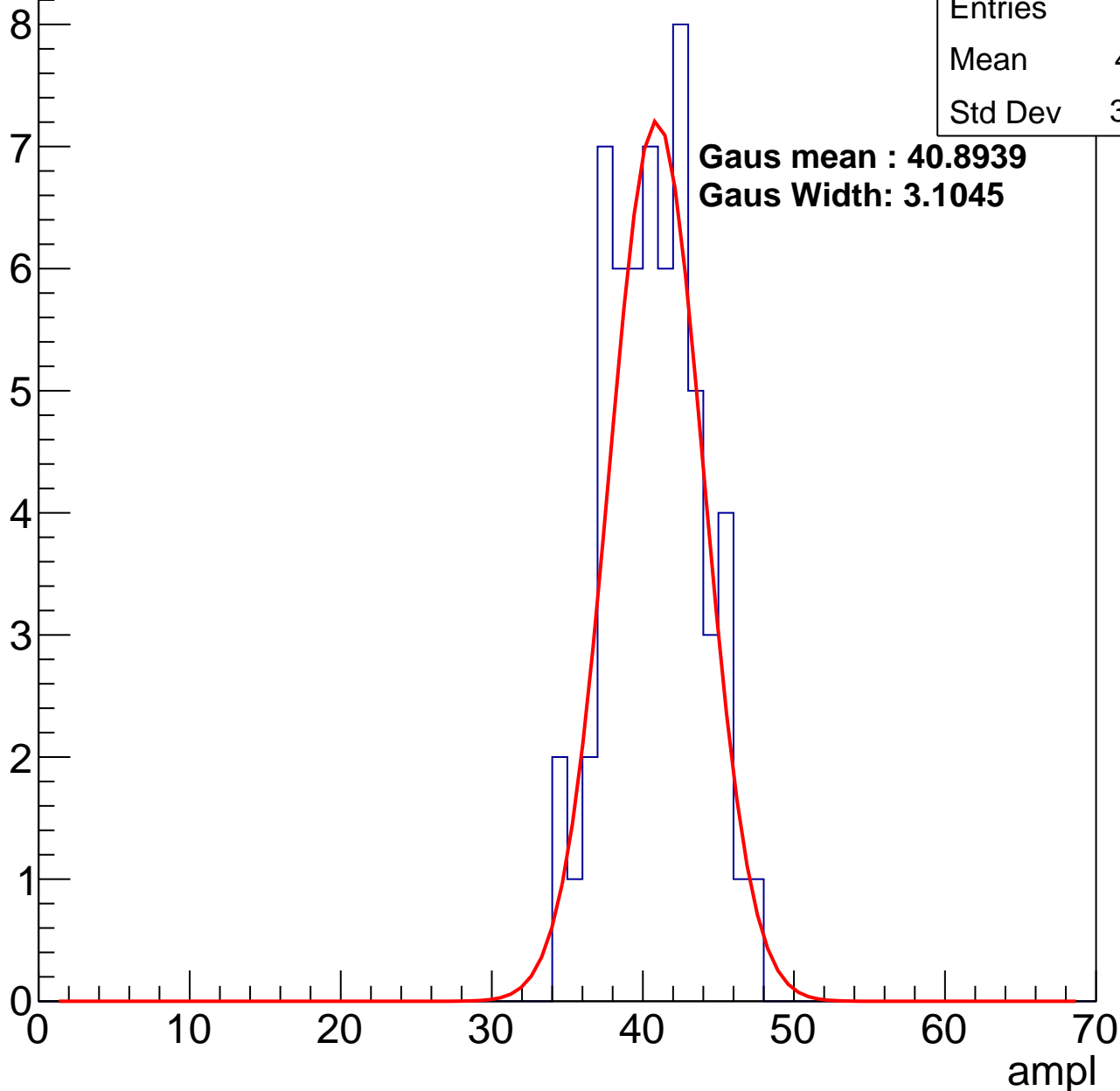
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.31
Std Dev	3.004

**Gaus mean : 40.8939**

**Gaus Width: 3.1045**

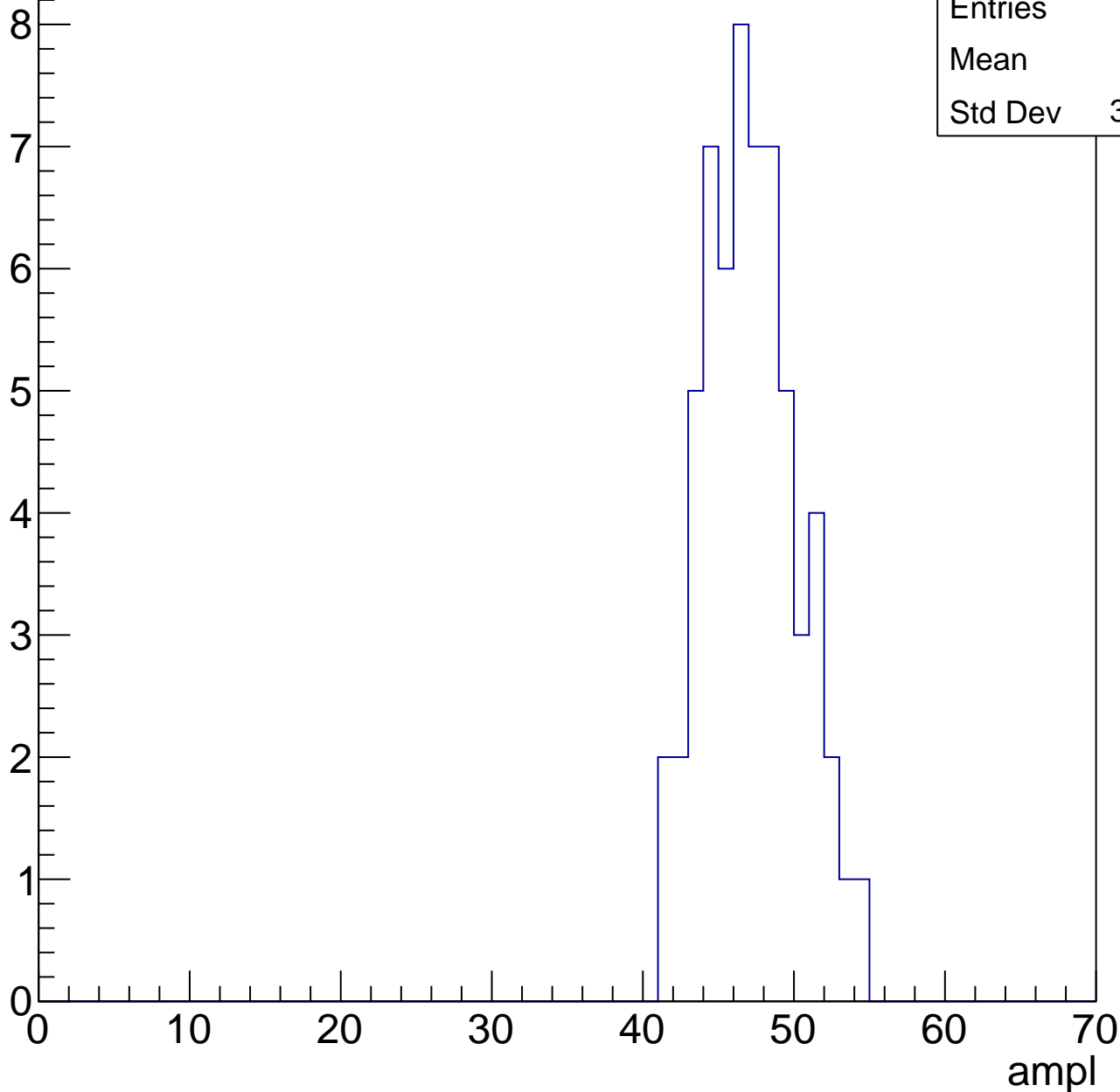


# B1L002S, U3-ch19, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

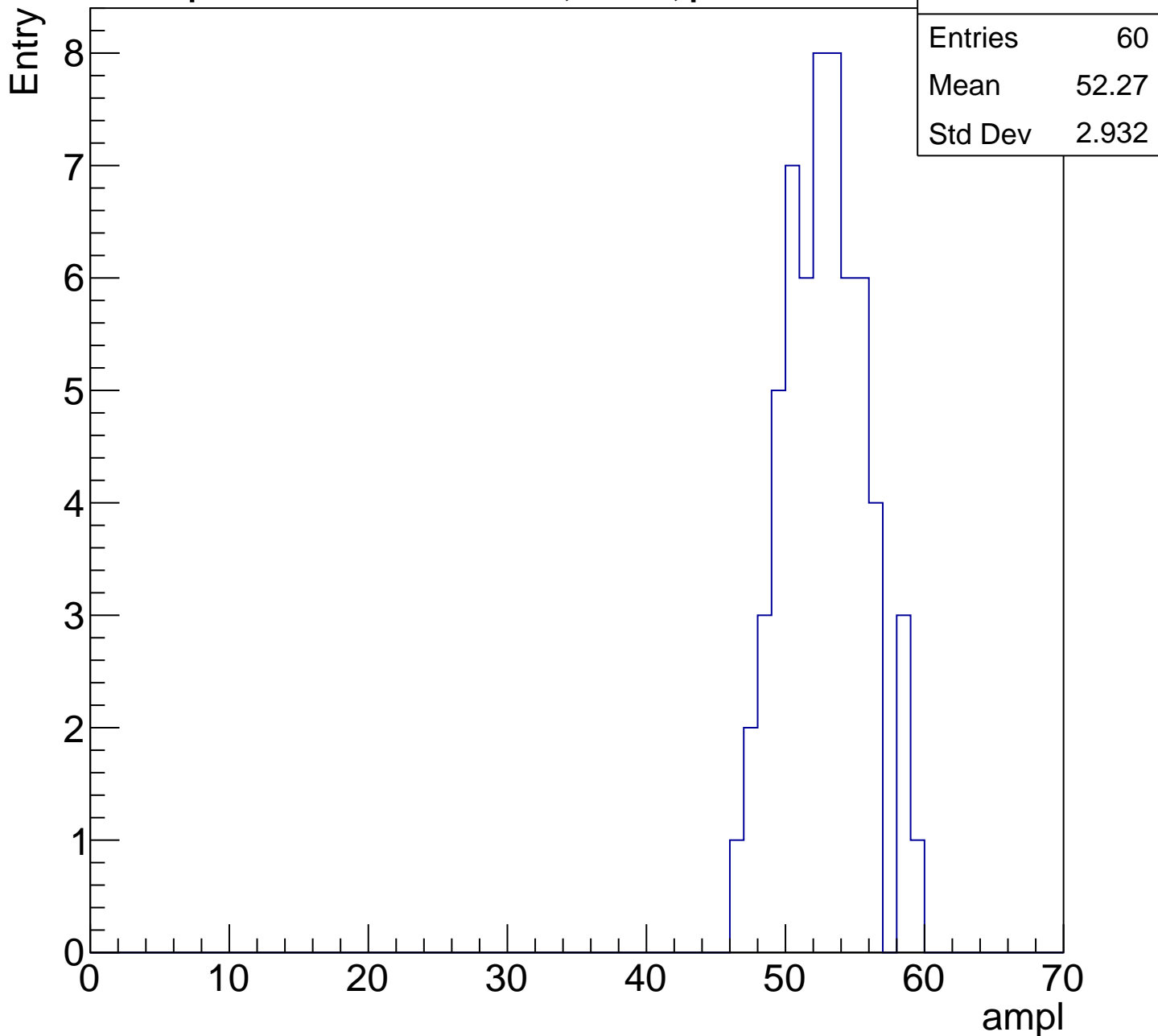
Entry

Entries	60
Mean	46.7
Std Dev	3.013



# B1L002S, U3-ch19, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch19, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

8

6

4

2

0

0

10

20

30

40

50

60

70

ampl

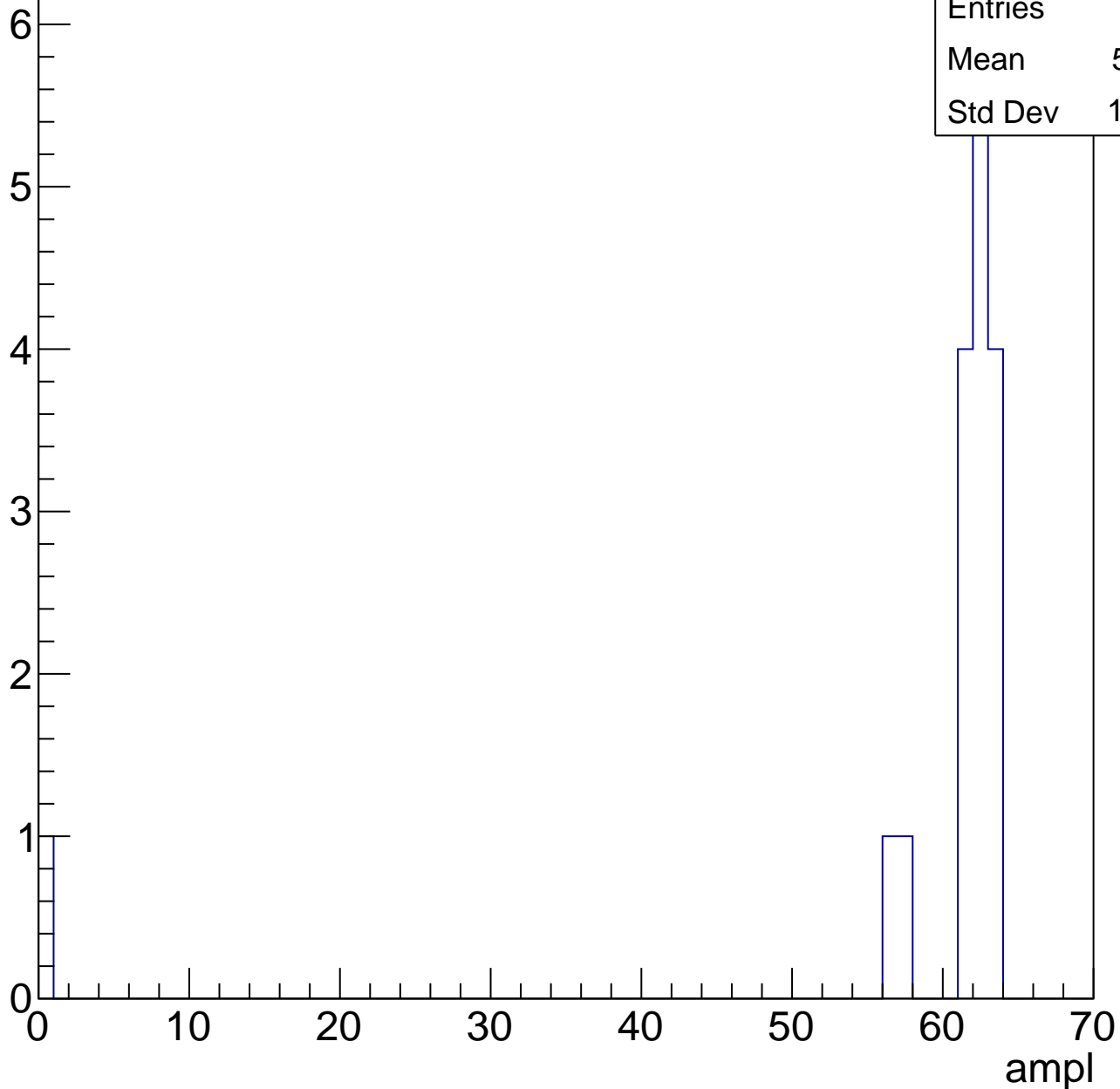
Entries	63
Mean	58.63
Std Dev	2.534

# B1L002S, U3-ch19, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	57.71
Std Dev	14.55

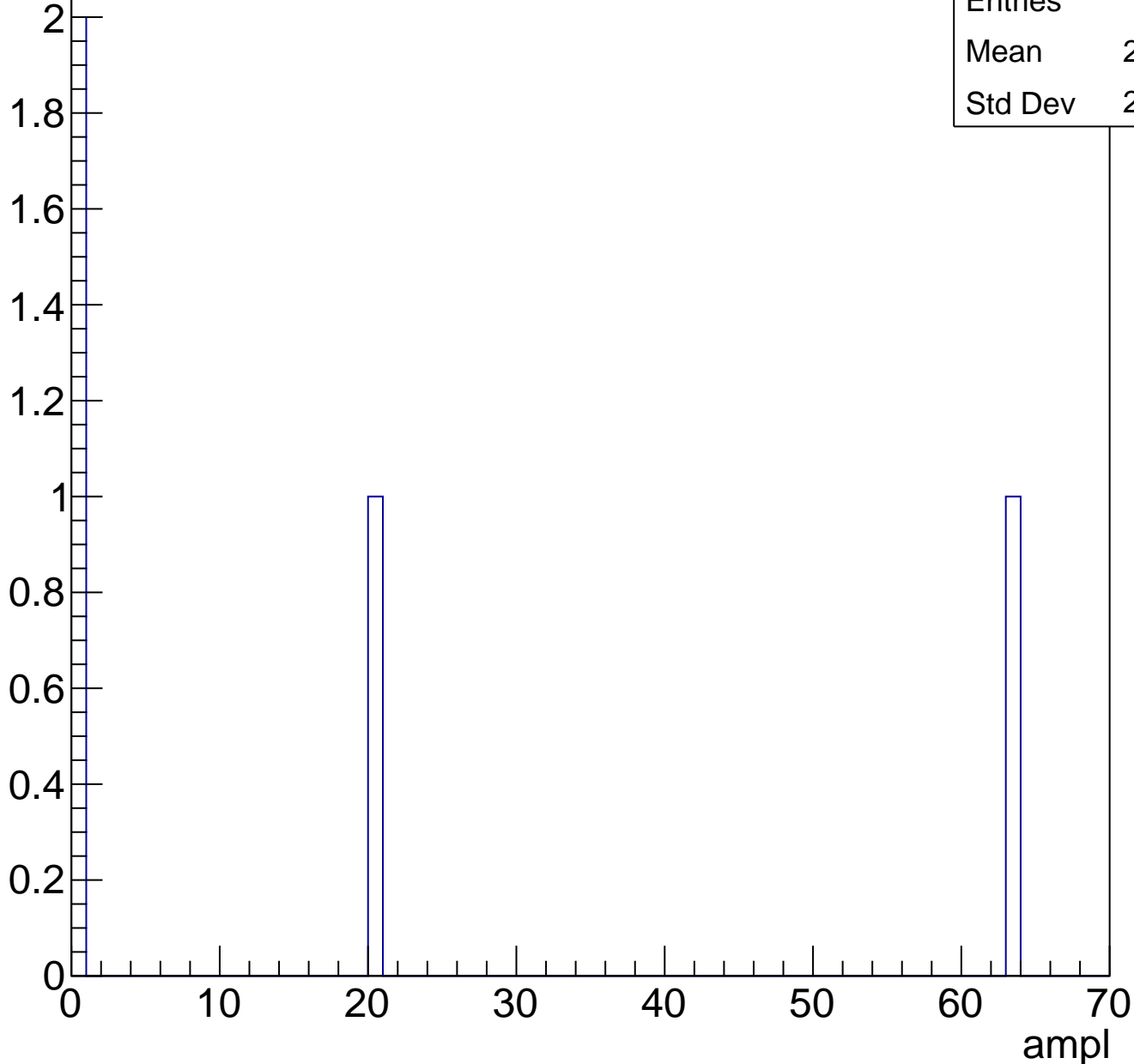




# B1L002S, U3-ch19, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.75
Std Dev	25.72

# B1L002S, U3-ch20, adc0

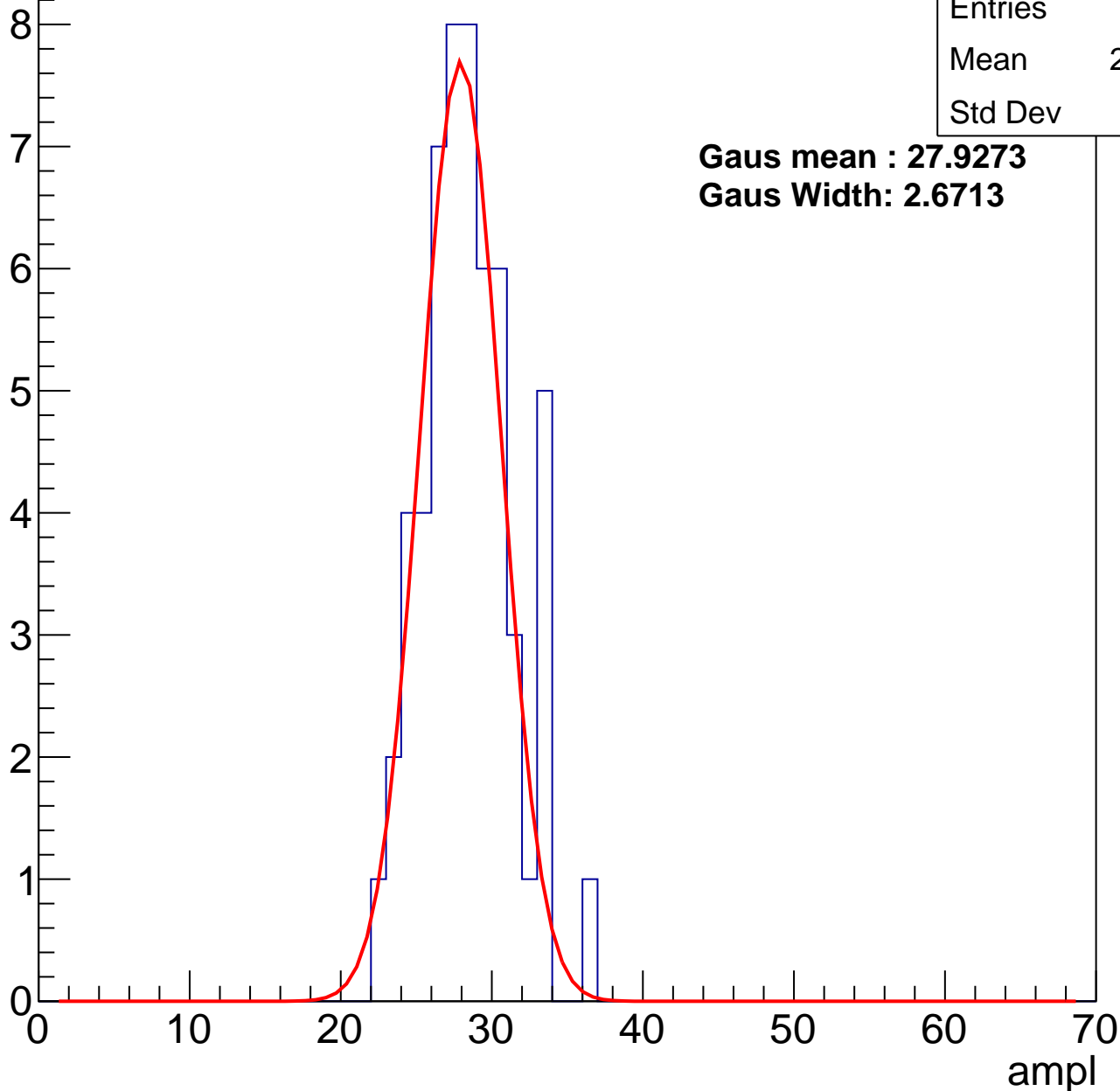
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	27.96
Std Dev	2.94

**Gaus mean : 27.9273**

**Gaus Width: 2.6713**



# B1L002S, U3-ch20, adc1

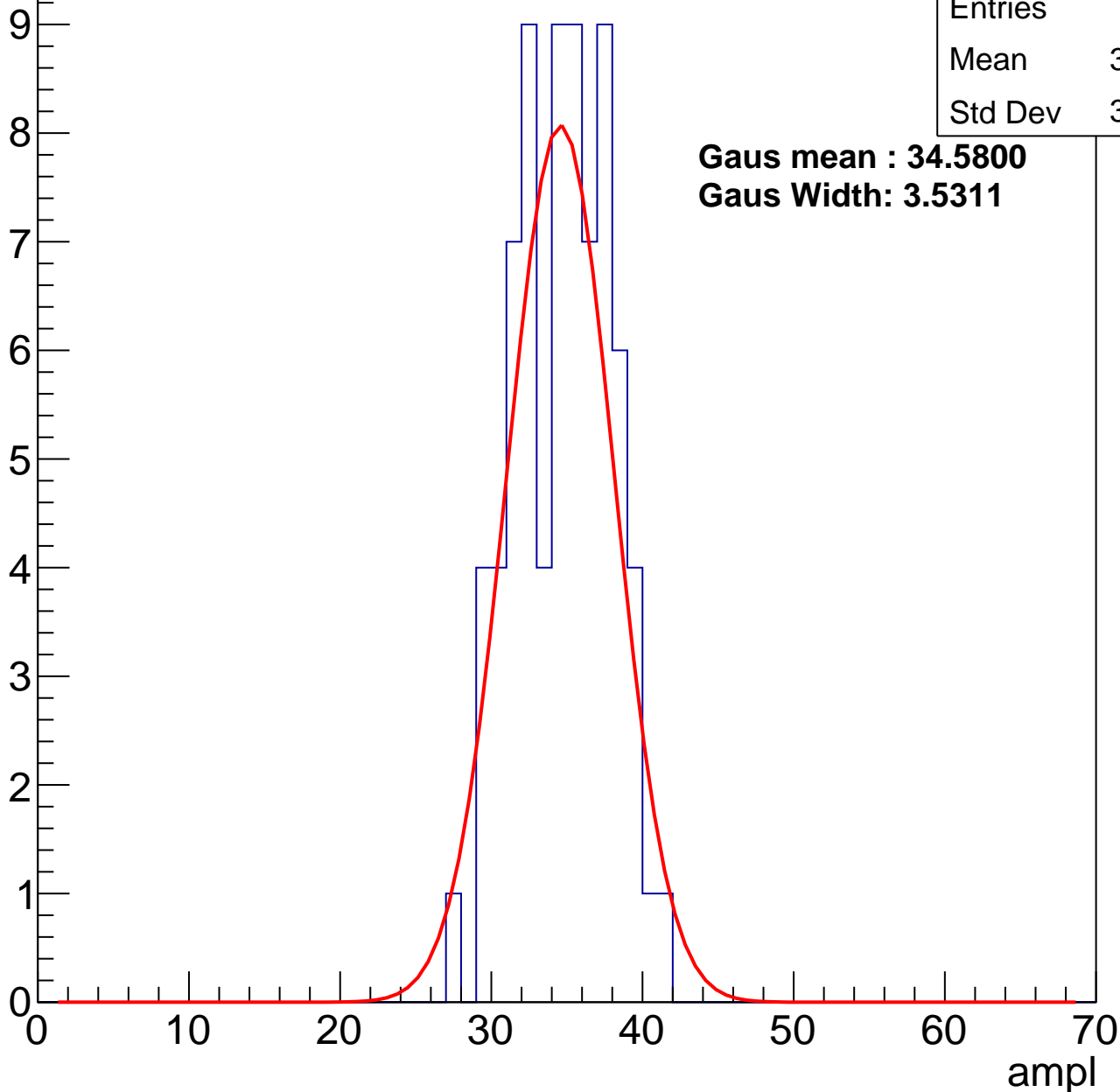
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	34.28
Std Dev	3.075

**Gaus mean : 34.5800**

**Gaus Width: 3.5311**



# B1L002S, U3-ch20, adc2

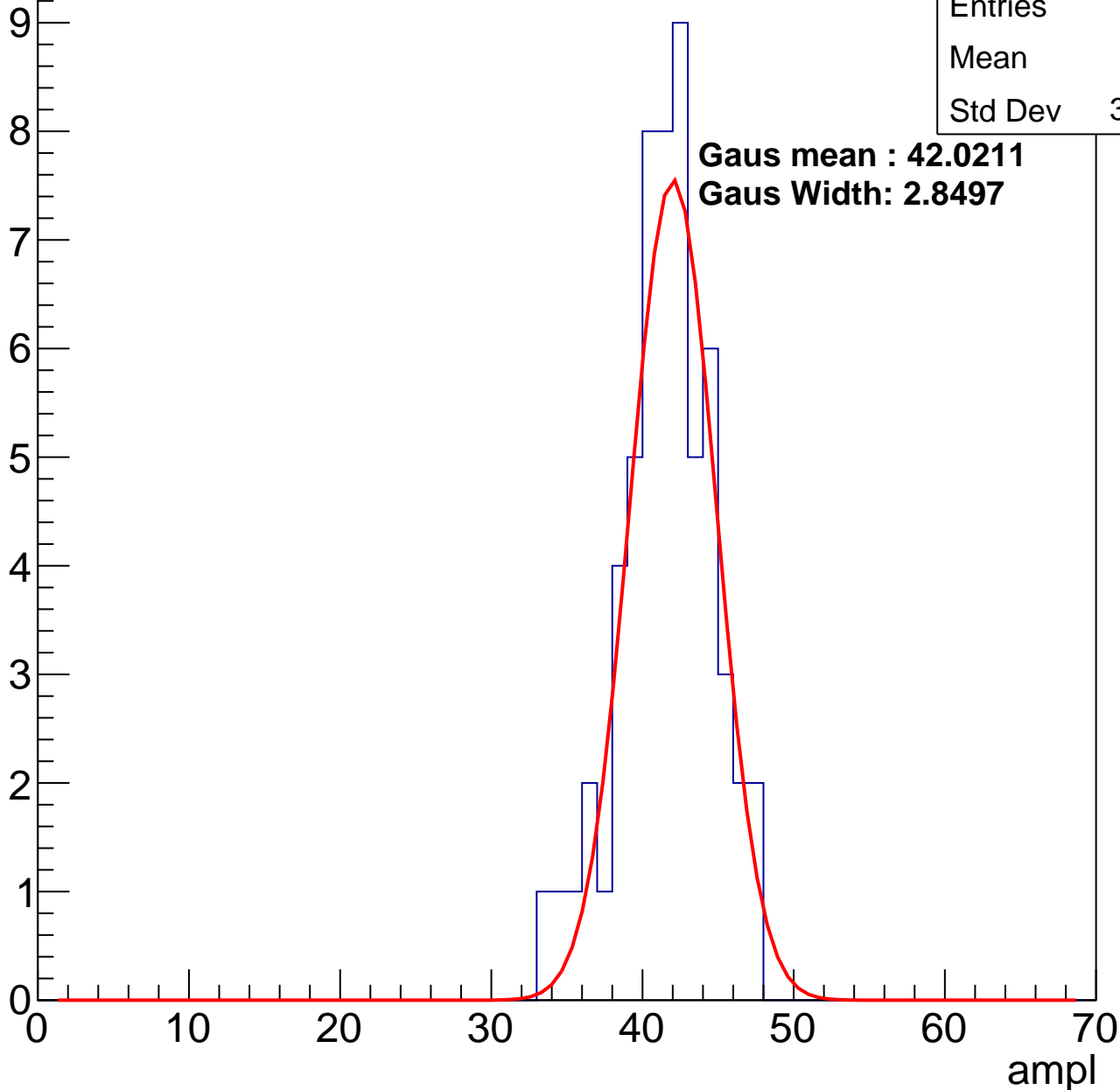
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.1
Std Dev	3.027

**Gaus mean : 42.0211**

**Gaus Width: 2.8497**

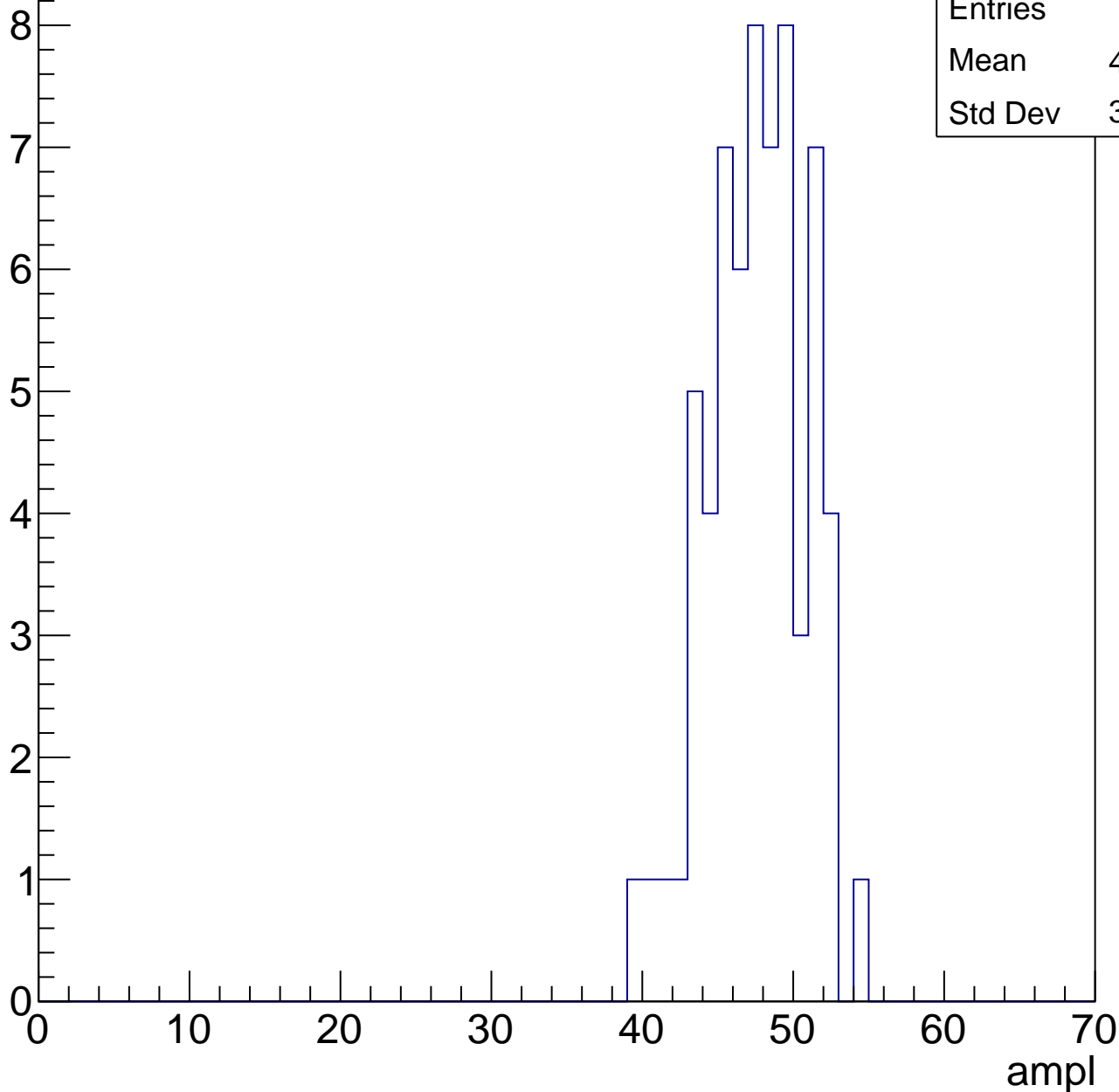


# B1L002S, U3-ch20, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

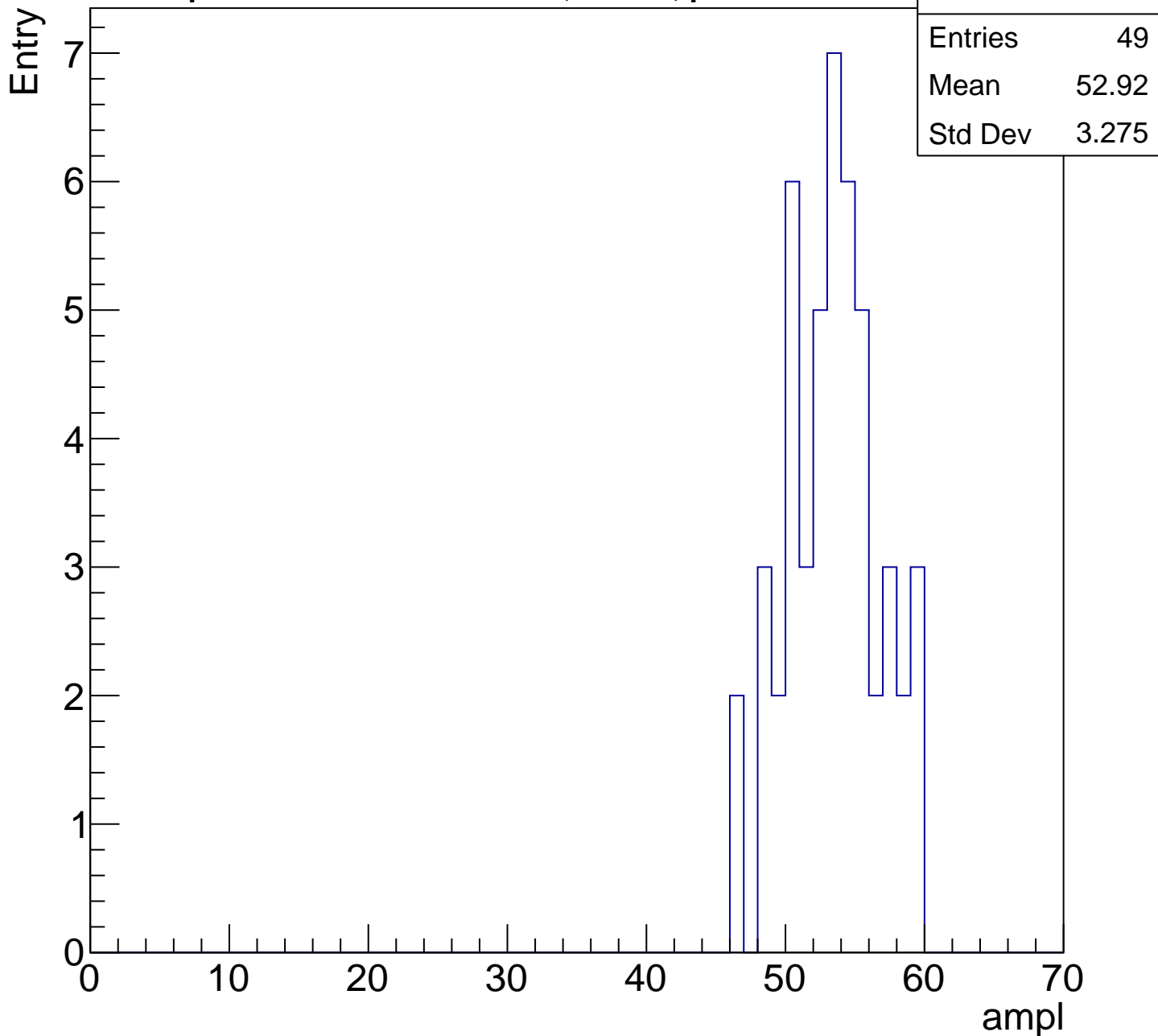
Entry

Entries	64
Mean	47.14
Std Dev	3.186



# B1L002S, U3-ch20, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch20, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

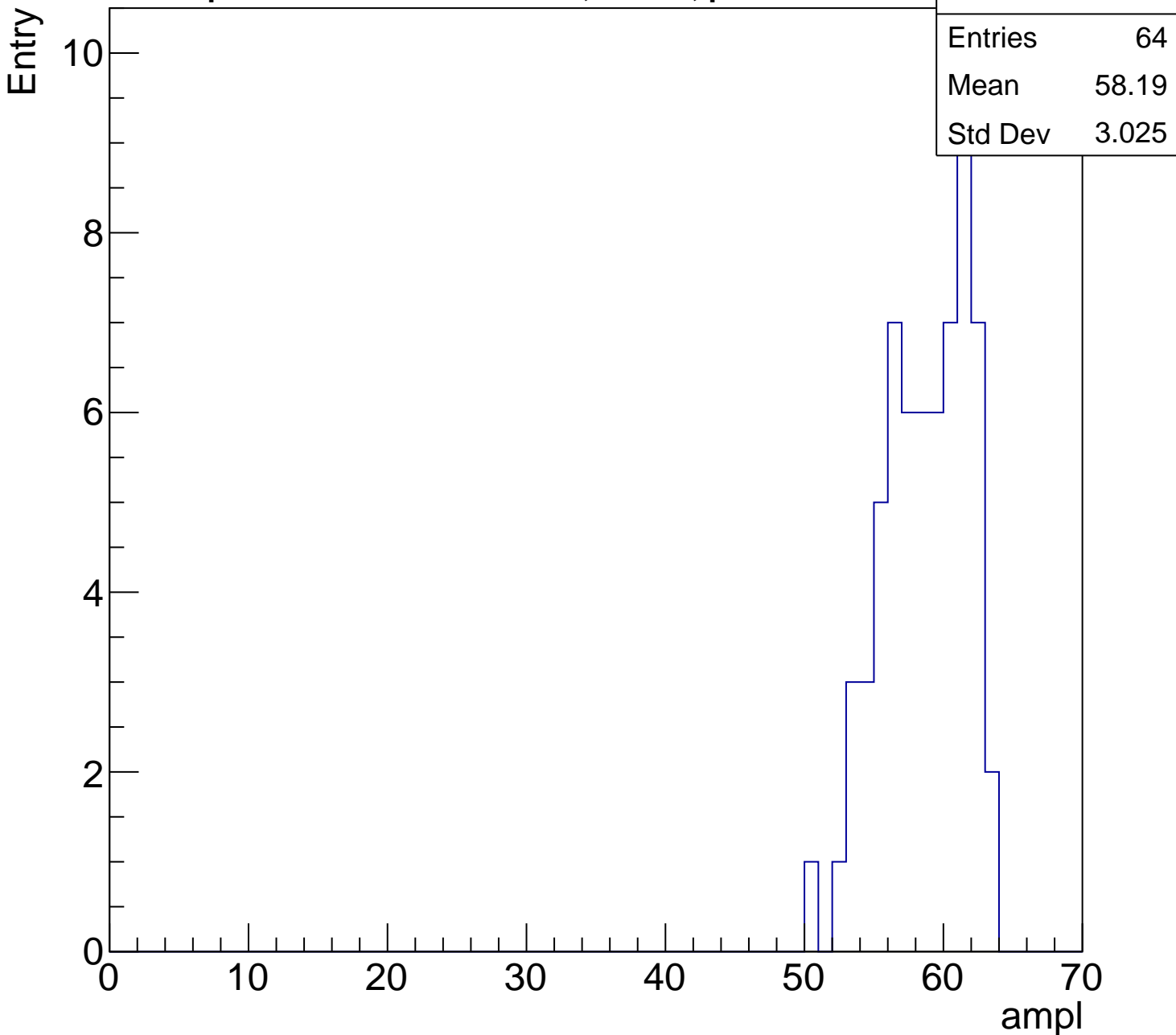
Entries	64
Mean	58.19
Std Dev	3.025

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

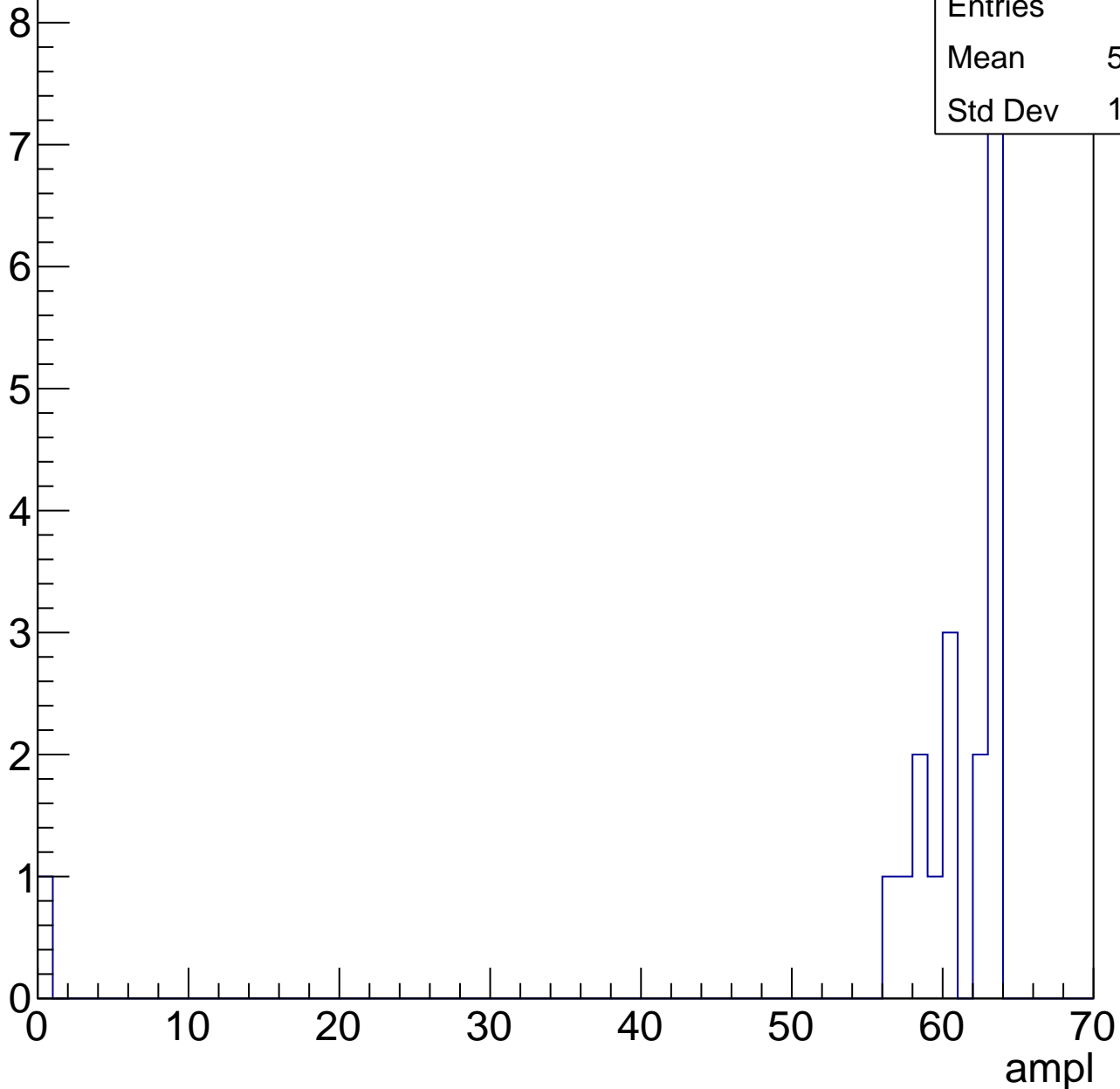


# B1L002S, U3-ch20, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	57.68
Std Dev	13.79

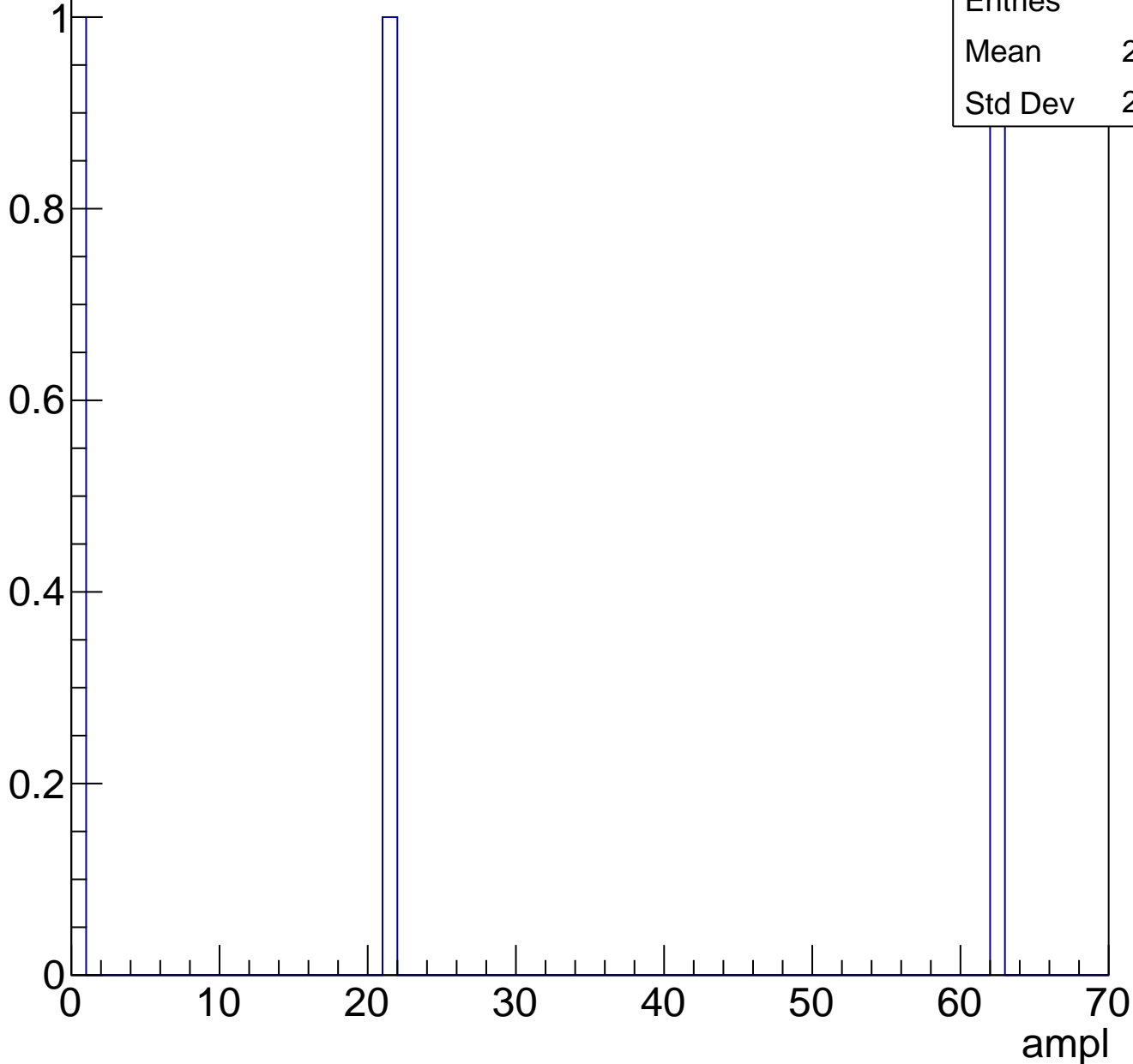




# B1L002S, U3-ch20, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L002S, U3-ch21, adc0

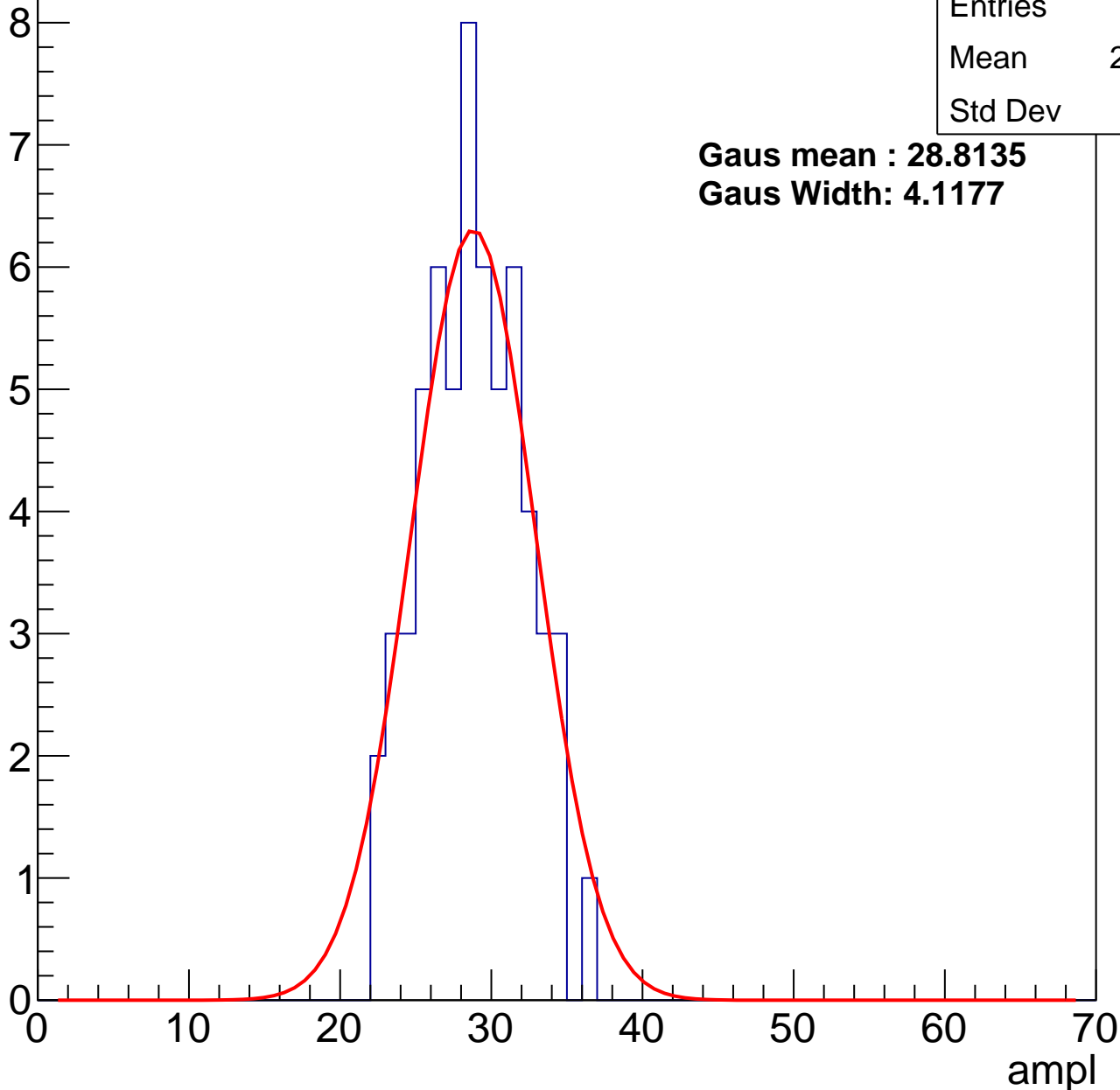
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.33
Std Dev	3.3

**Gaus mean : 28.8135**

**Gaus Width: 4.1177**



# B1L002S, U3-ch21, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	34.74
Std Dev	3.589

**Gaus mean : 35.2139**

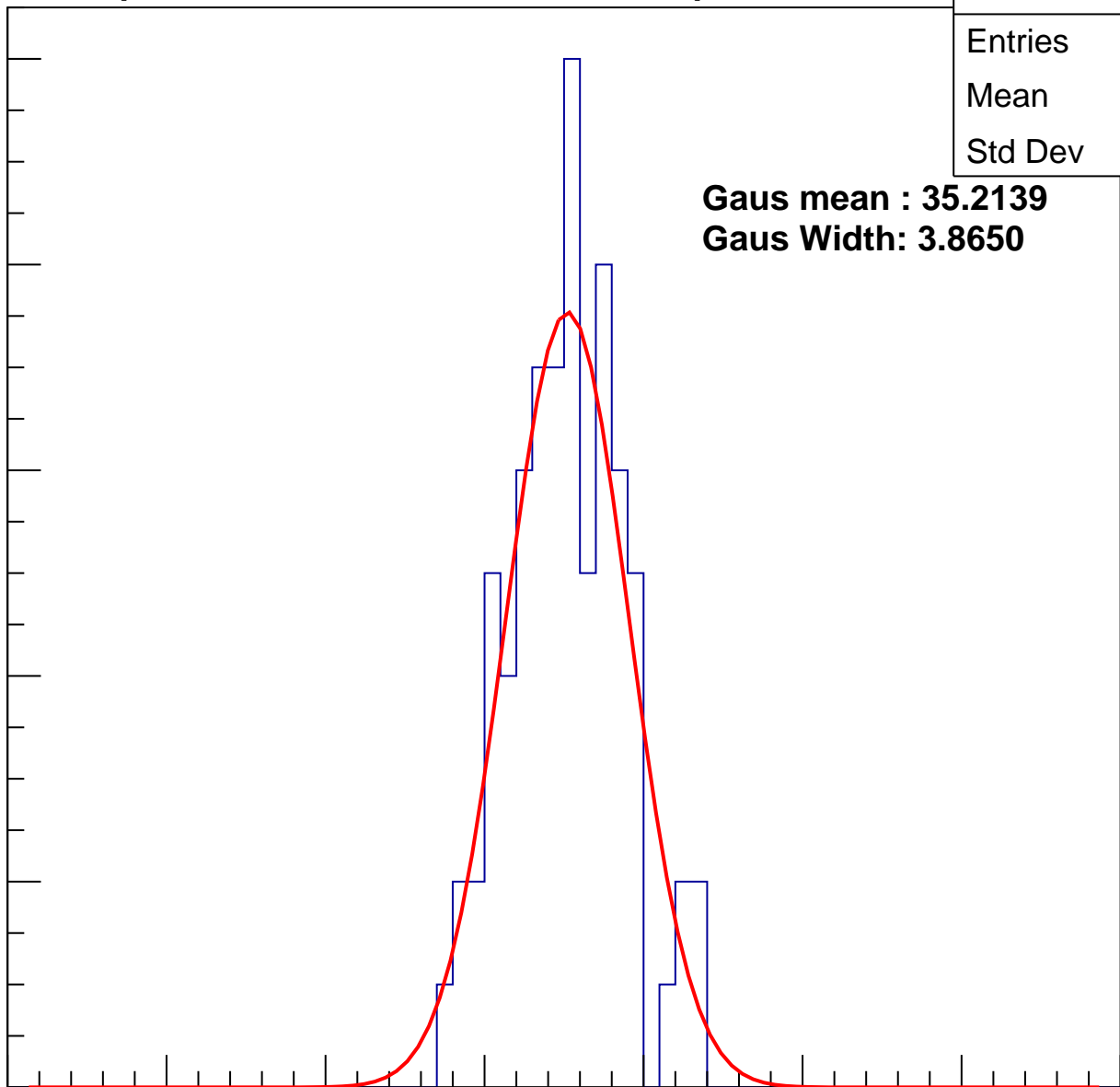
**Gaus Width: 3.8650**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch21, adc2

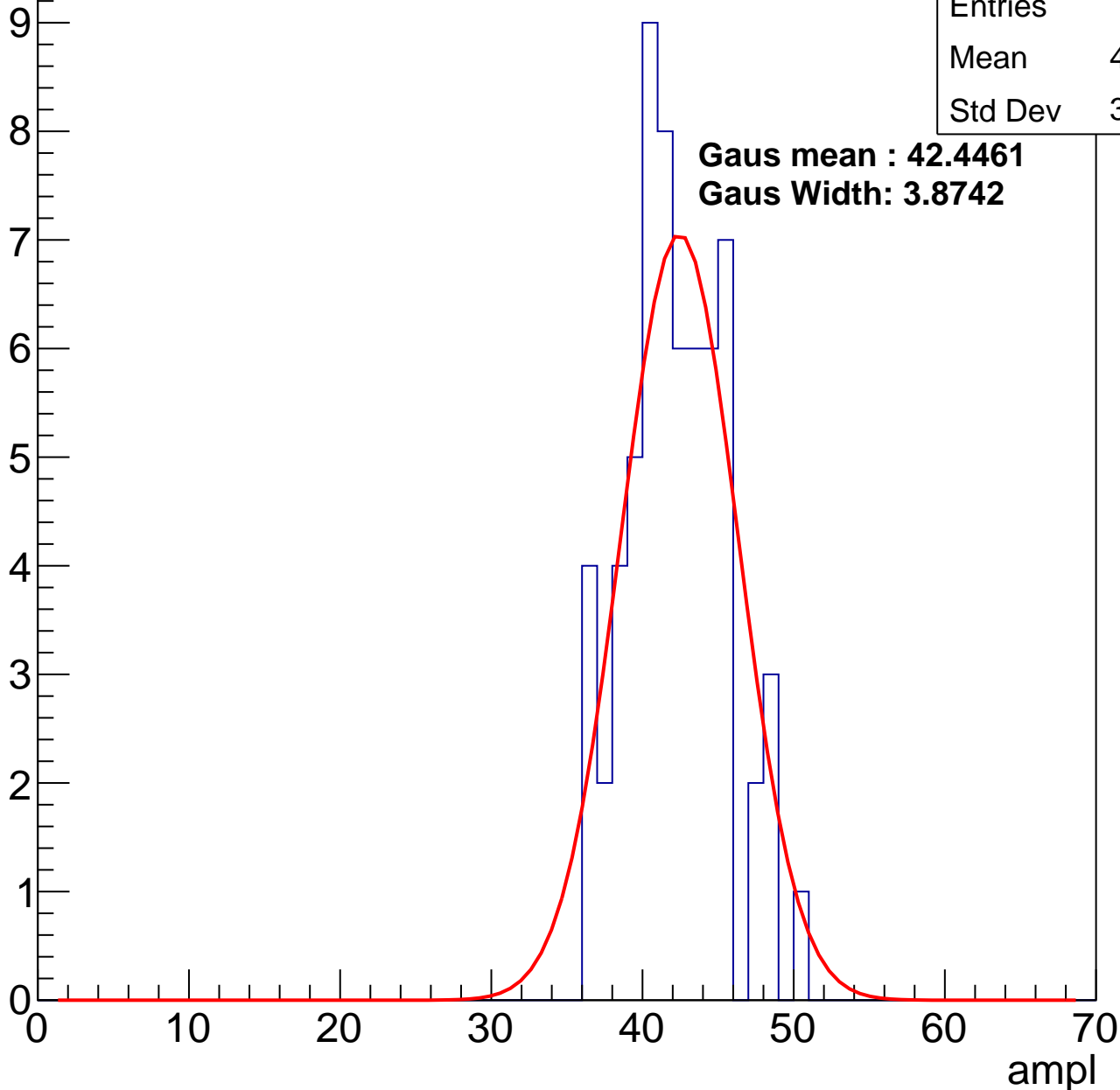
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	41.75
Std Dev	3.232

**Gaus mean : 42.4461**

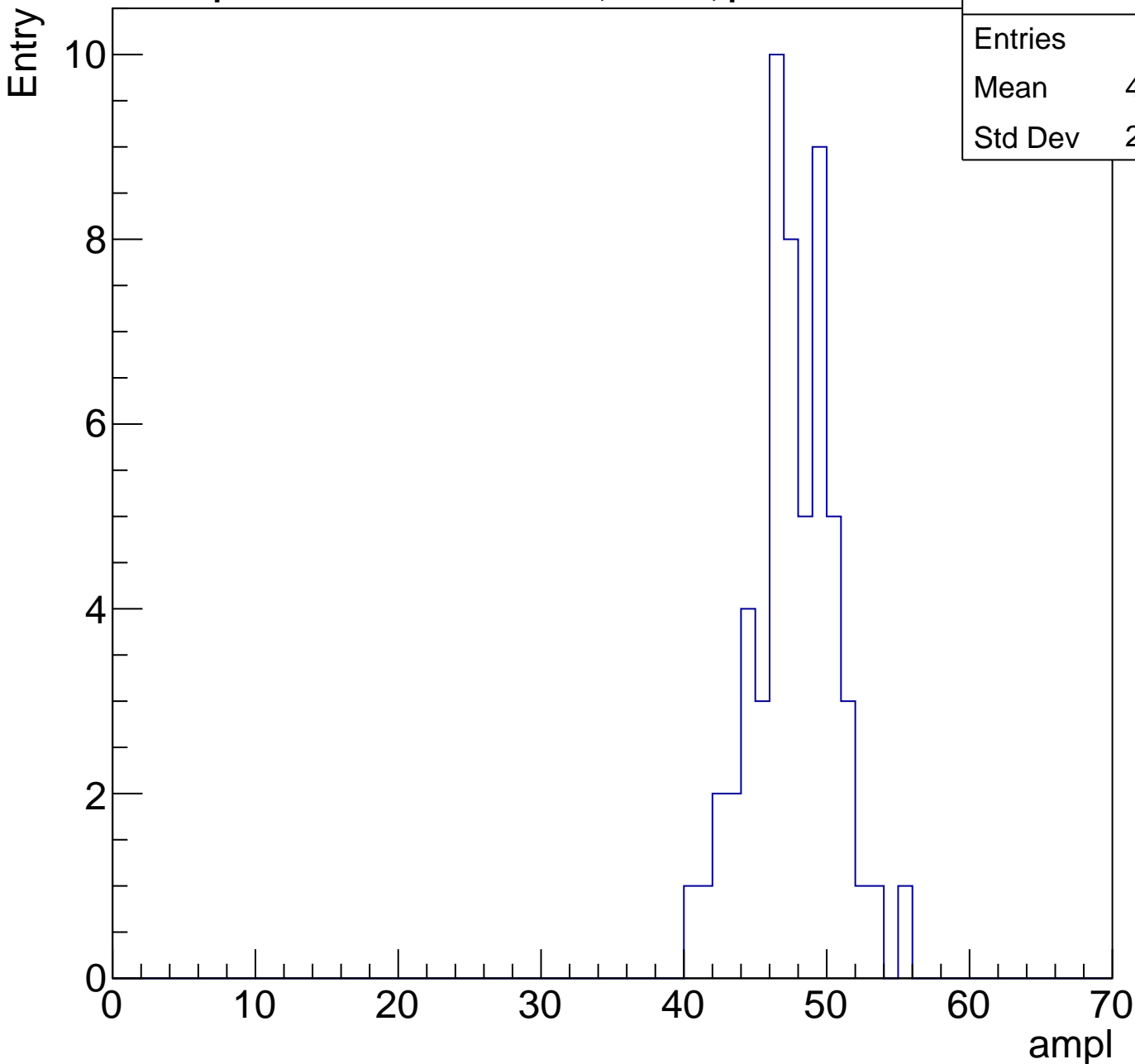
**Gaus Width: 3.8742**



# B1L002S, U3-ch21, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	56
Mean	47.18
Std Dev	2.934

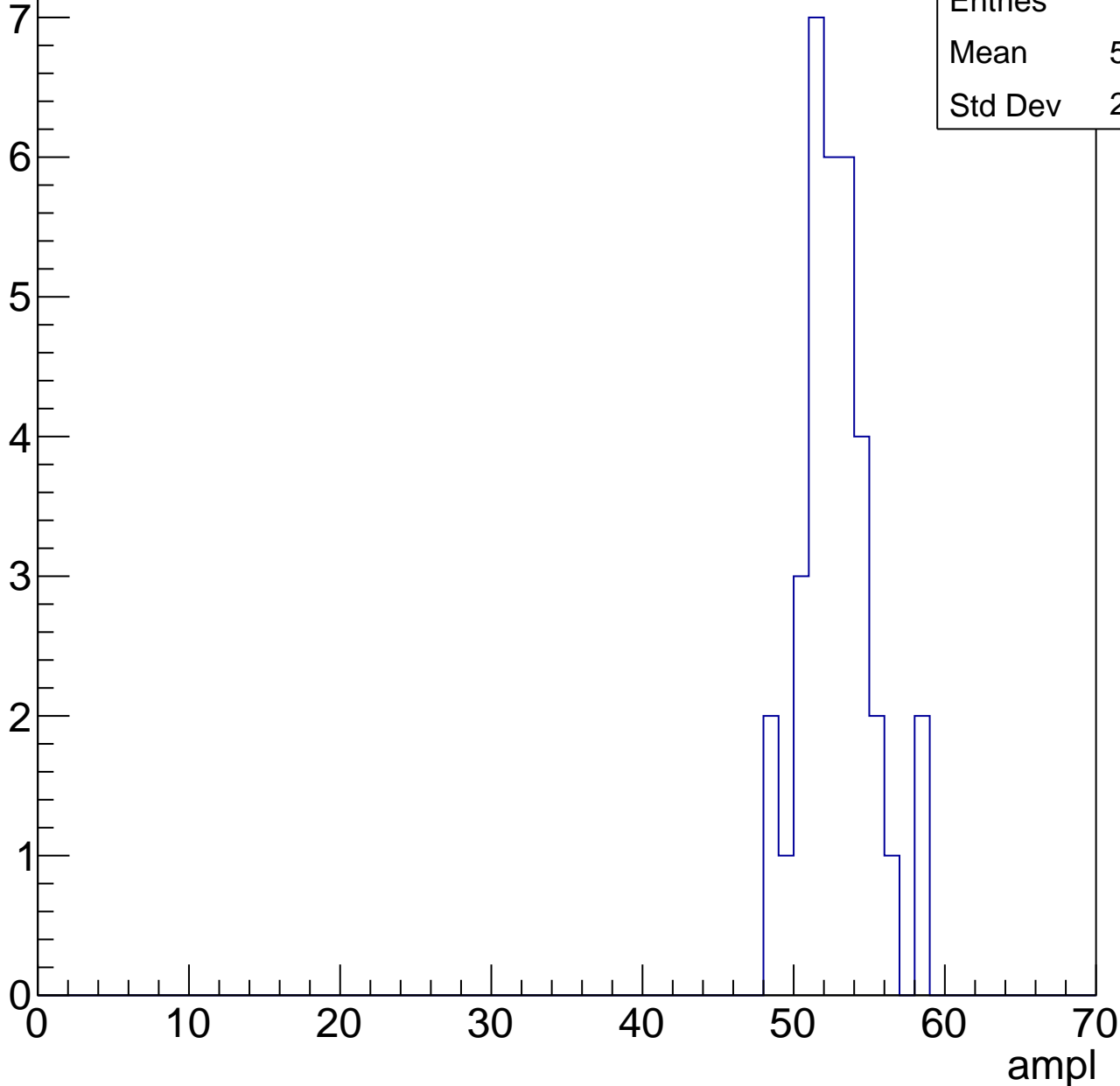


# B1L002S, U3-ch21, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	34
Mean	52.35
Std Dev	2.325



# B1L002S, U3-ch21, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	78
Mean	57.56
Std Dev	2.99

Entry

10

8

6

4

2

0

0

10

20

30

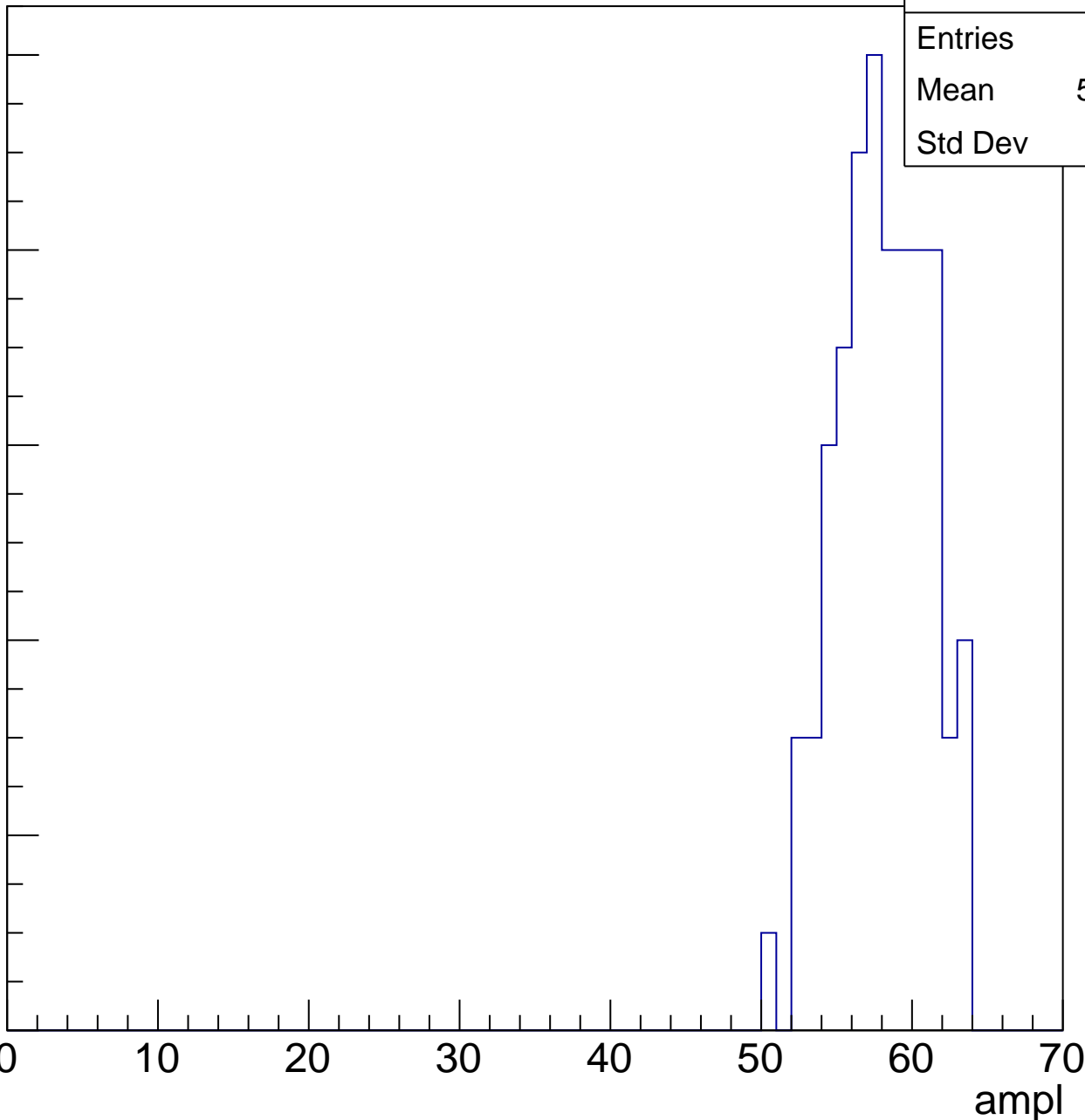
40

50

60

70

ampl

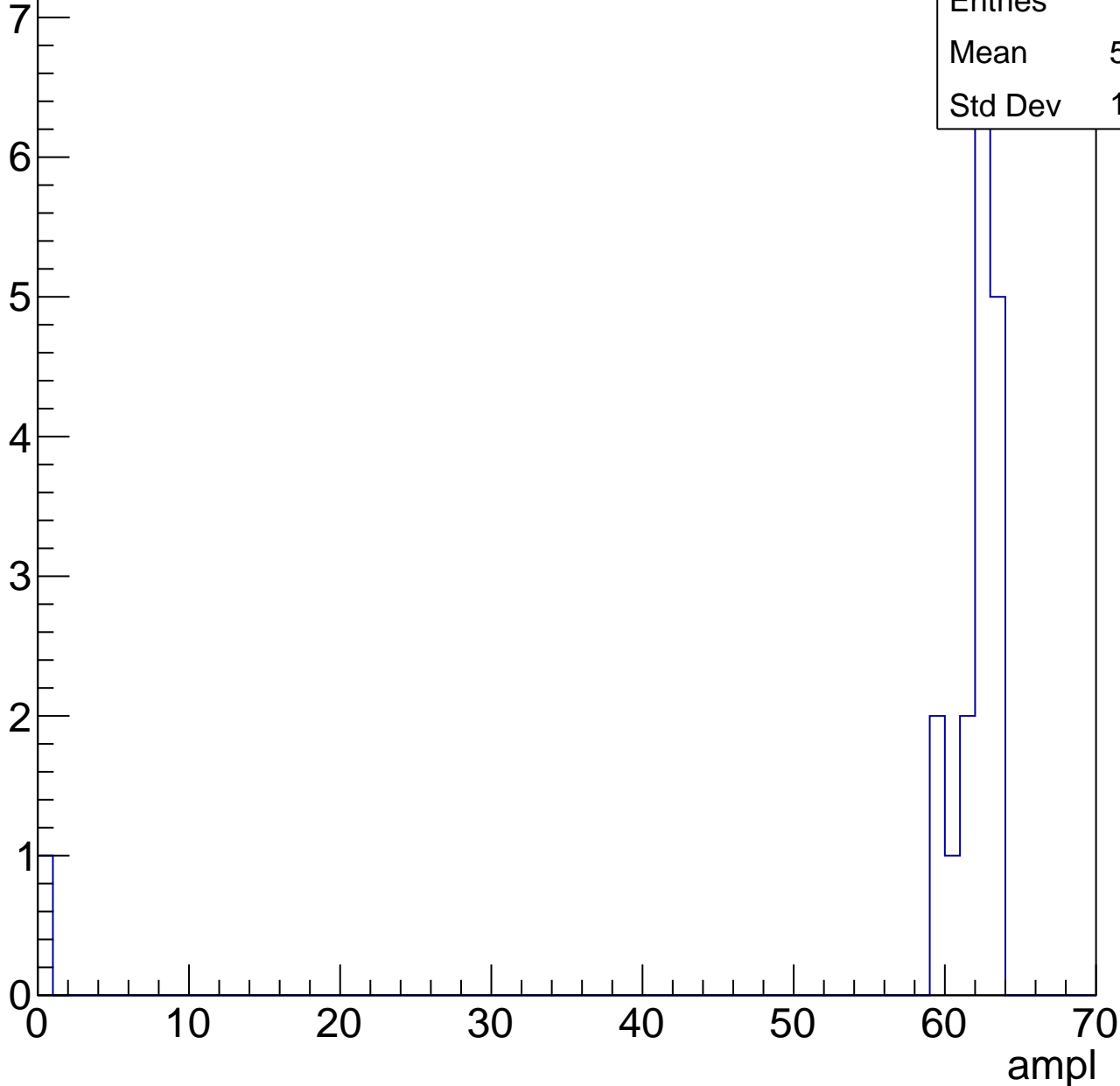


# B1L002S, U3-ch21, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	58.28
Std Dev	14.19

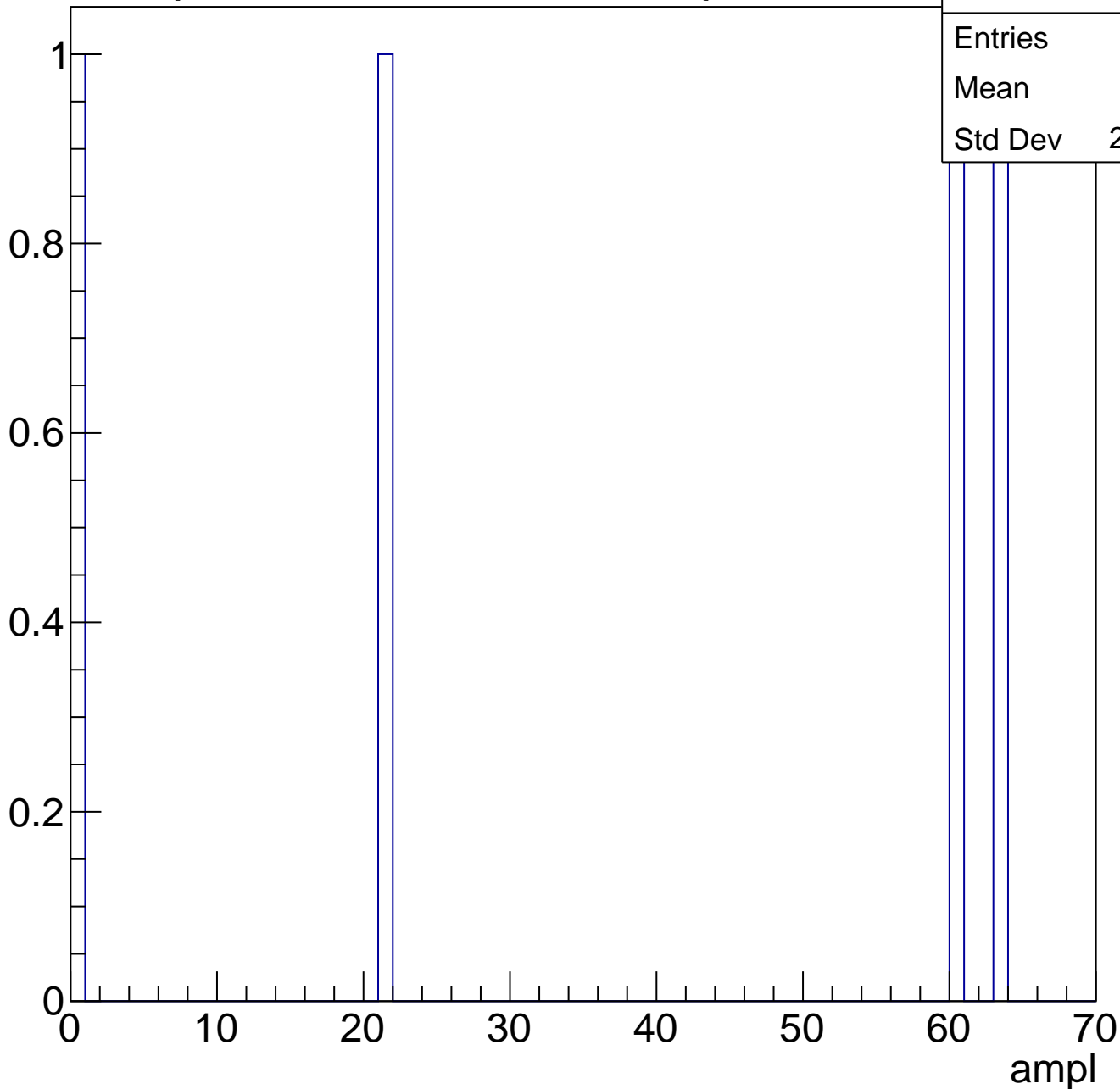




# B1L002S, U3-ch21, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch22, adc0

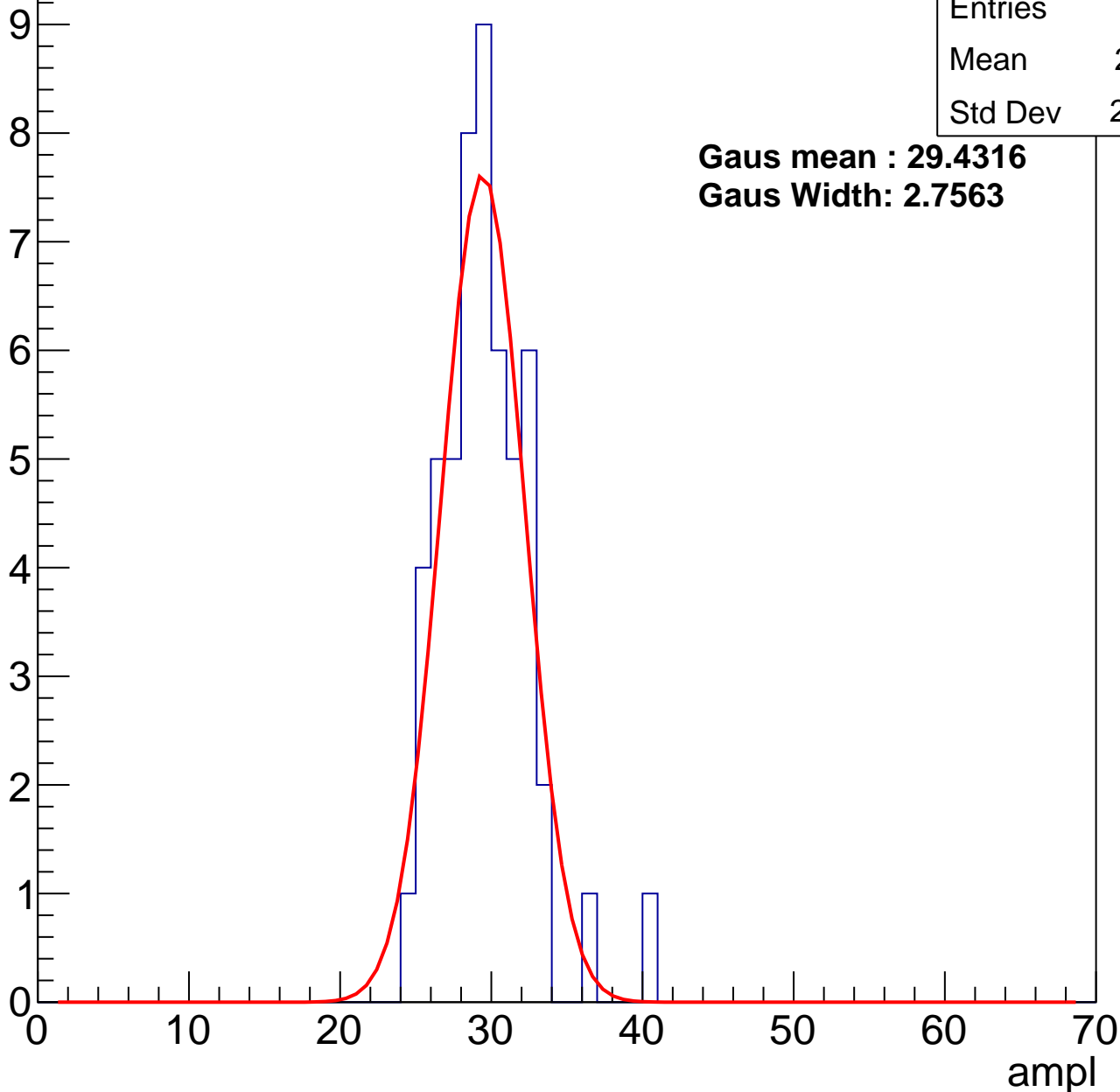
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	29.11
Std Dev	2.892

**Gaus mean : 29.4316**

**Gaus Width: 2.7563**



# B1L002S, U3-ch22, adc1

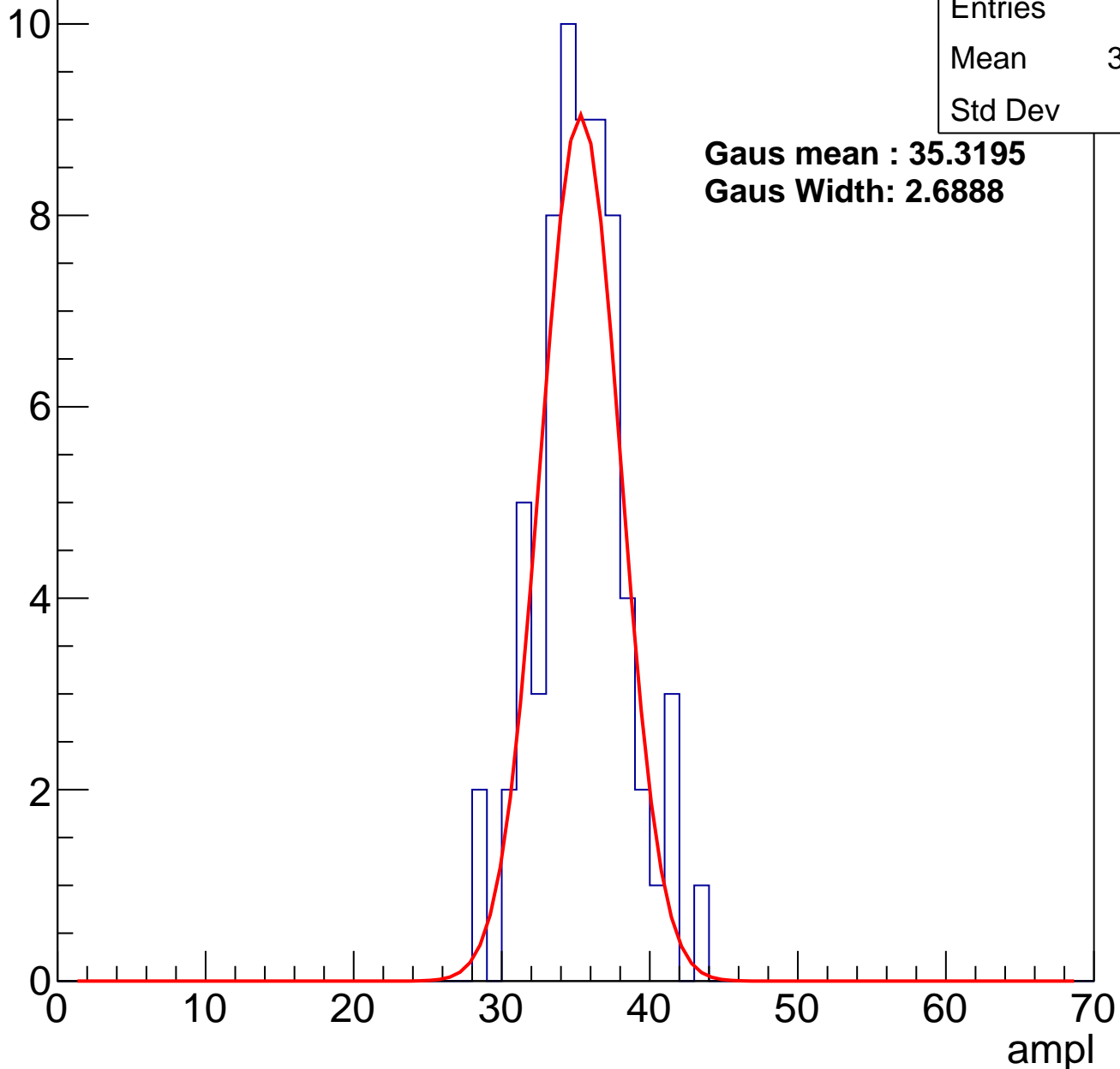
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	34.96
Std Dev	3

**Gaus mean : 35.3195**

**Gaus Width: 2.6888**

Entry



# B1L002S, U3-ch22, adc2

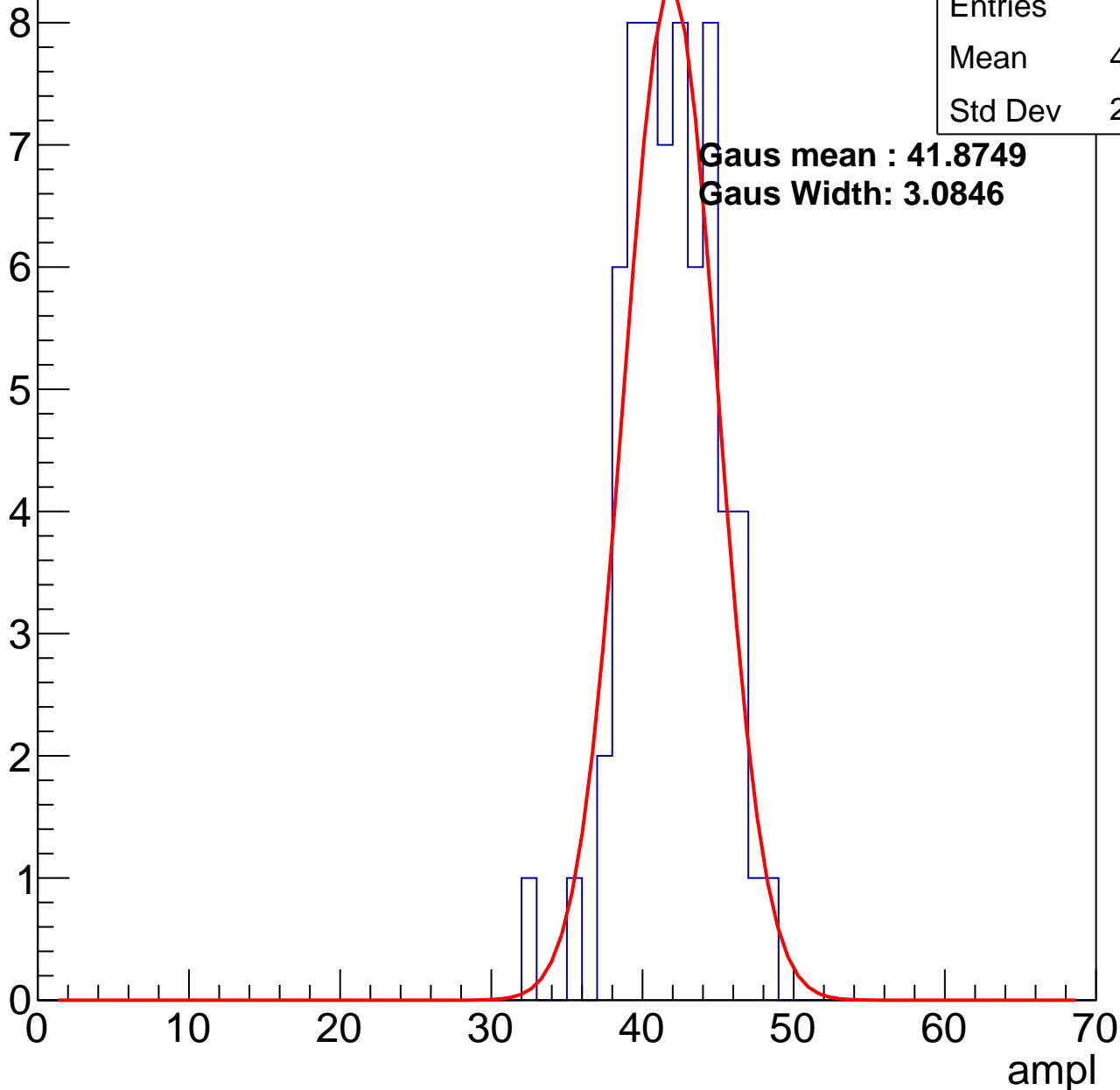
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	41.43
Std Dev	2.997

**Gaus mean : 41.8749**

**Gaus Width: 3.0846**

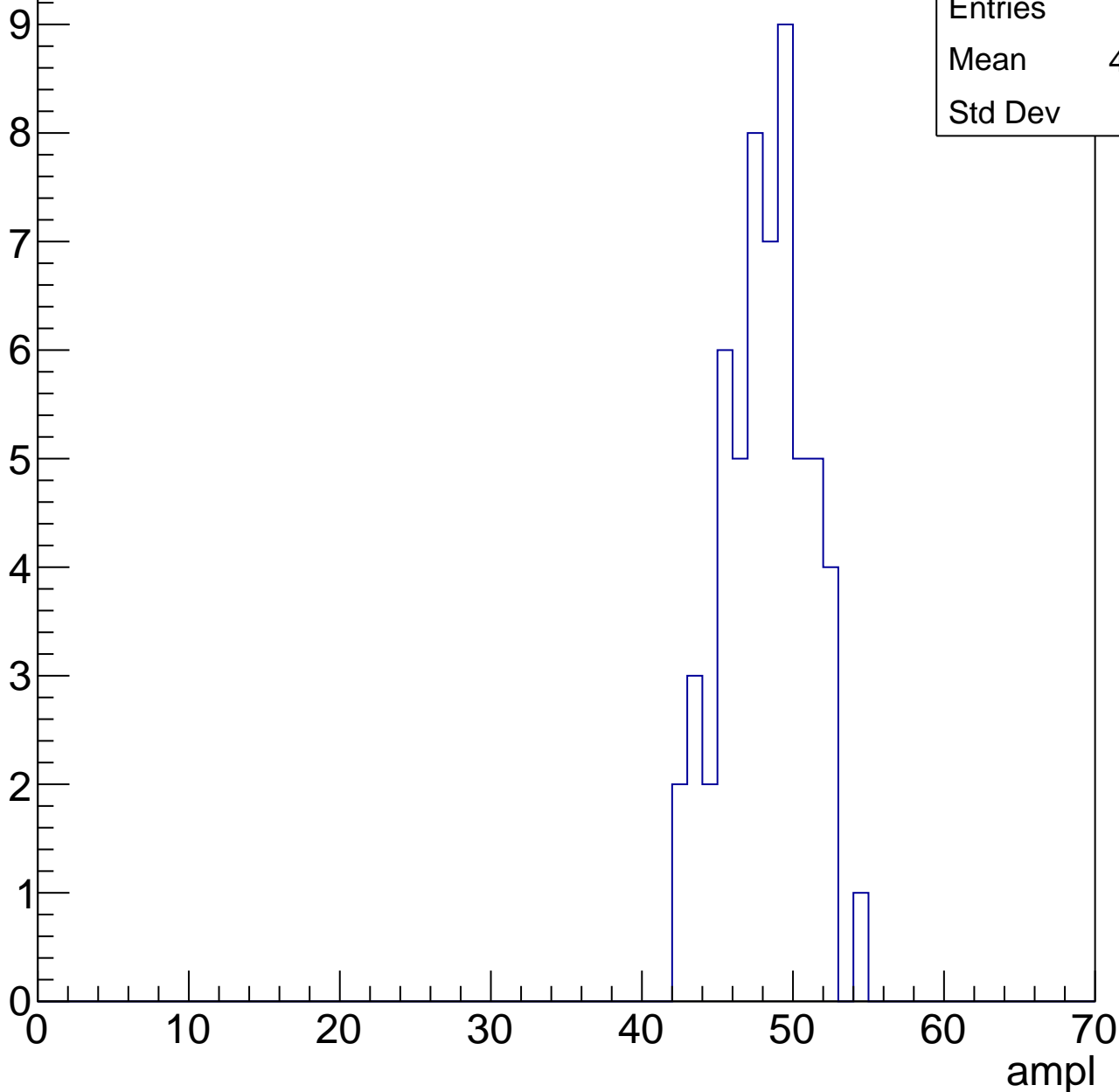


# B1L002S, U3-ch22, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

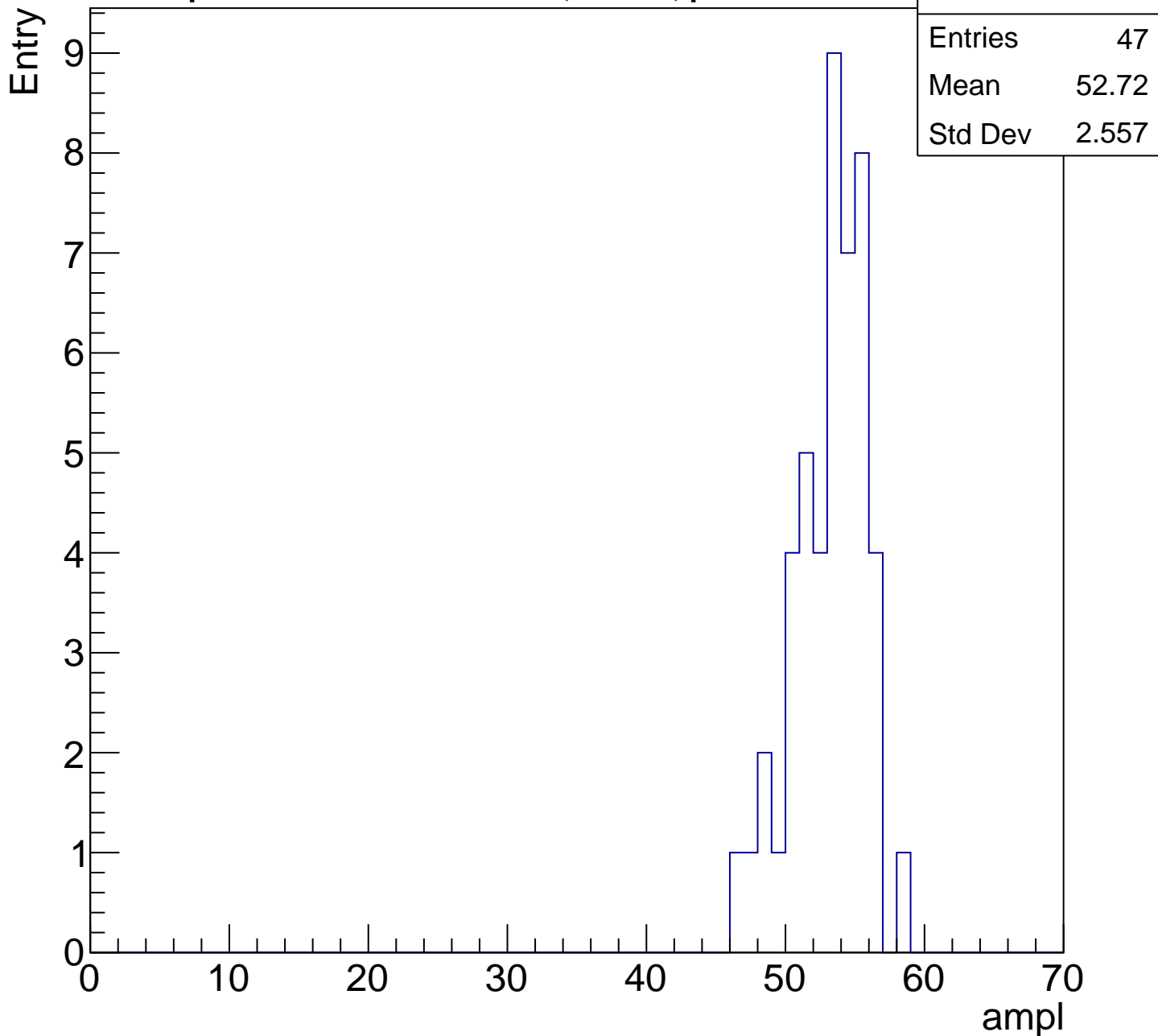
Entry

Entries	57
Mean	47.74
Std Dev	2.75



# B1L002S, U3-ch22, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

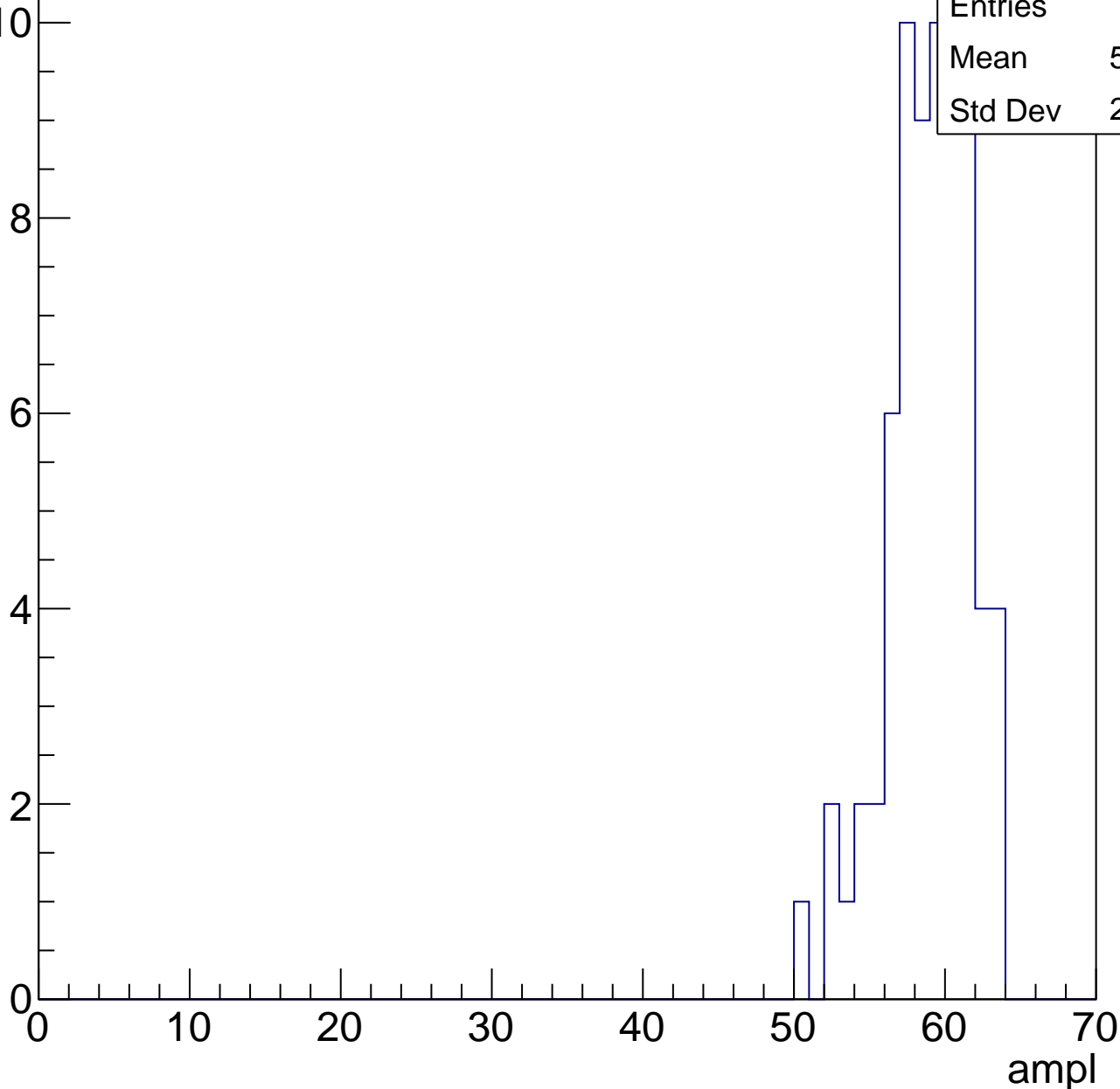


# B1L002S, U3-ch22, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	58.43
Std Dev	2.753

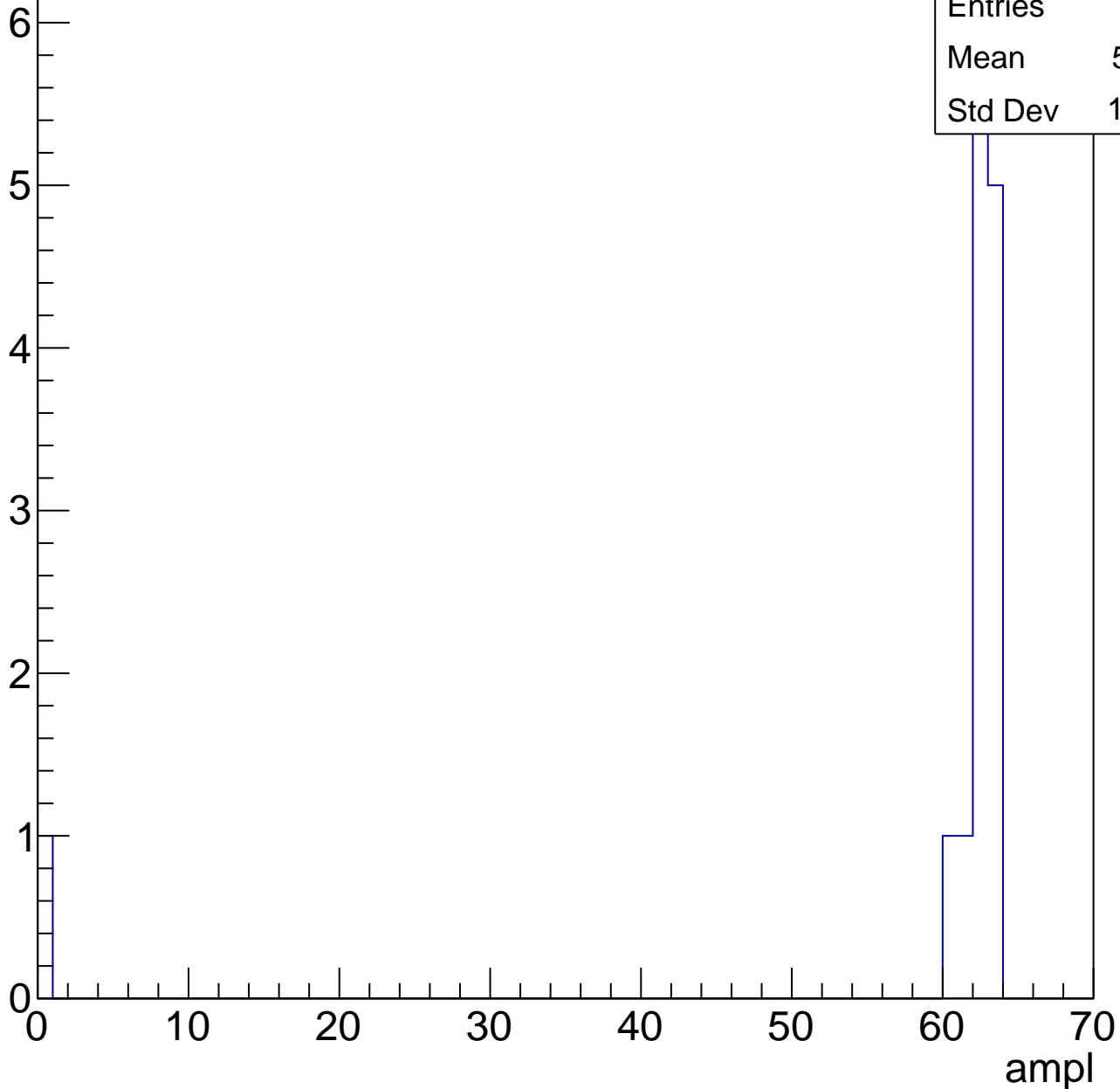


# B1L002S, U3-ch22, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	57.71
Std Dev	16.03





# B1L002S, U3-ch22, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	43
Std Dev	20

ampl

# B1L002S, U3-ch23, adc0

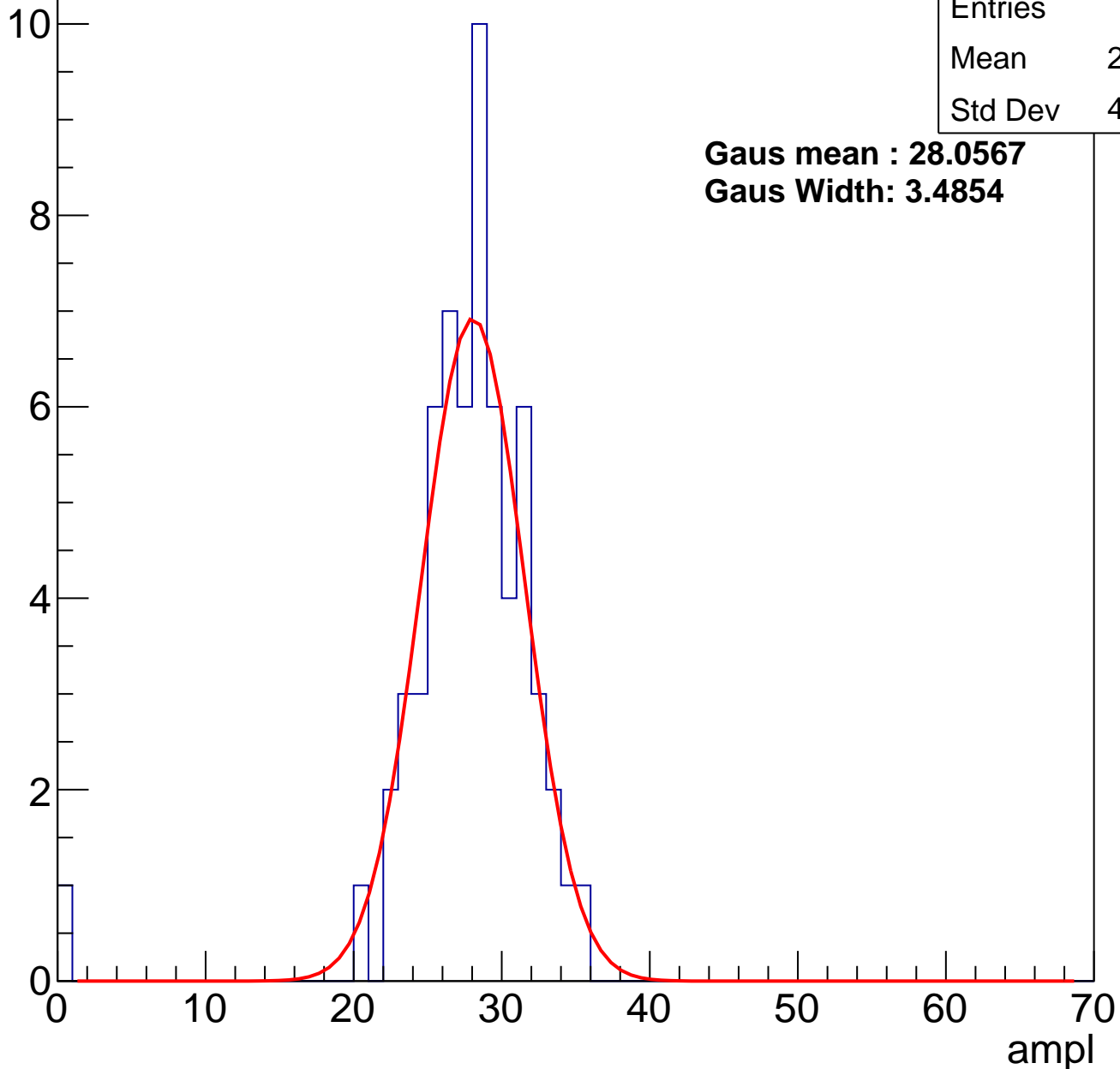
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	62
Mean	27.26
Std Dev	4.676

**Gaus mean : 28.0567**

**Gaus Width: 3.4854**

Entry



# B1L002S, U3-ch23, adc1

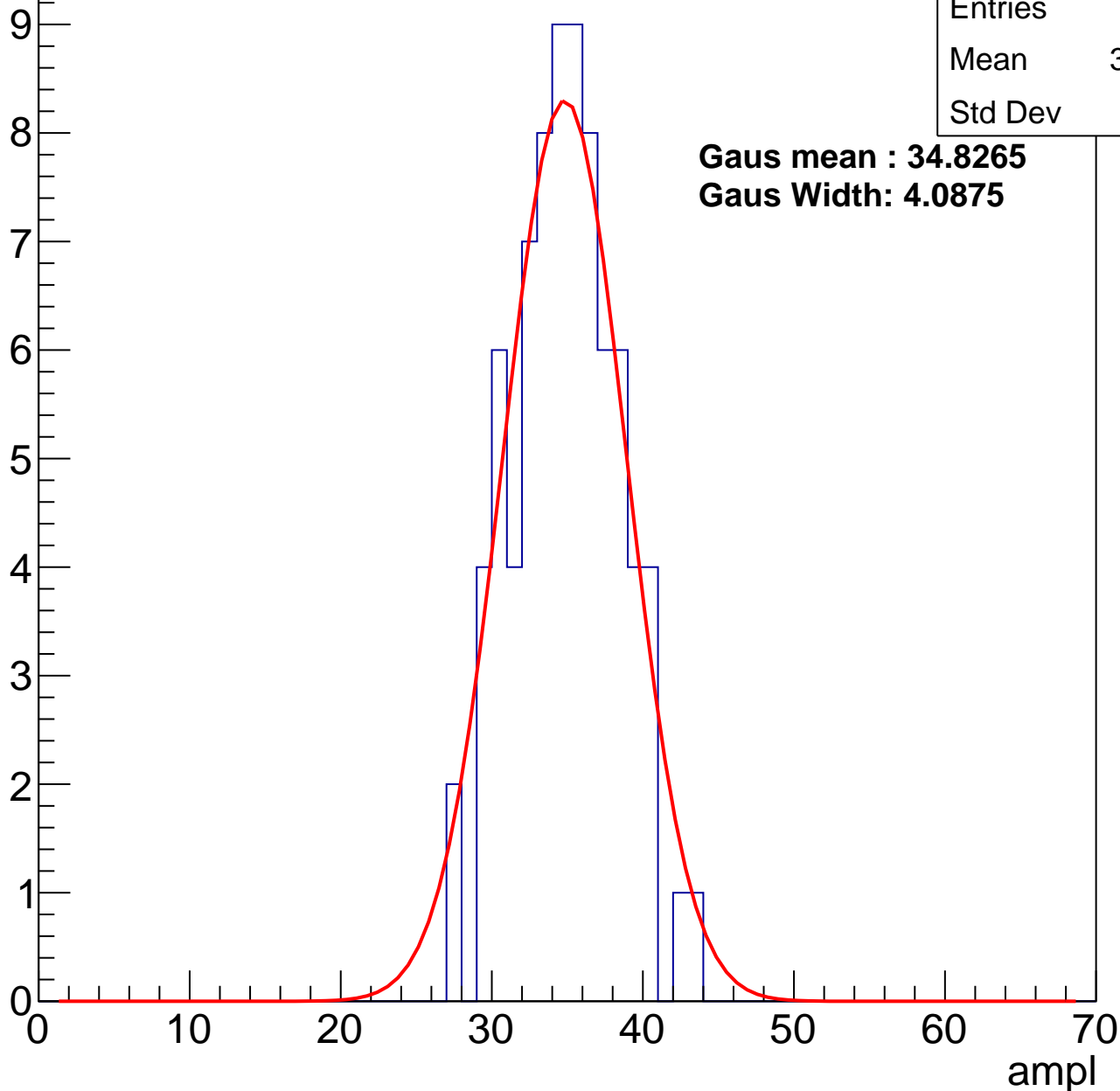
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	34.46
Std Dev	3.43

**Gaus mean : 34.8265**

**Gaus Width: 4.0875**



# B1L002S, U3-ch23, adc2

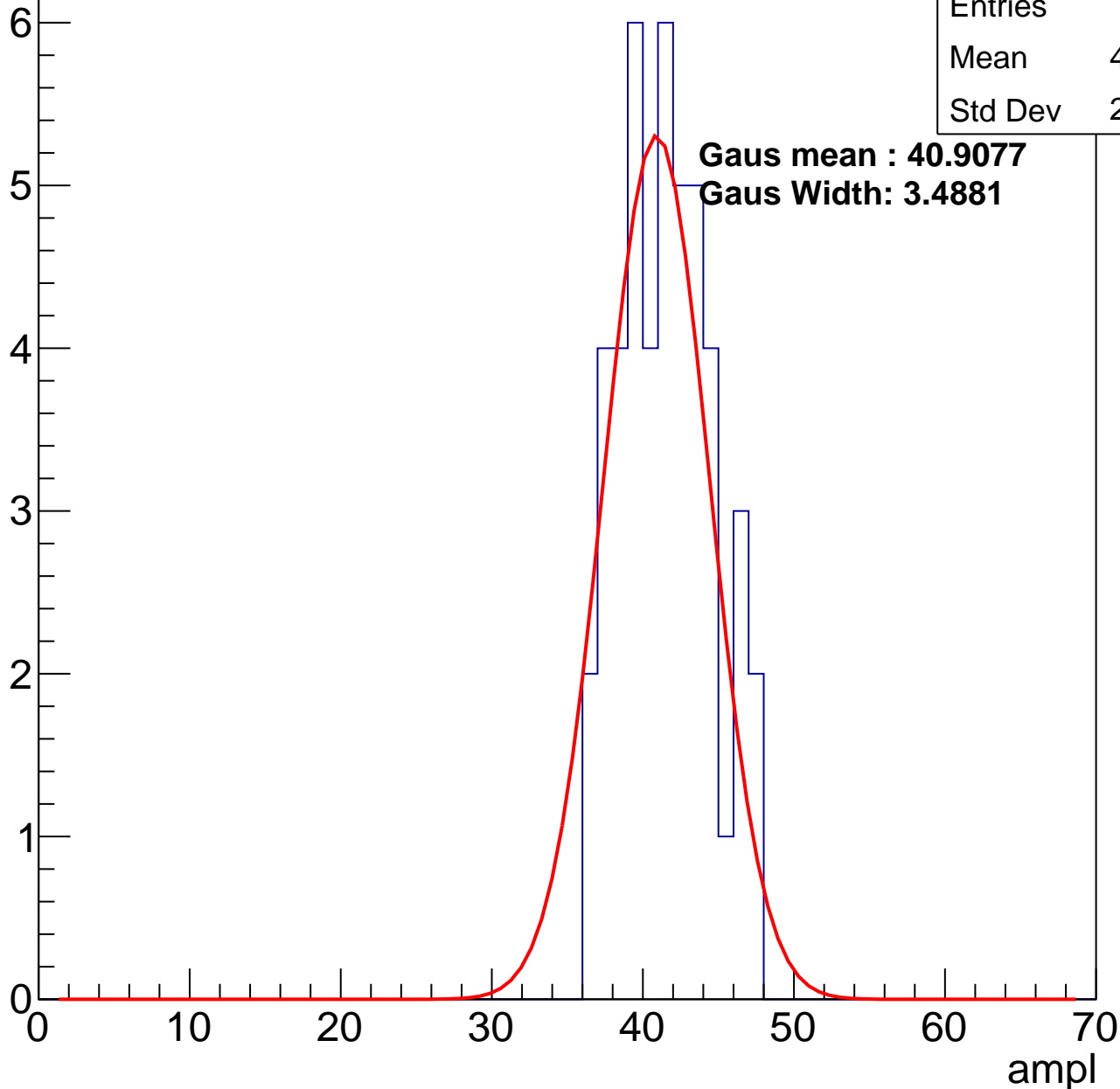
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	41.09
Std Dev	2.955

**Gaus mean : 40.9077**

**Gaus Width: 3.4881**

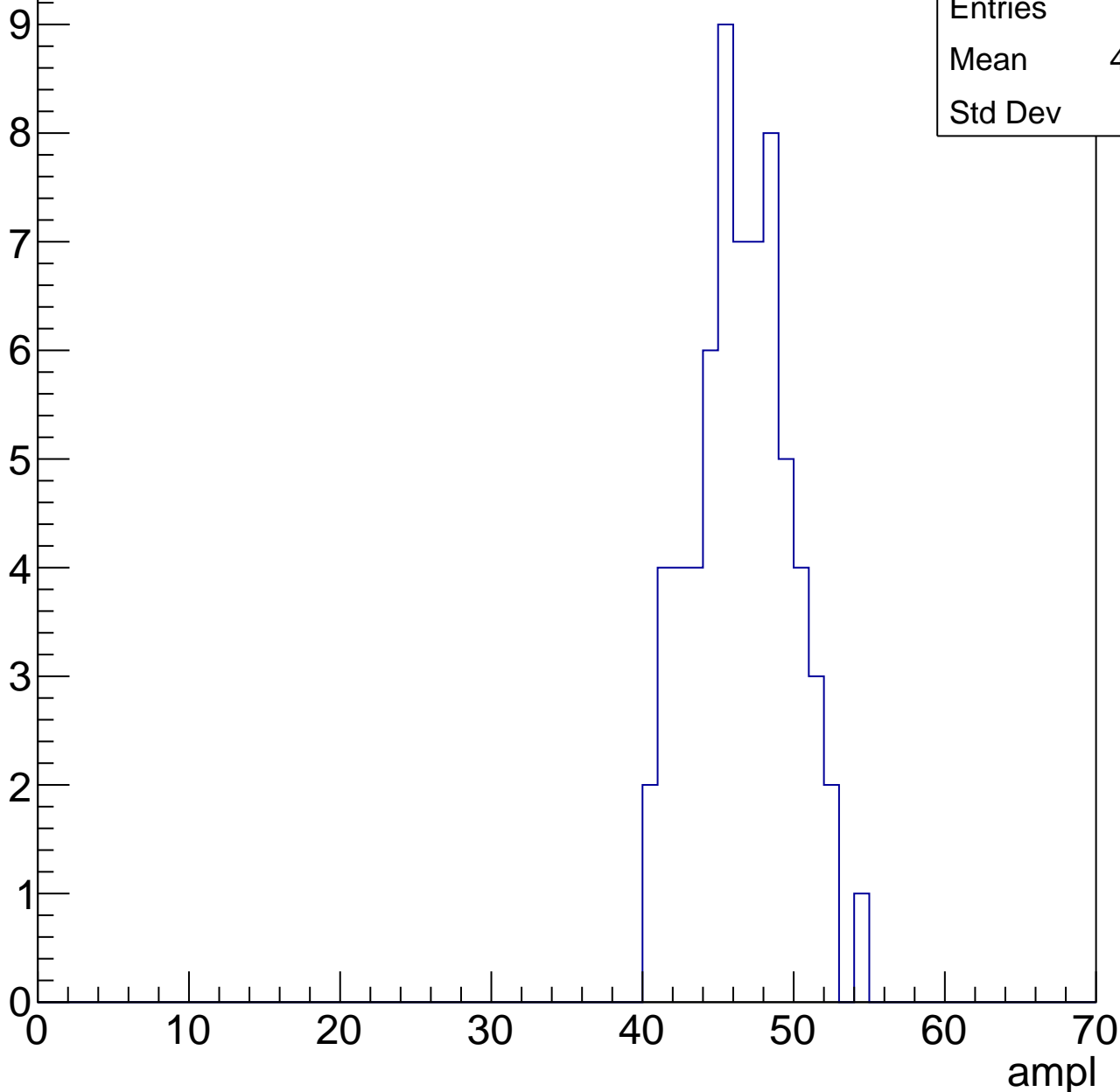


# B1L002S, U3-ch23, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

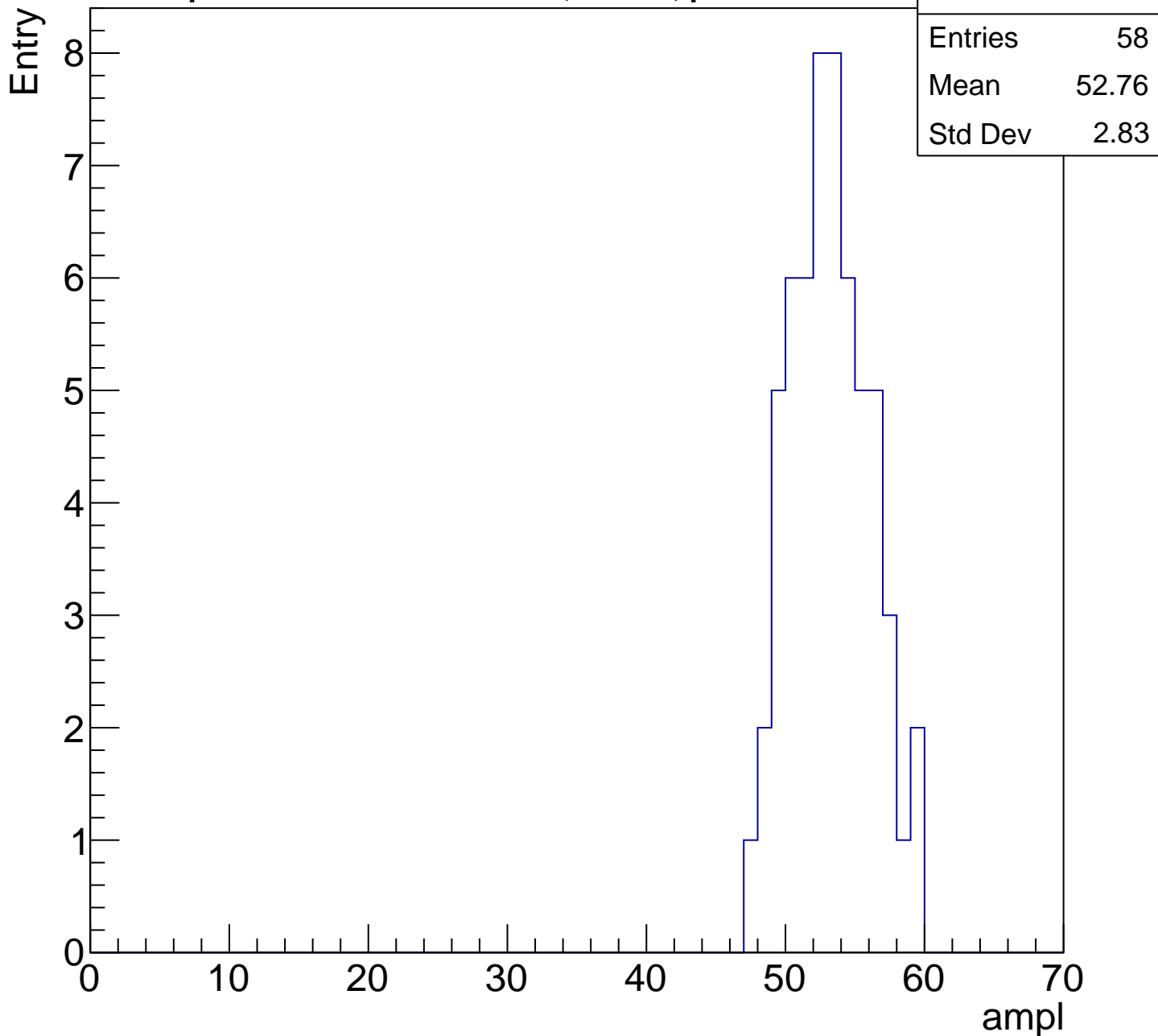
Entry

Entries	66
Mean	46.12
Std Dev	3.17



# B1L002S, U3-ch23, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

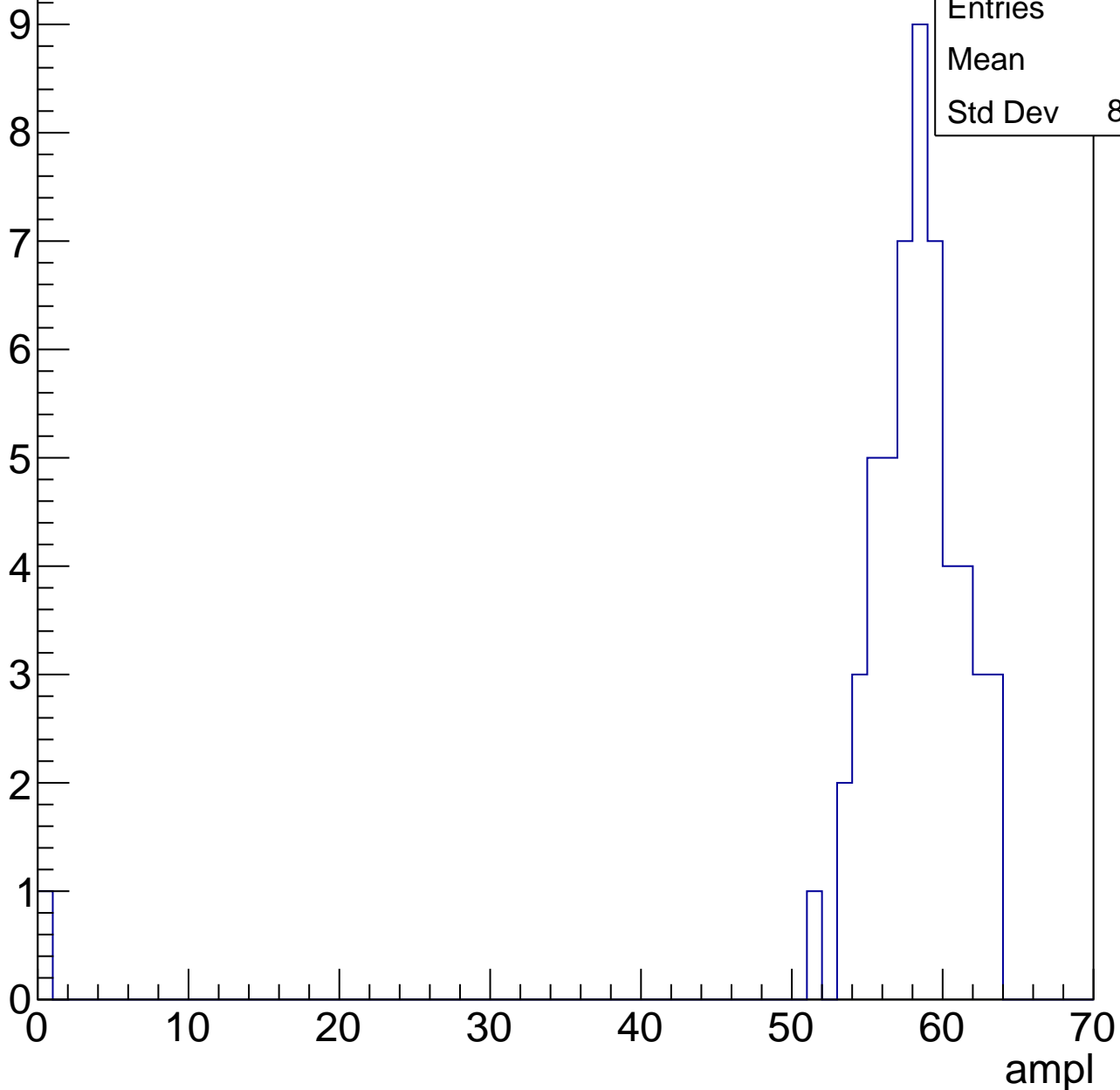


# B1L002S, U3-ch23, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	56.8
Std Dev	8.263

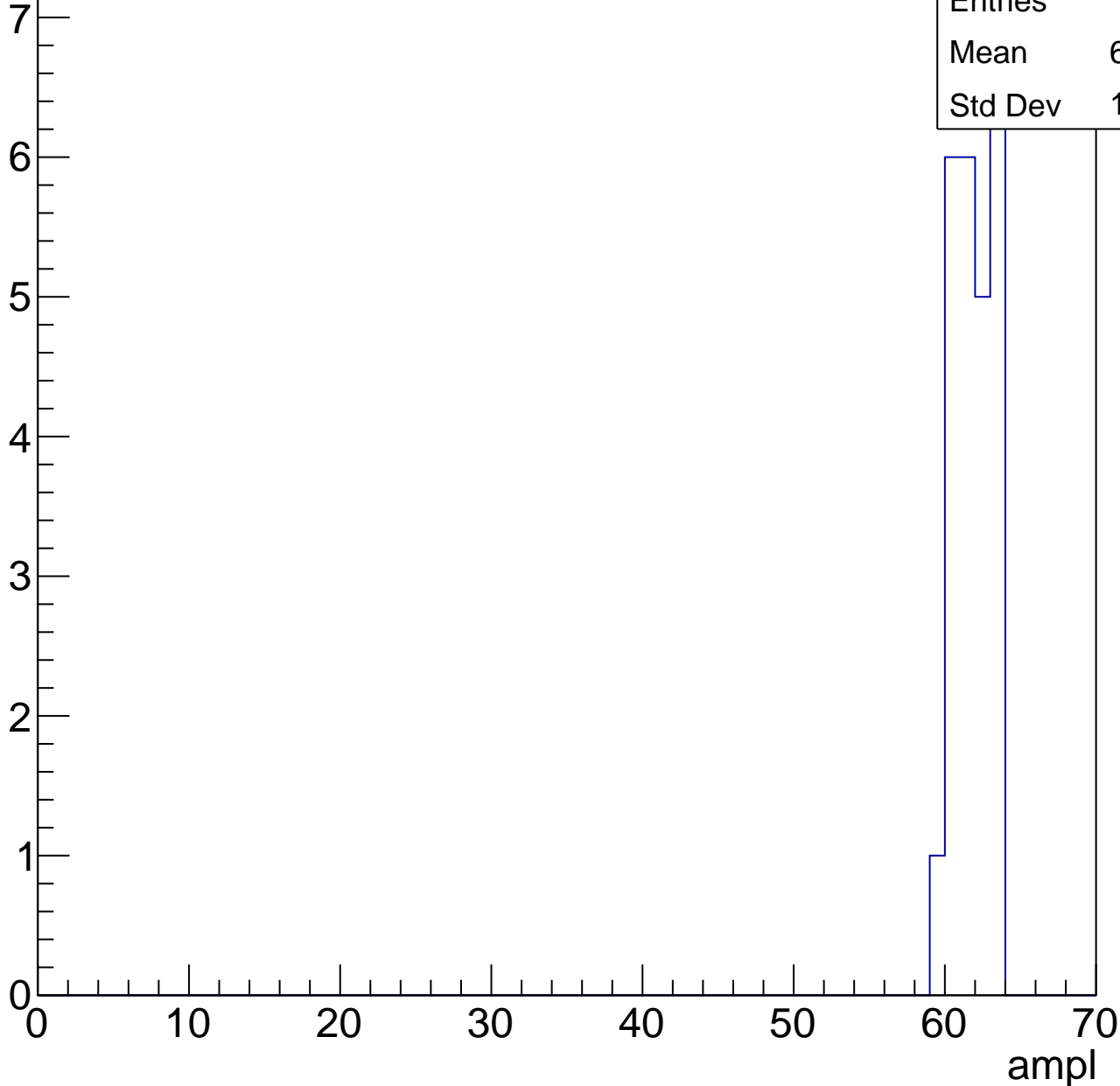


# B1L002S, U3-ch23, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.44
Std Dev	1.235

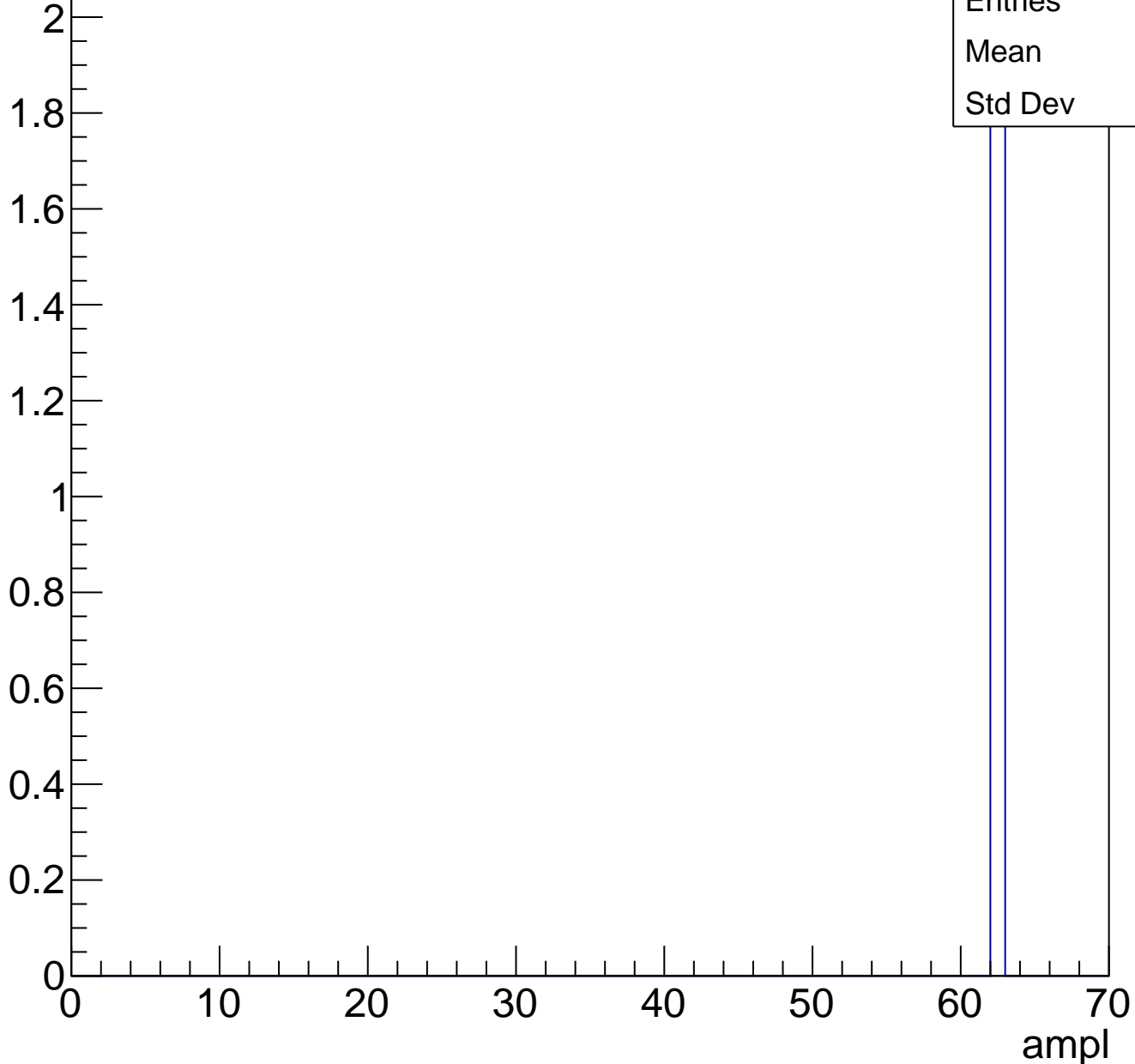




# B1L002S, U3-ch23, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch24, adc0

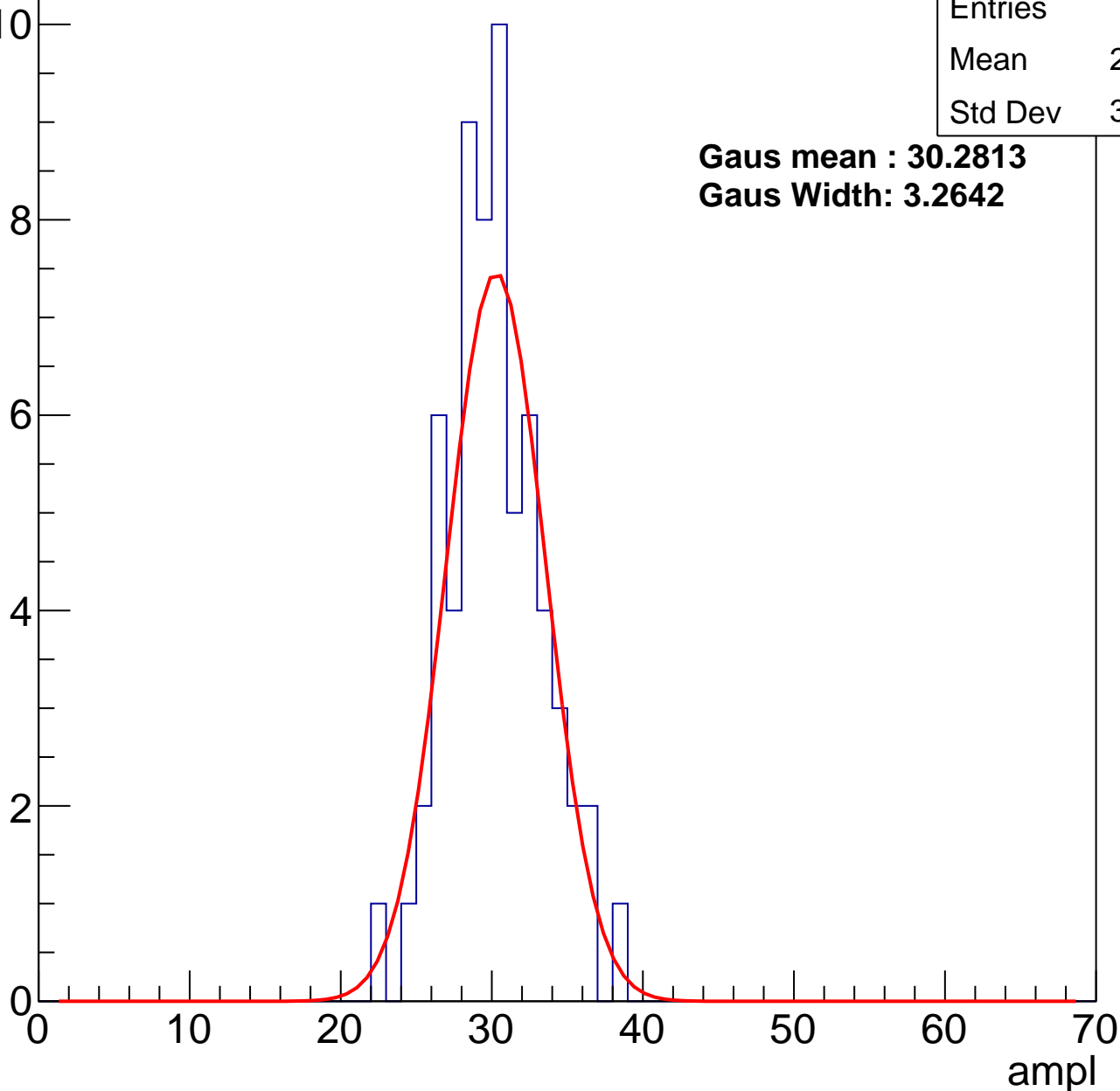
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	29.77
Std Dev	3.116

**Gaus mean : 30.2813**

**Gaus Width: 3.2642**



# B1L002S, U3-ch24, adc1

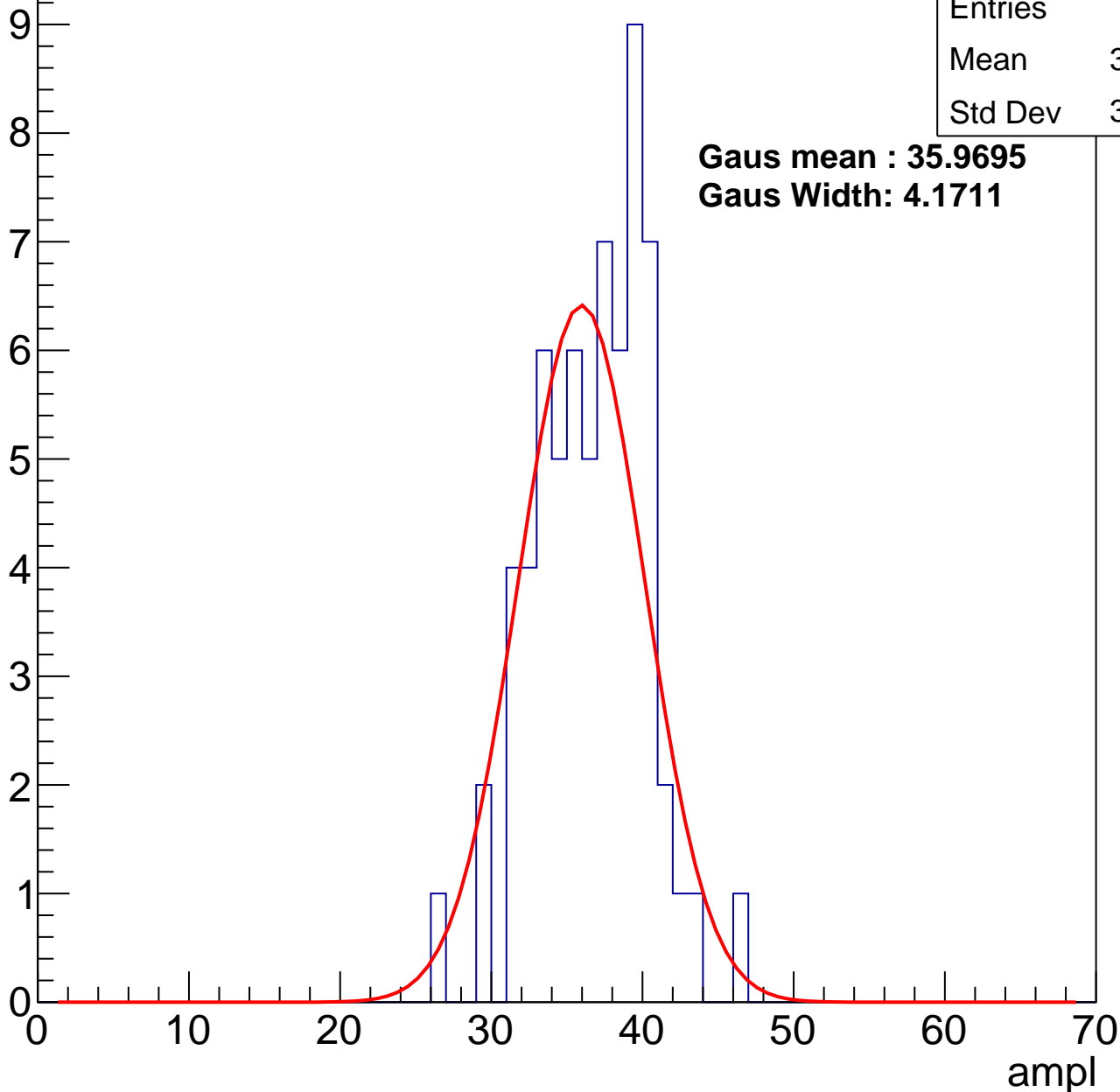
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	36.19
Std Dev	3.662

**Gaus mean : 35.9695**

**Gaus Width: 4.1711**



# B1L002S, U3-ch24, adc2

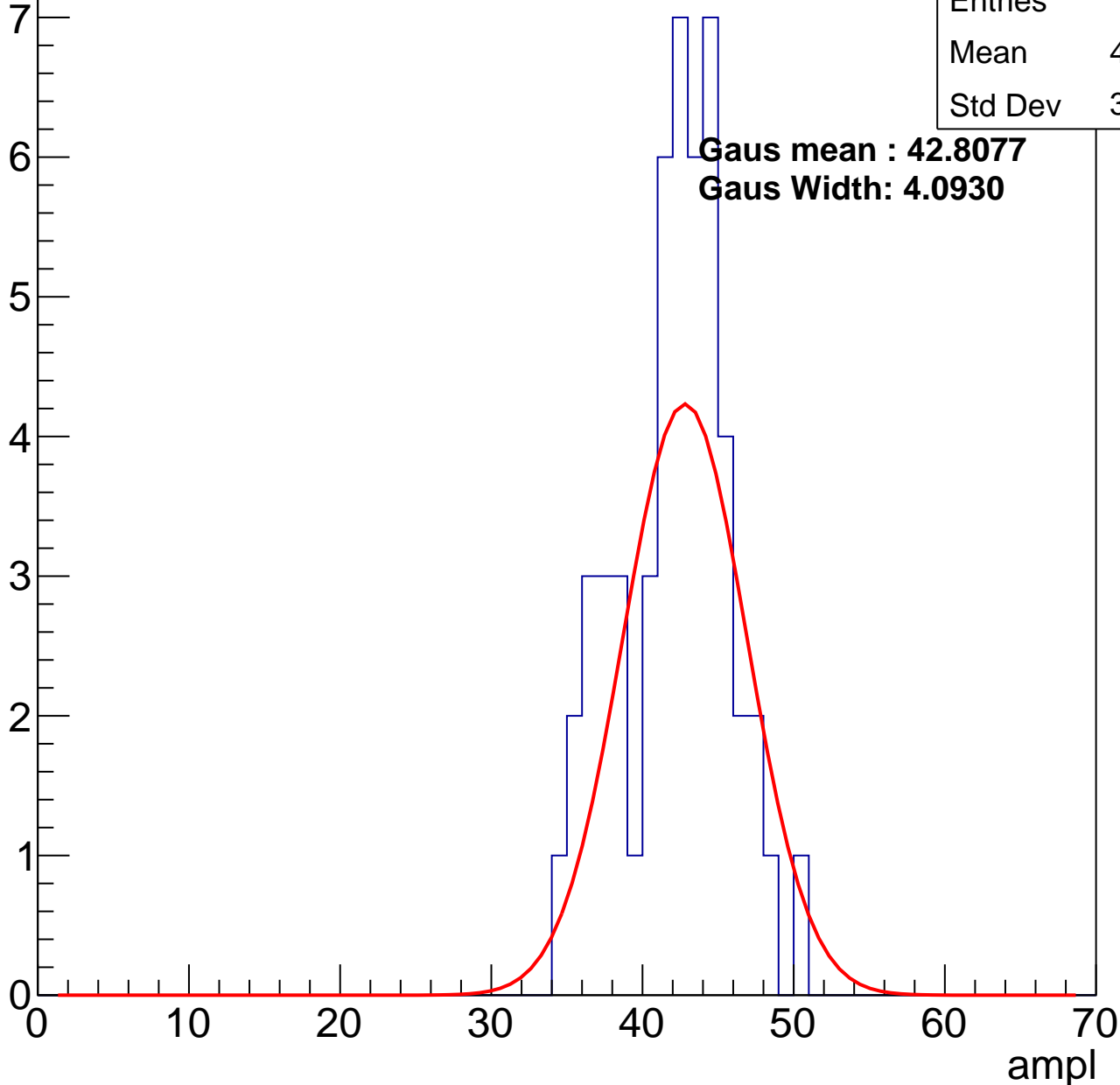
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	41.65
Std Dev	3.578

**Gaus mean : 42.8077**

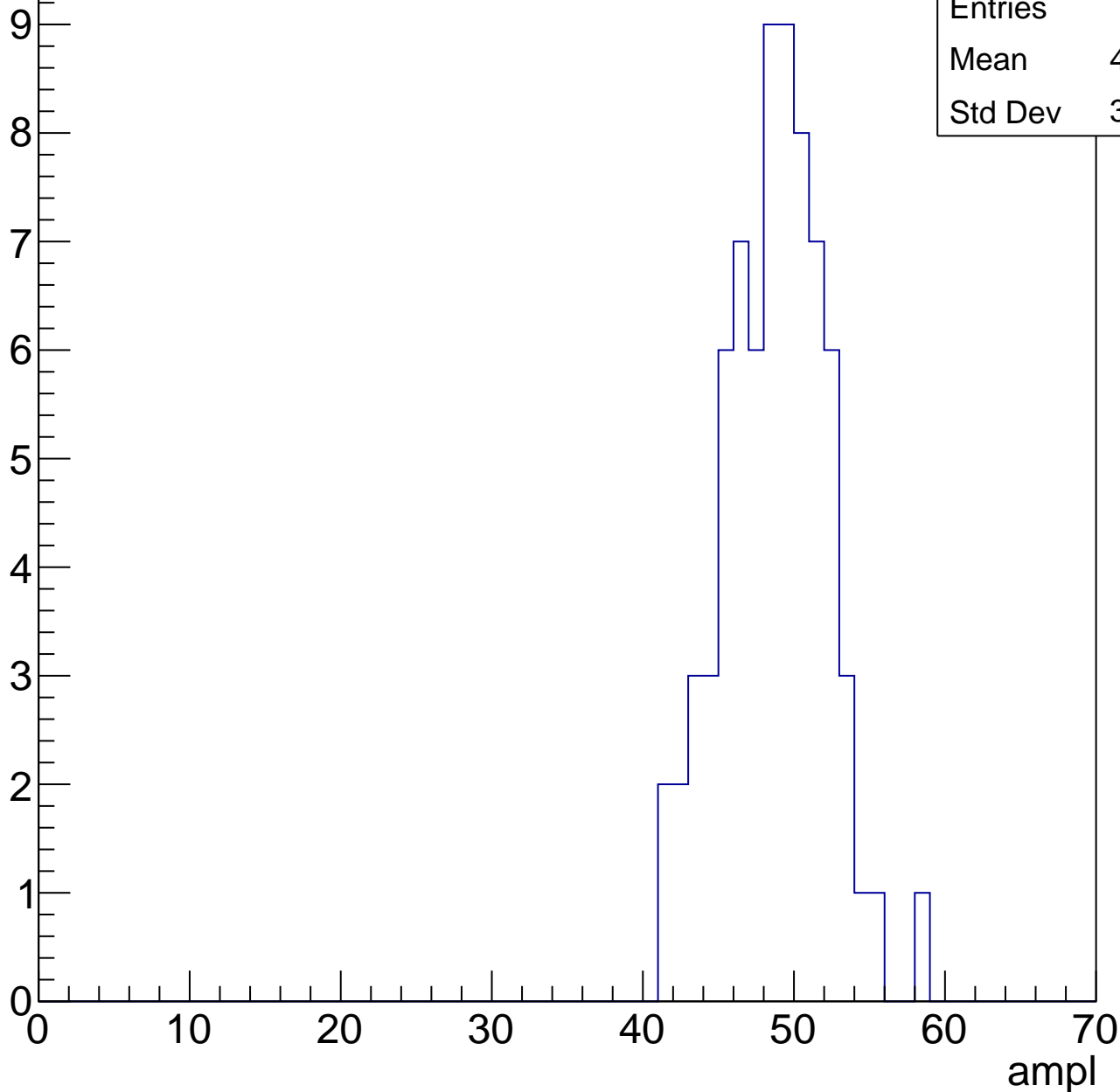
**Gaus Width: 4.0930**



# B1L002S, U3-ch24, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



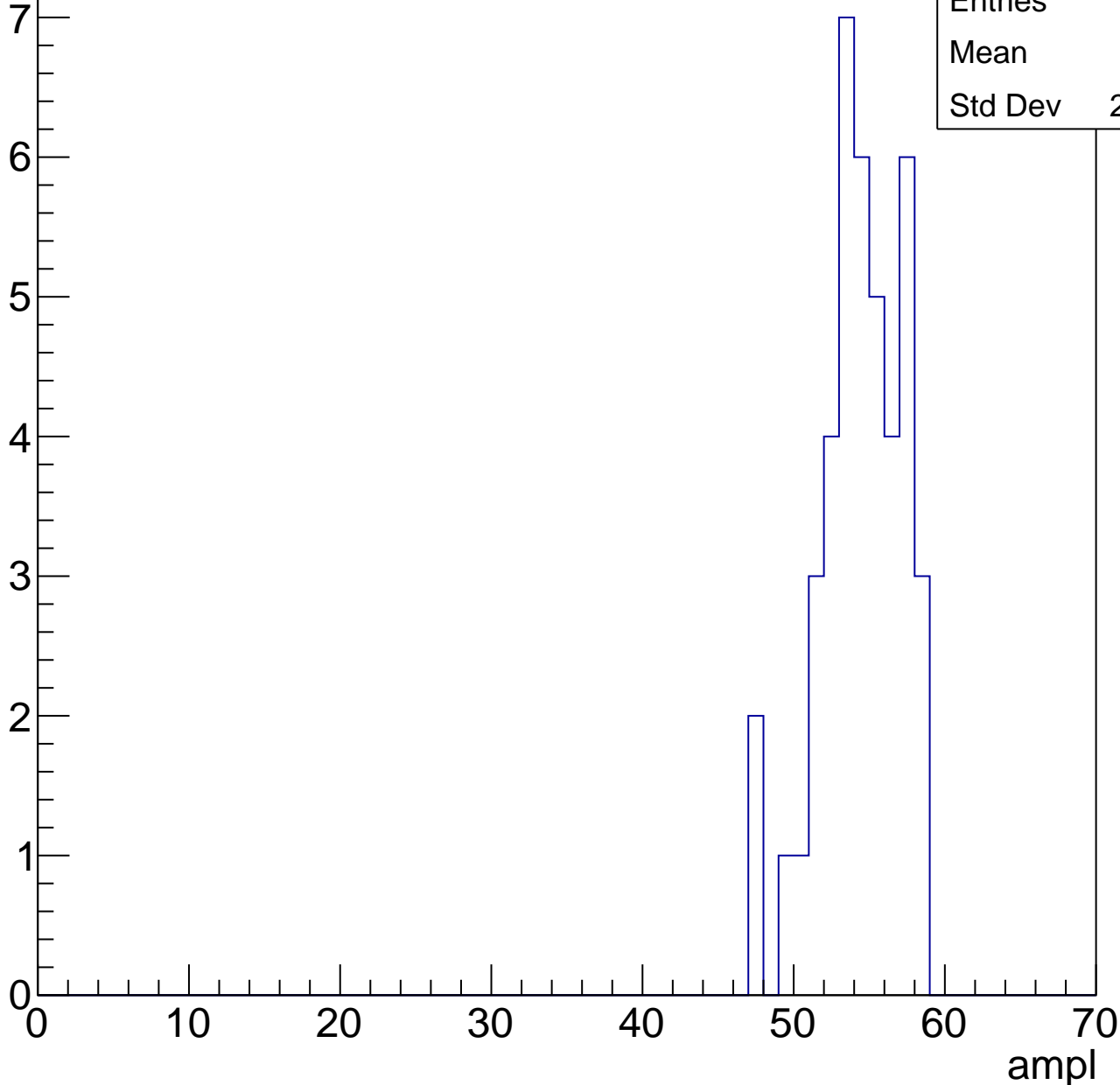
Entries	74
Mean	48.23
Std Dev	3.363

# B1L002S, U3-ch24, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	53.9
Std Dev	2.724

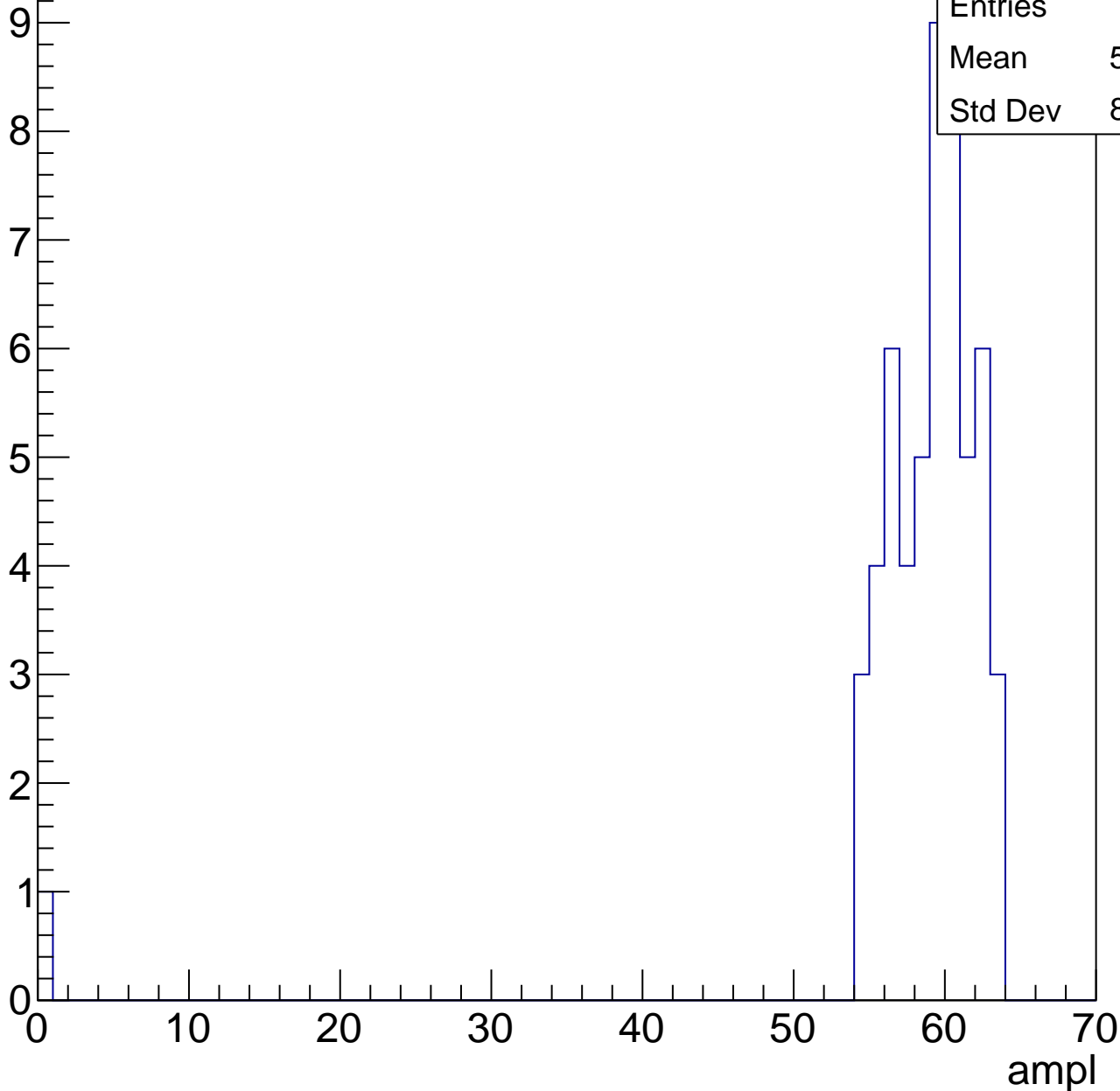


# B1L002S, U3-ch24, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.65
Std Dev	8.307

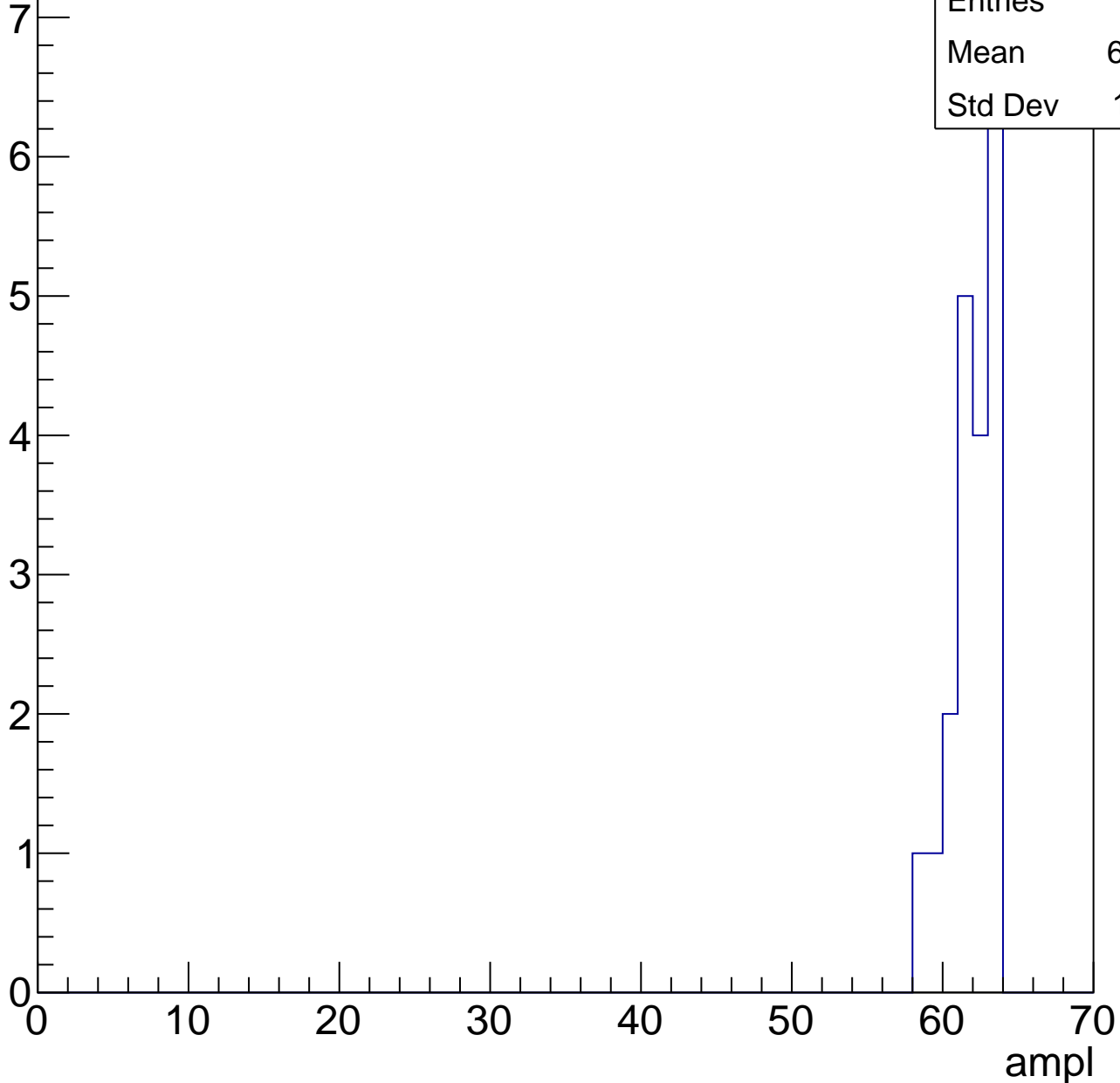


# B1L002S, U3-ch24, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	61.55
Std Dev	1.431





# B1L002S, U3-ch24, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U3-ch25, adc0

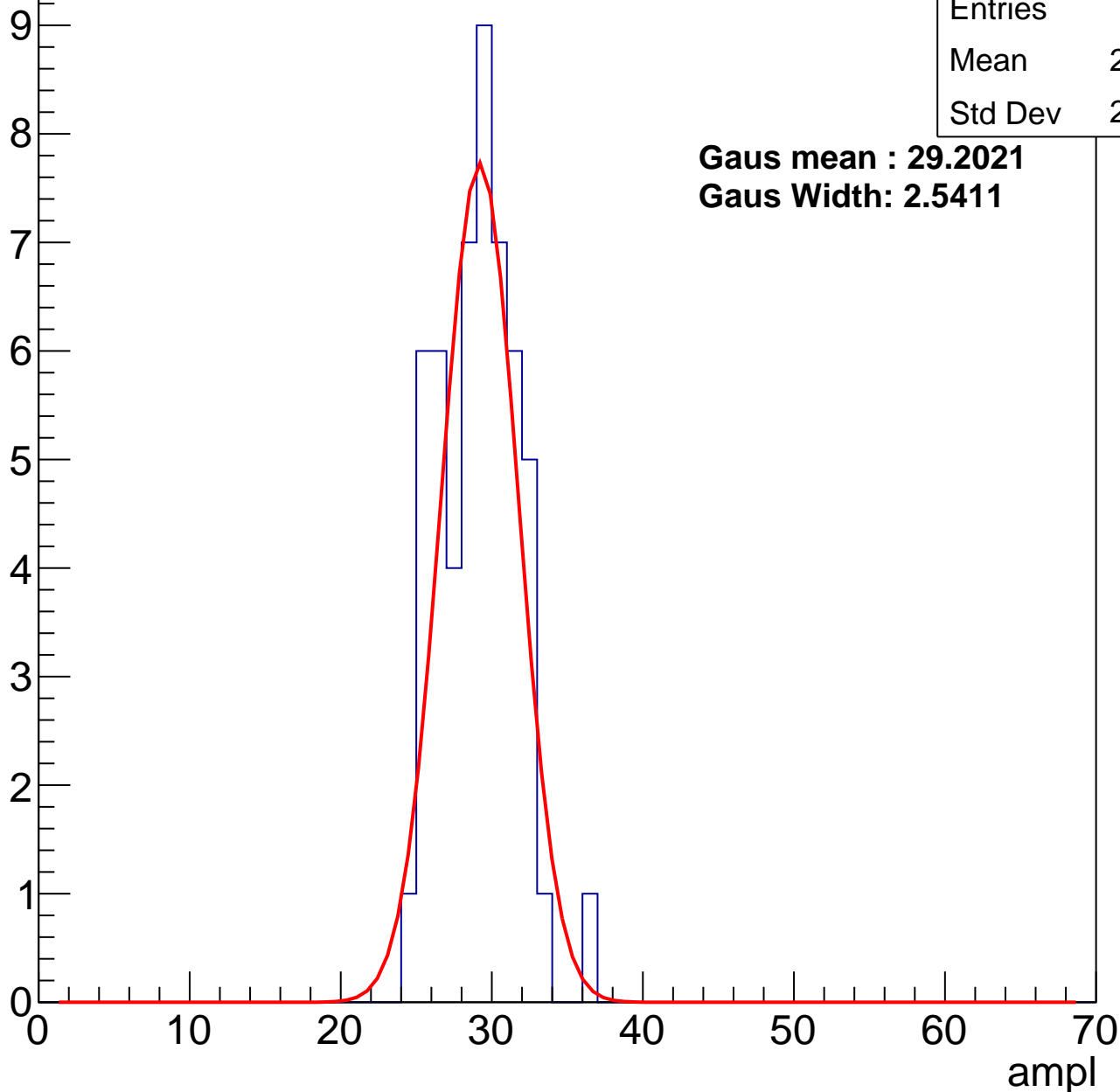
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.68
Std Dev	2.509

**Gaus mean : 29.2021**

**Gaus Width: 2.5411**



# B1L002S, U3-ch25, adc1

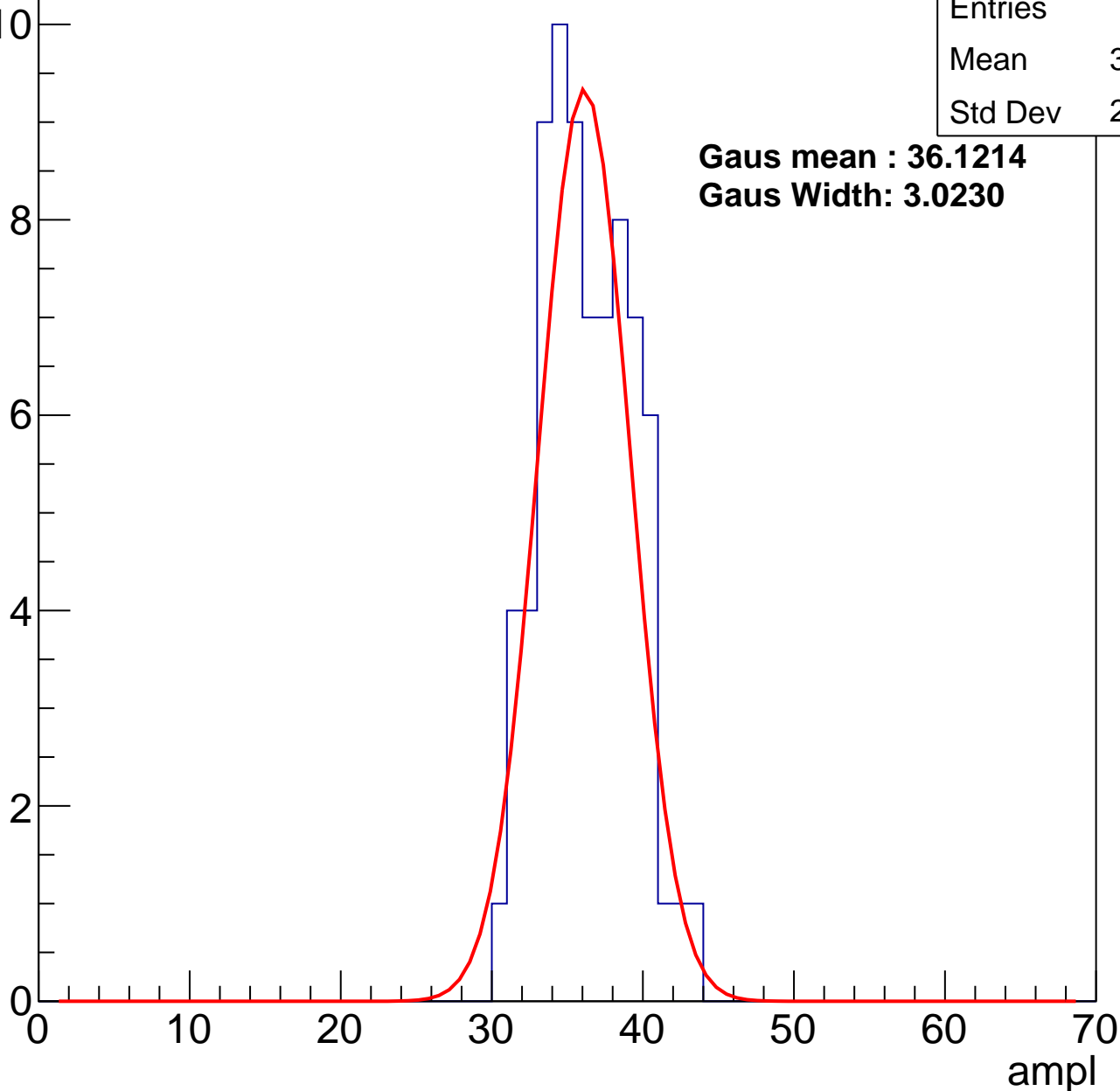
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	35.84
Std Dev	2.912

**Gaus mean : 36.1214**

**Gaus Width: 3.0230**



# B1L002S, U3-ch25, adc2

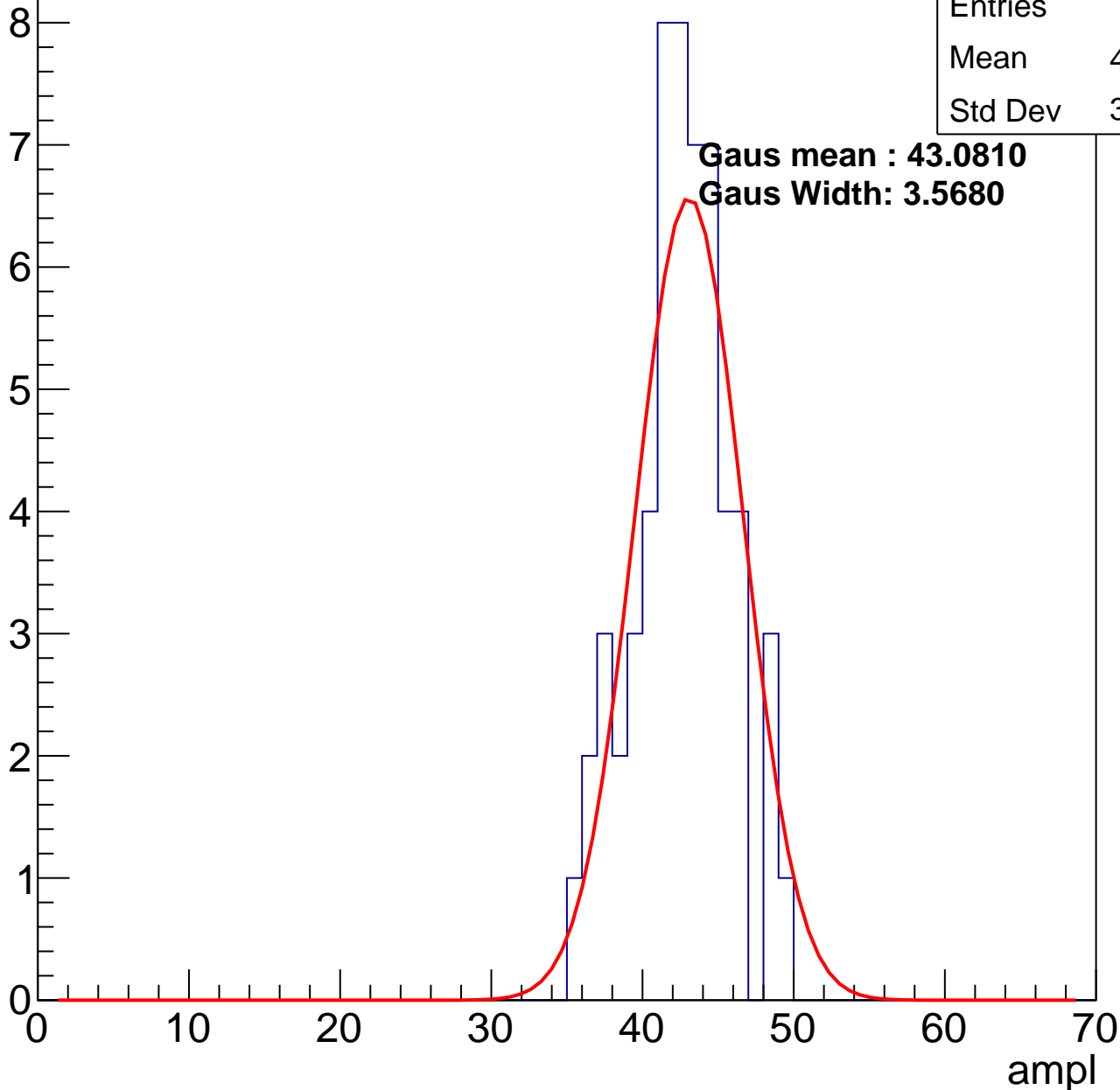
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	42.12
Std Dev	3.163

**Gaus mean : 43.0810**

**Gaus Width: 3.5680**

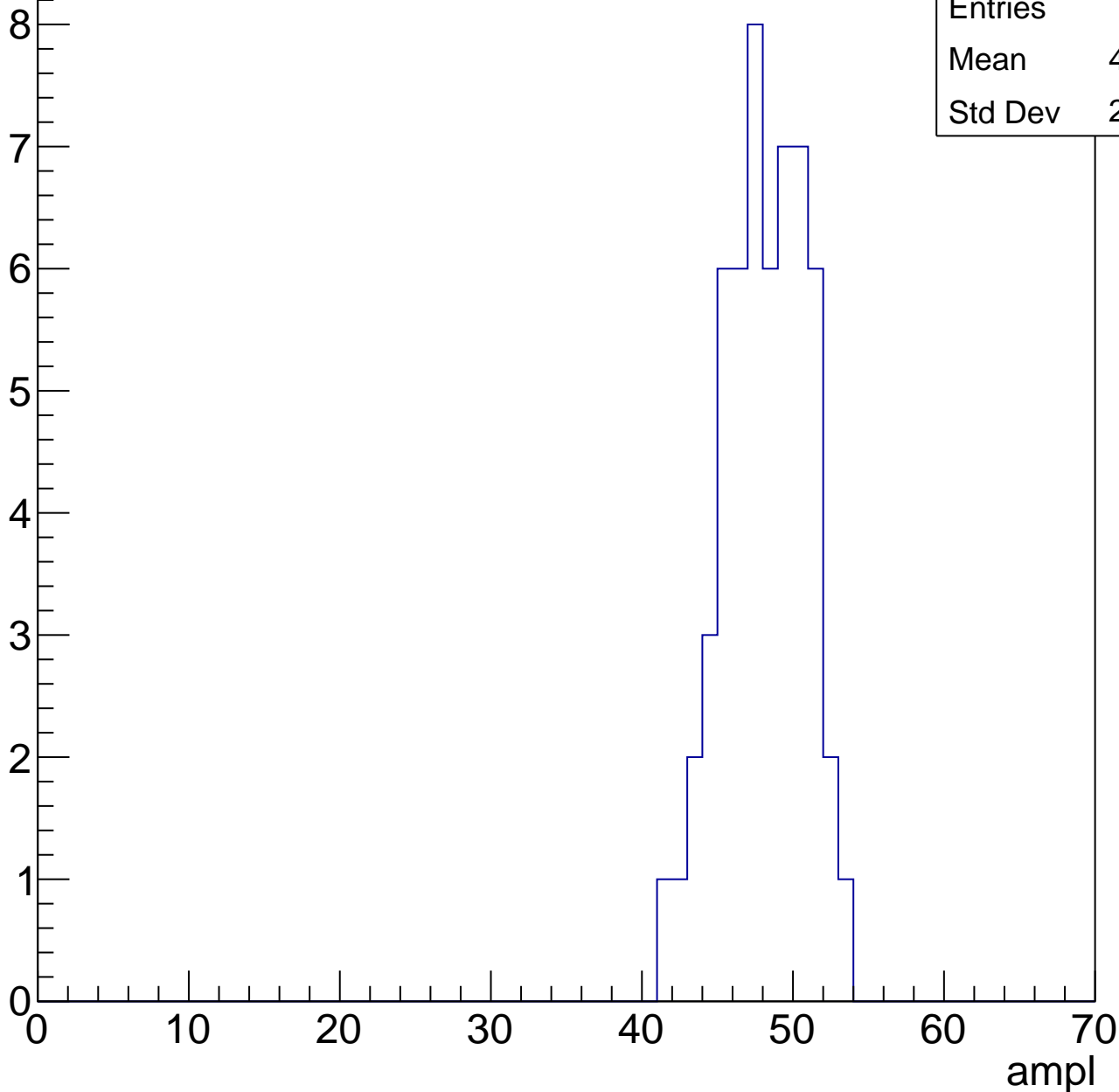


# B1L002S, U3-ch25, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

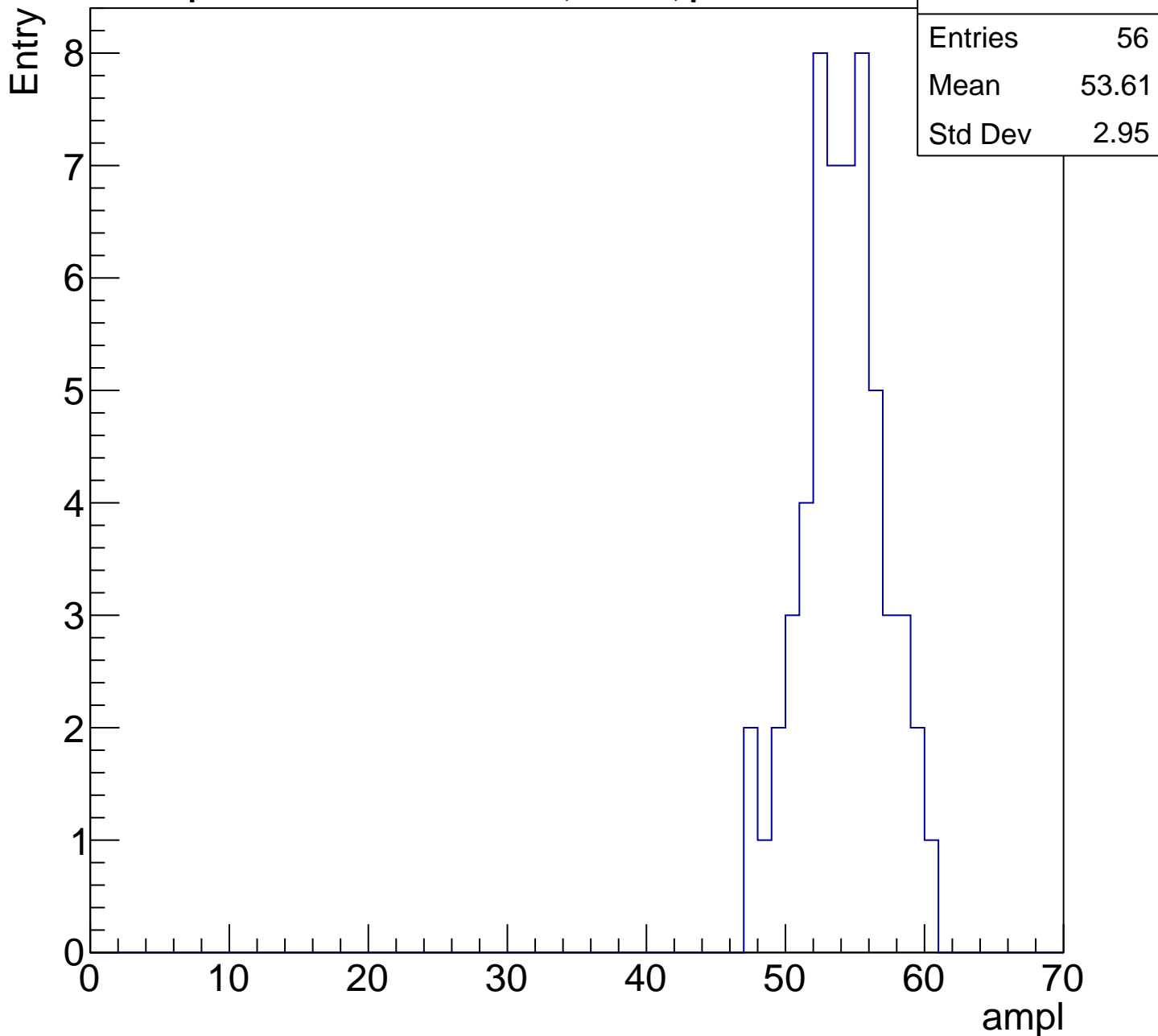
Entry

Entries	56
Mean	47.62
Std Dev	2.696



# B1L002S, U3-ch25, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

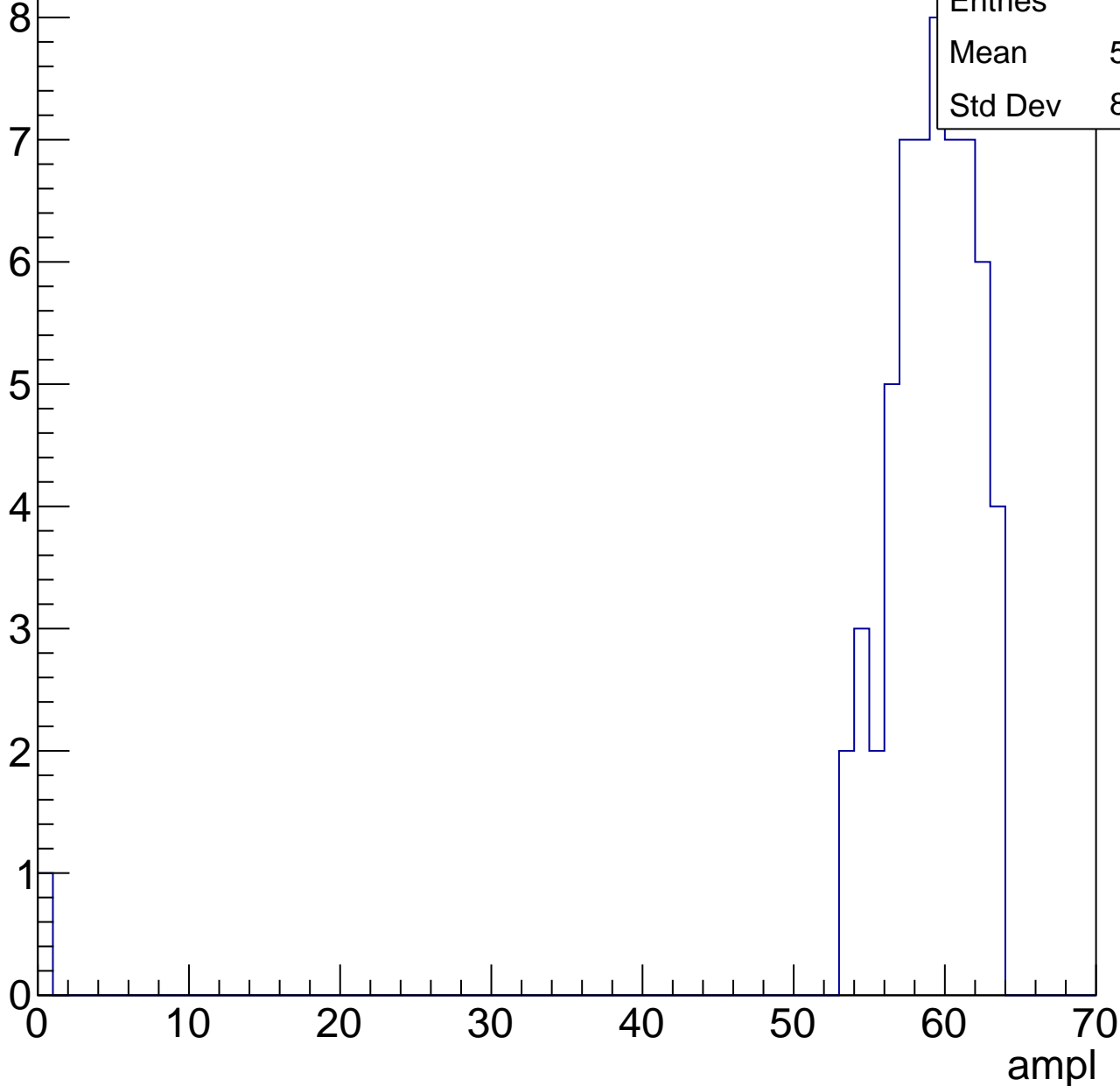


# B1L002S, U3-ch25, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	57.73
Std Dev	8.023

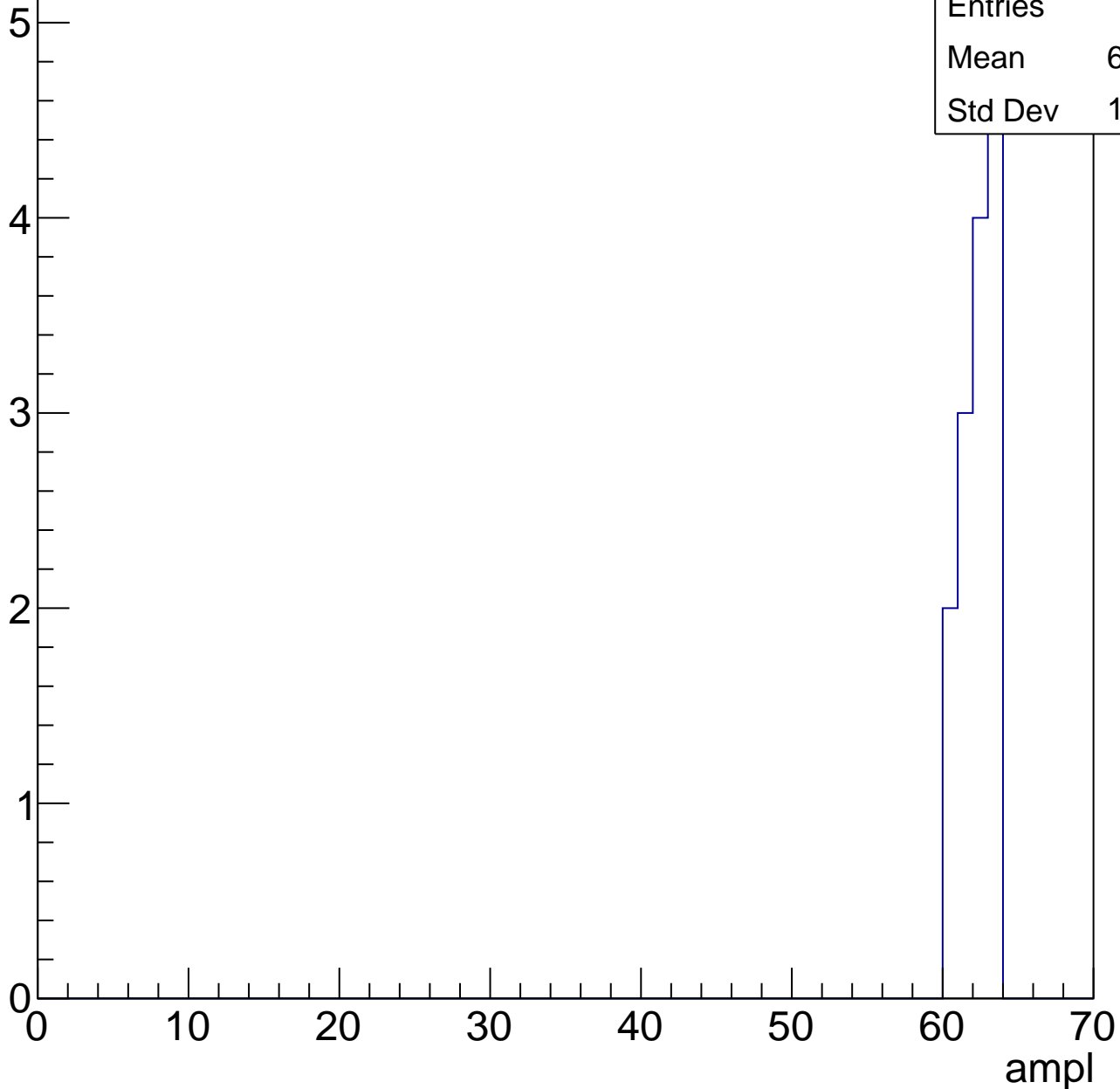


# B1L002S, U3-ch25, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.86
Std Dev	1.059





# B1L002S, U3-ch25, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch26, adc0

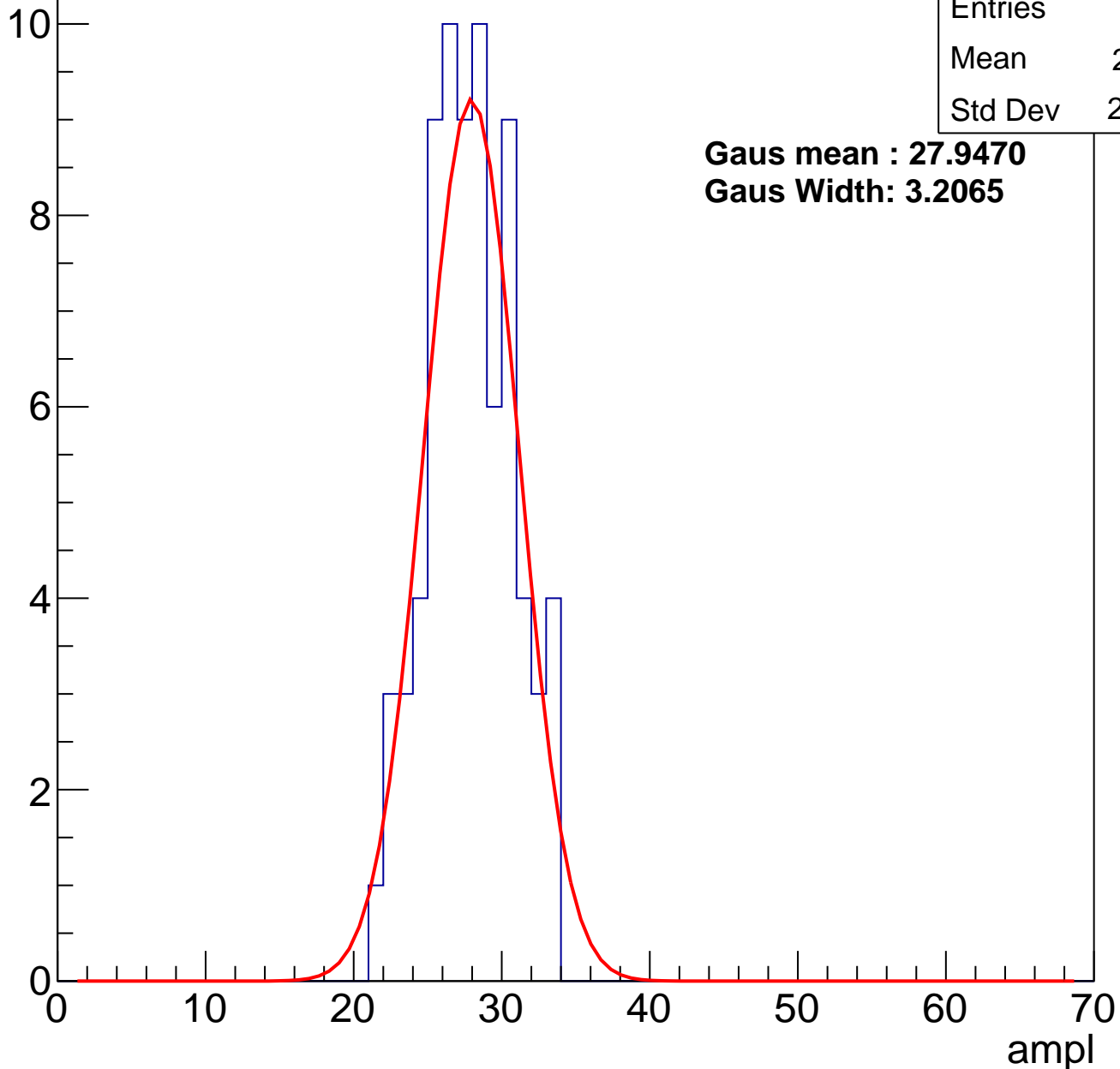
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	27.41
Std Dev	2.889

**Gaus mean : 27.9470**

**Gaus Width: 3.2065**

Entry



# B1L002S, U3-ch26, adc1

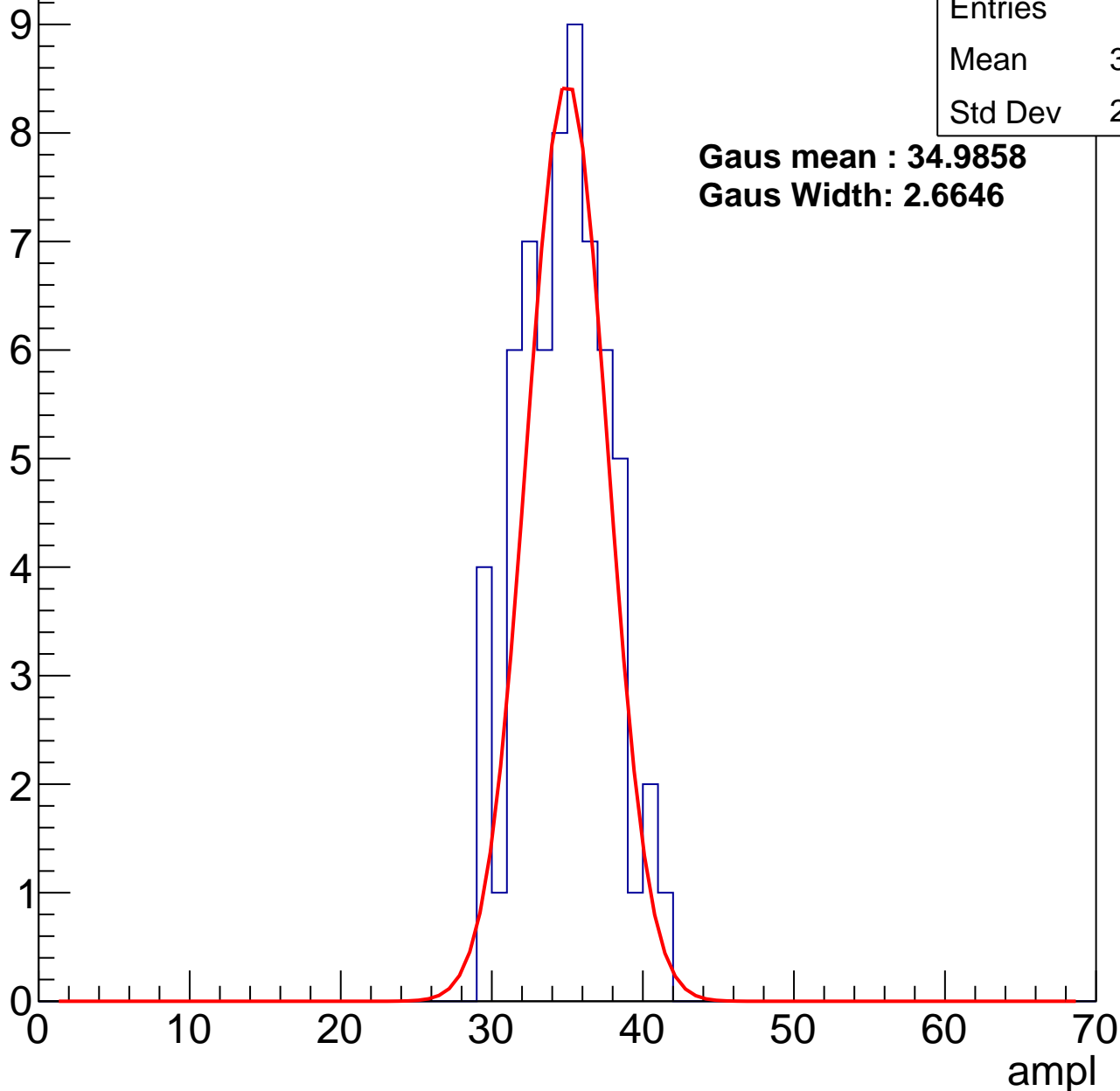
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.37
Std Dev	2.852

**Gaus mean : 34.9858**

**Gaus Width: 2.6646**



# B1L002S, U3-ch26, adc2

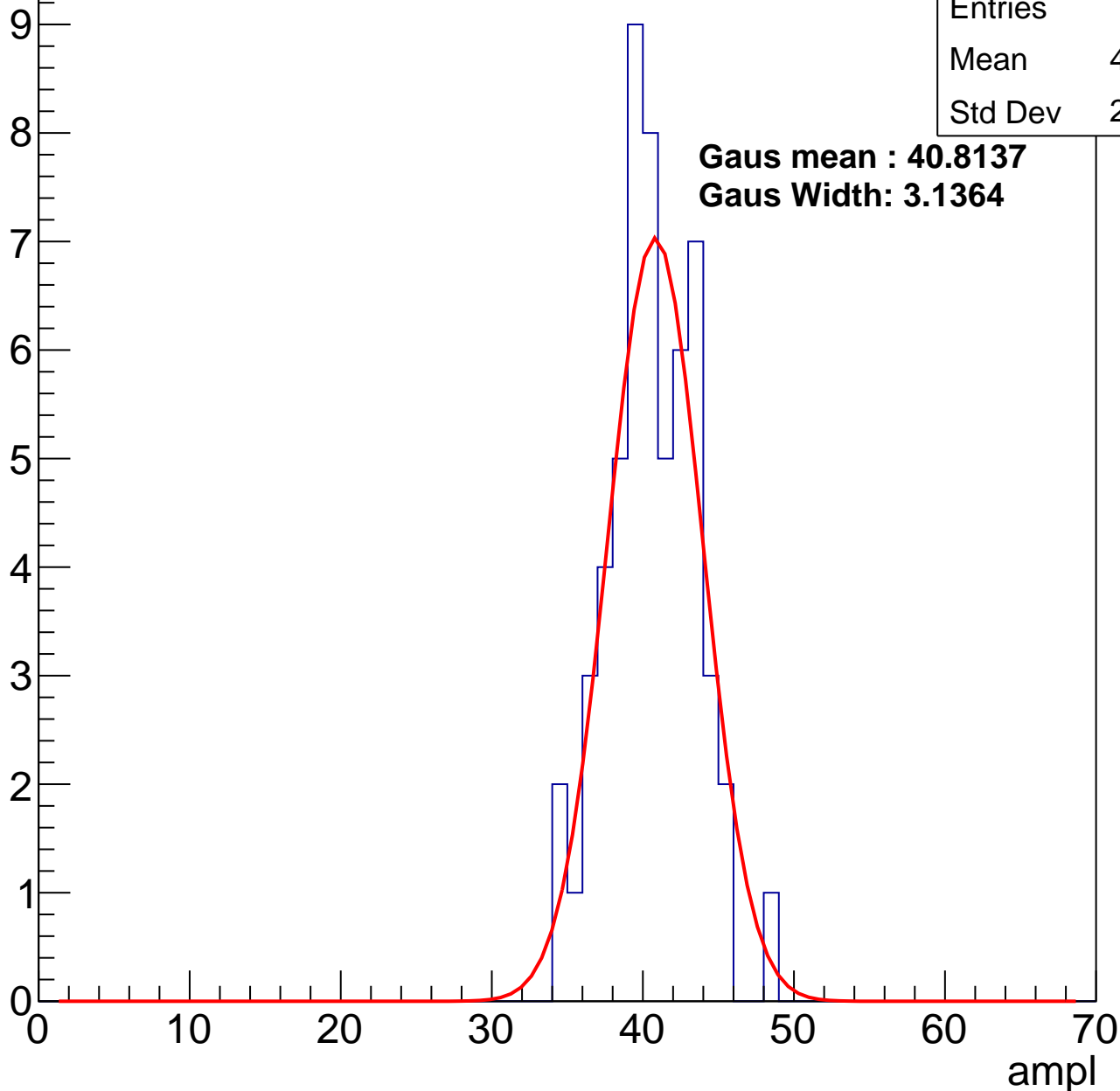
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.14
Std Dev	2.875

**Gaus mean : 40.8137**

**Gaus Width: 3.1364**



# B1L002S, U3-ch26, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	45.98
Std Dev	2.746

Entry

10

8

6

4

2

0

0

10

20

30

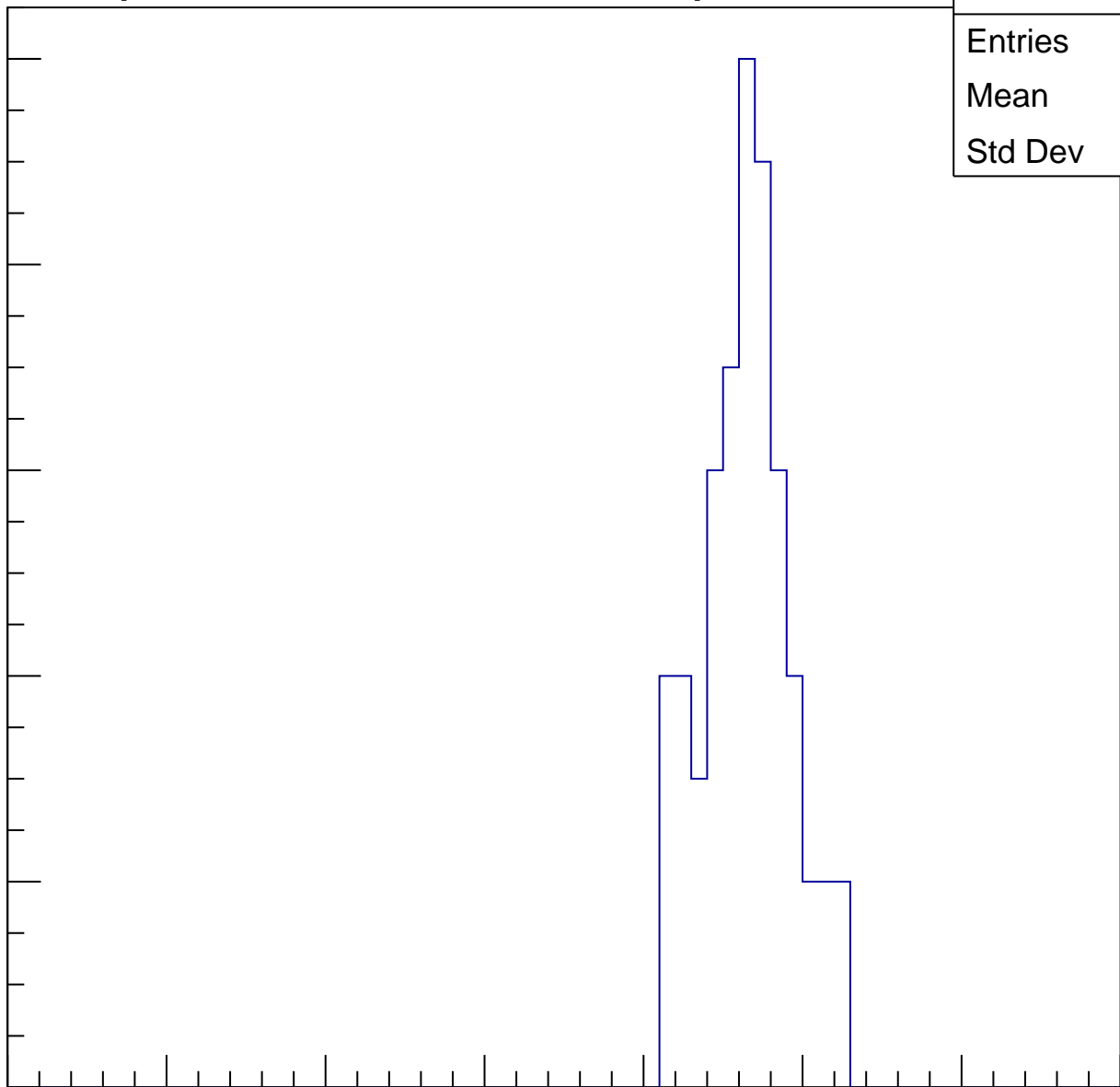
40

50

60

70

ampl

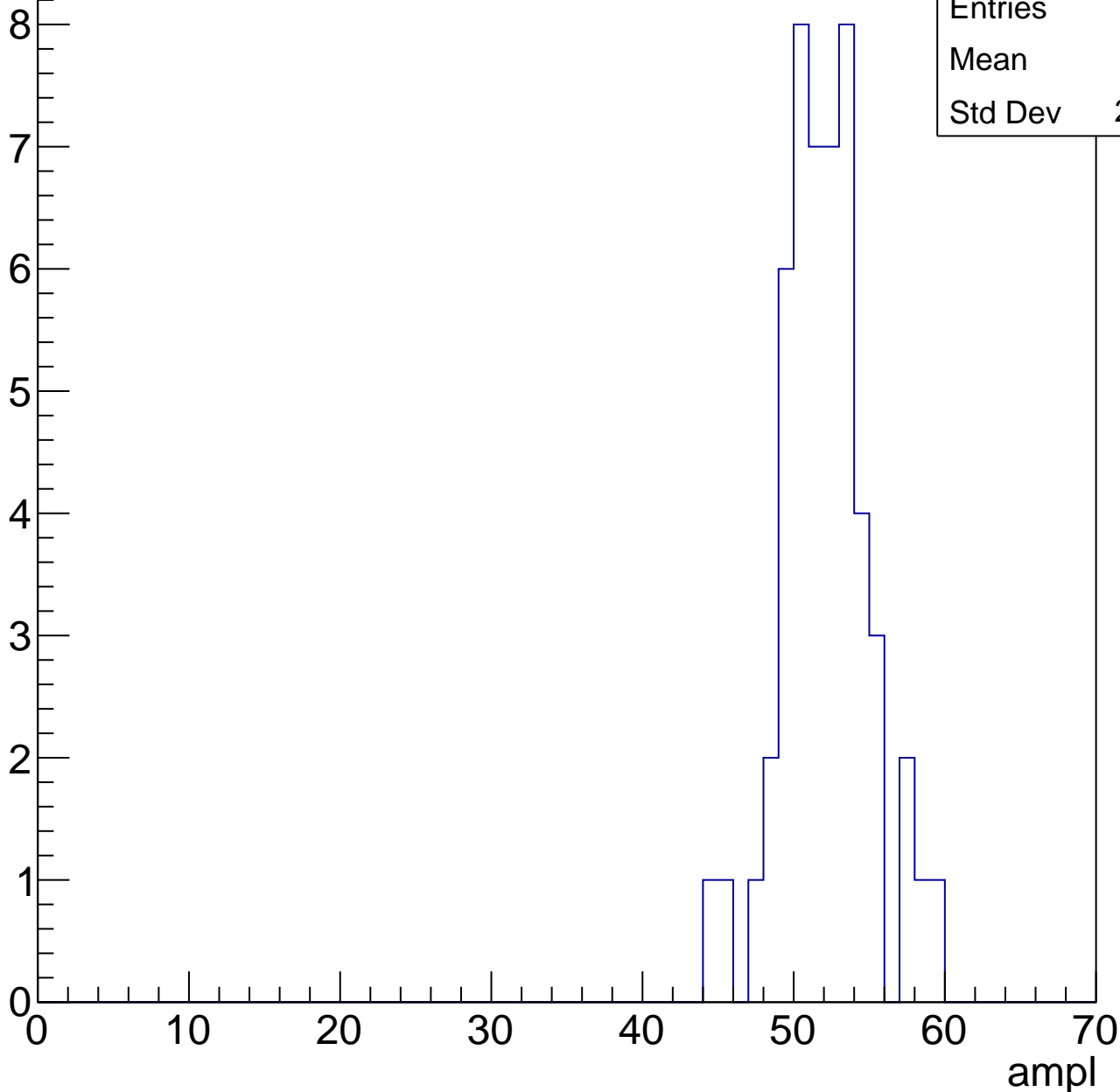


# B1L002S, U3-ch26, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

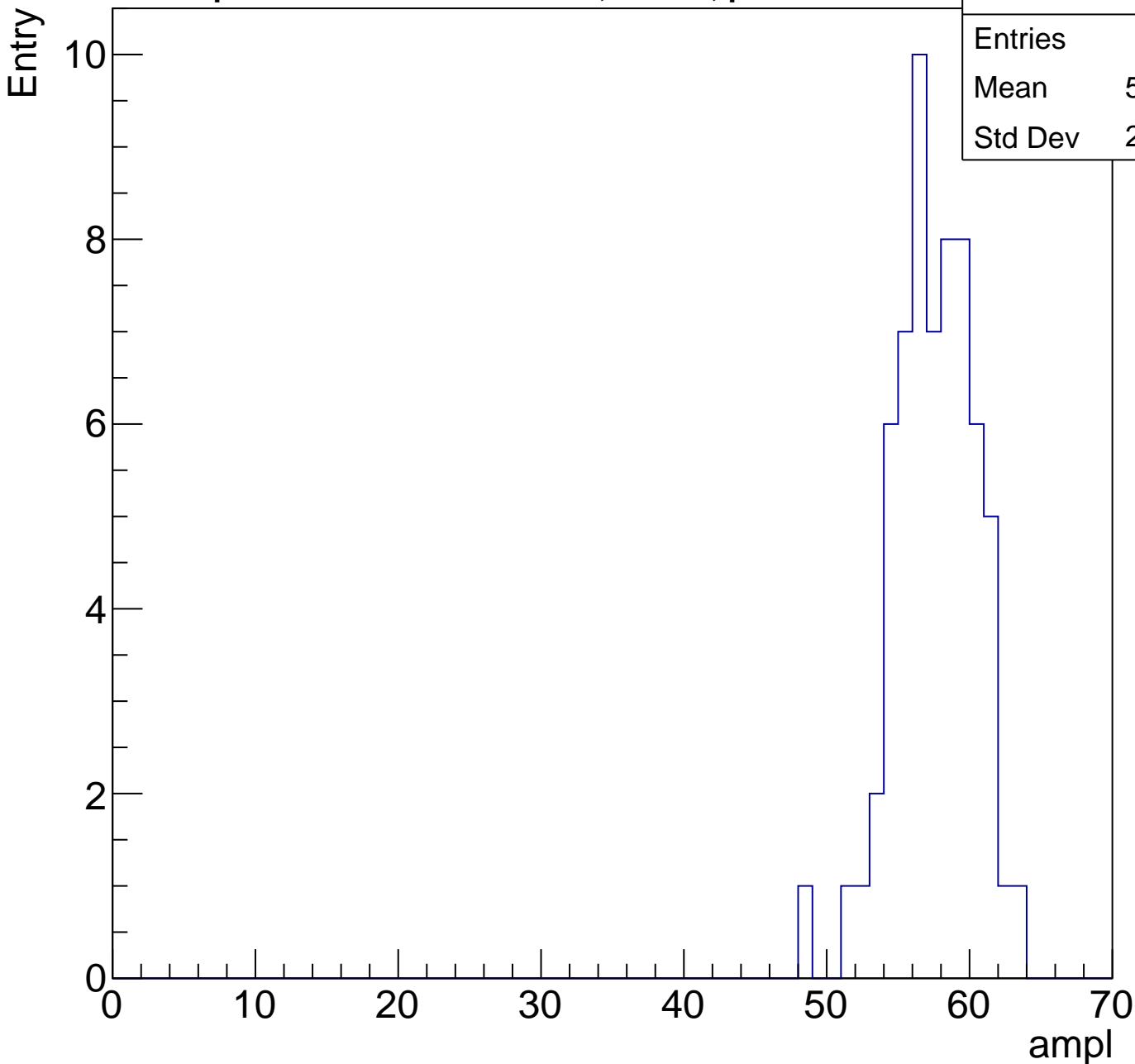
Entries	52
Mean	51.6
Std Dev	2.911



# B1L002S, U3-ch26, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	57.05
Std Dev	2.797

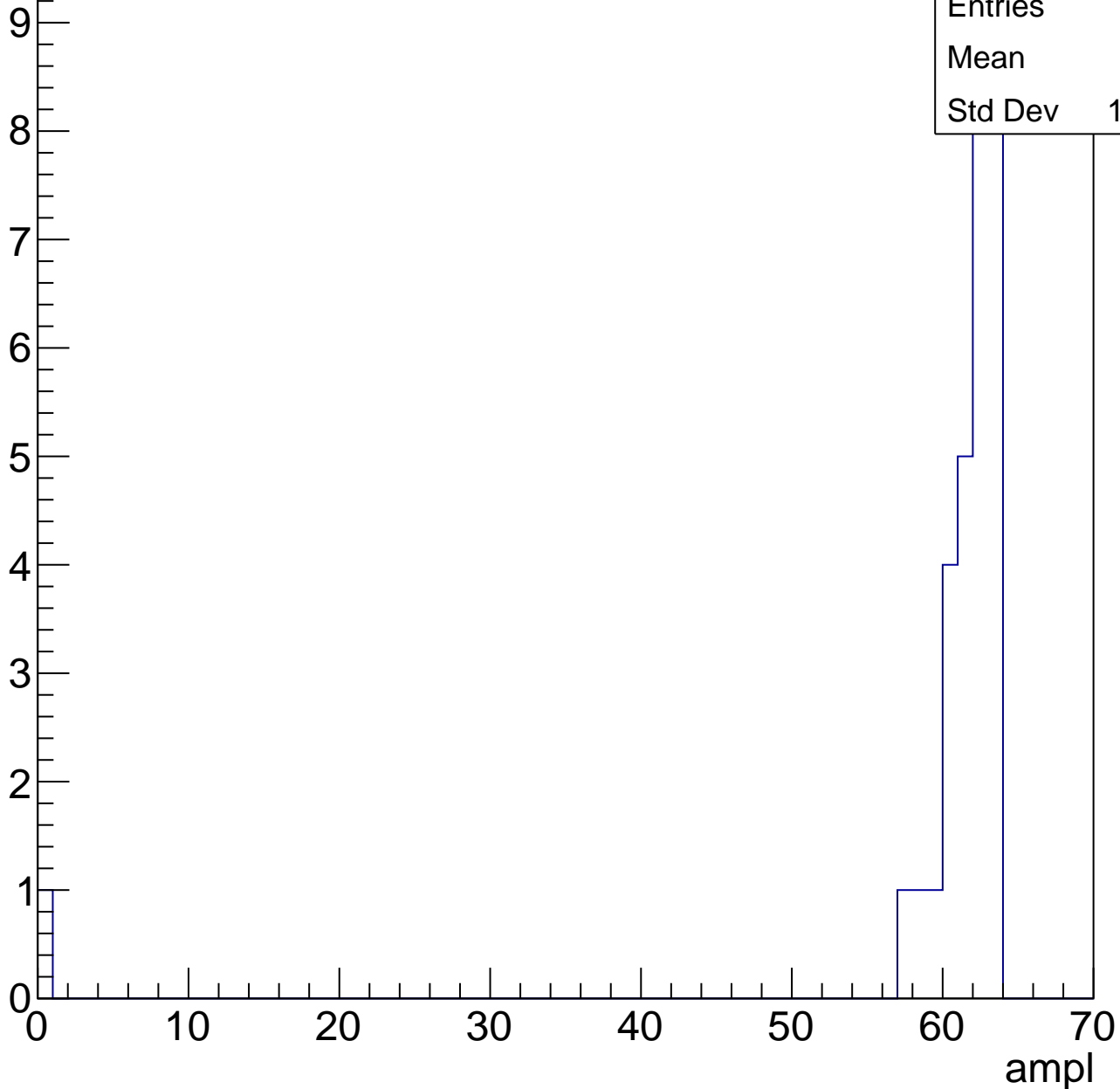


# B1L002S, U3-ch26, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59.4
Std Dev	11.14

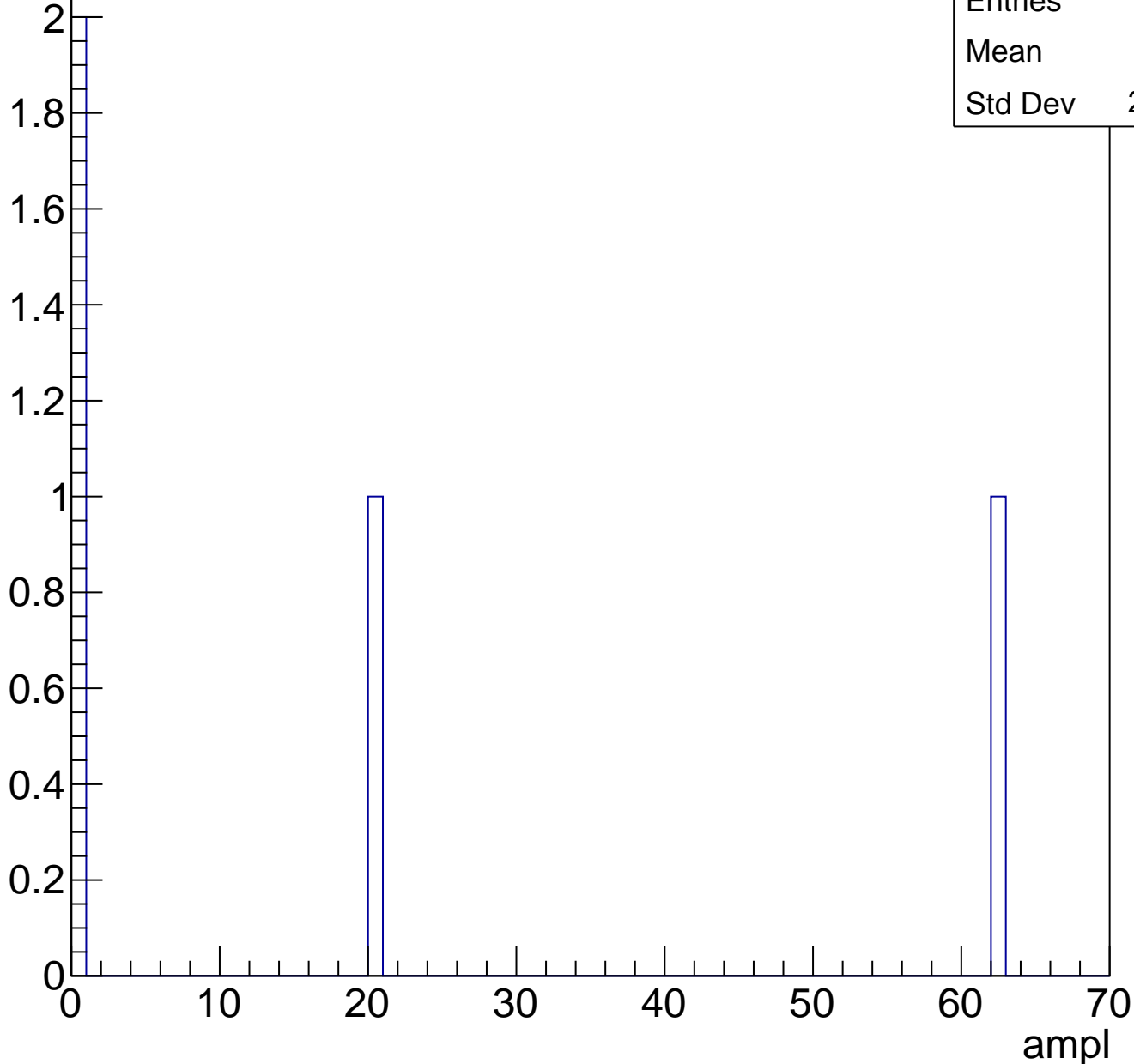




# B1L002S, U3-ch26, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	20.5
Std Dev	25.31

# B1L002S, U3-ch27, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	26.95
Std Dev	4.557

**Gaus mean : 27.7103**

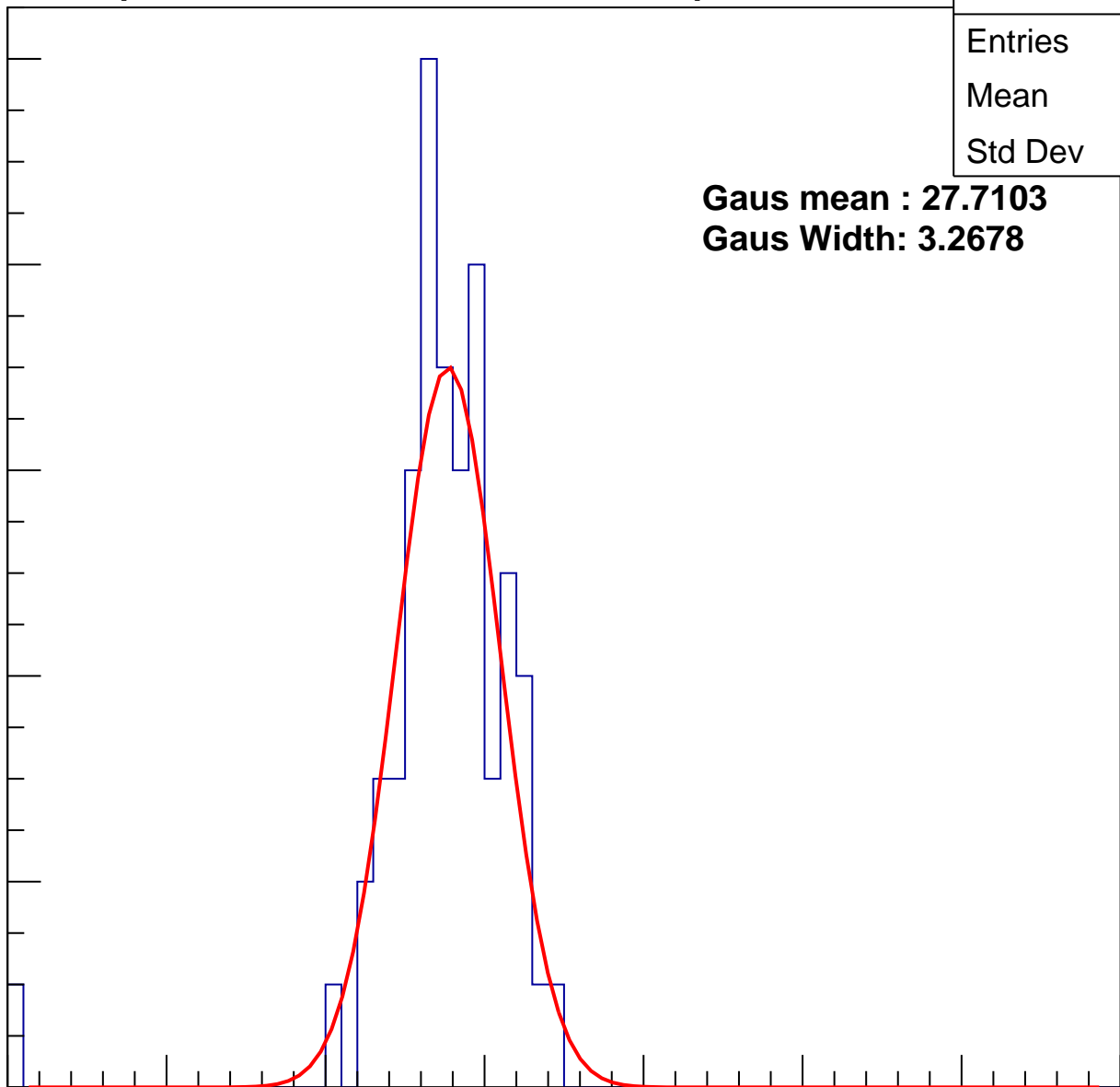
**Gaus Width: 3.2678**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch27, adc1

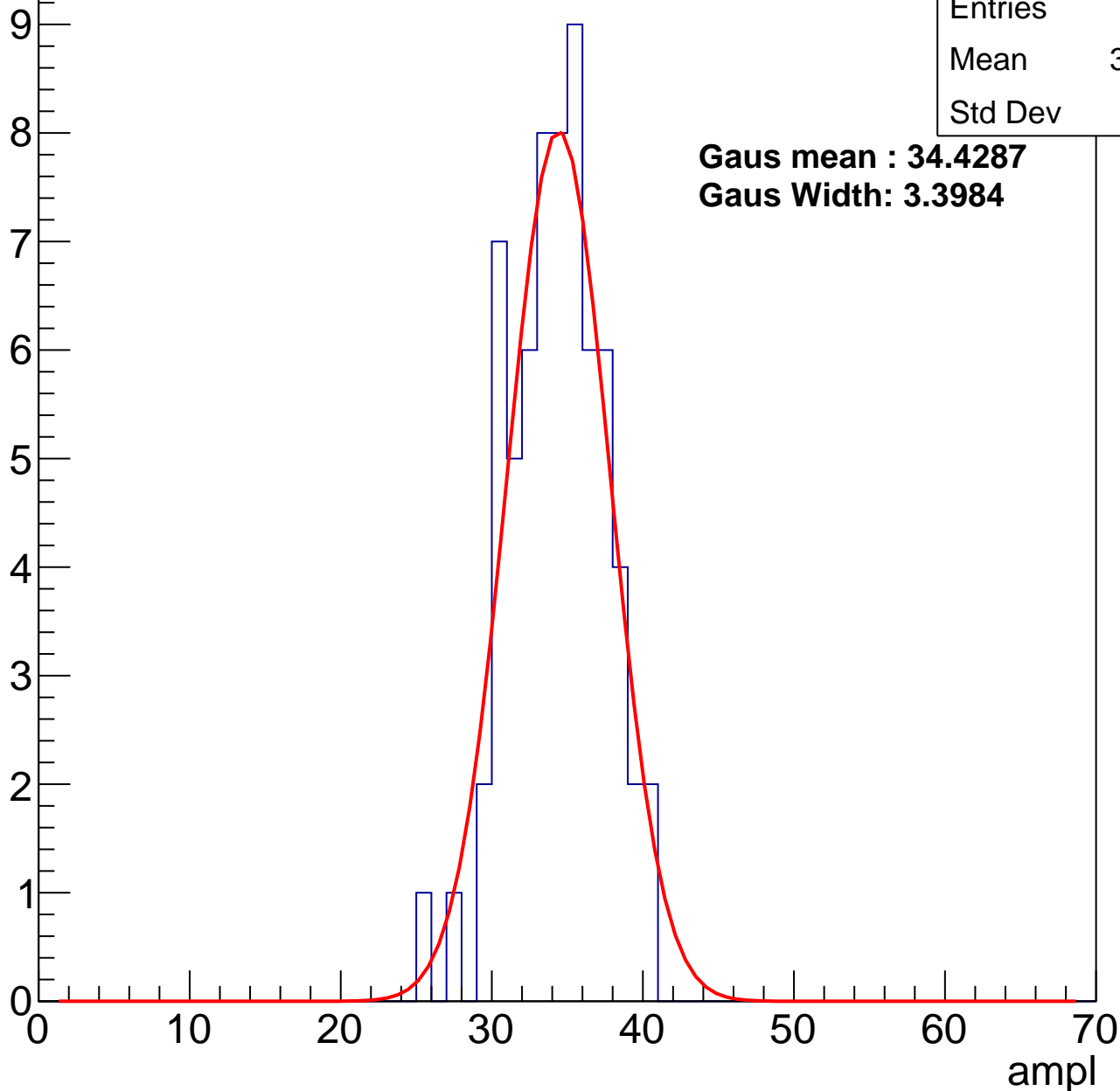
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	33.82
Std Dev	3.09

**Gaus mean : 34.4287**

**Gaus Width: 3.3984**



# B1L002S, U3-ch27, adc2

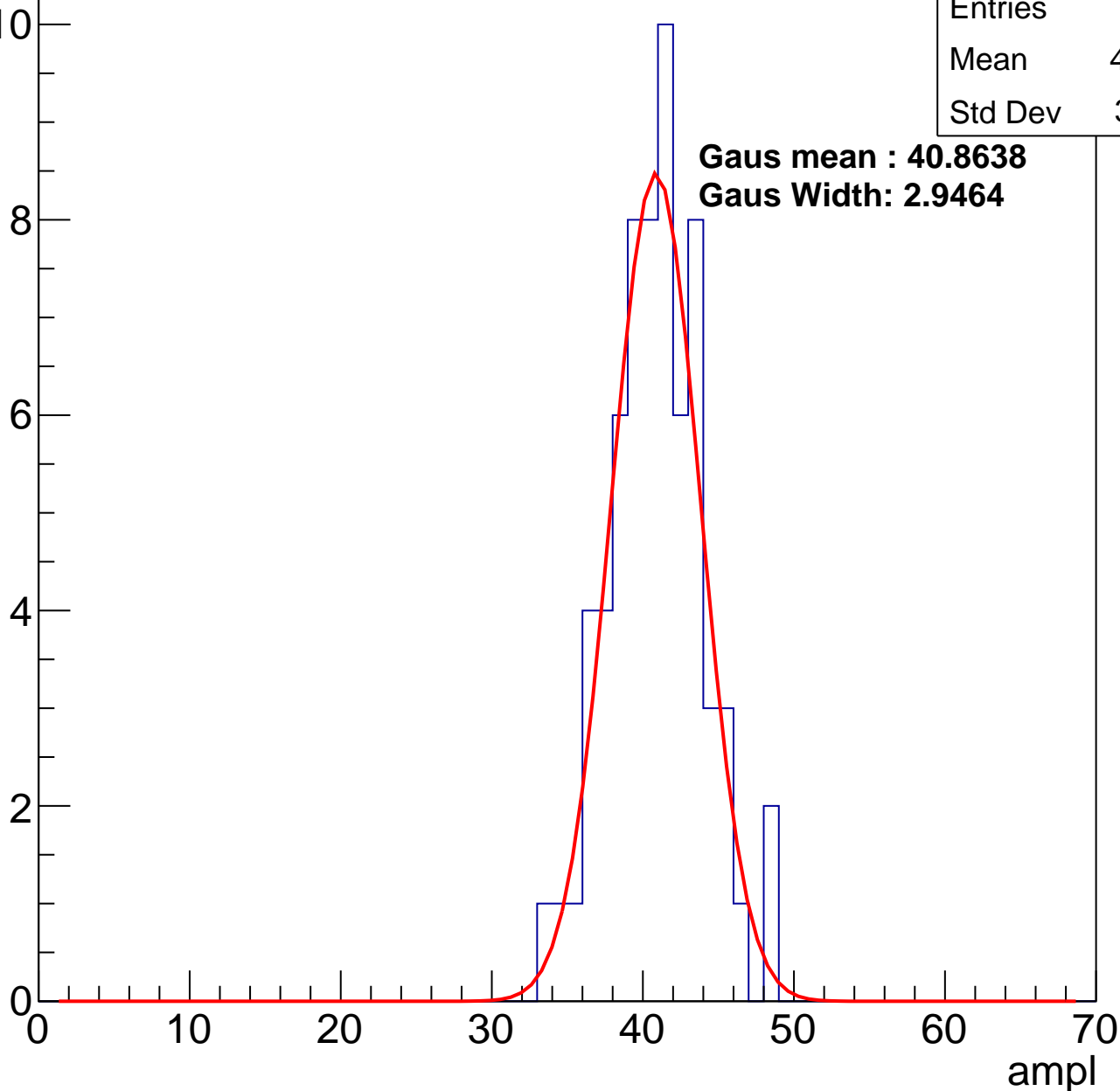
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.44
Std Dev	3.071

**Gaus mean : 40.8638**

**Gaus Width: 2.9464**

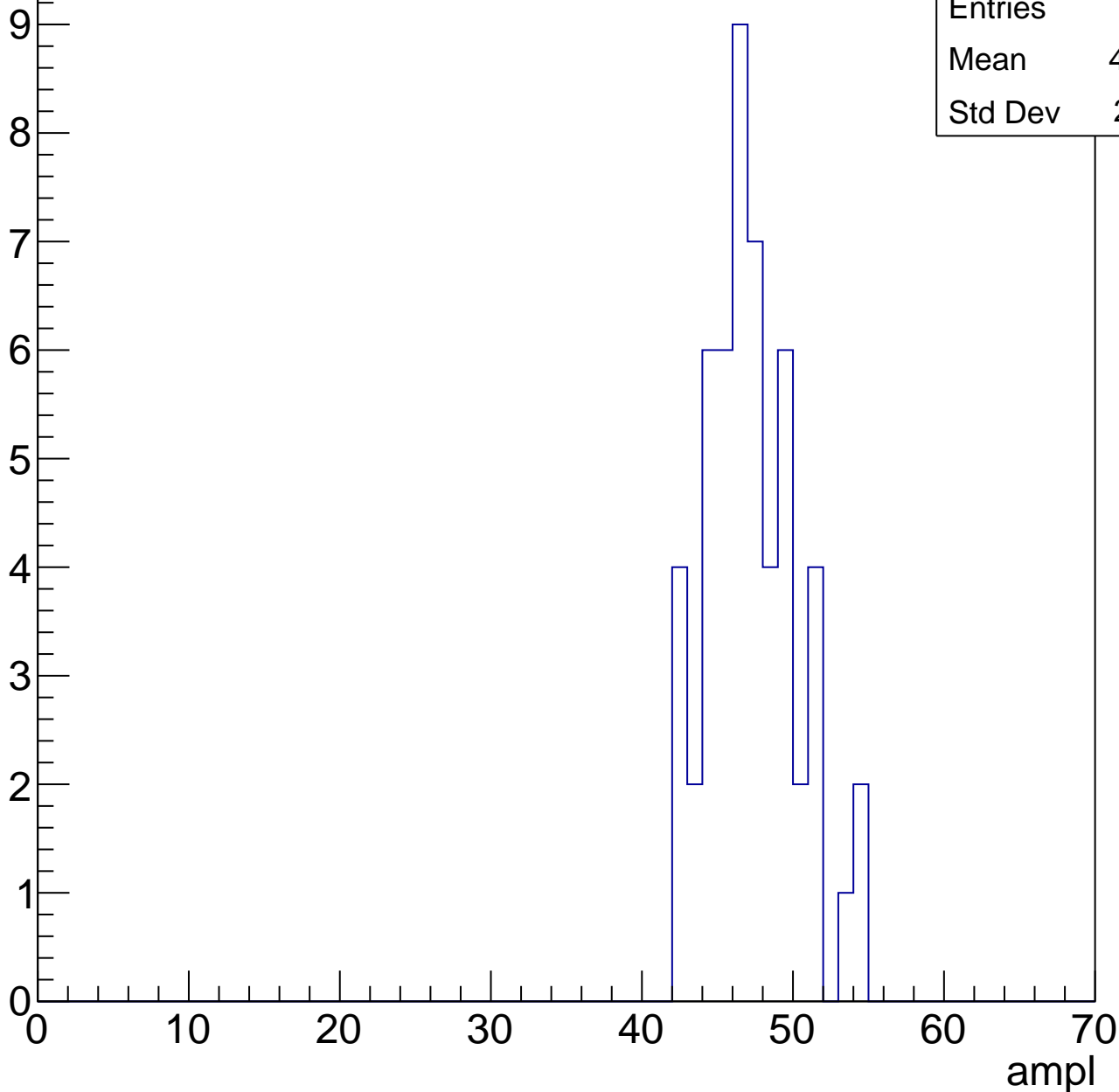


# B1L002S, U3-ch27, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	46.83
Std Dev	2.951

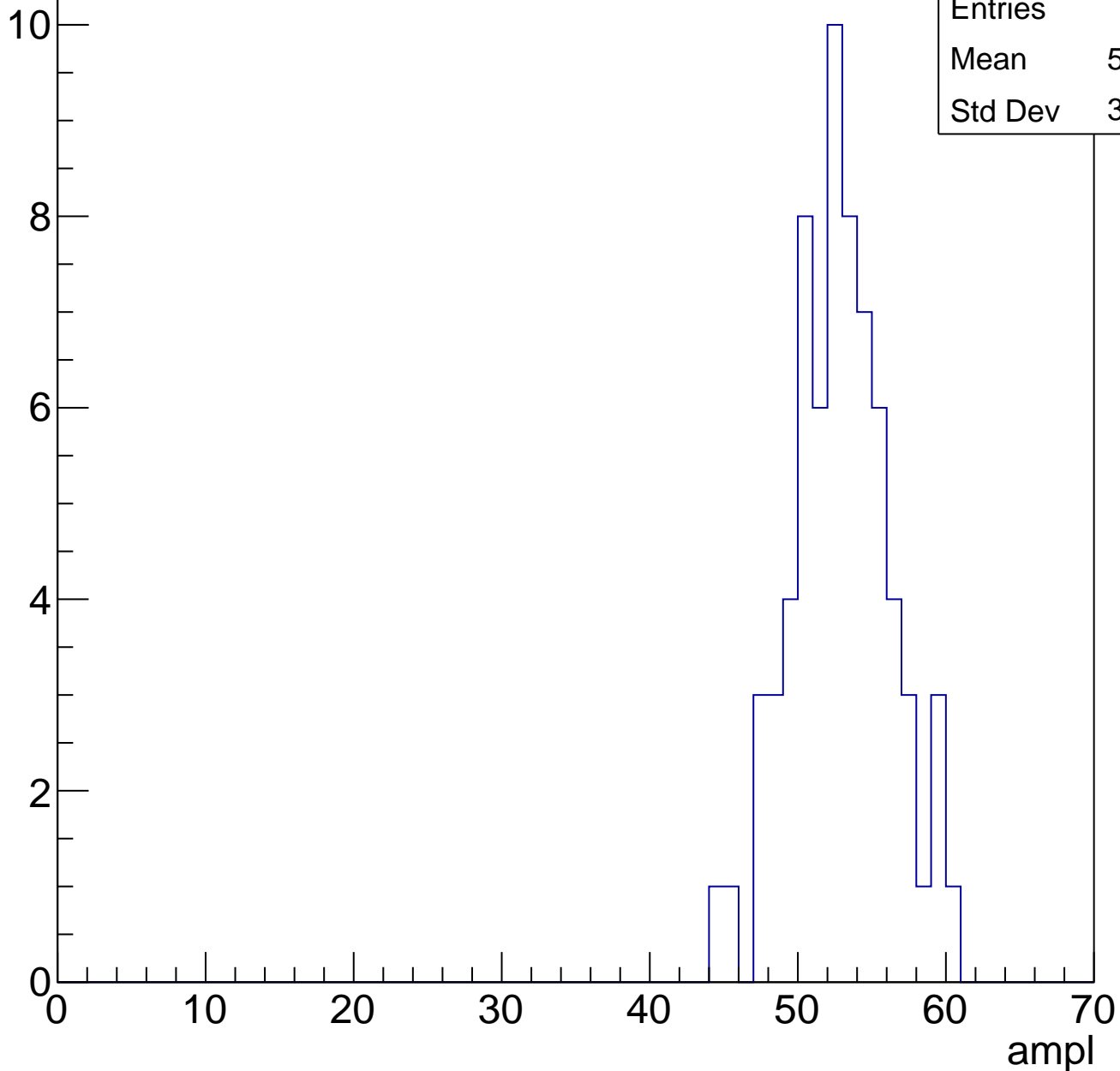


# B1L002S, U3-ch27, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	52.43
Std Dev	3.347

Entry

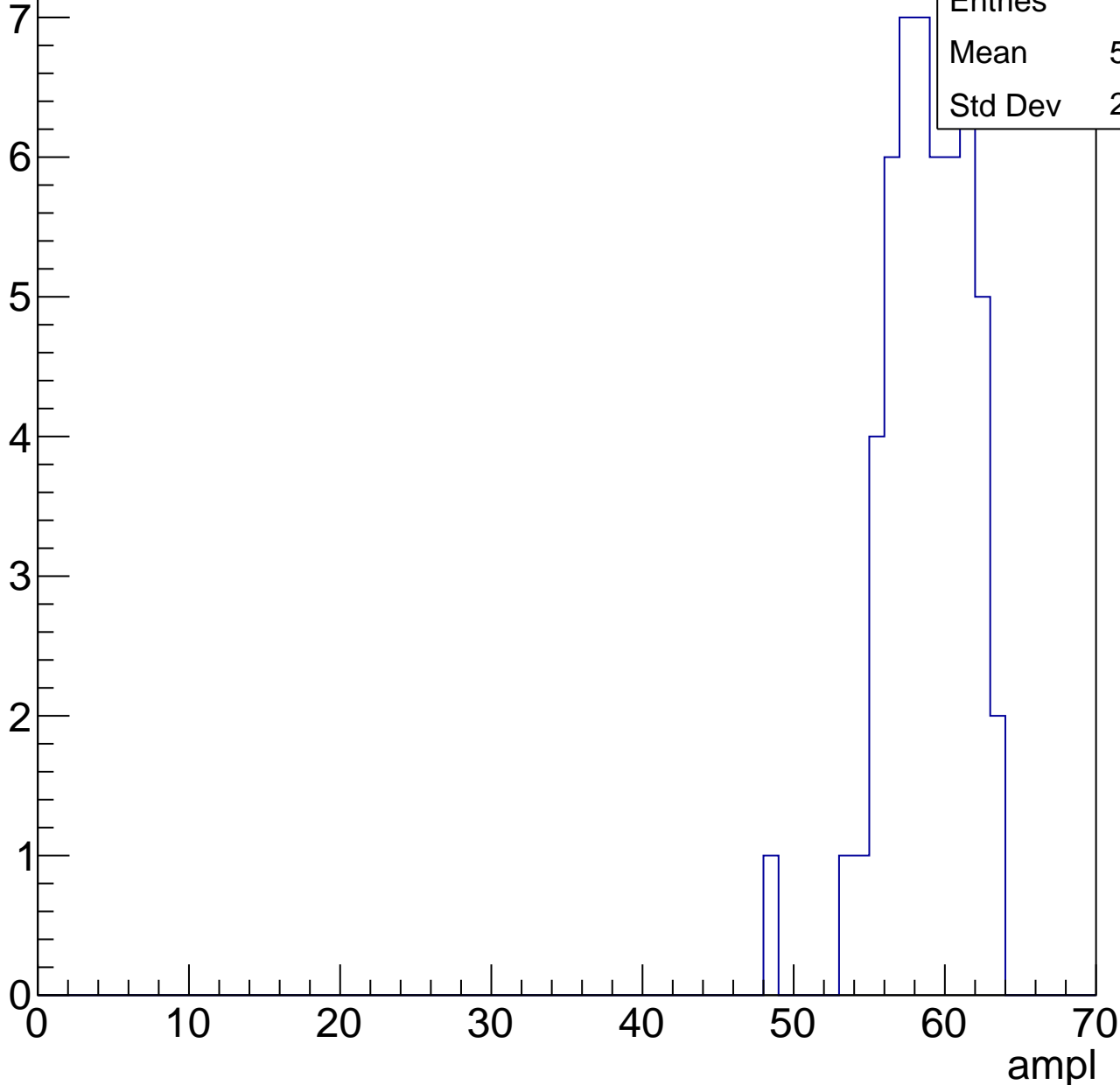


# B1L002S, U3-ch27, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	58.36
Std Dev	2.829

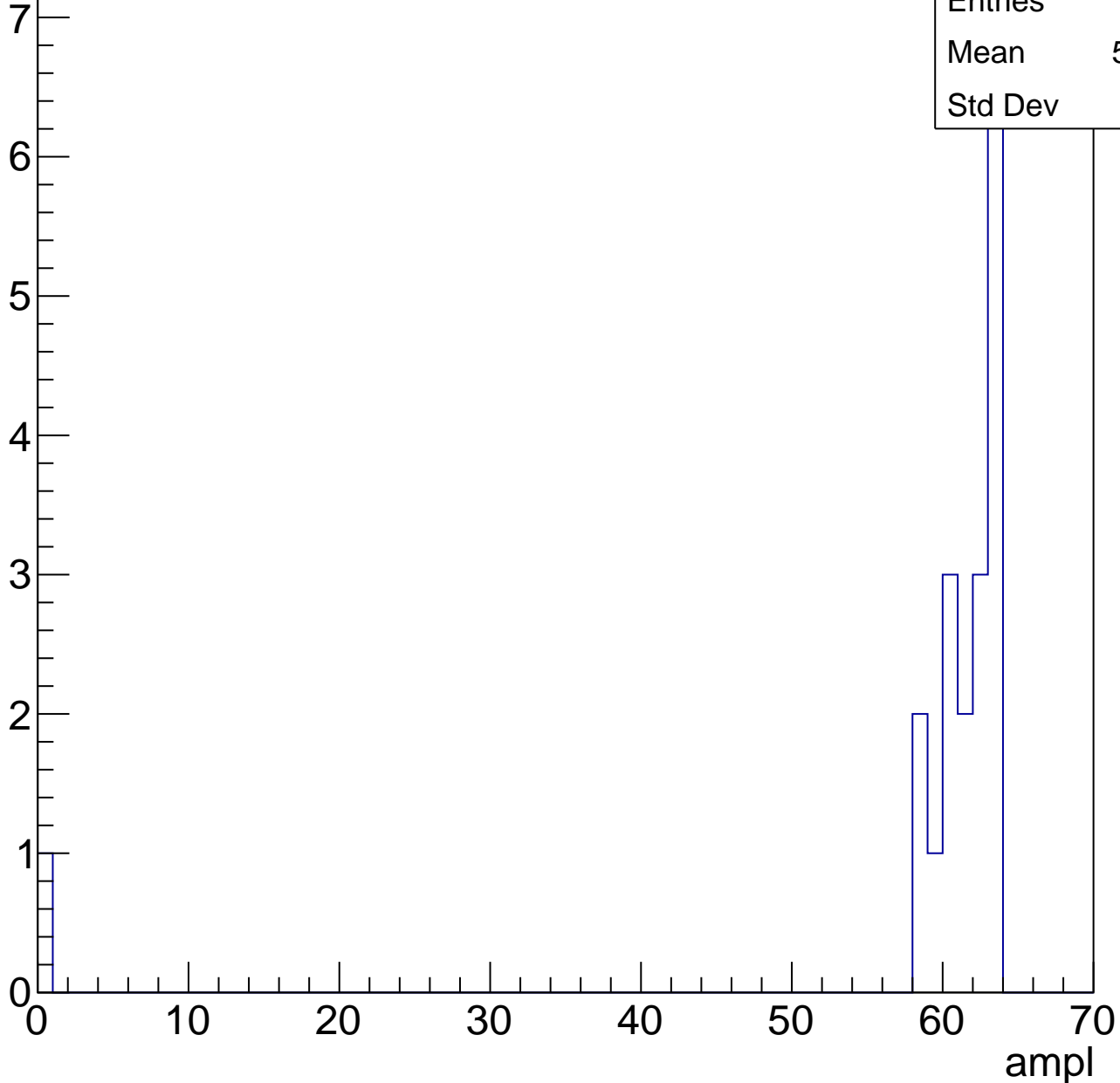


# B1L002S, U3-ch27, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.11
Std Dev	13.8





# B1L002S, U3-ch27, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

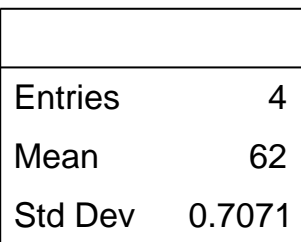
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch28, adc0

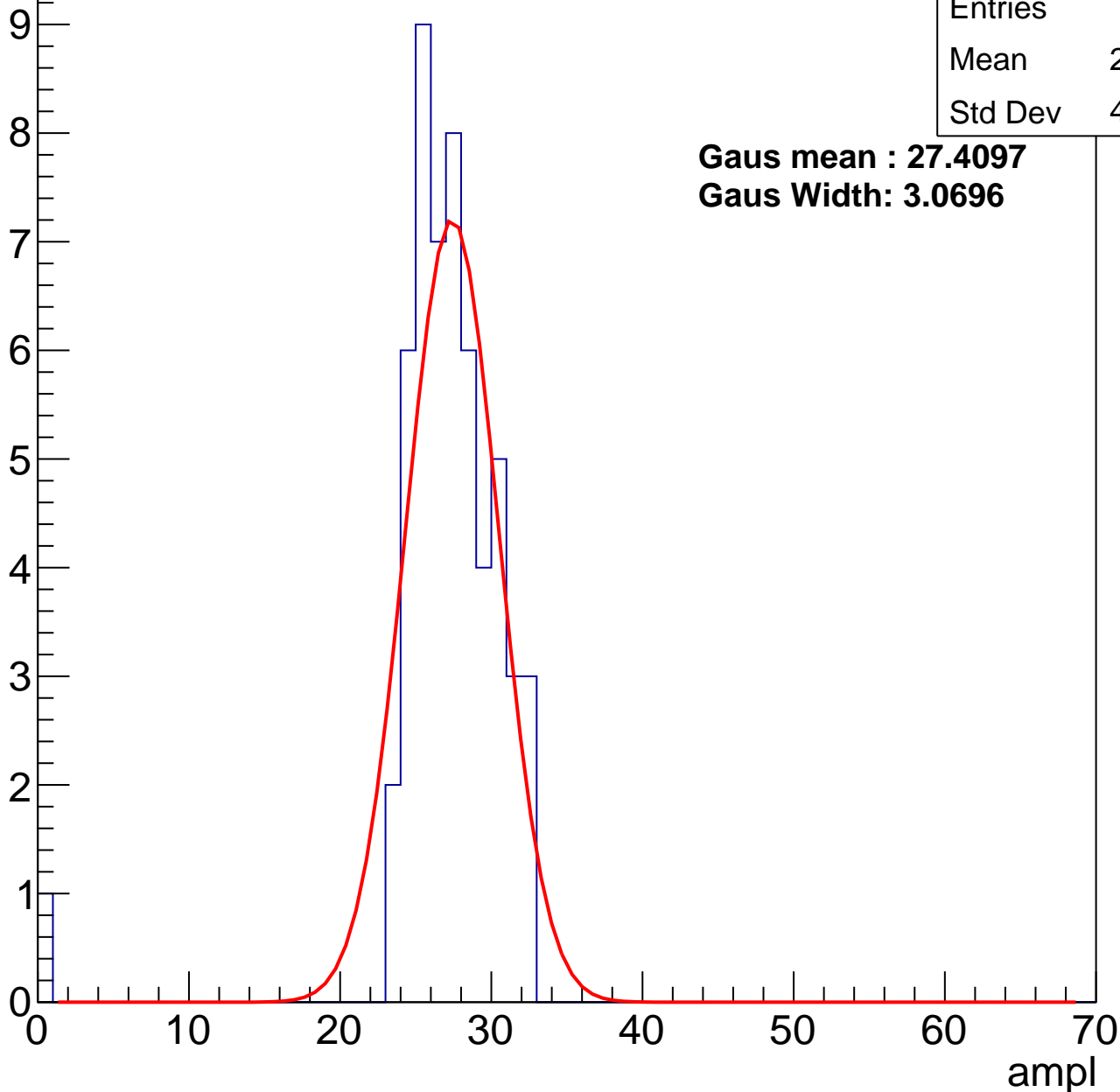
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	26.59
Std Dev	4.387

**Gaus mean : 27.4097**

**Gaus Width: 3.0696**



# B1L002S, U3-ch28, adc1

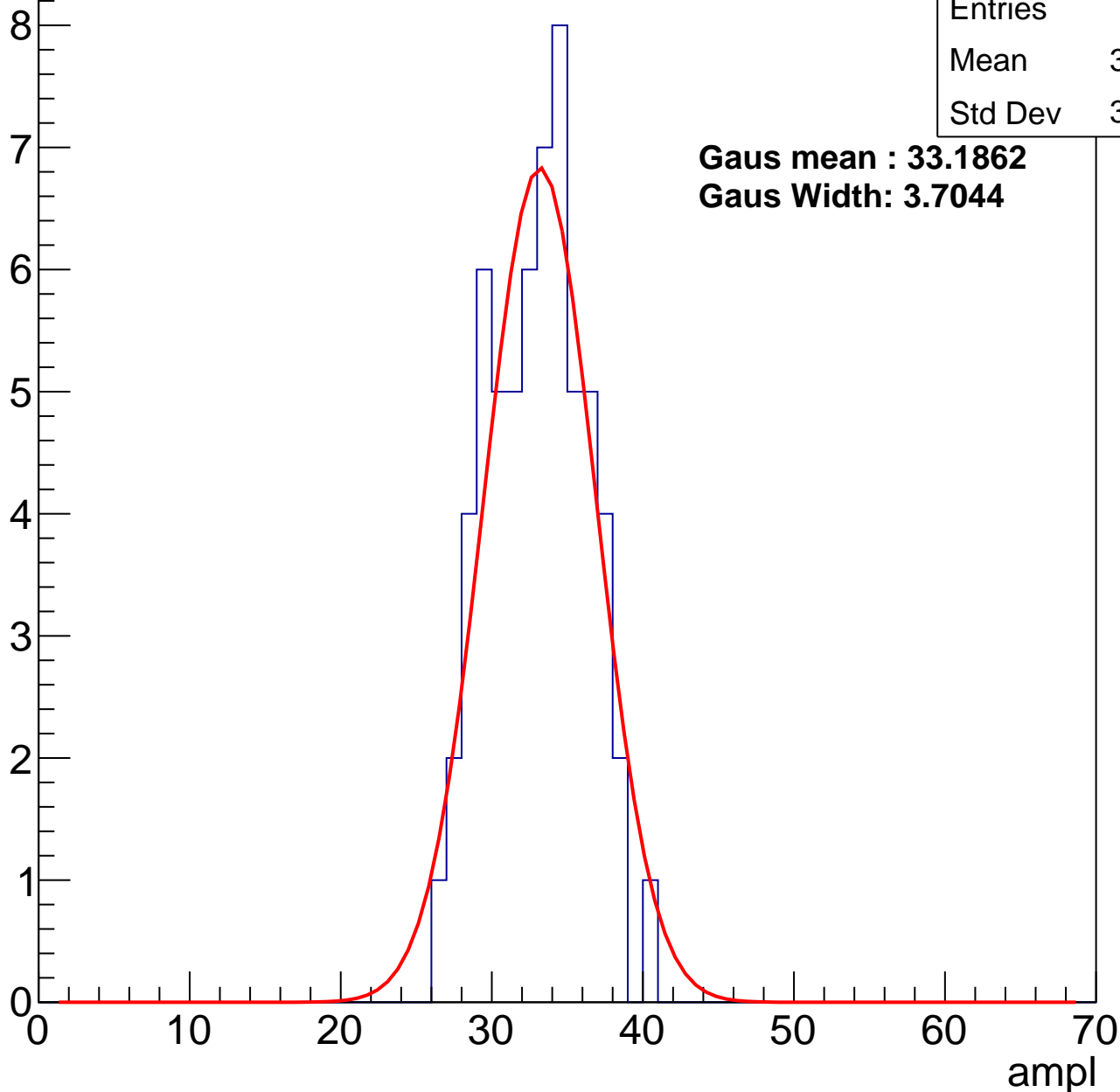
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	32.54
Std Dev	3.165

**Gaus mean : 33.1862**

**Gaus Width: 3.7044**



# B1L002S, U3-ch28, adc2

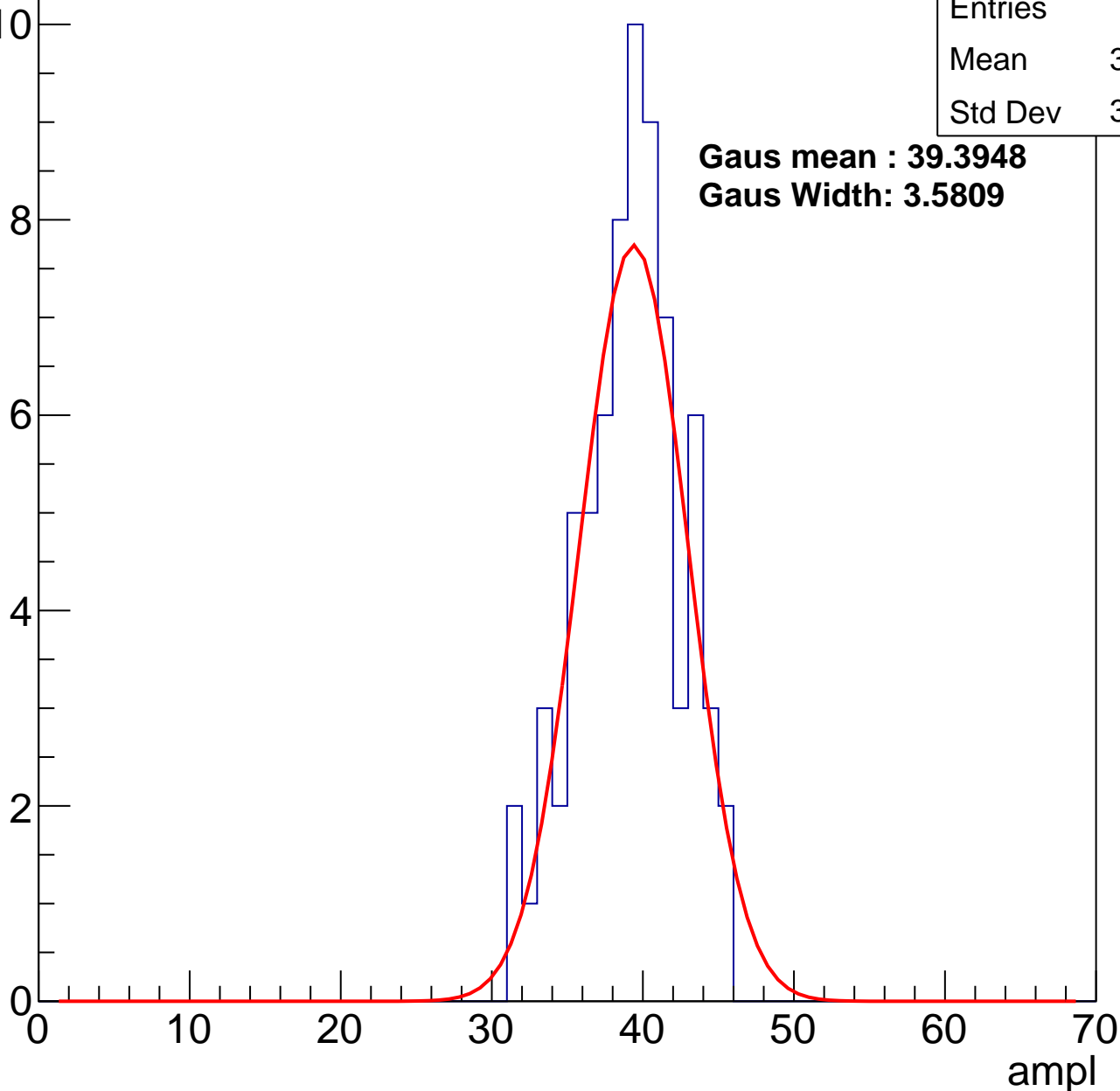
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	38.68
Std Dev	3.316

**Gaus mean : 39.3948**

**Gaus Width: 3.5809**

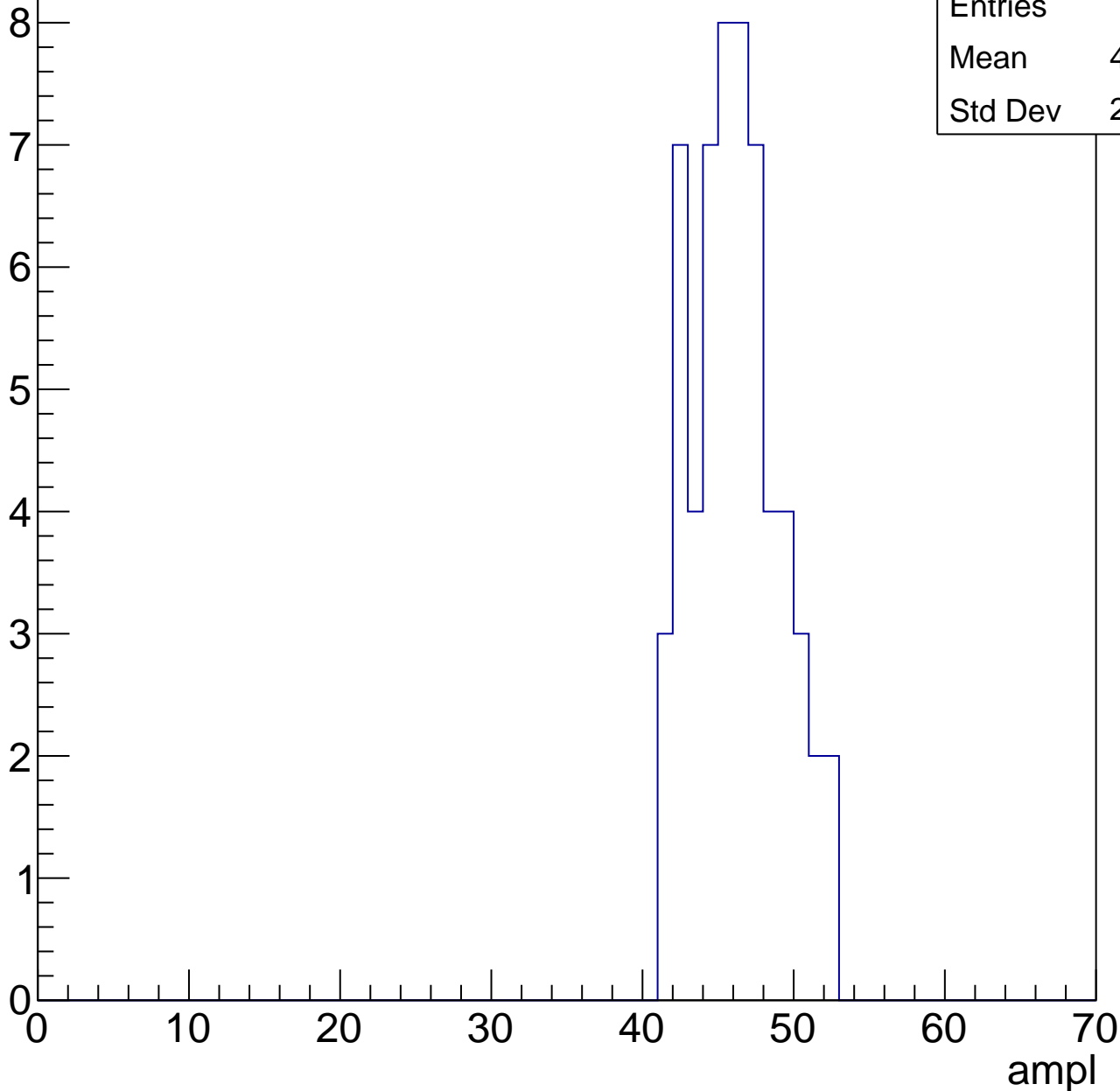


# B1L002S, U3-ch28, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

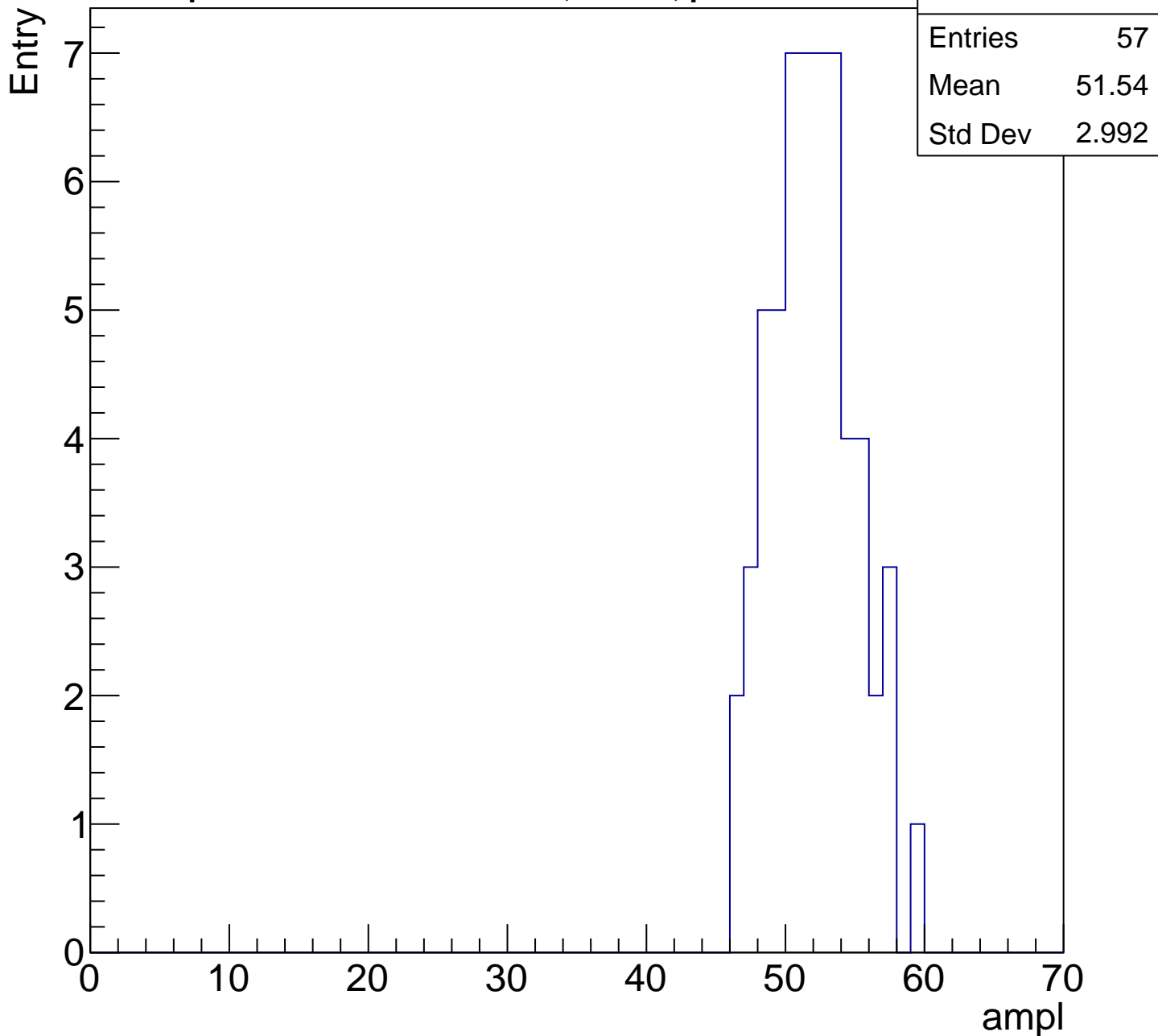
Entry

Entries	59
Mean	45.73
Std Dev	2.863



# B1L002S, U3-ch28, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

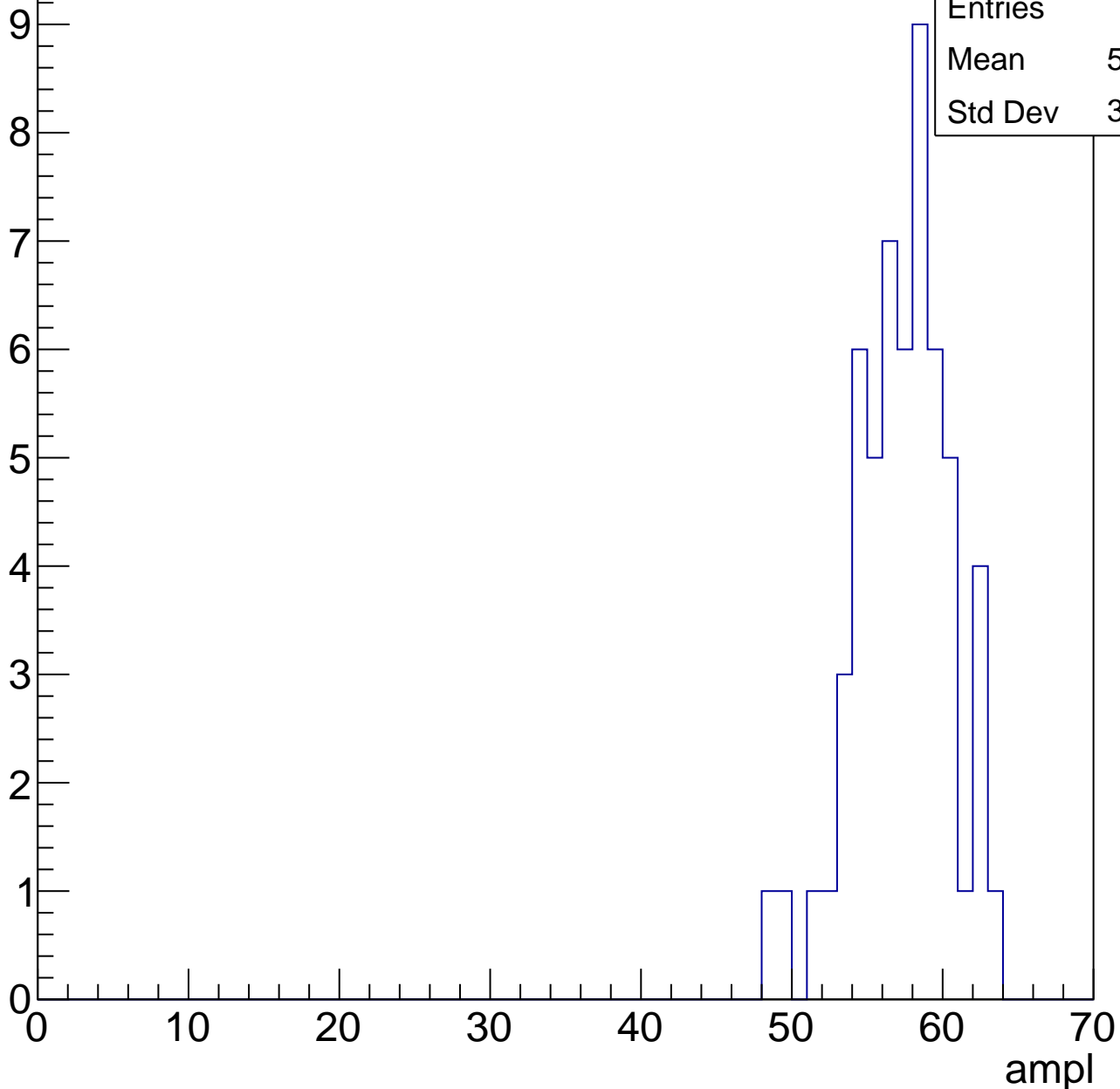


# B1L002S, U3-ch28, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

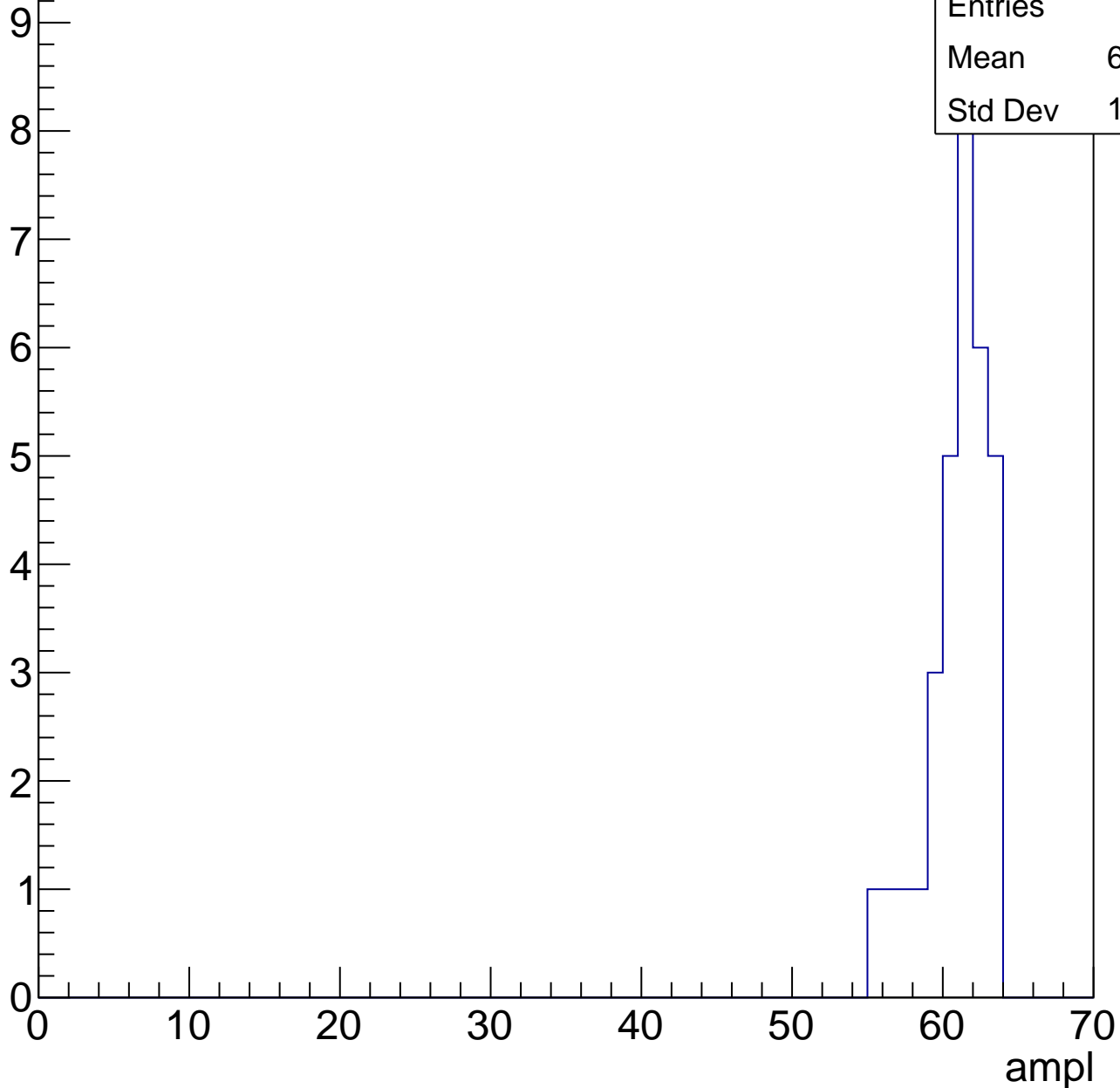
Entries	57
Mean	56.84
Std Dev	3.144



# B1L002S, U3-ch28, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

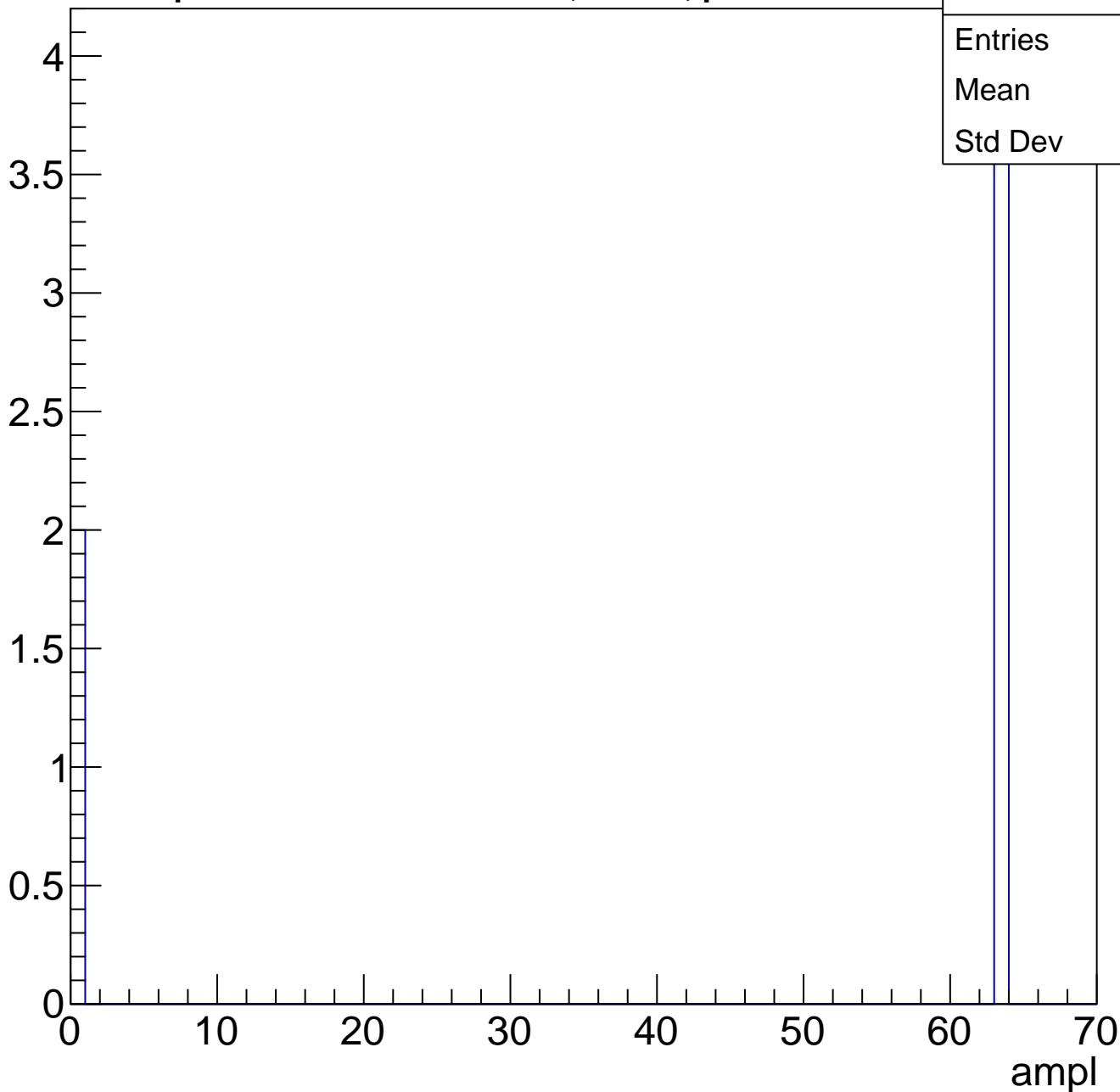




# B1L002S, U3-ch28, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch29, adc0

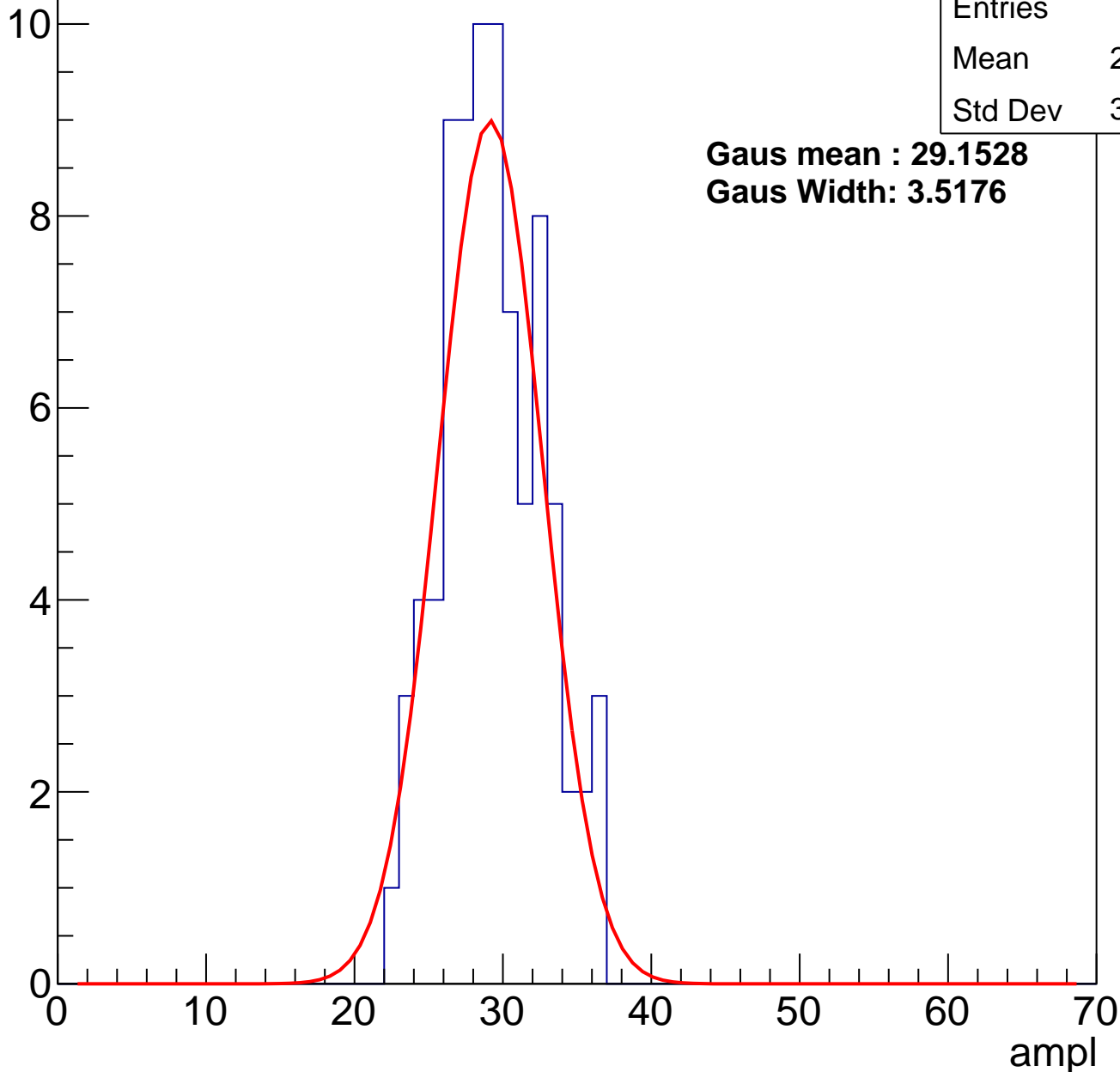
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	82
Mean	28.85
Std Dev	3.302

**Gaus mean : 29.1528**

**Gaus Width: 3.5176**

Entry



# B1L002S, U3-ch29, adc1

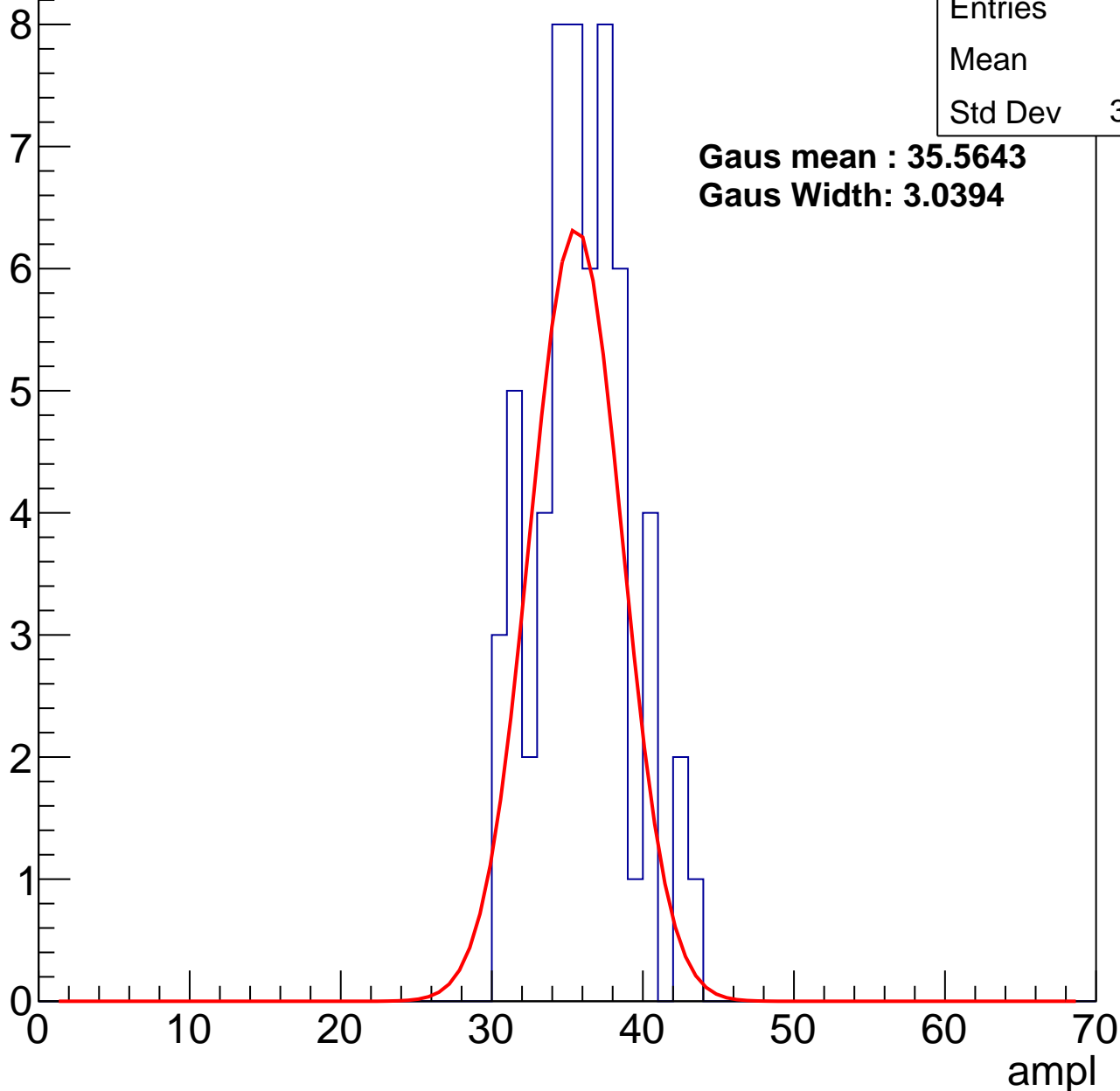
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.5
Std Dev	3.086

**Gaus mean : 35.5643**

**Gaus Width: 3.0394**



# B1L002S, U3-ch29, adc2

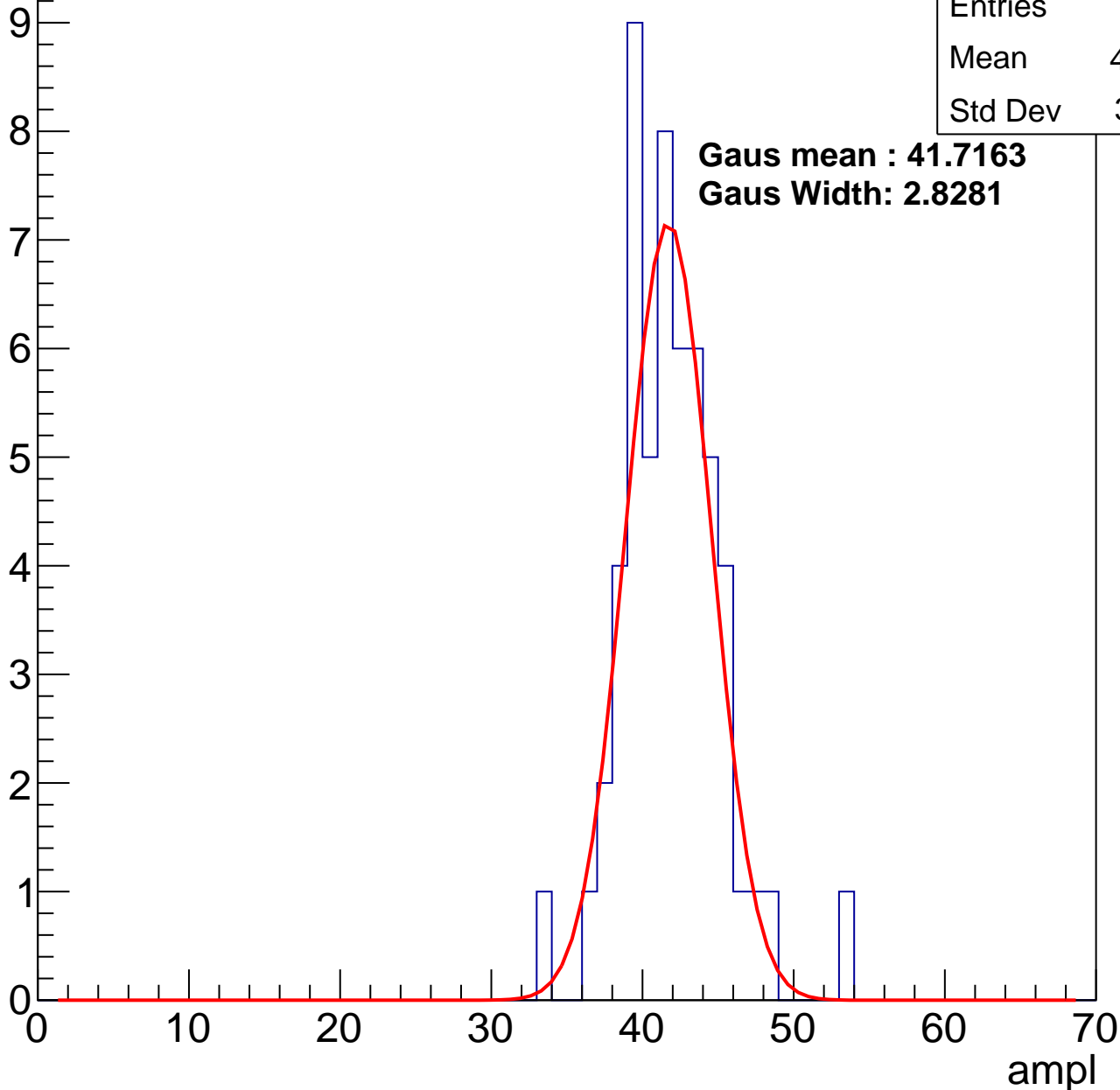
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.42
Std Dev	3.251

**Gaus mean : 41.7163**

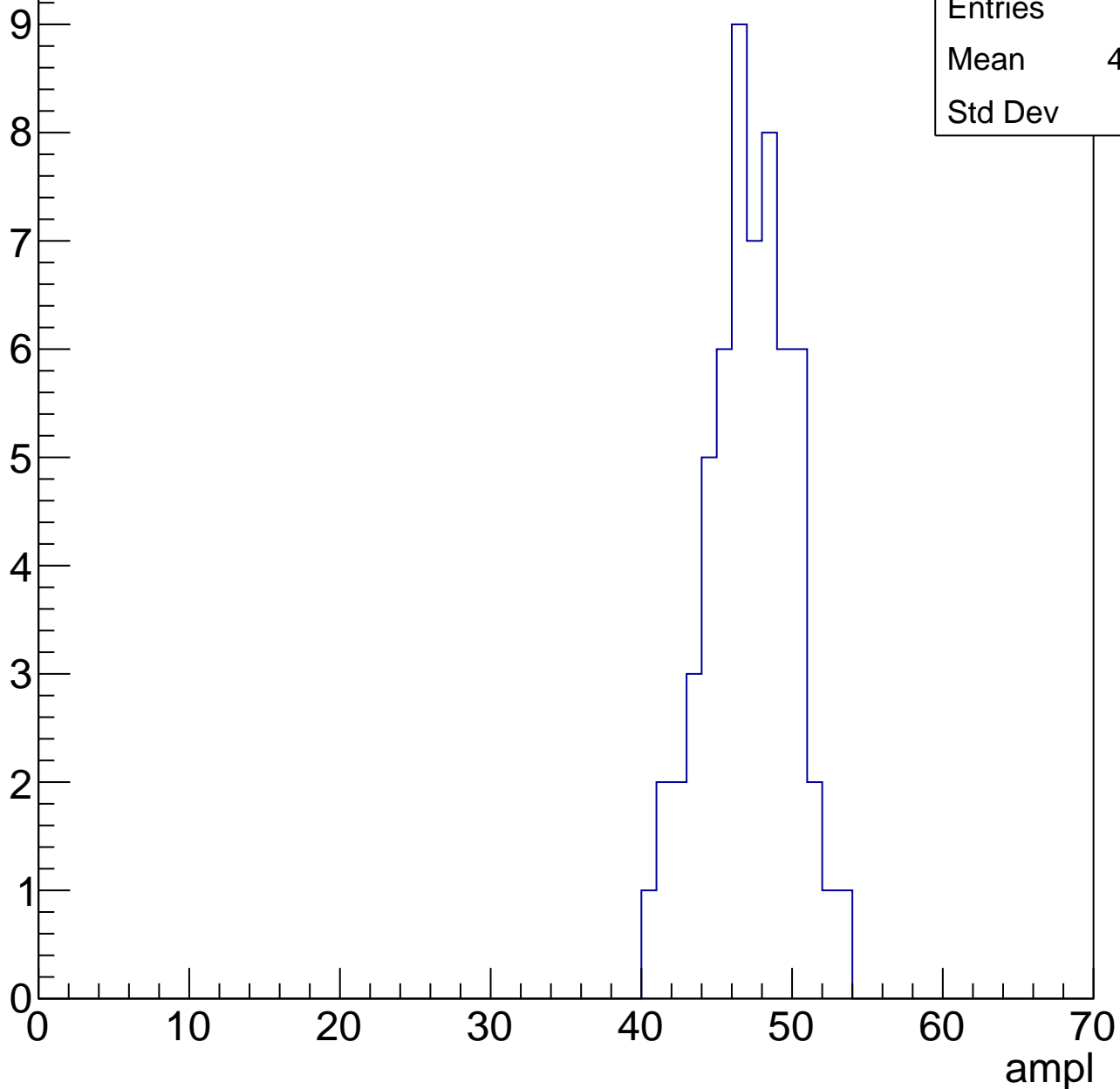
**Gaus Width: 2.8281**



# B1L002S, U3-ch29, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



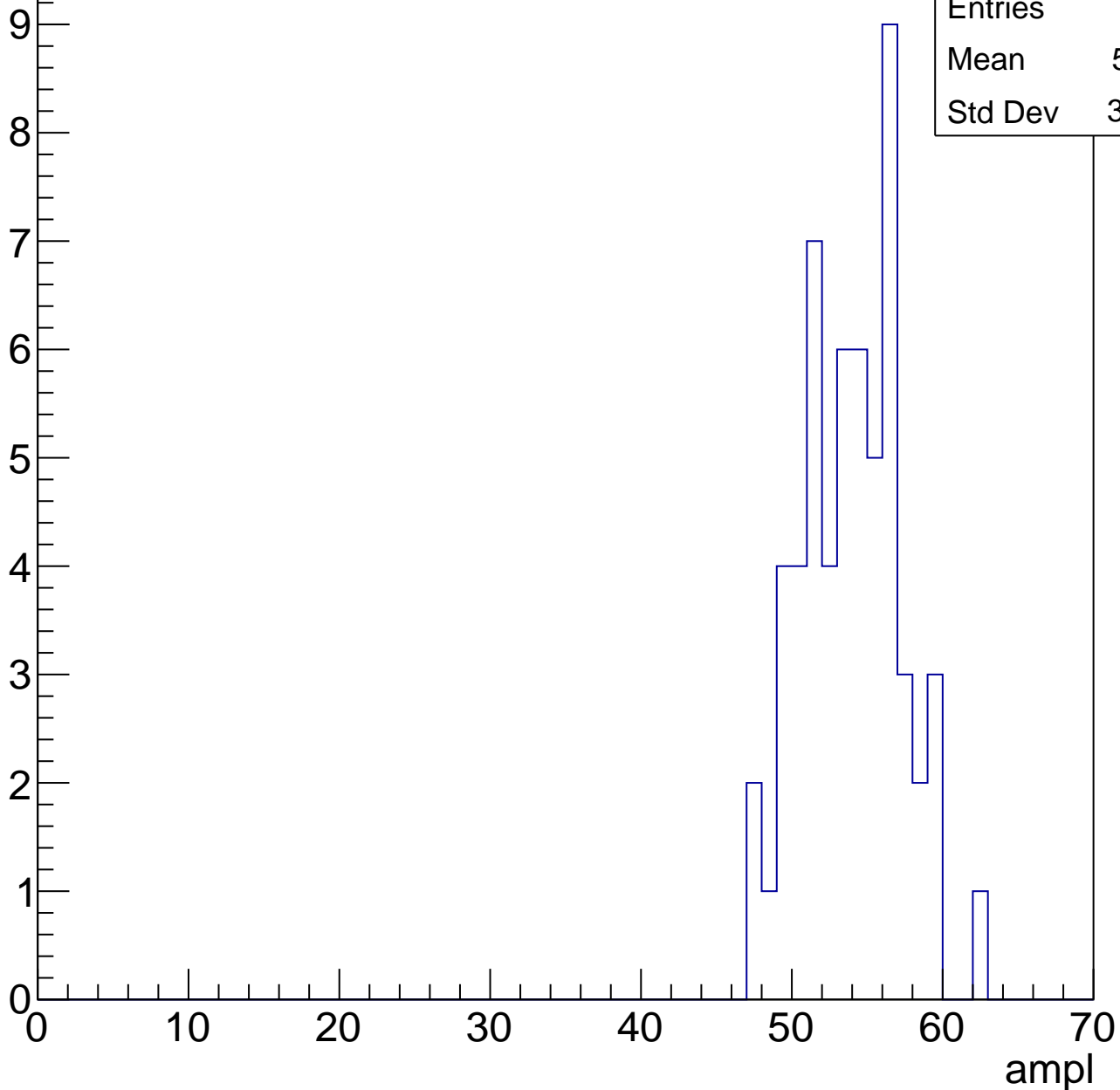
Entries	59
Mean	46.66
Std Dev	2.82

# B1L002S, U3-ch29, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	53.51
Std Dev	3.267

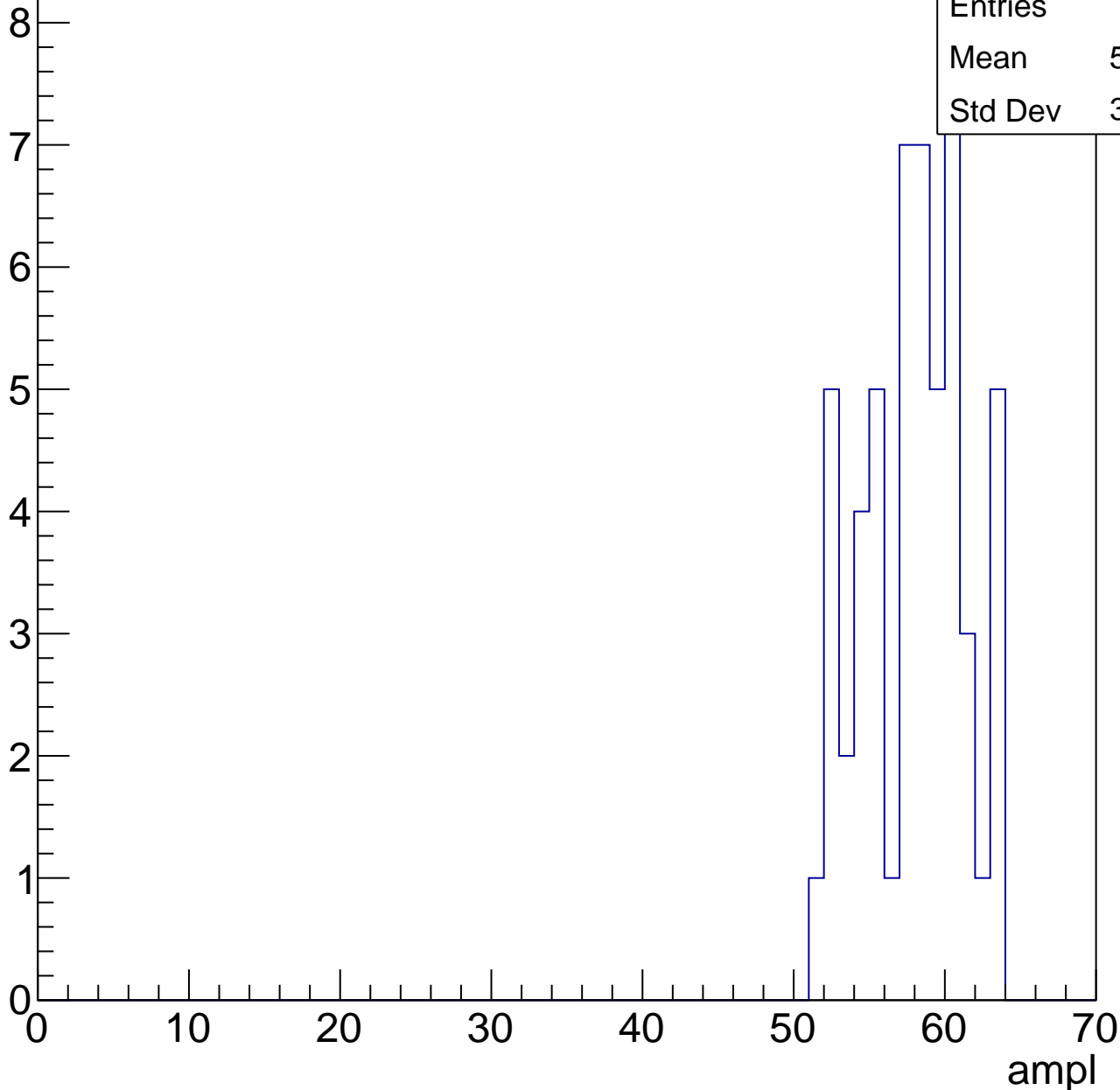


# B1L002S, U3-ch29, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	57.48
Std Dev	3.304

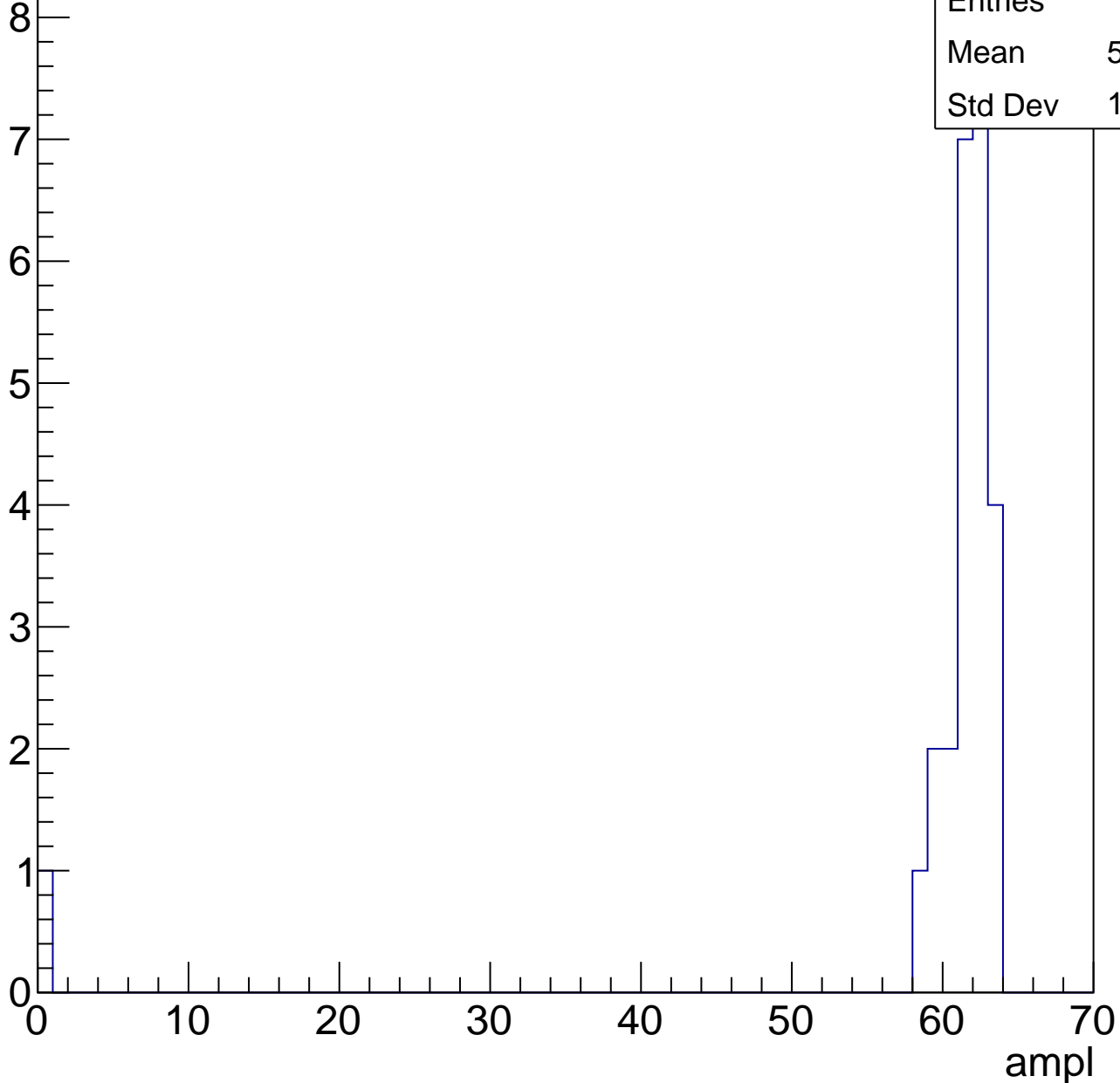


# B1L002S, U3-ch29, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.84
Std Dev	12.08

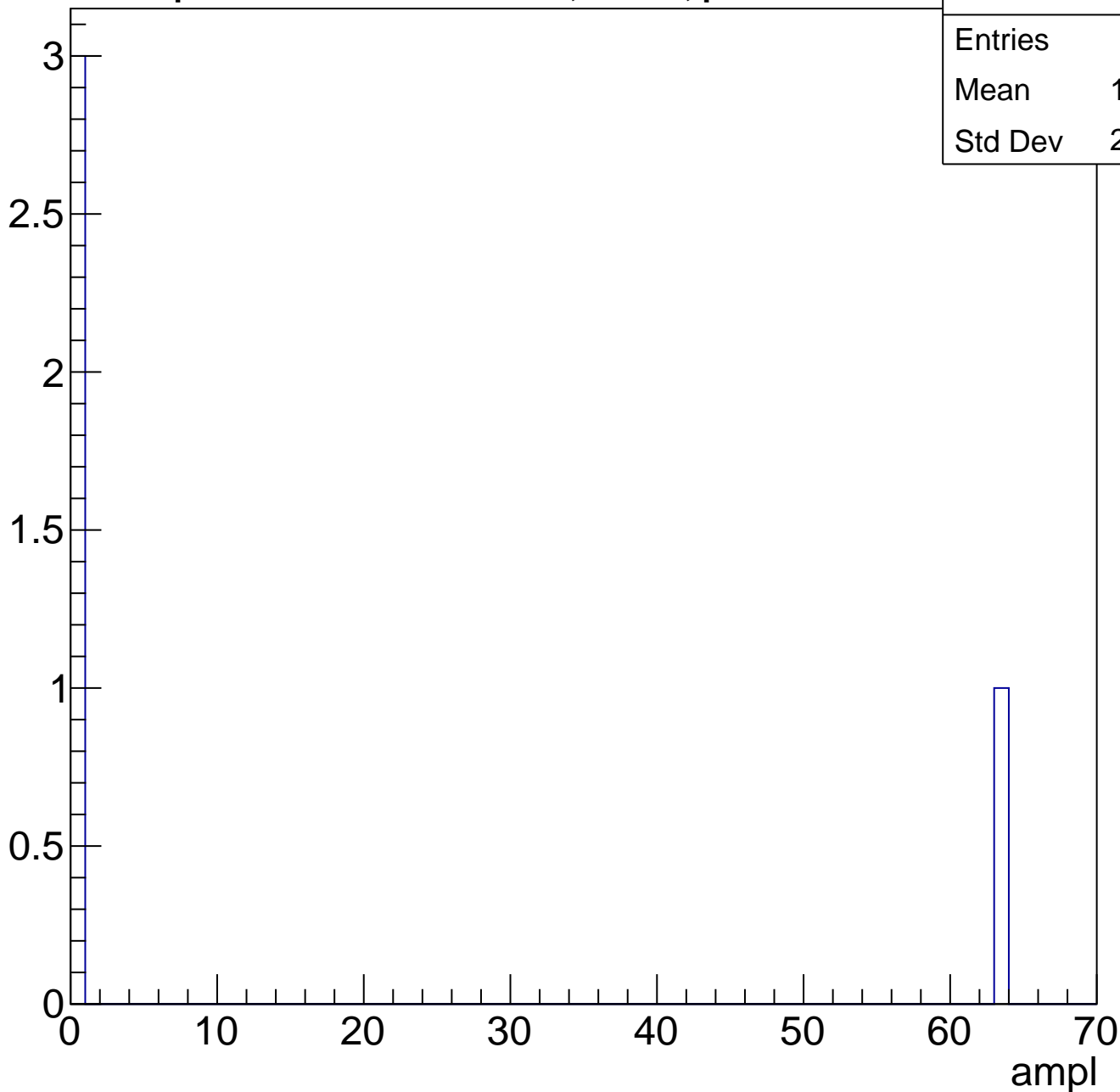




# B1L002S, U3-ch29, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch30, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	84
Mean	27.95
Std Dev	4.345

**Gaus mean : 28.8499**

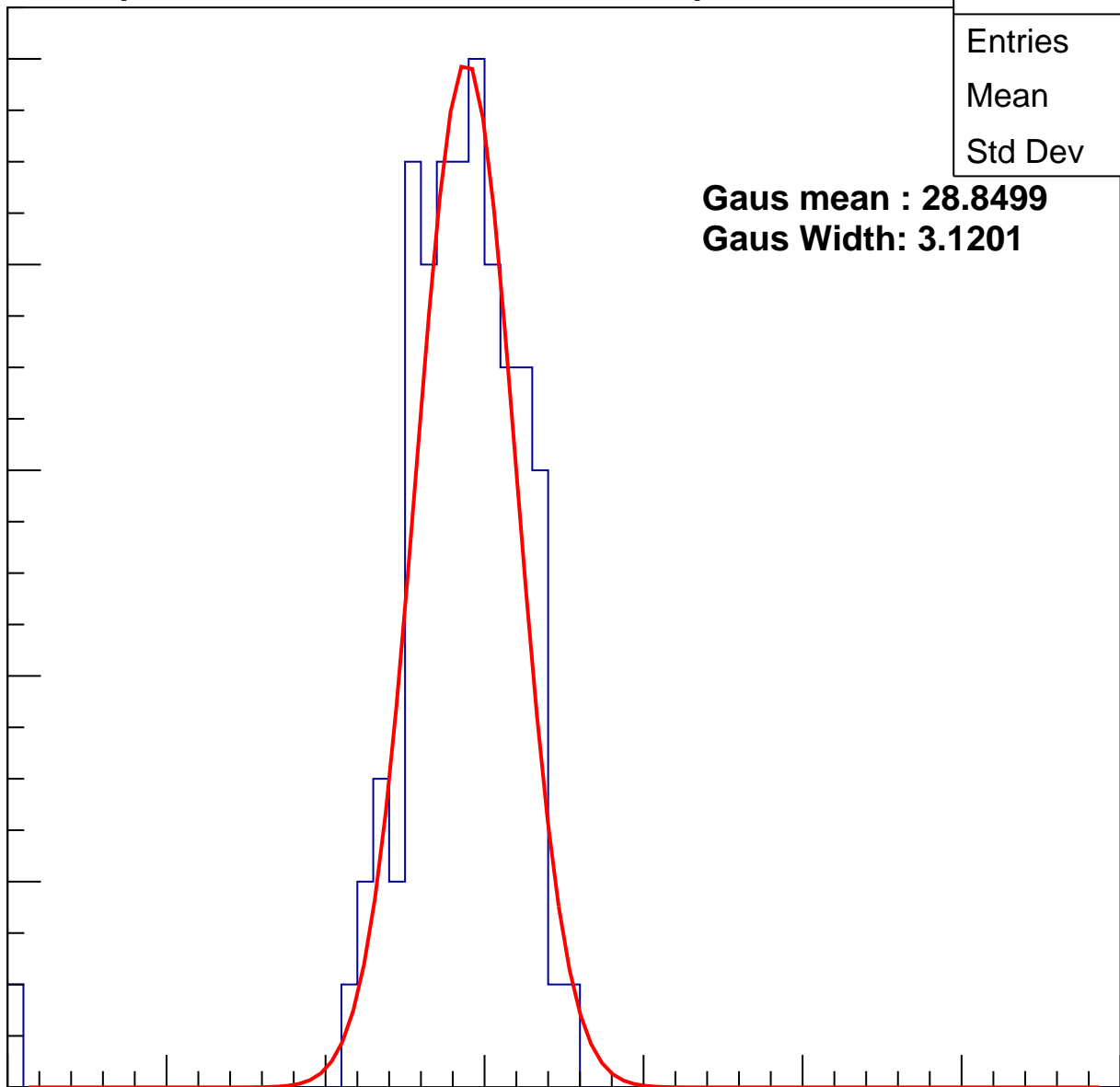
**Gaus Width: 3.1201**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch30, adc1

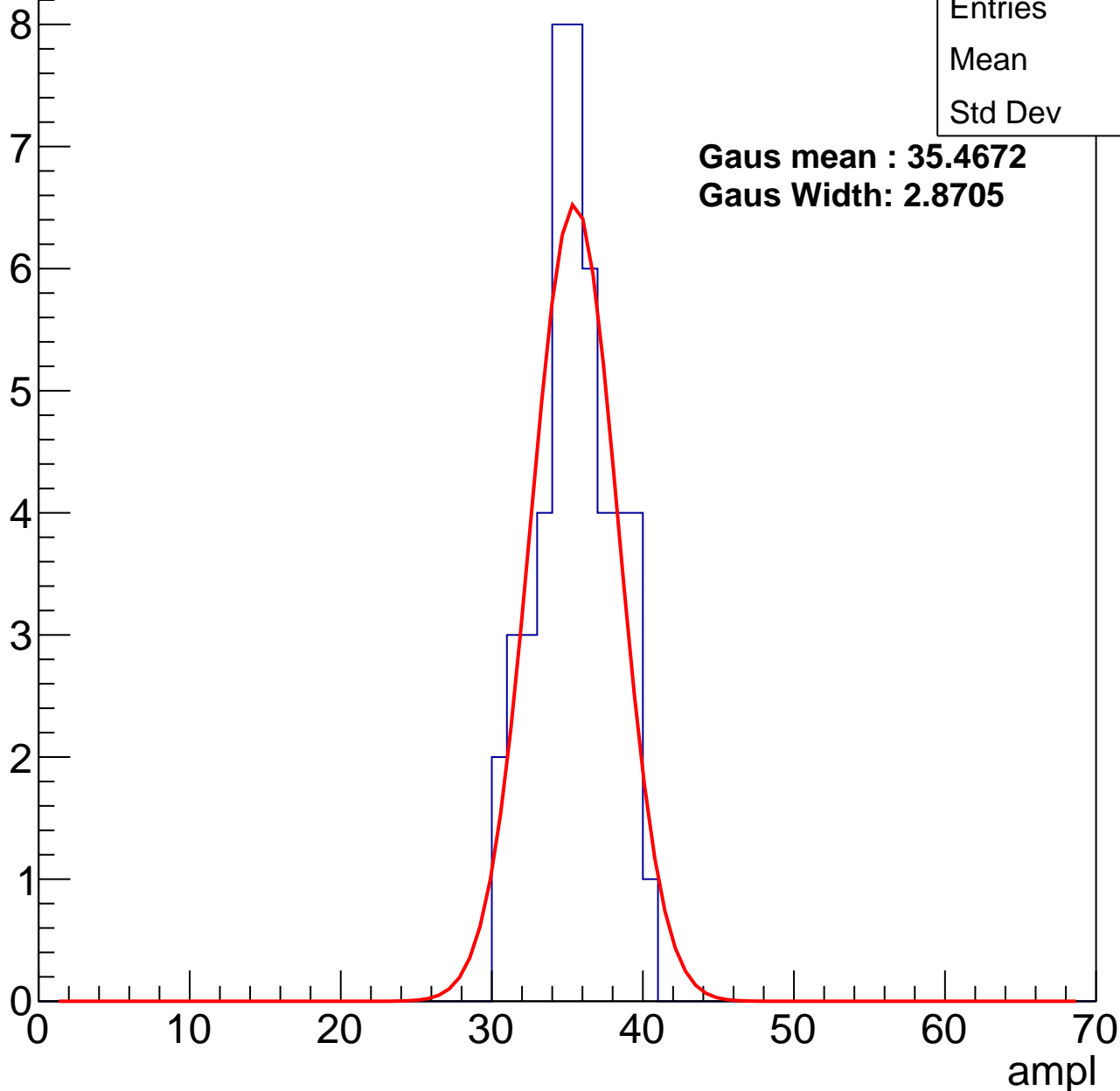
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	35
Std Dev	2.51

**Gaus mean : 35.4672**

**Gaus Width: 2.8705**



# B1L002S, U3-ch30, adc2

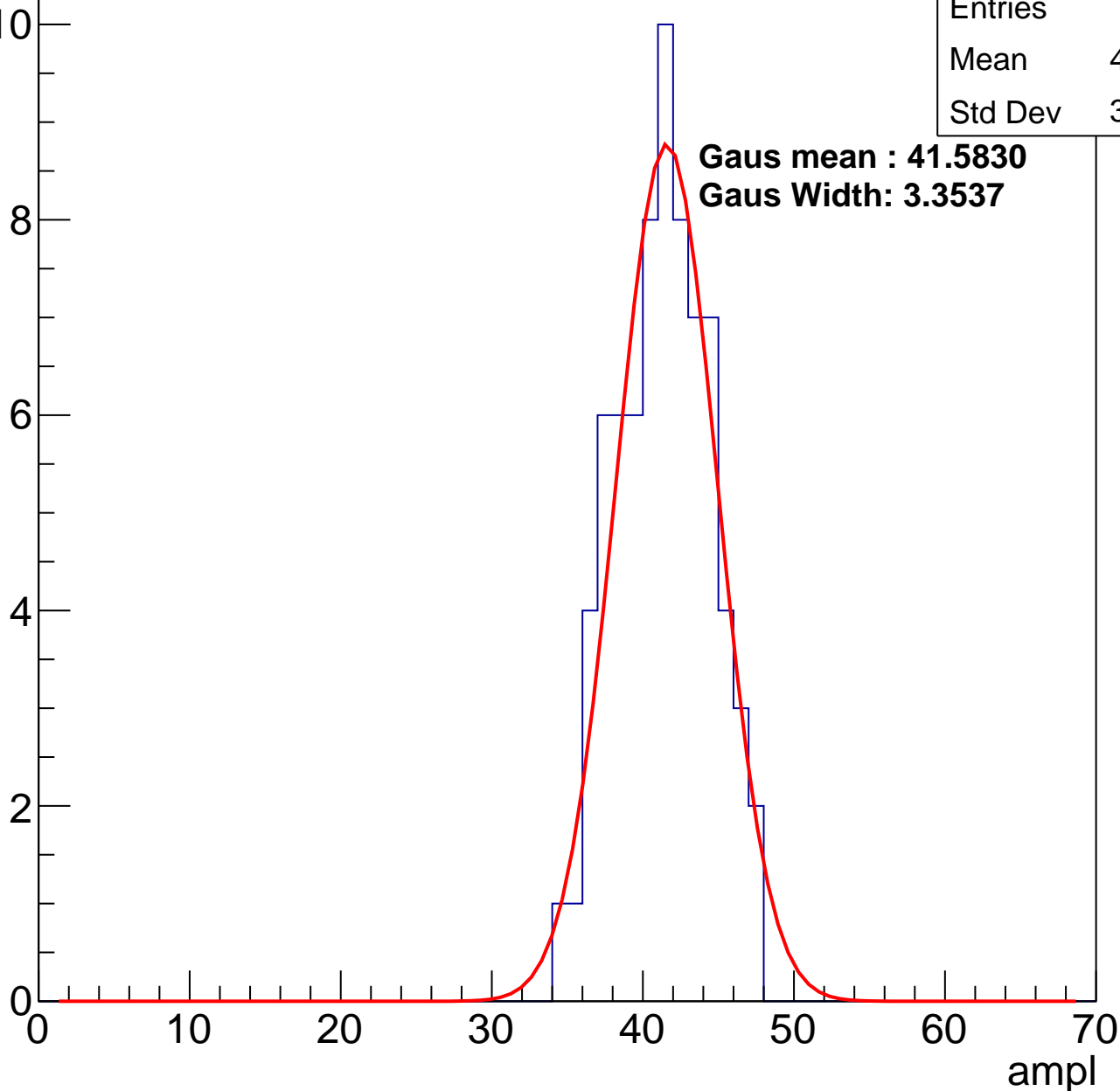
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	40.88
Std Dev	3.043

**Gaus mean : 41.5830**

**Gaus Width: 3.3537**

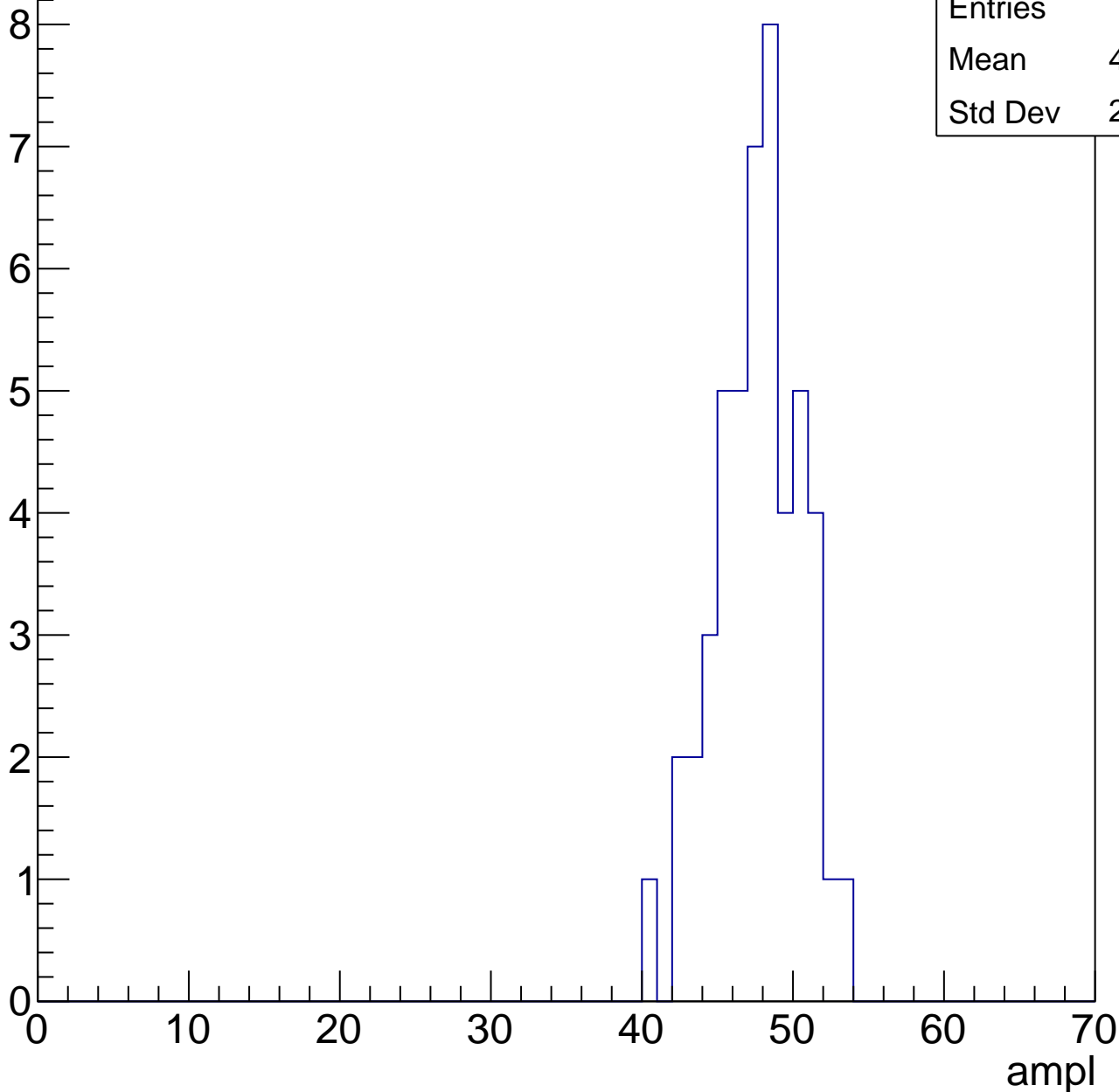


# B1L002S, U3-ch30, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	47.19
Std Dev	2.796

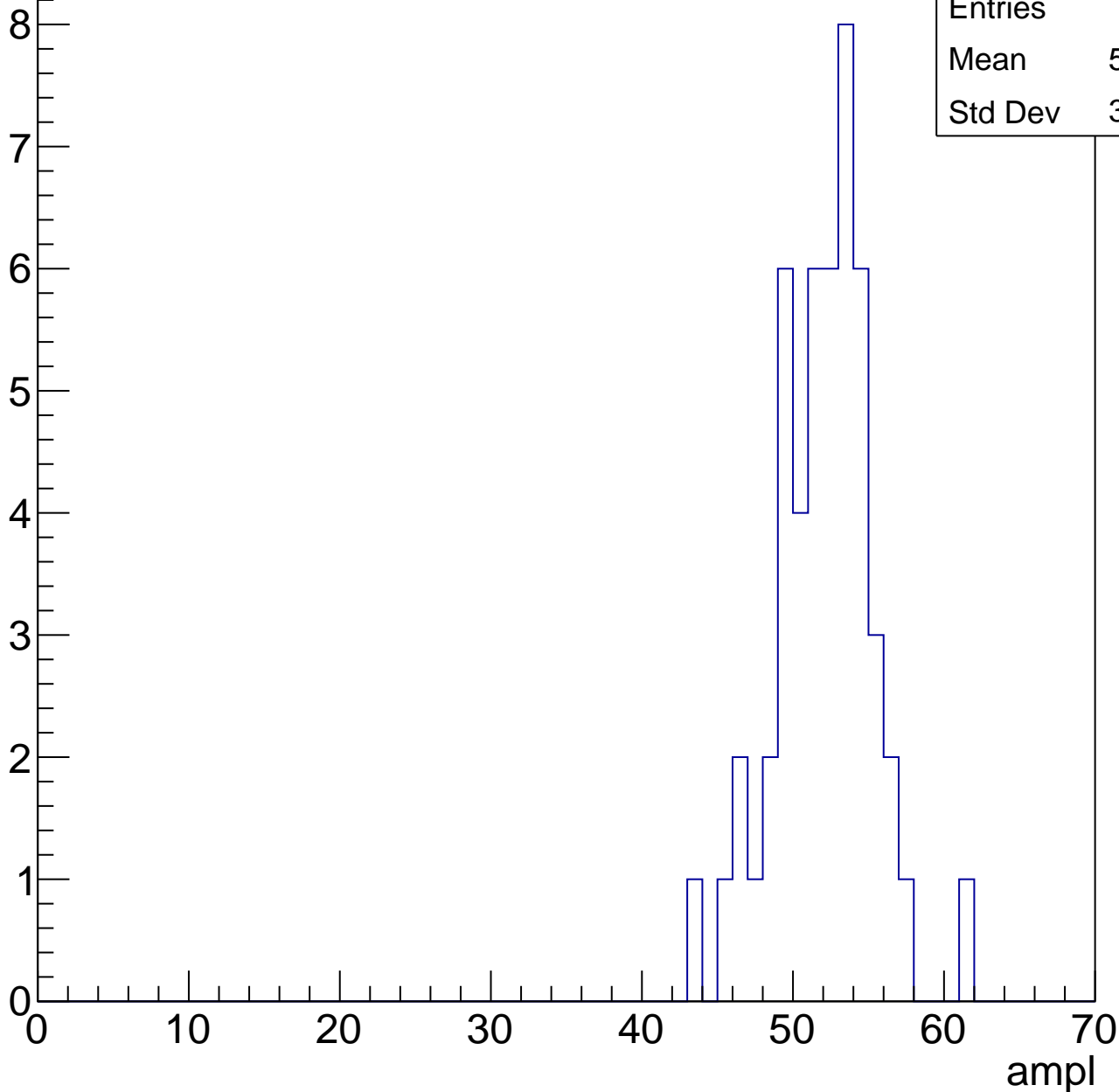


# B1L002S, U3-ch30, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	51.56
Std Dev	3.232

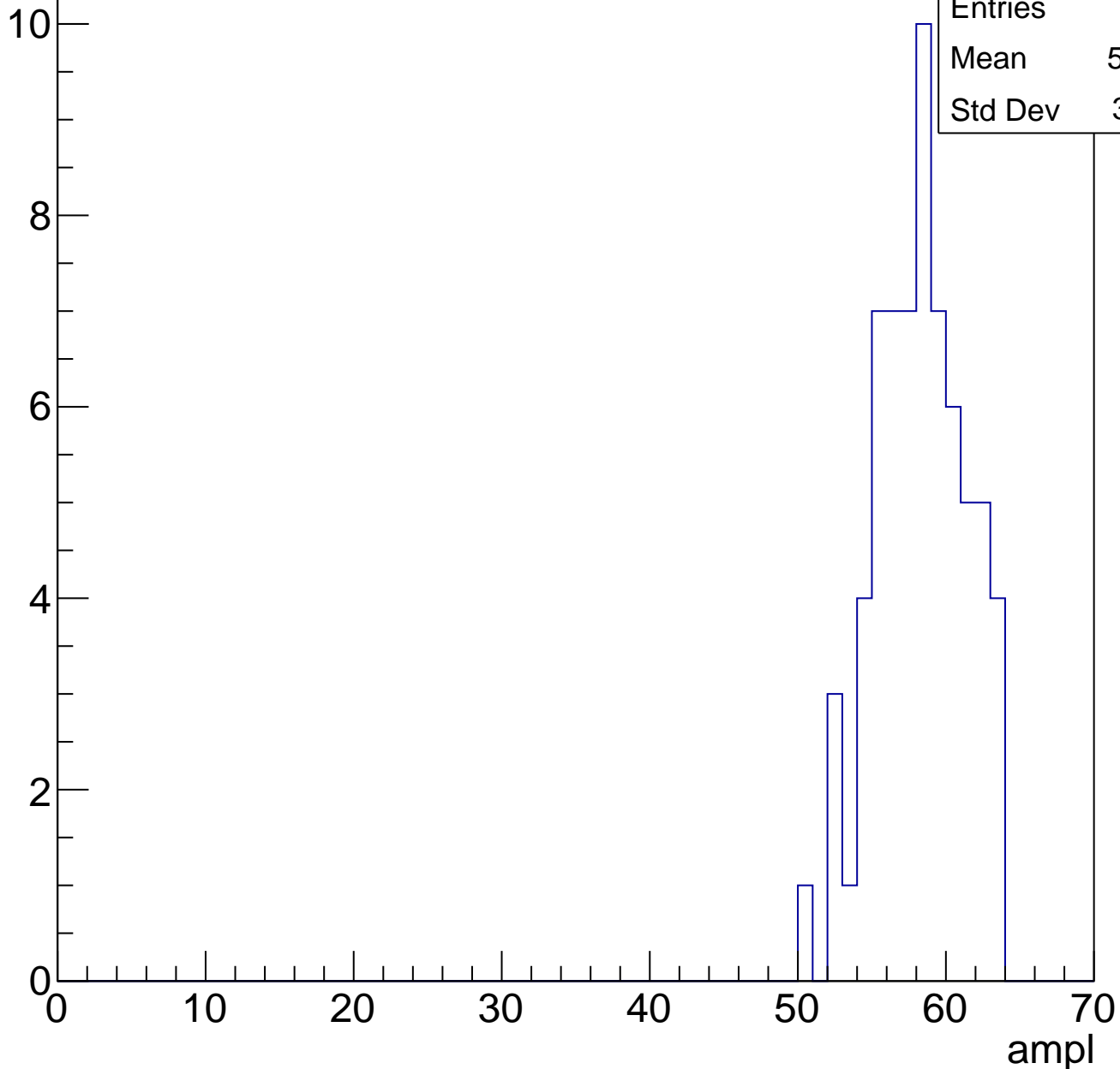


# B1L002S, U3-ch30, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	57.78
Std Dev	3.021

Entry

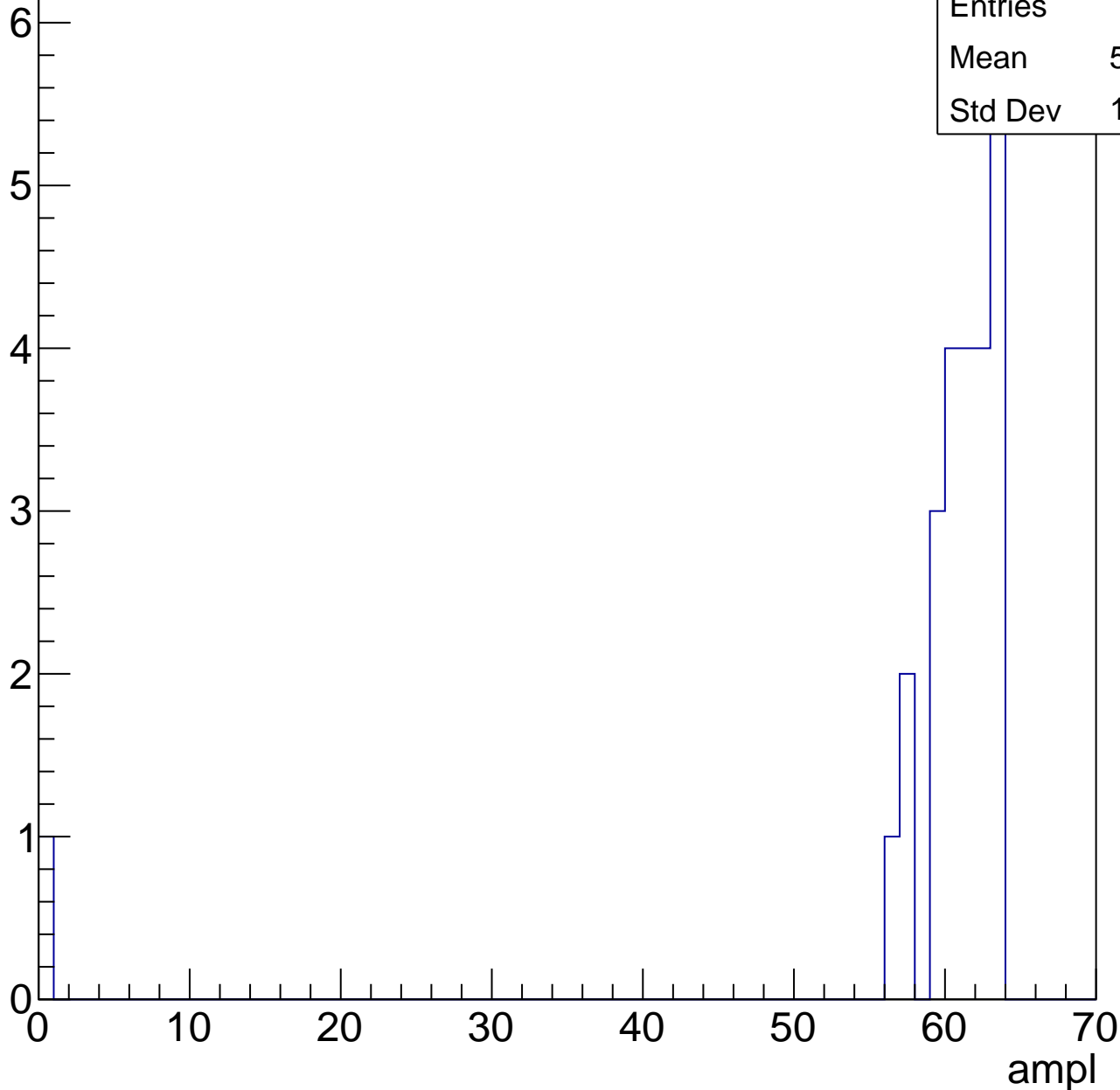


# B1L002S, U3-ch30, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.28
Std Dev	12.06





# B1L002S, U3-ch30, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B1L002S, U3-ch31, adc0

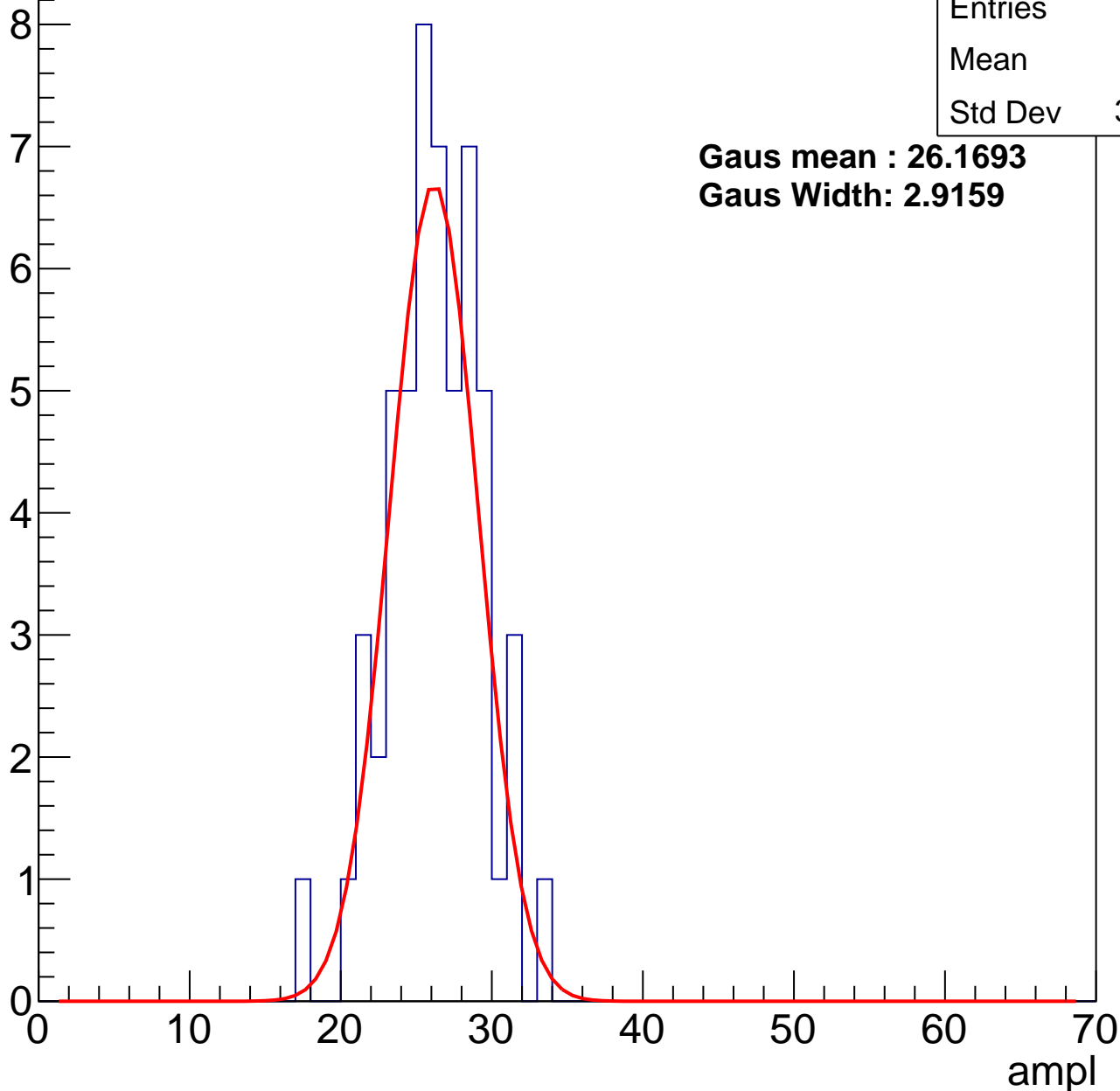
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	25.8
Std Dev	3.081

**Gaus mean : 26.1693**

**Gaus Width: 2.9159**



# B1L002S, U3-ch31, adc1

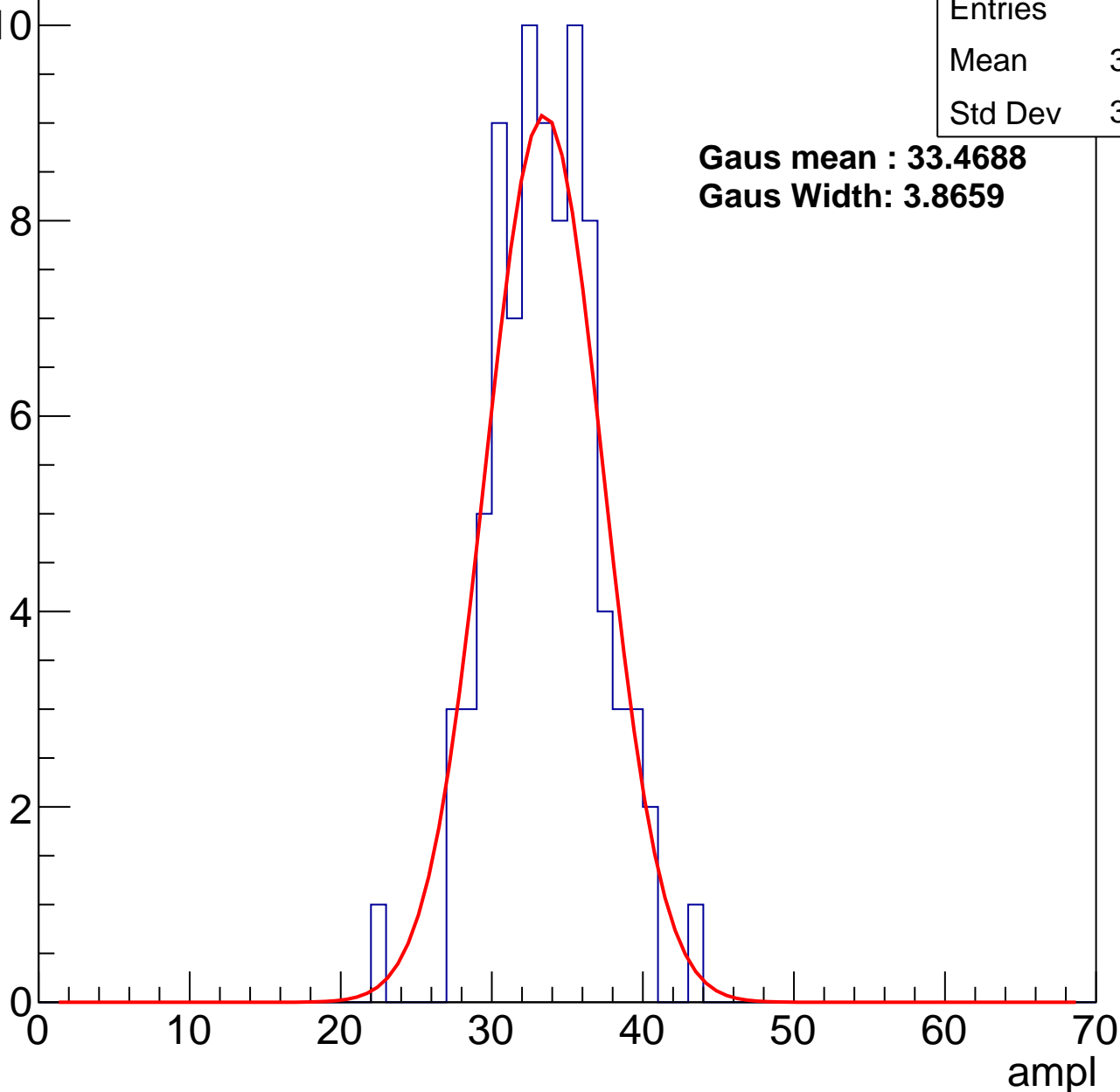
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	86
Mean	33.12
Std Dev	3.522

**Gaus mean : 33.4688**

**Gaus Width: 3.8659**



# B1L002S, U3-ch31, adc2

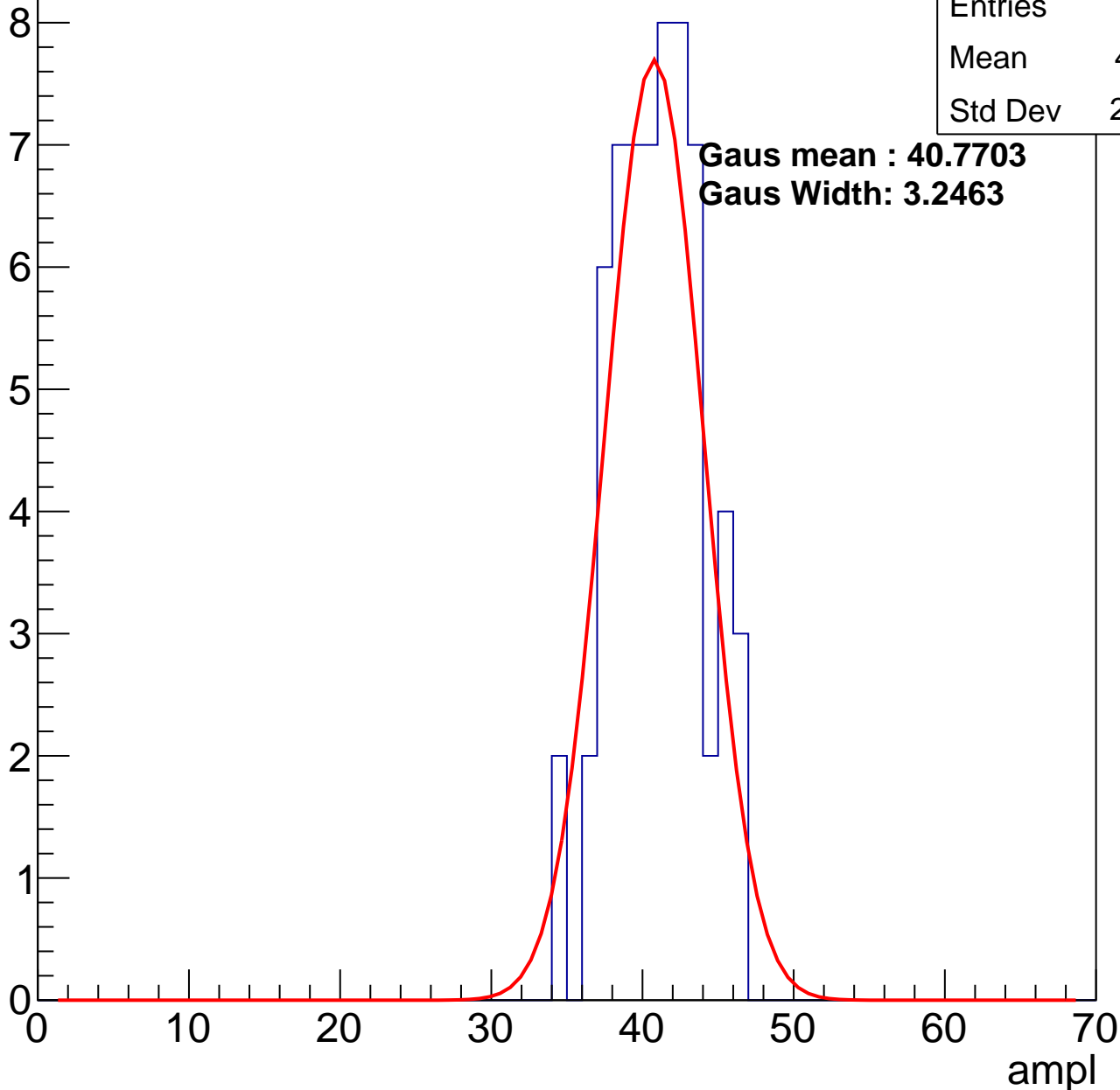
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	40.51
Std Dev	2.872

**Gaus mean : 40.7703**

**Gaus Width: 3.2463**

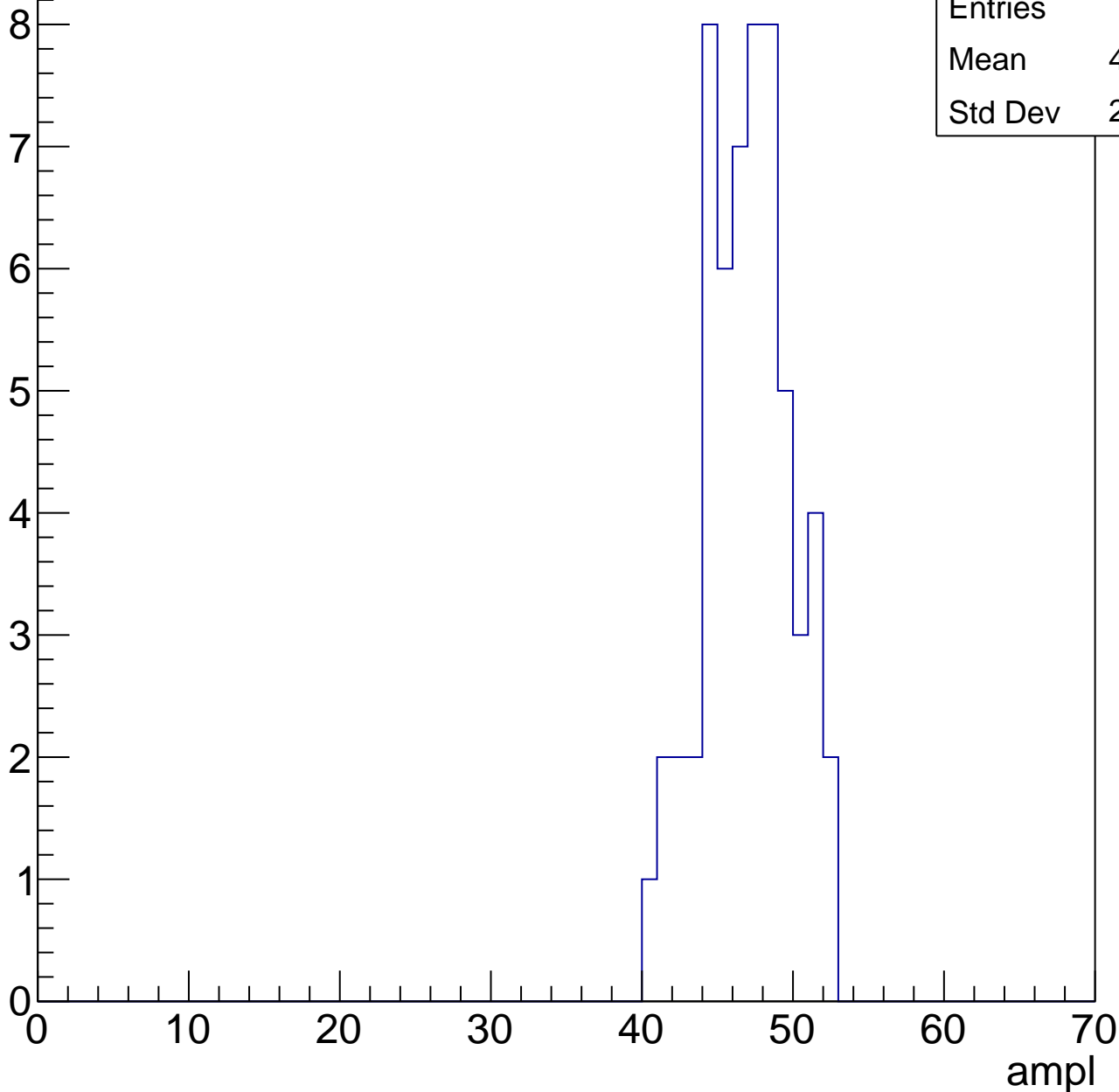


# B1L002S, U3-ch31, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

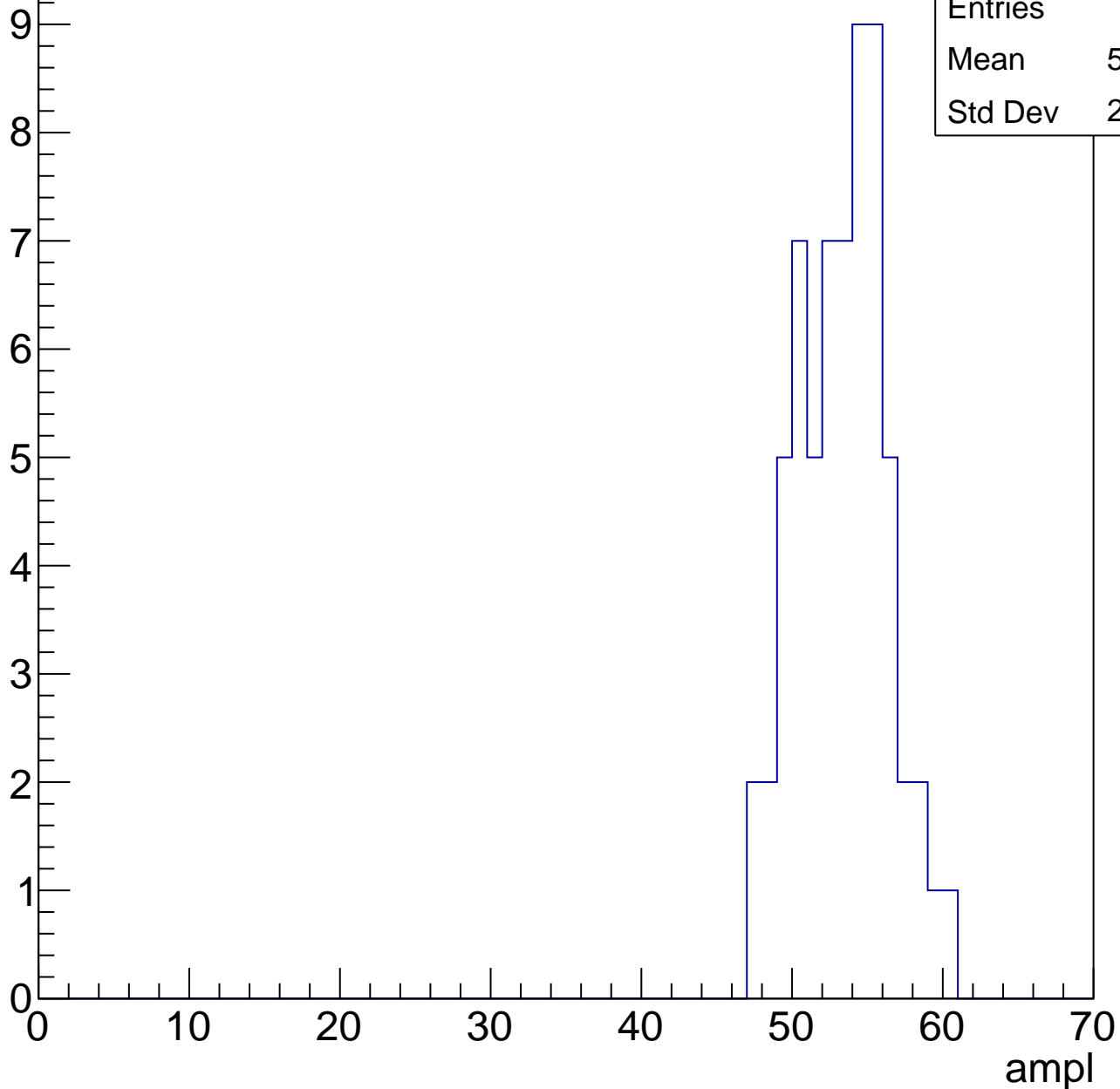
Entries	58
Mean	46.53
Std Dev	2.824



# B1L002S, U3-ch31, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



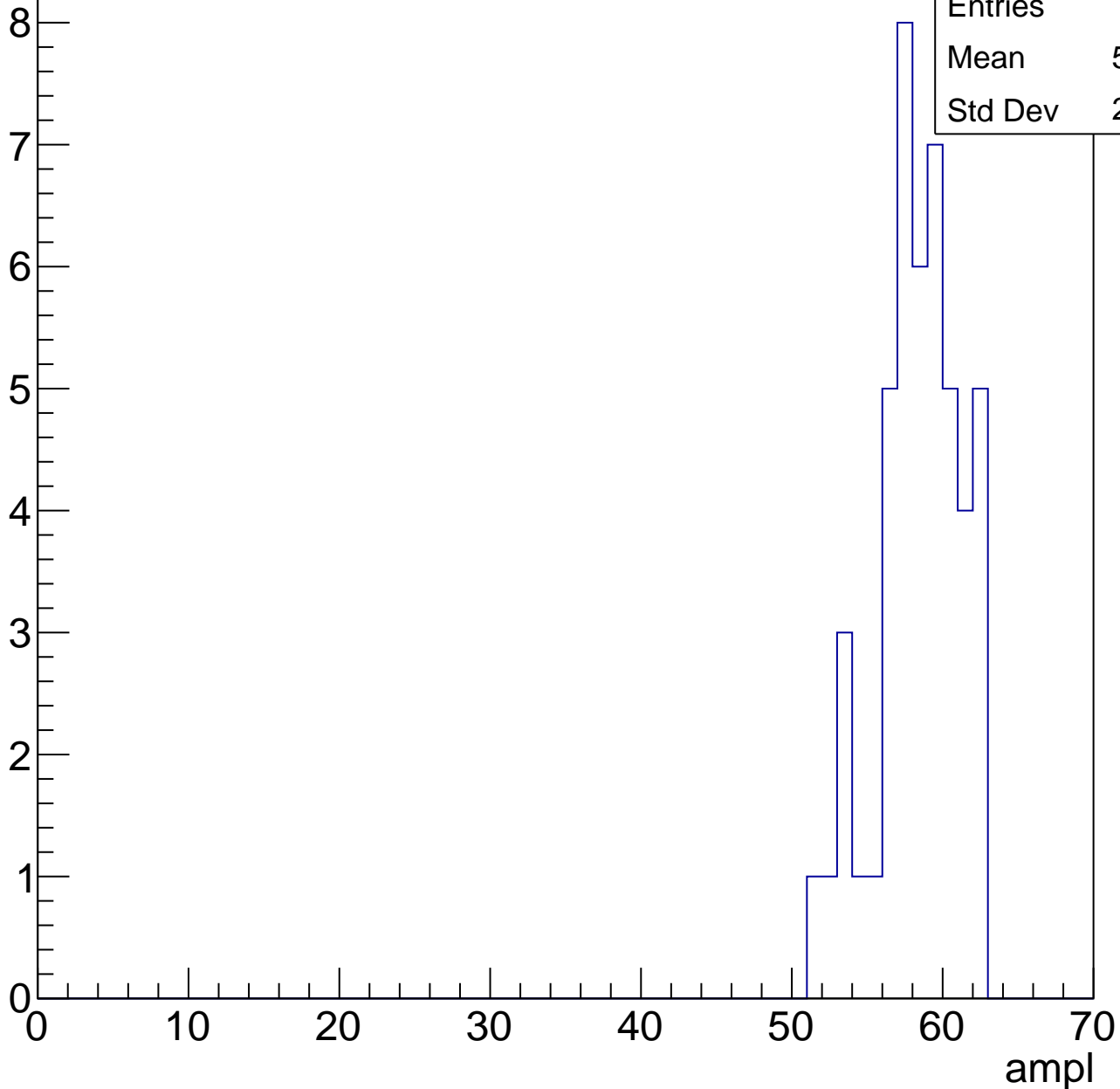
Entries	64
Mean	52.89
Std Dev	2.927

# B1L002S, U3-ch31, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.91
Std Dev	2.751

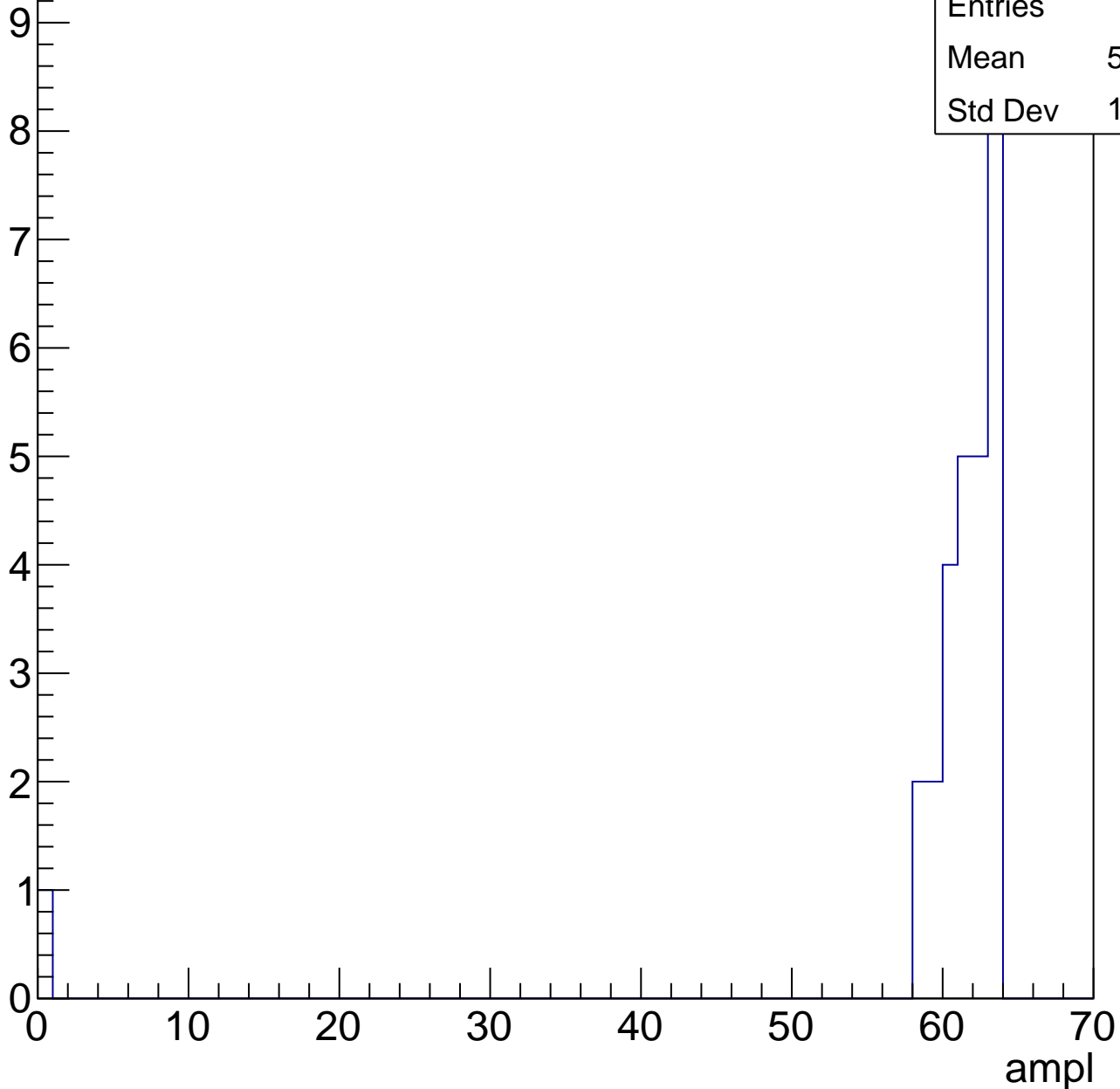


# B1L002S, U3-ch31, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	59.14
Std Dev	11.49

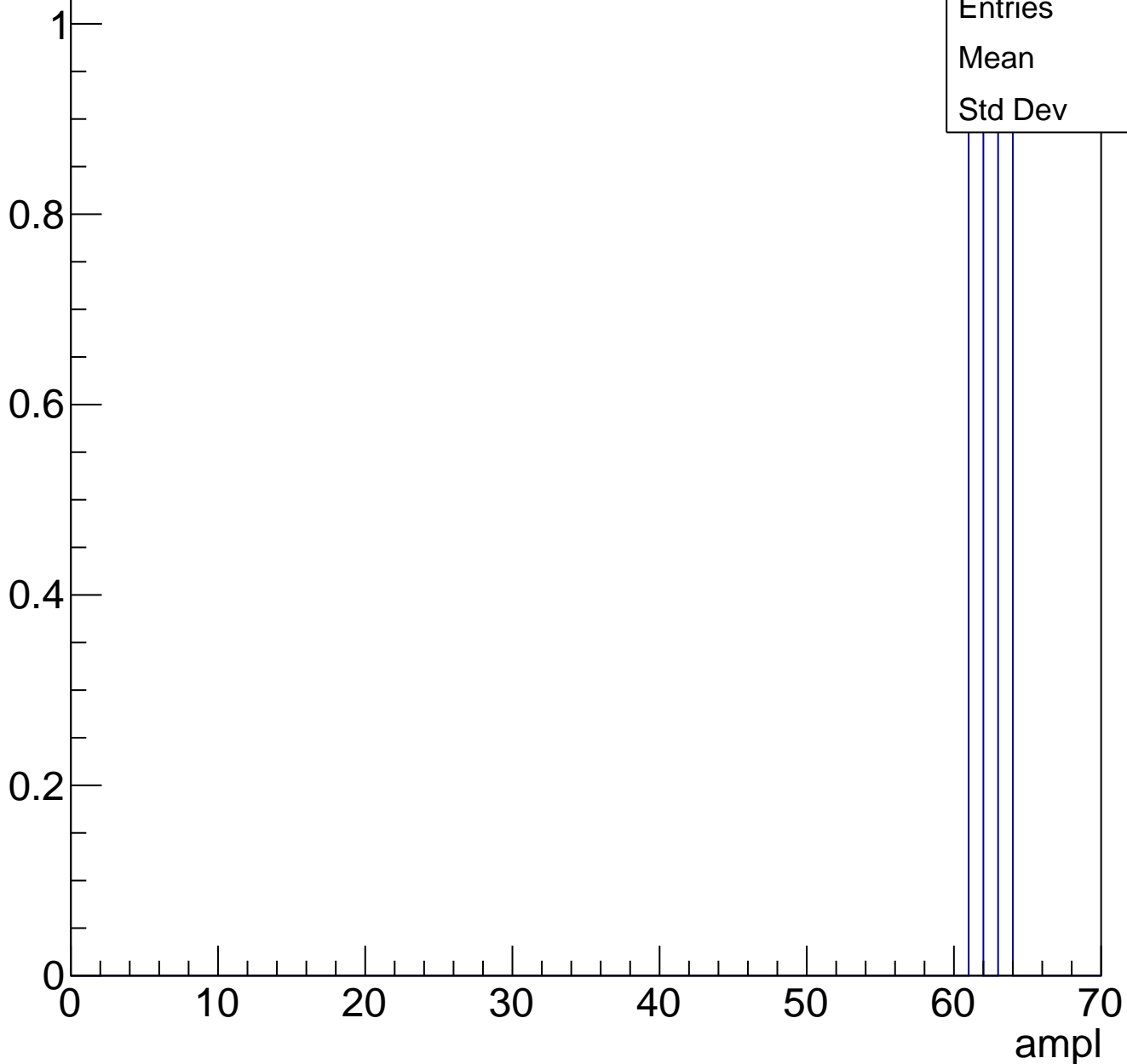




# B1L002S, U3-ch31, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch32, adc0

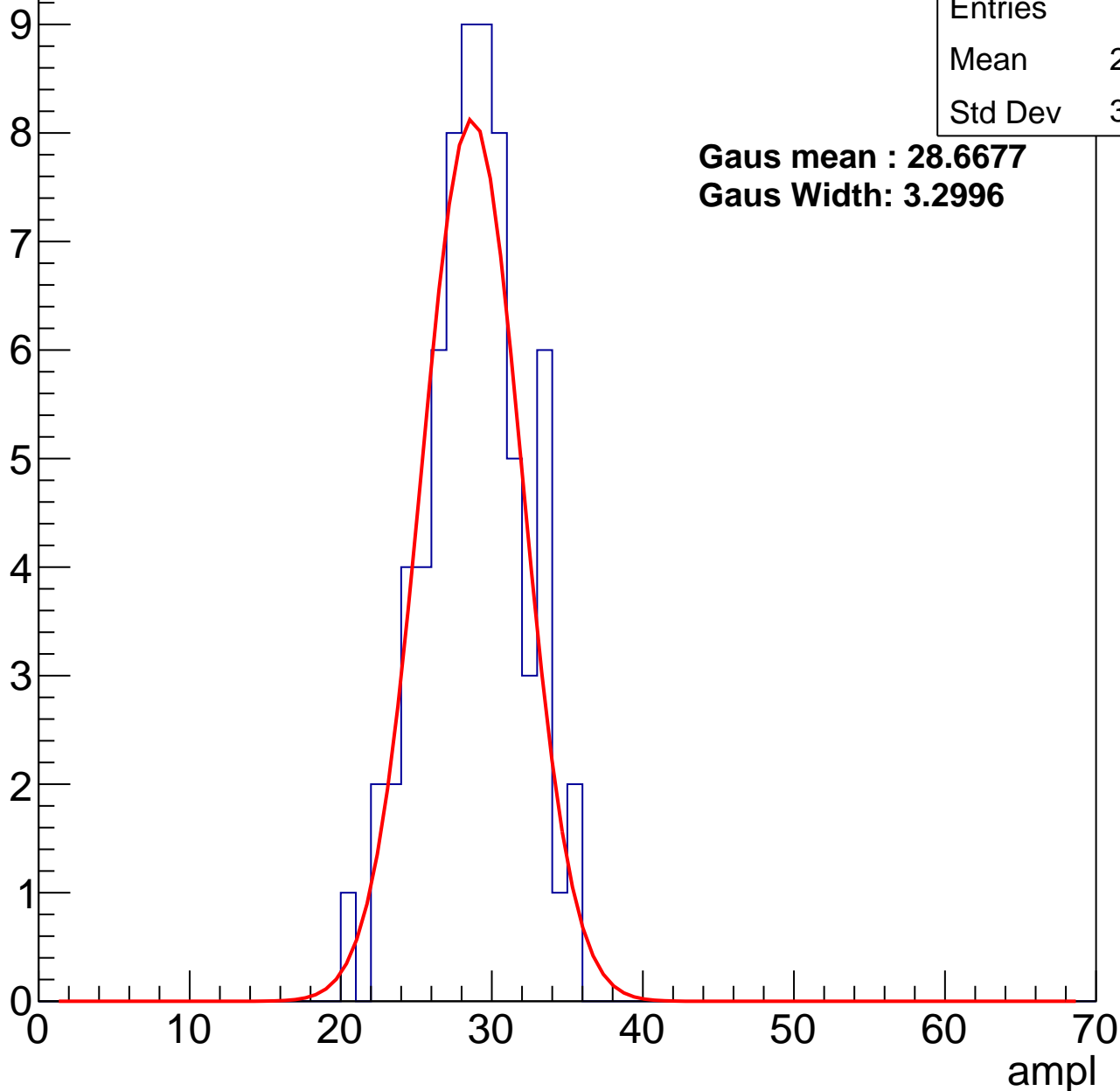
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	28.34
Std Dev	3.224

**Gaus mean : 28.6677**

**Gaus Width: 3.2996**



# B1L002S, U3-ch32, adc1

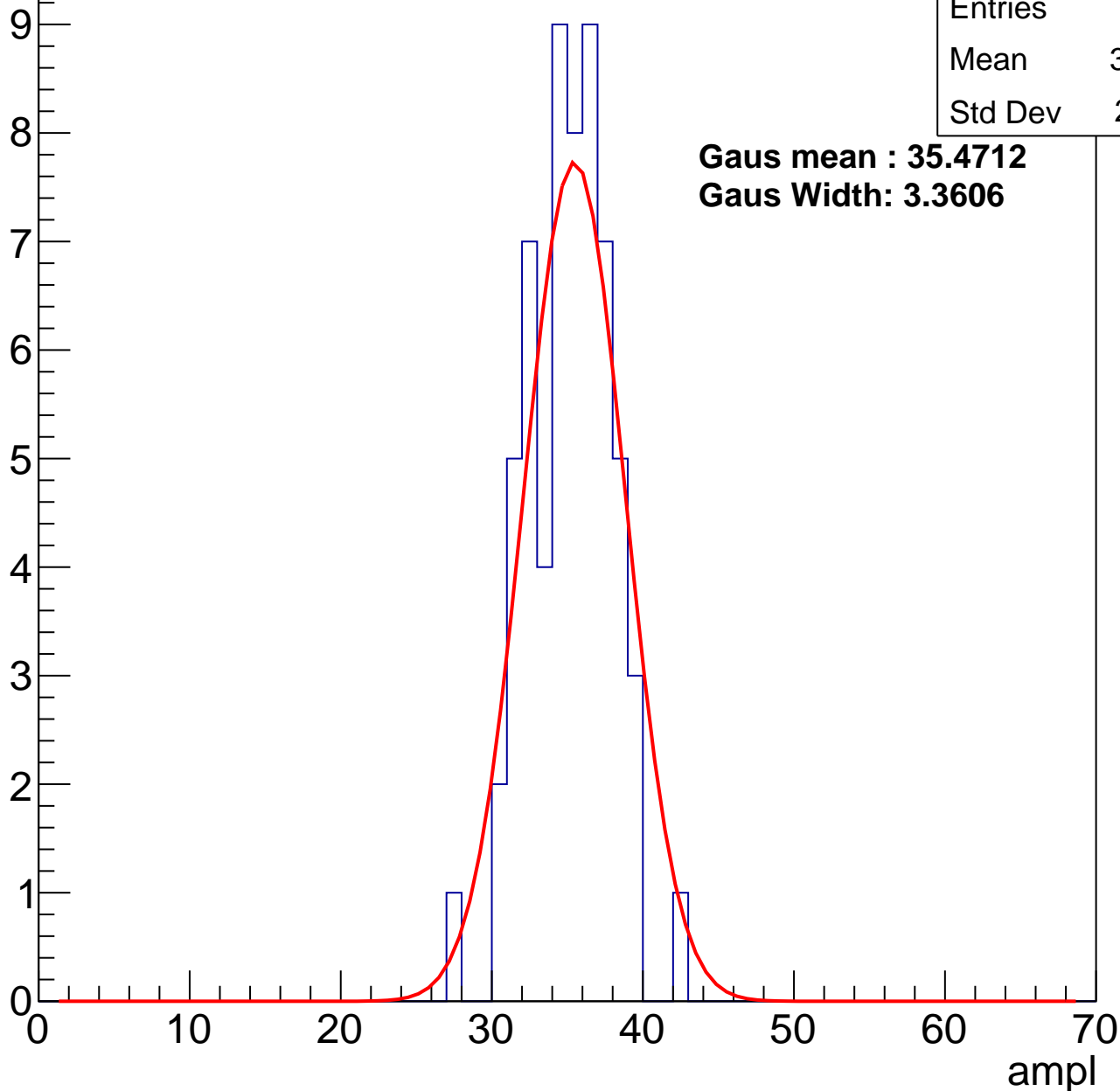
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34.69
Std Dev	2.731

**Gaus mean : 35.4712**

**Gaus Width: 3.3606**



# B1L002S, U3-ch32, adc2

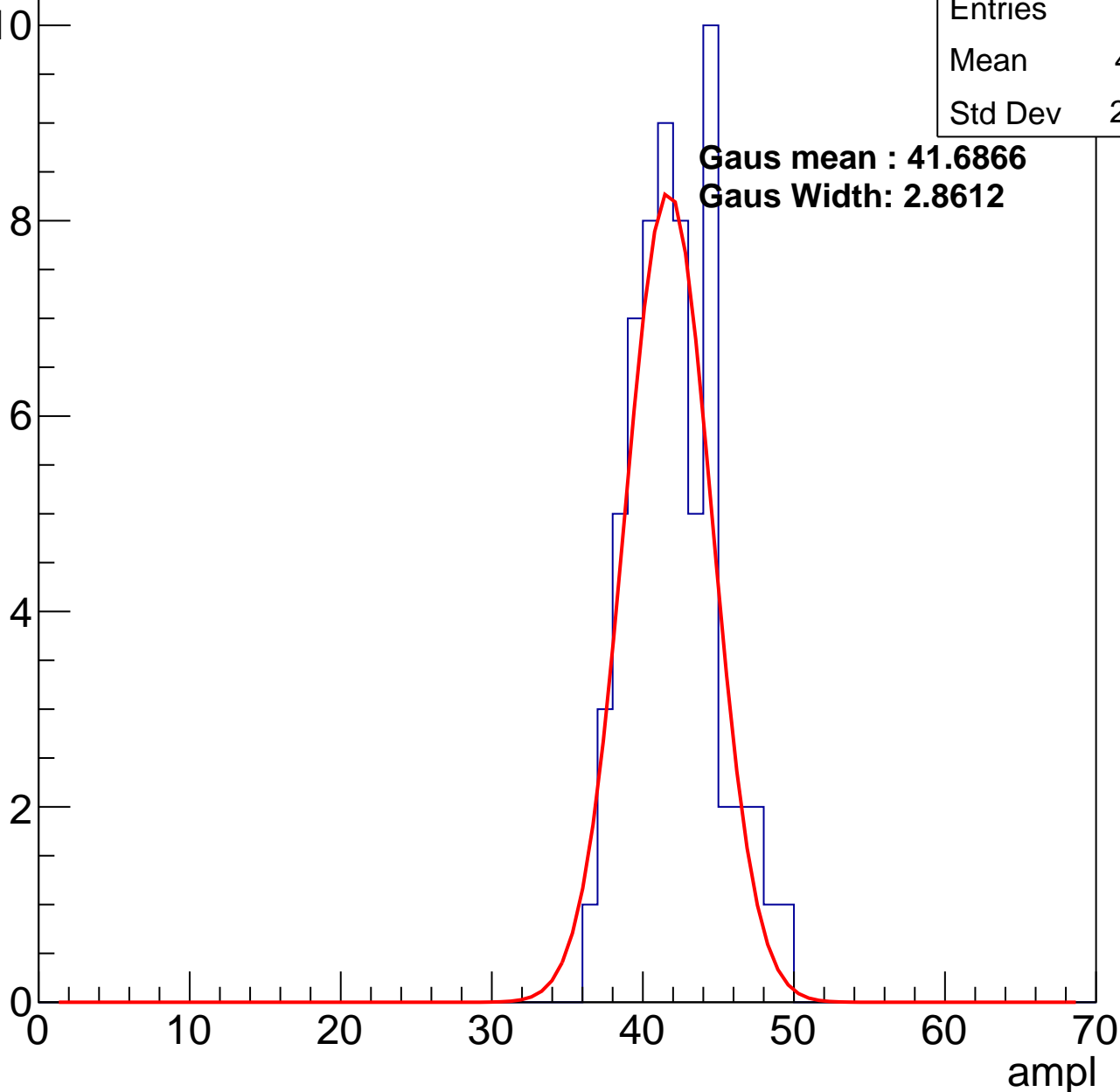
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	41.61
Std Dev	2.837

**Gaus mean : 41.6866**

**Gaus Width: 2.8612**

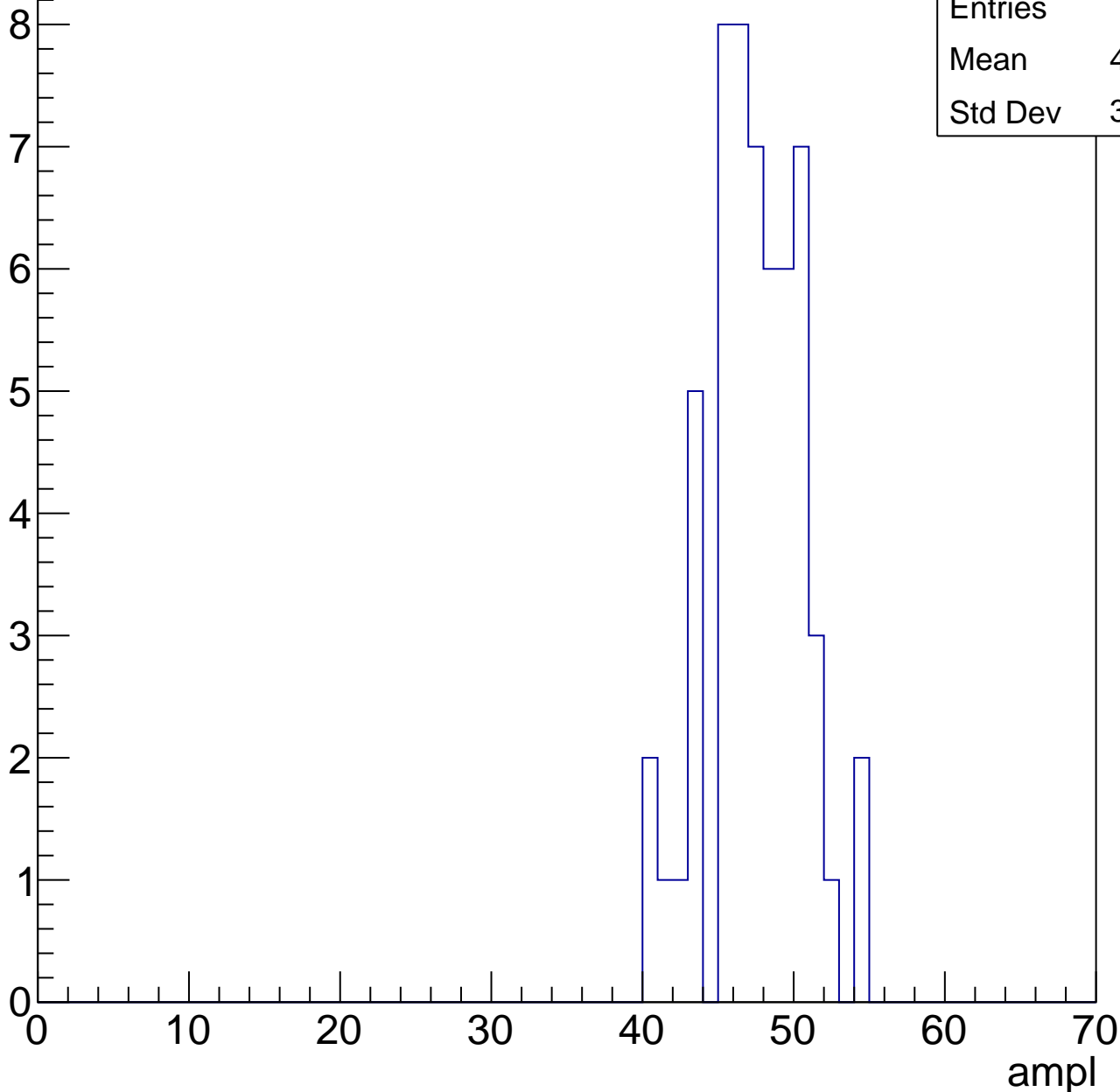


# B1L002S, U3-ch32, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	47.02
Std Dev	3.086

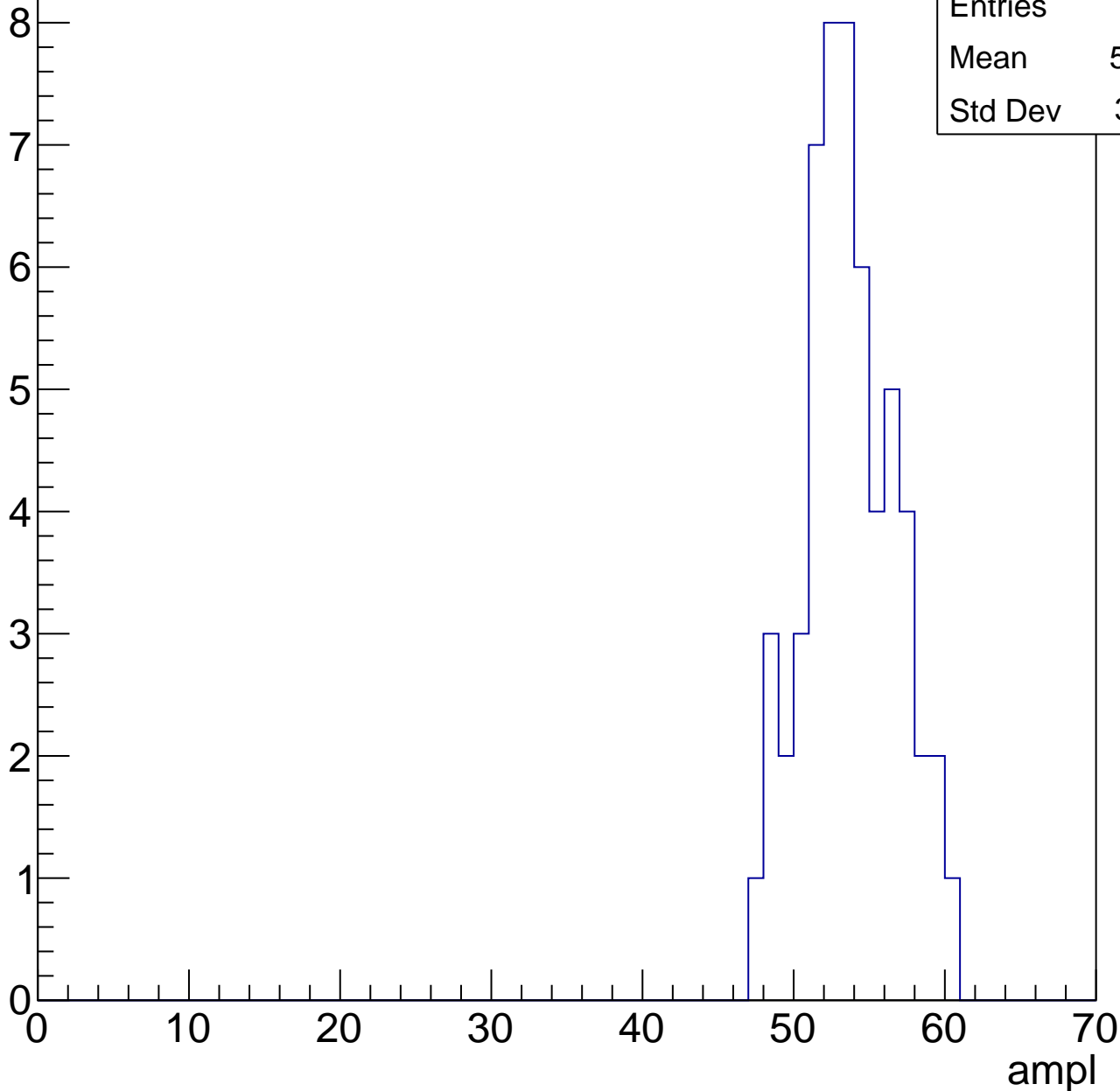


# B1L002S, U3-ch32, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.25
Std Dev	3.001



# B1L002S, U3-ch32, adc5

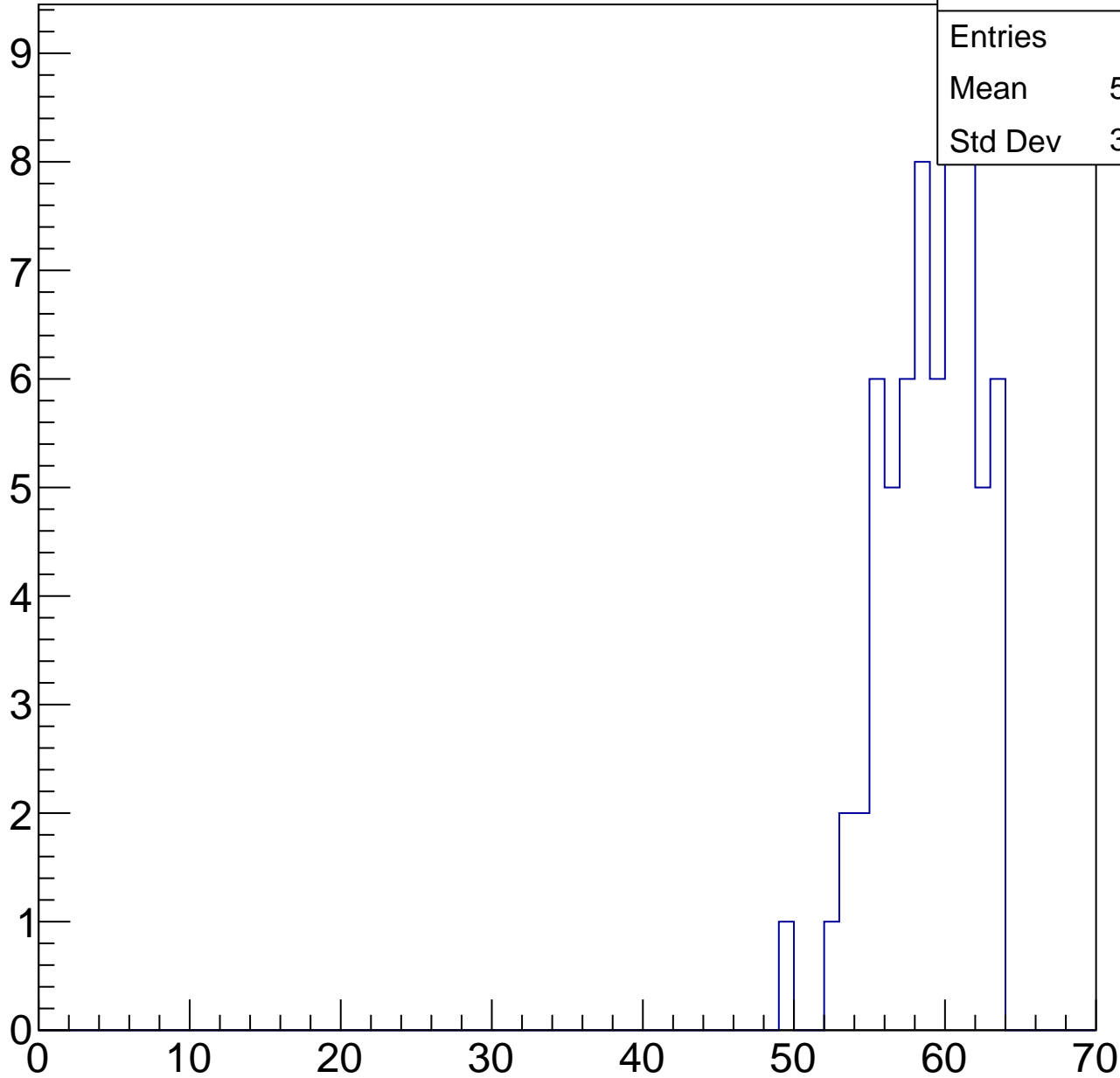
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	65
Mean	58.48
Std Dev	3.059

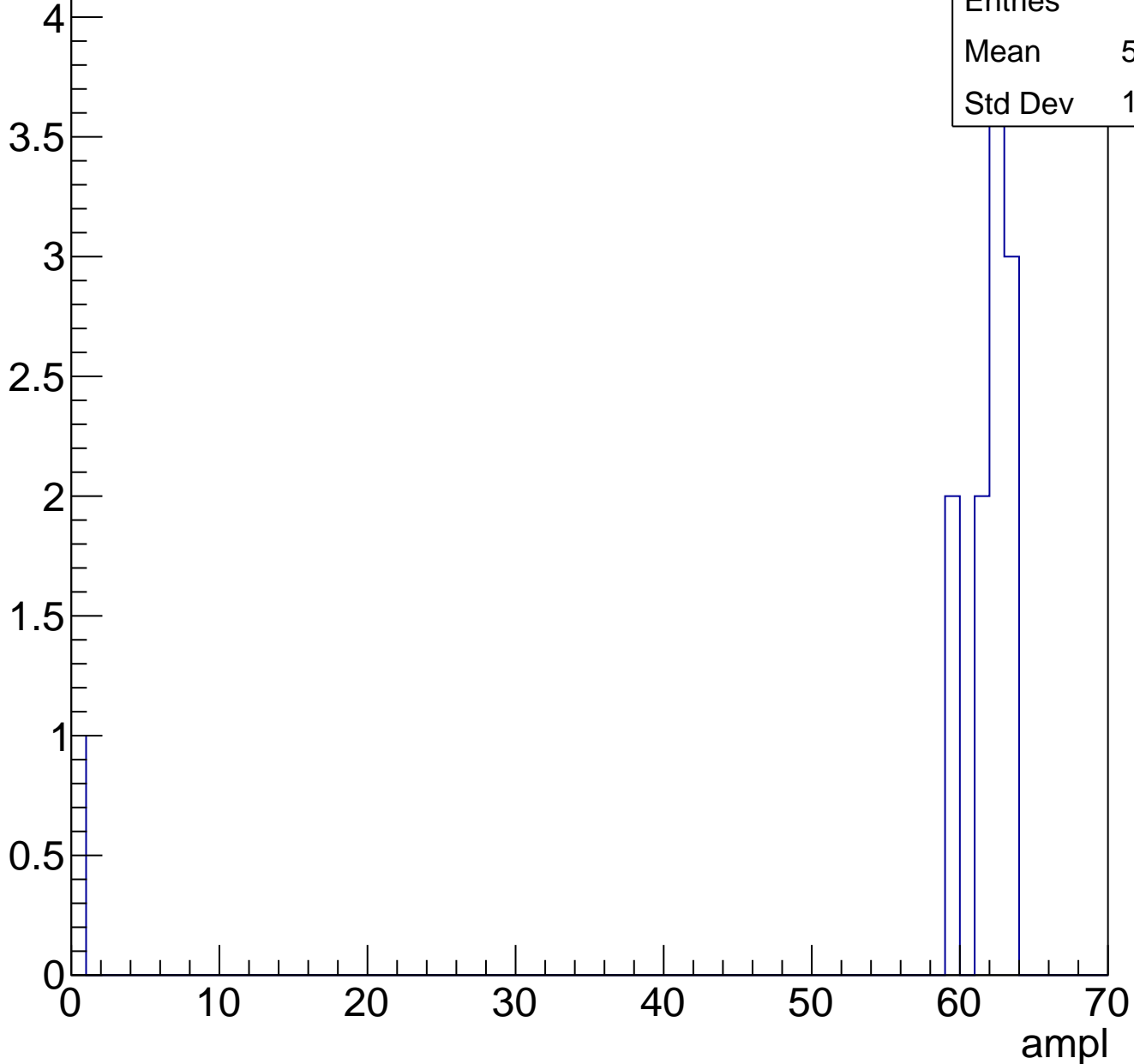
ampl



# B1L002S, U3-ch32, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	12
Mean	56.42
Std Dev	17.06



# B1L002S, U3-ch32, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch33, adc0

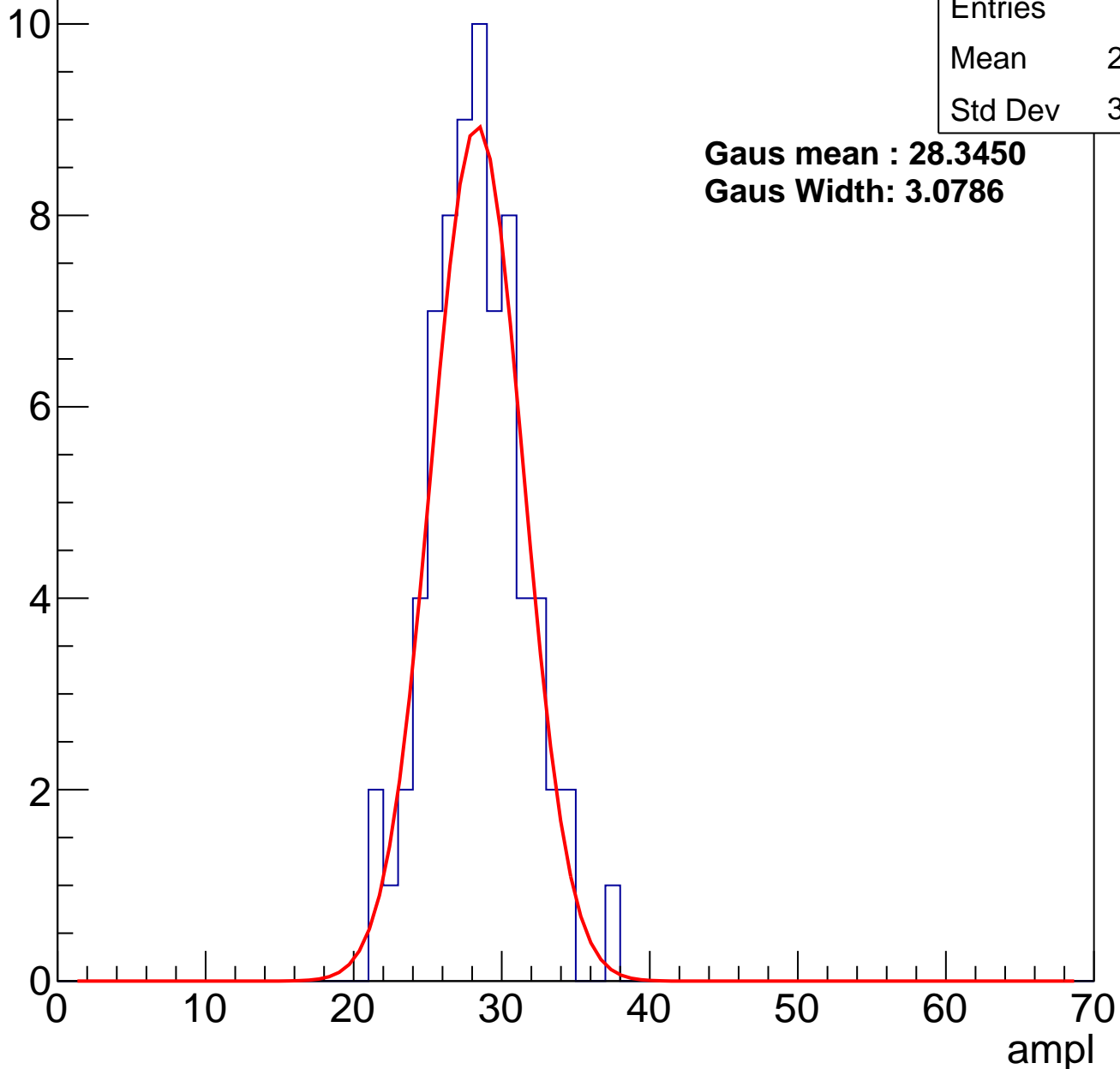
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	27.86
Std Dev	3.123

**Gaus mean : 28.3450**

**Gaus Width: 3.0786**

Entry



# B1L002S, U3-ch33, adc1

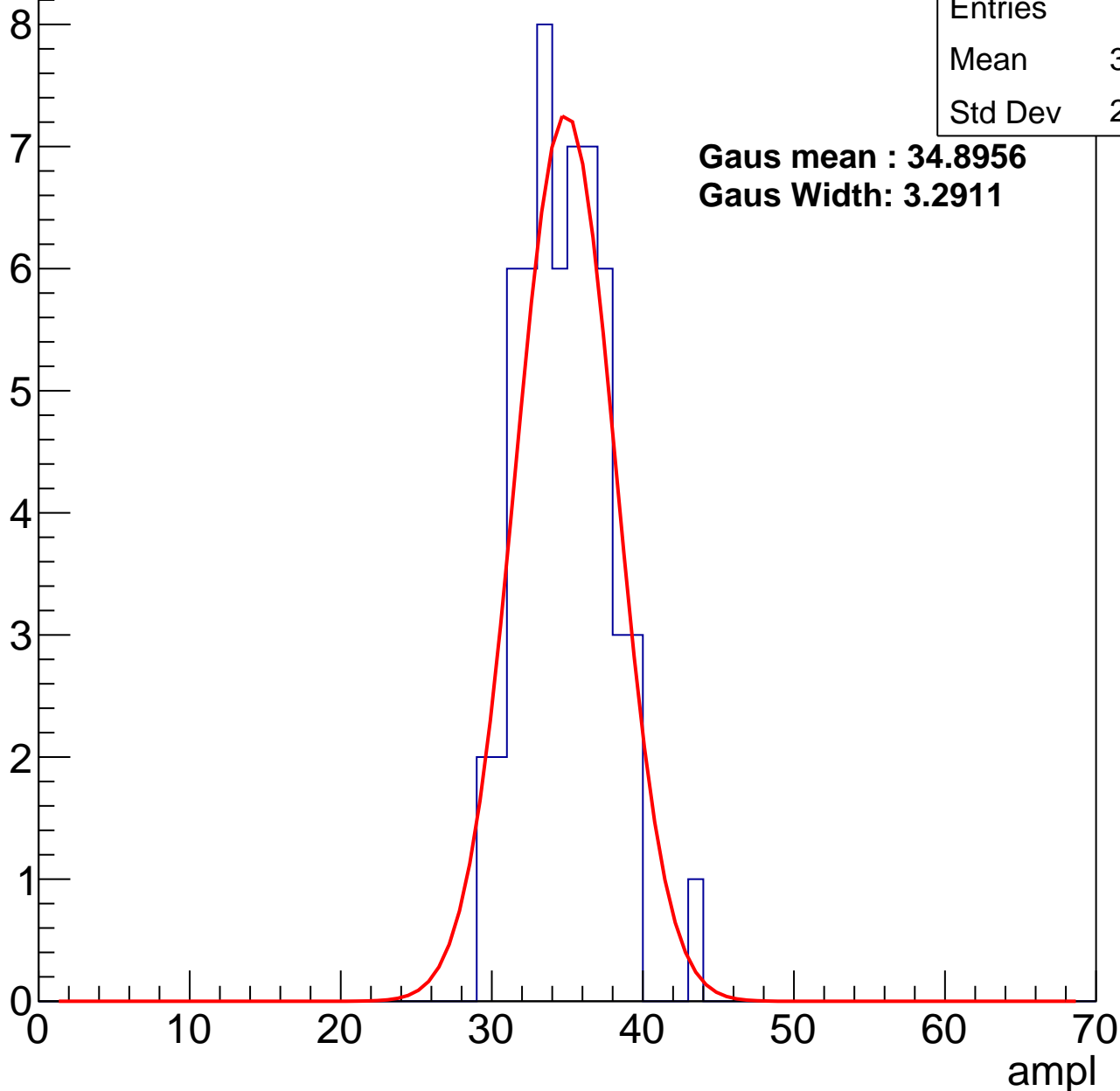
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	34.33
Std Dev	2.824

**Gaus mean : 34.8956**

**Gaus Width: 3.2911**



# B1L002S, U3-ch33, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	40.47
Std Dev	3.345

**Gaus mean : 40.2054**

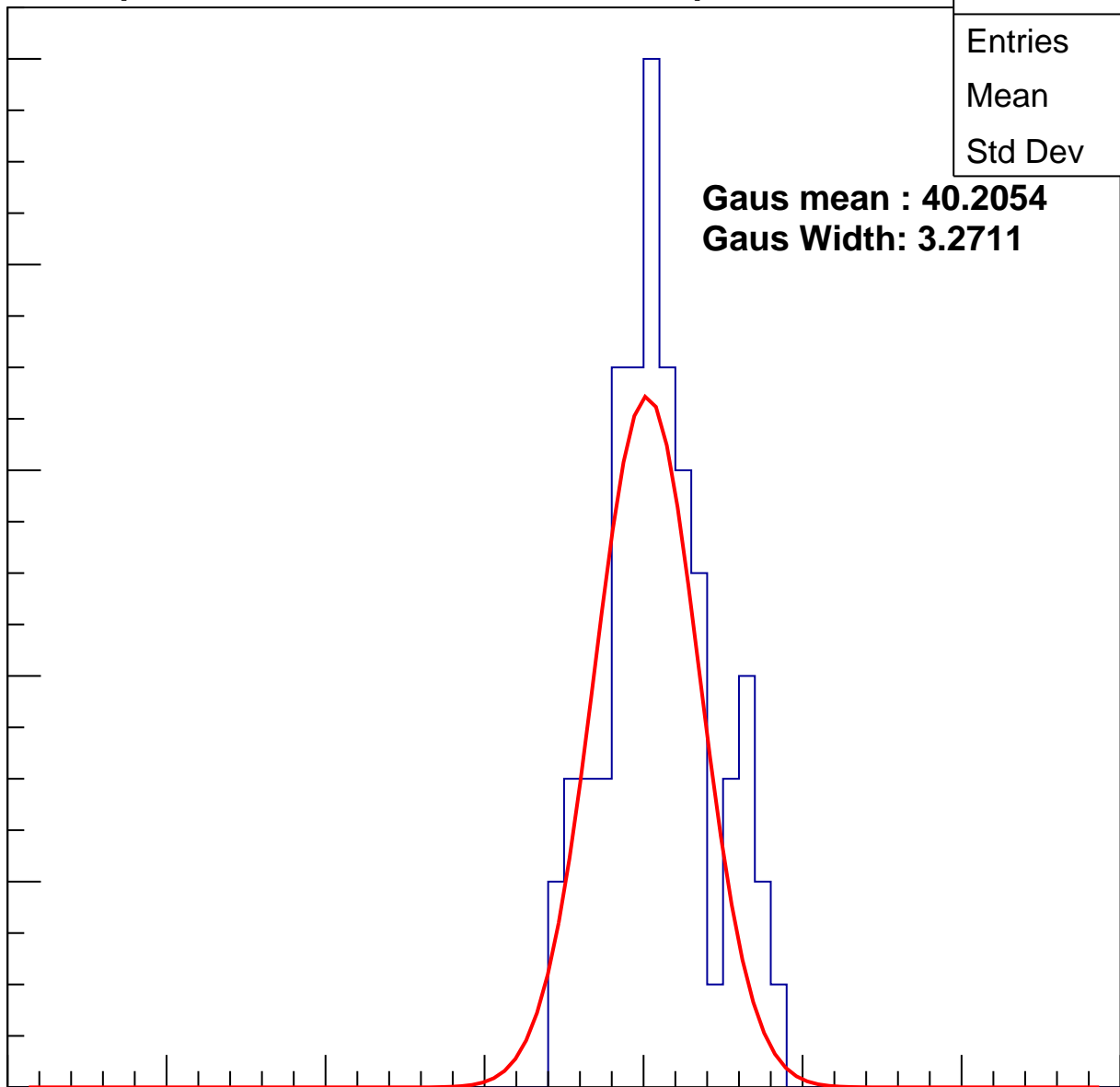
**Gaus Width: 3.2711**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

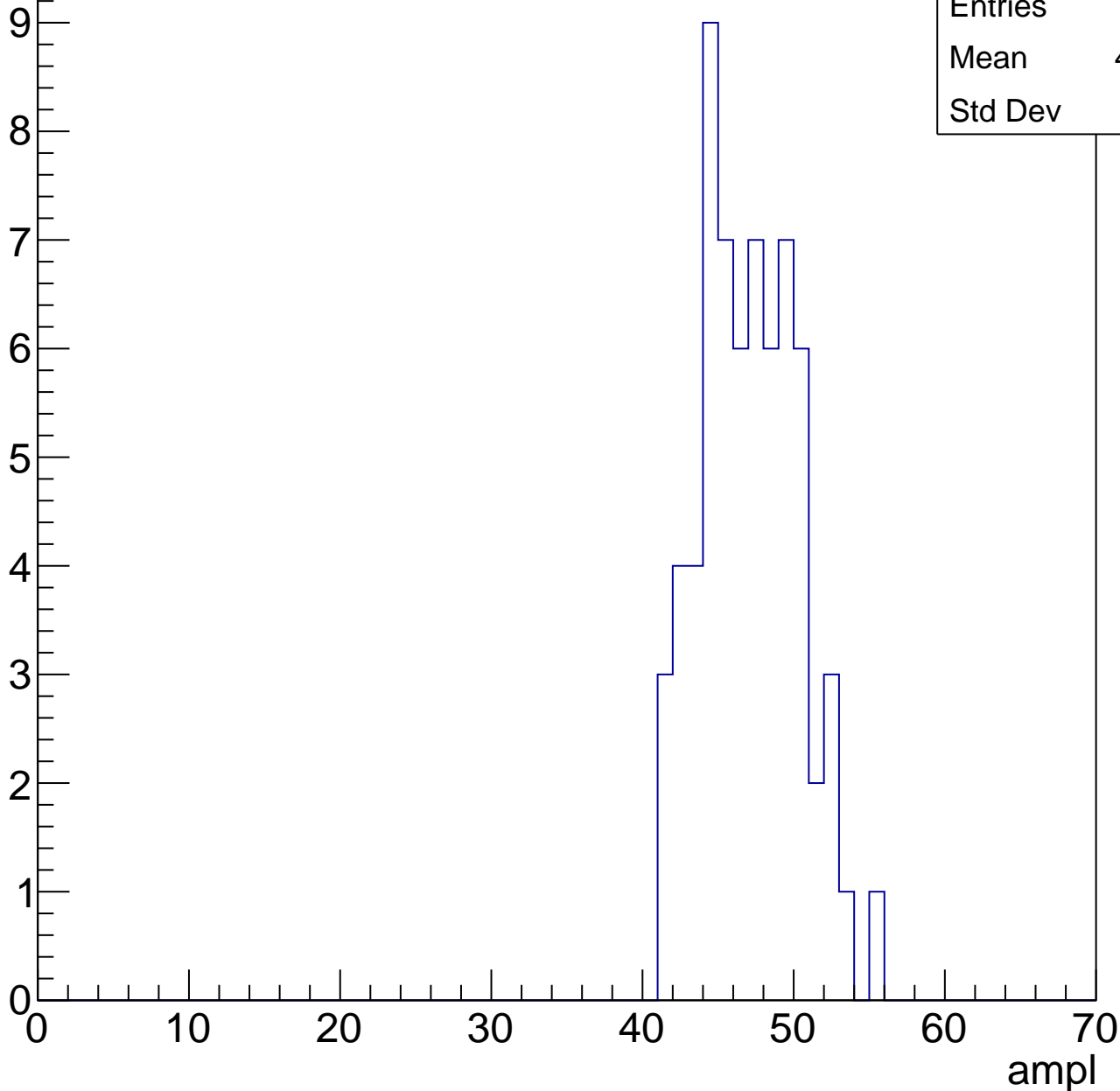


# B1L002S, U3-ch33, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	46.61
Std Dev	3.19

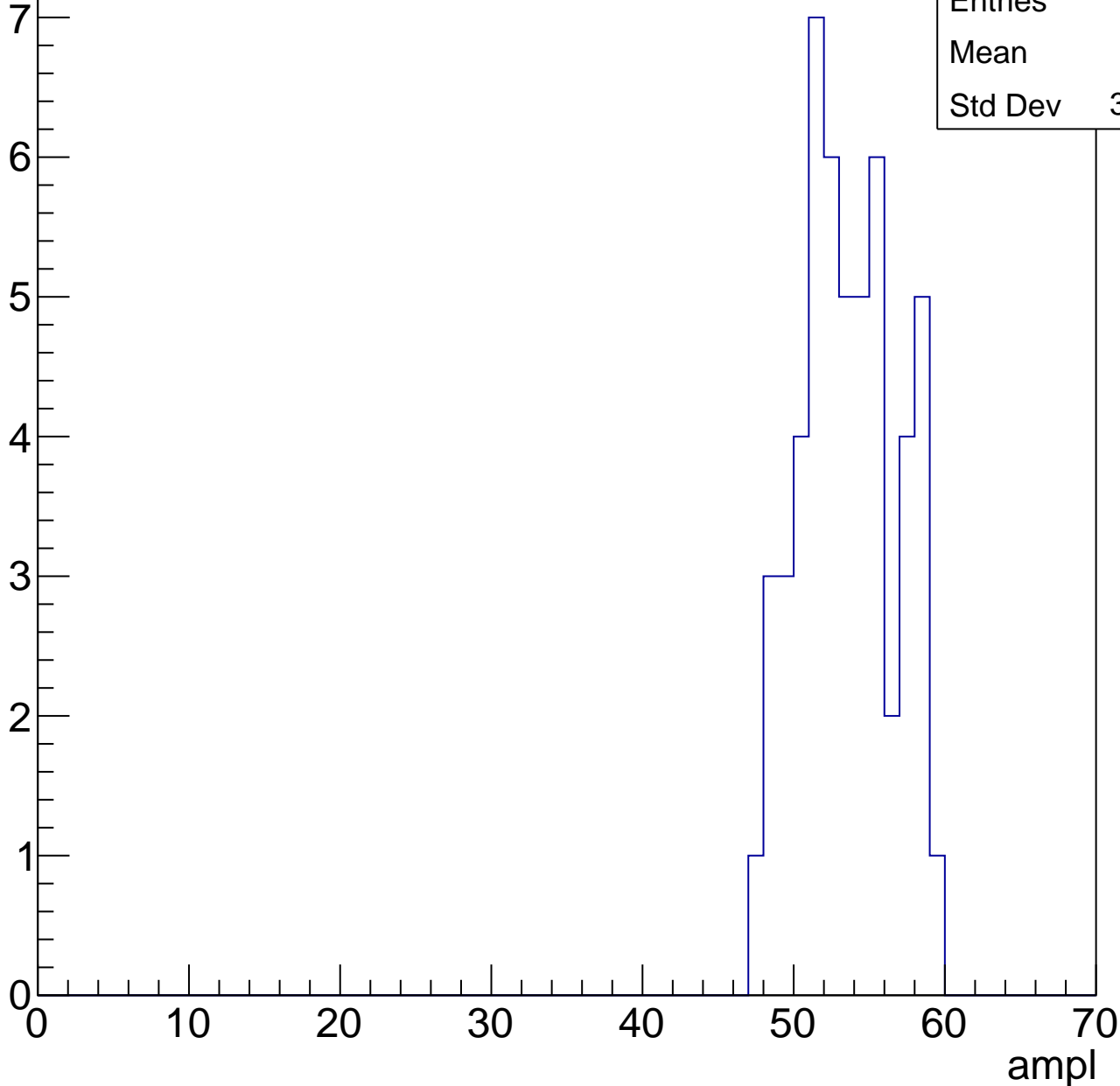


# B1L002S, U3-ch33, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.1
Std Dev	3.102

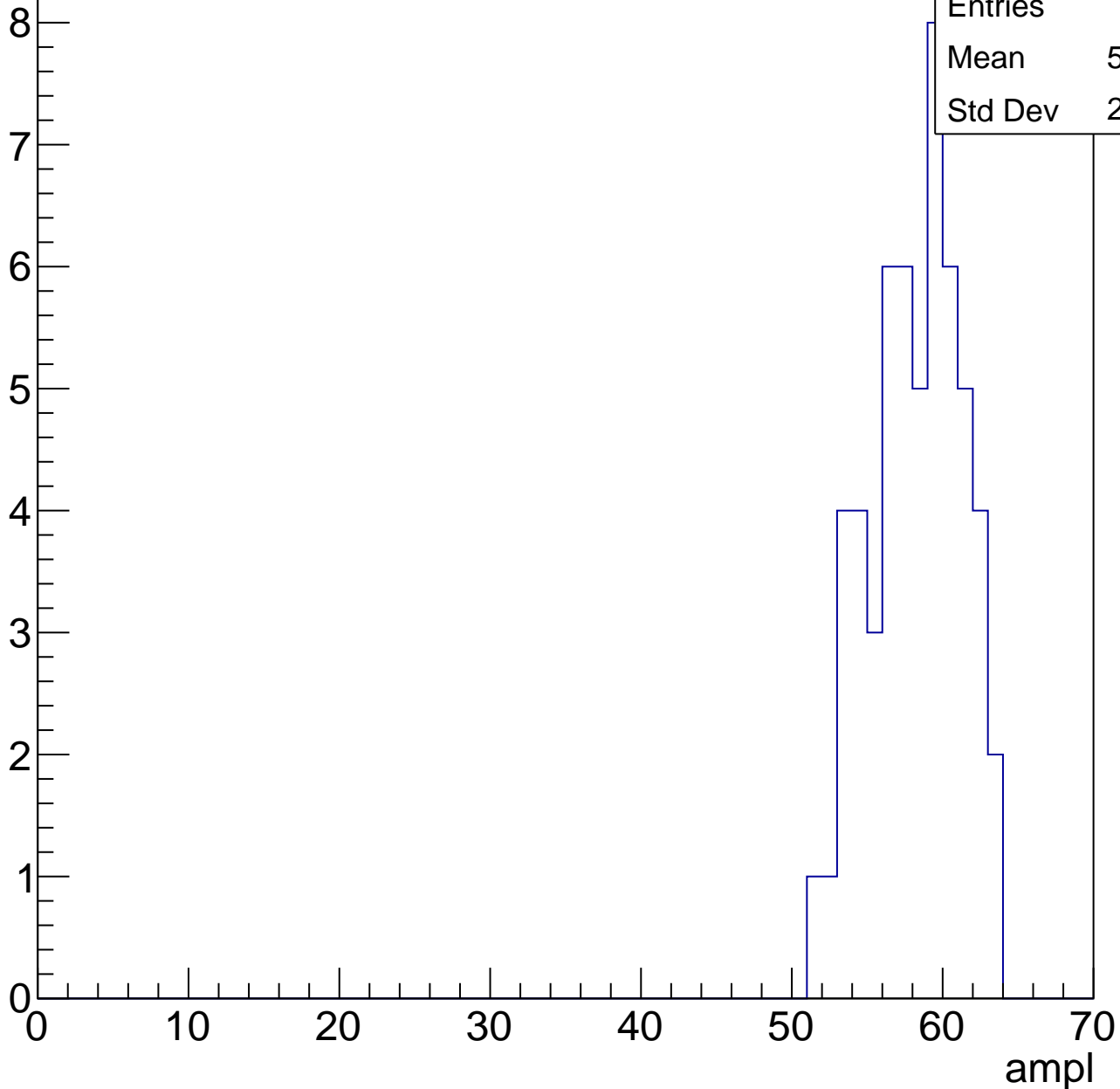


# B1L002S, U3-ch33, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.73
Std Dev	2.994

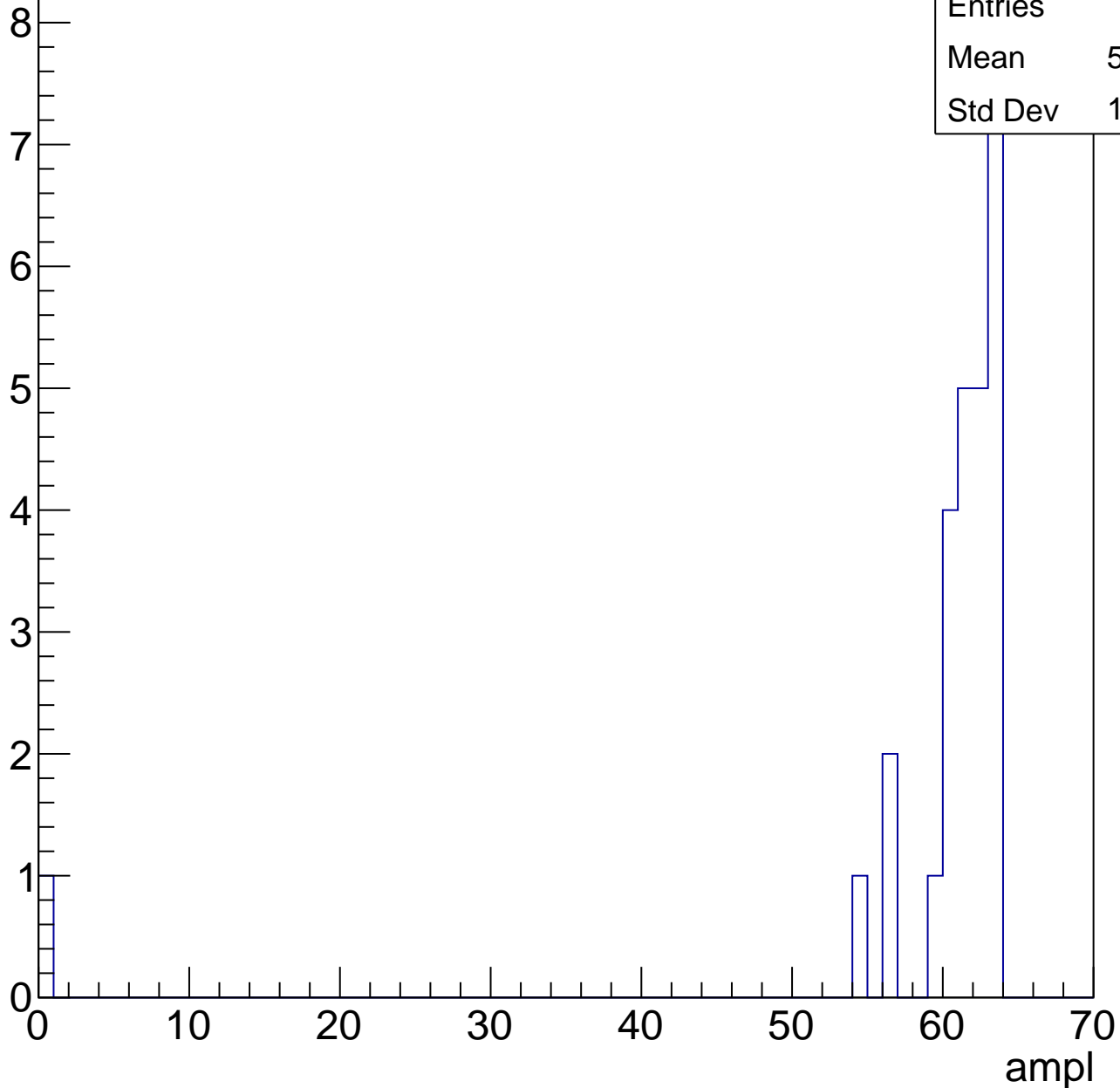


# B1L002S, U3-ch33, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.67
Std Dev	11.73





# B1L002S, U3-ch33, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch34, adc0

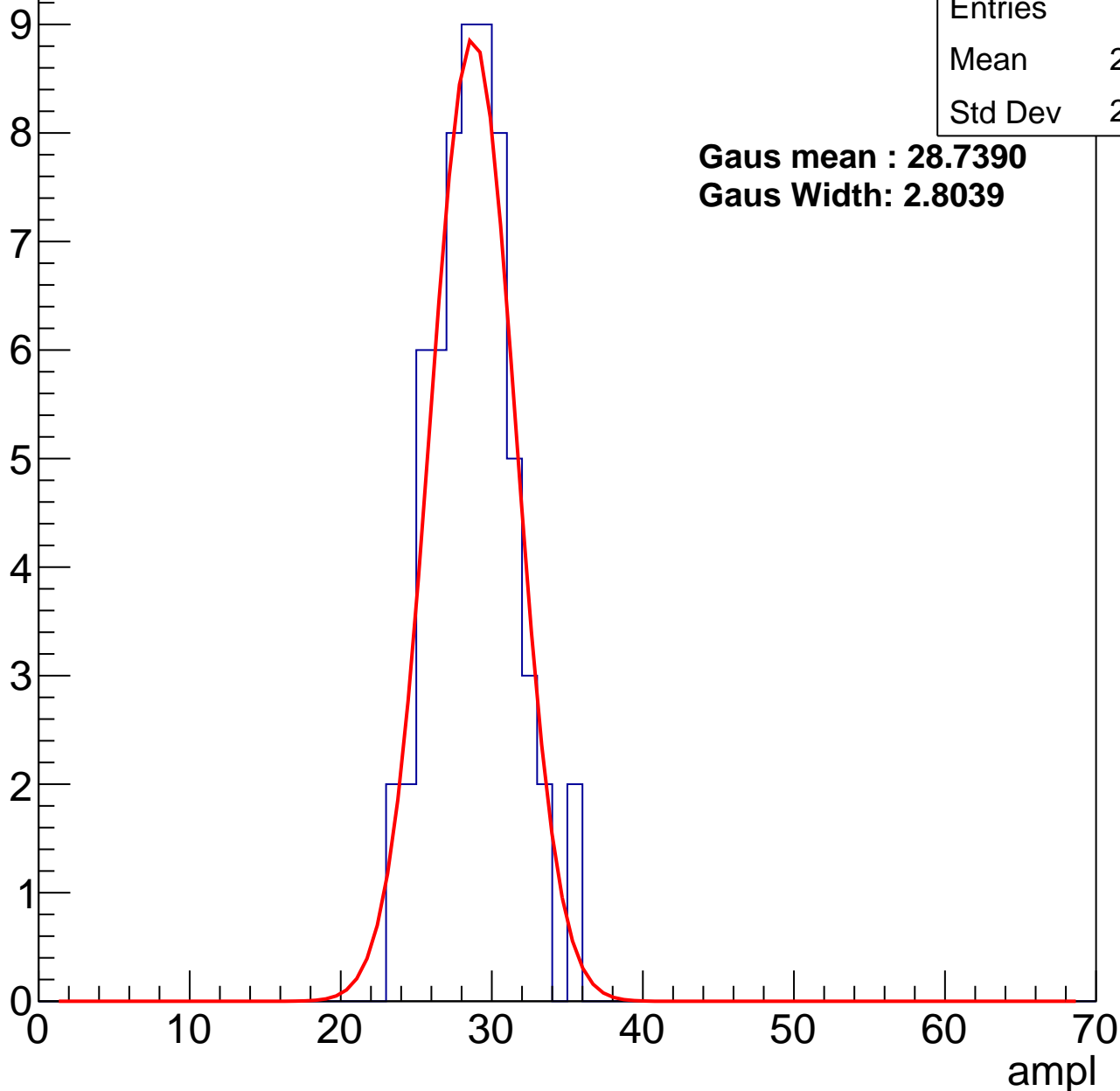
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.32
Std Dev	2.675

**Gaus mean : 28.7390**

**Gaus Width: 2.8039**



# B1L002S, U3-ch34, adc1

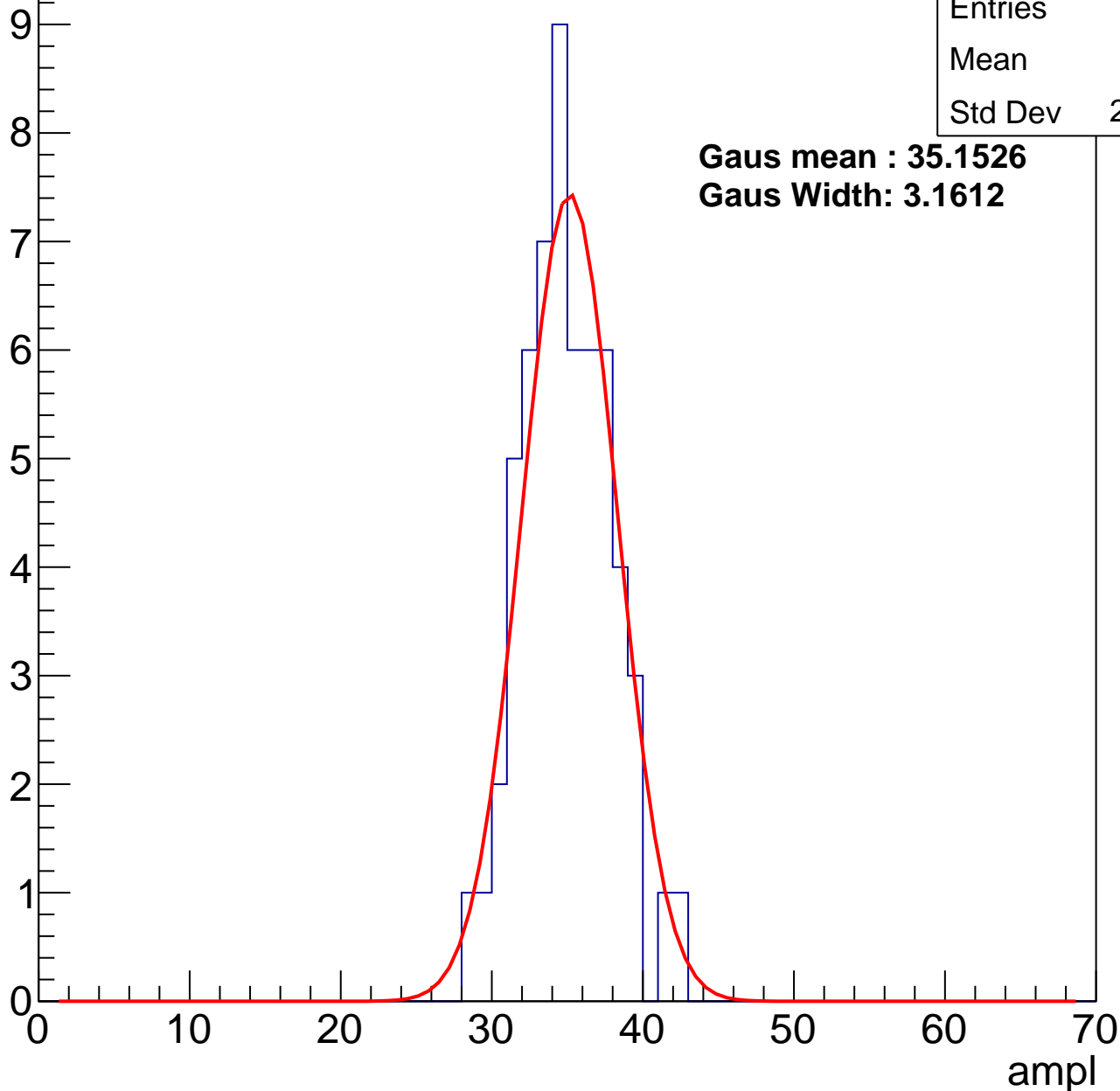
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	34.5
Std Dev	2.908

**Gaus mean : 35.1526**

**Gaus Width: 3.1612**



# B1L002S, U3-ch34, adc2

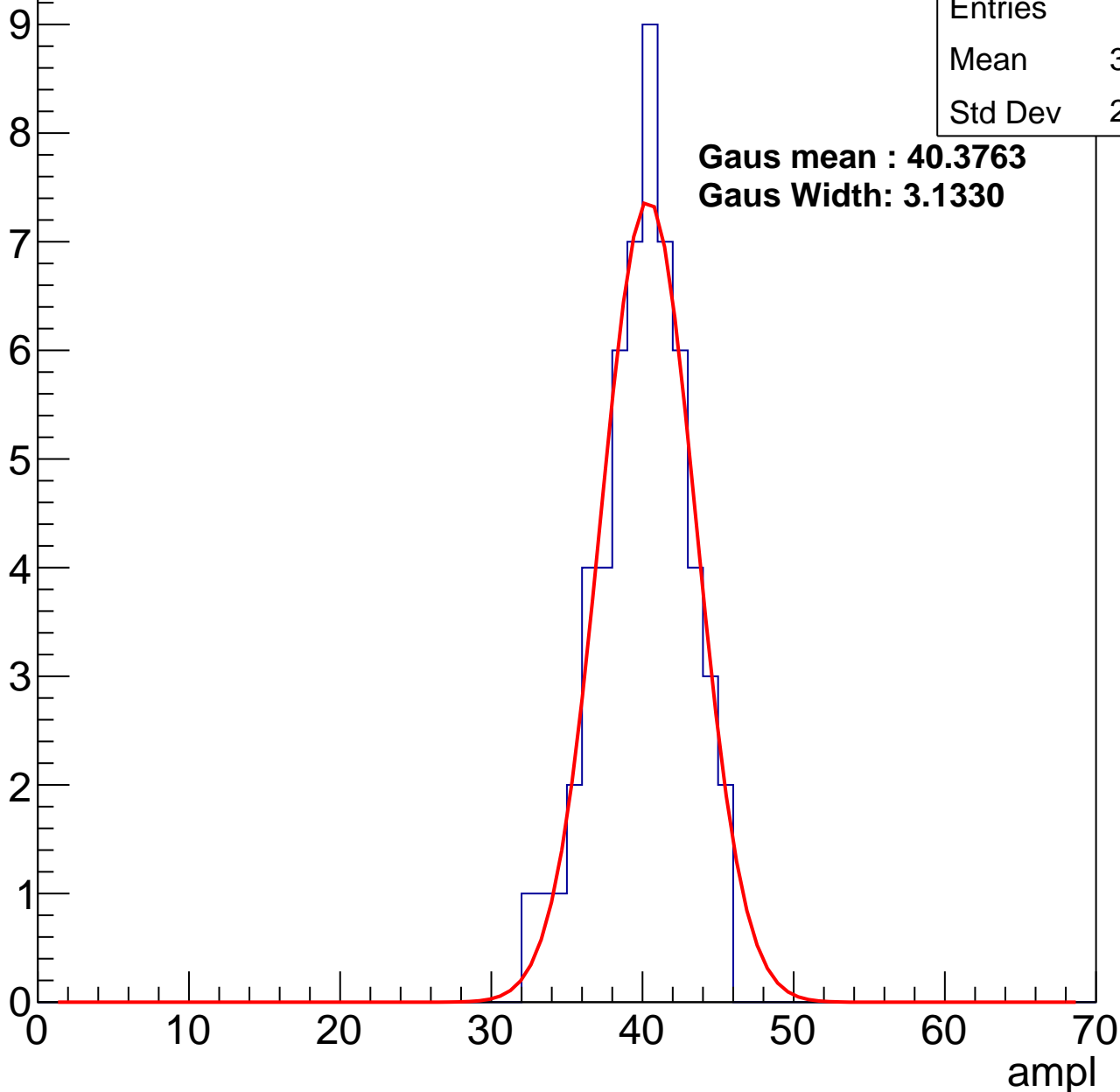
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.56
Std Dev	2.914

**Gaus mean : 40.3763**

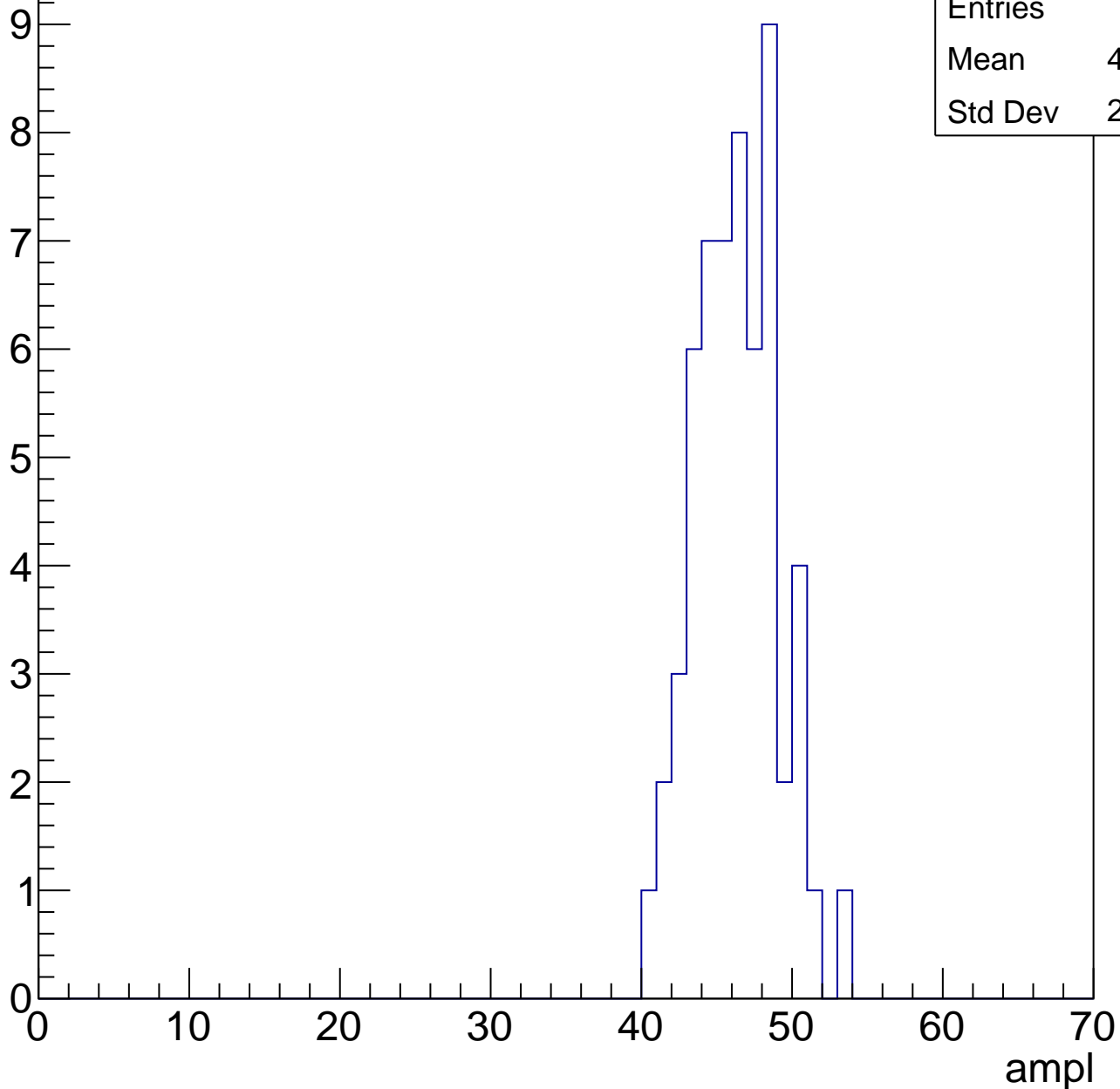
**Gaus Width: 3.1330**



# B1L002S, U3-ch34, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

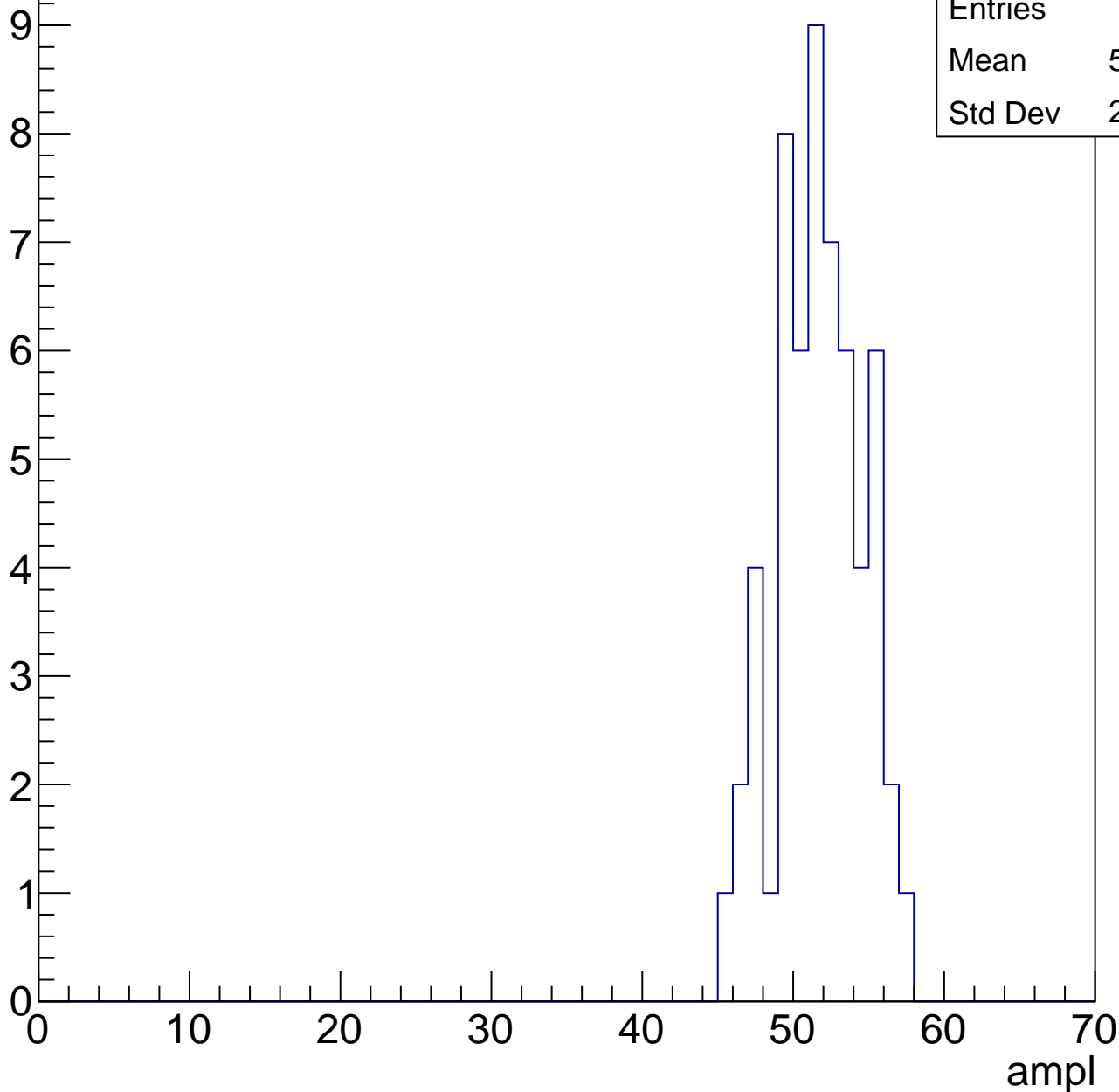


Entries	57
Mean	45.84
Std Dev	2.713

# B1L002S, U3-ch34, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

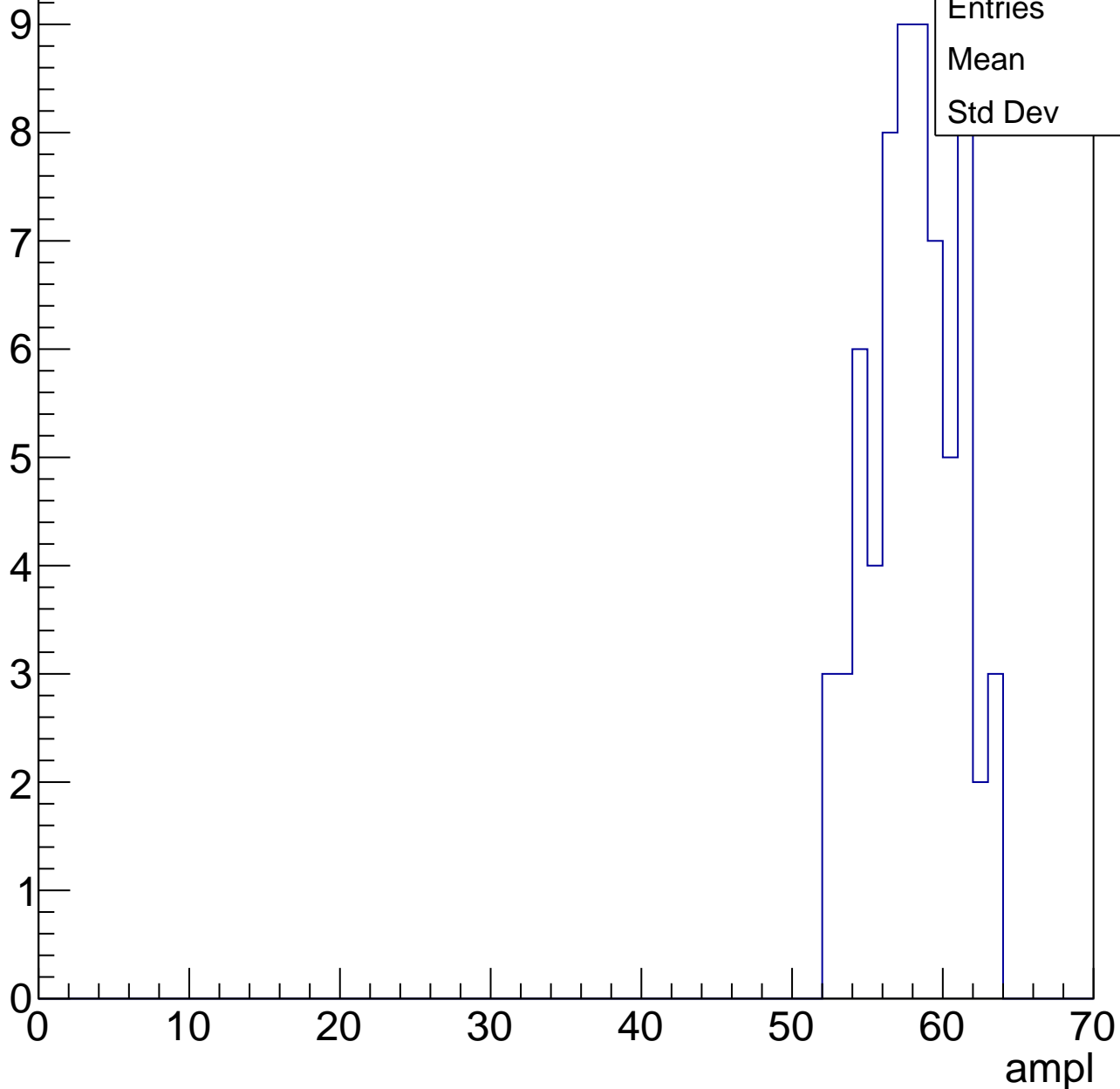
Entry



# B1L002S, U3-ch34, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

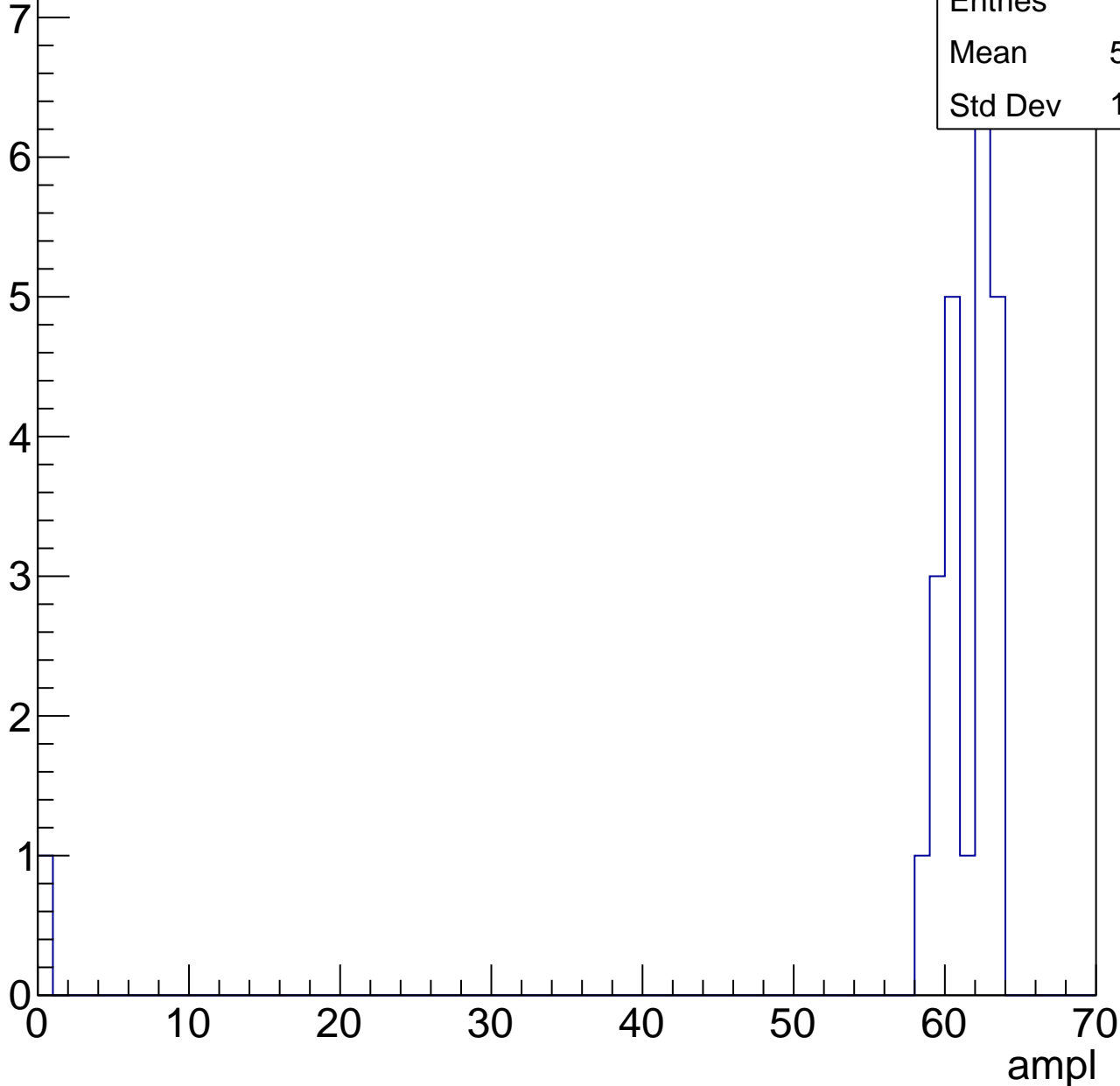


# B1L002S, U3-ch34, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.48
Std Dev	12.56

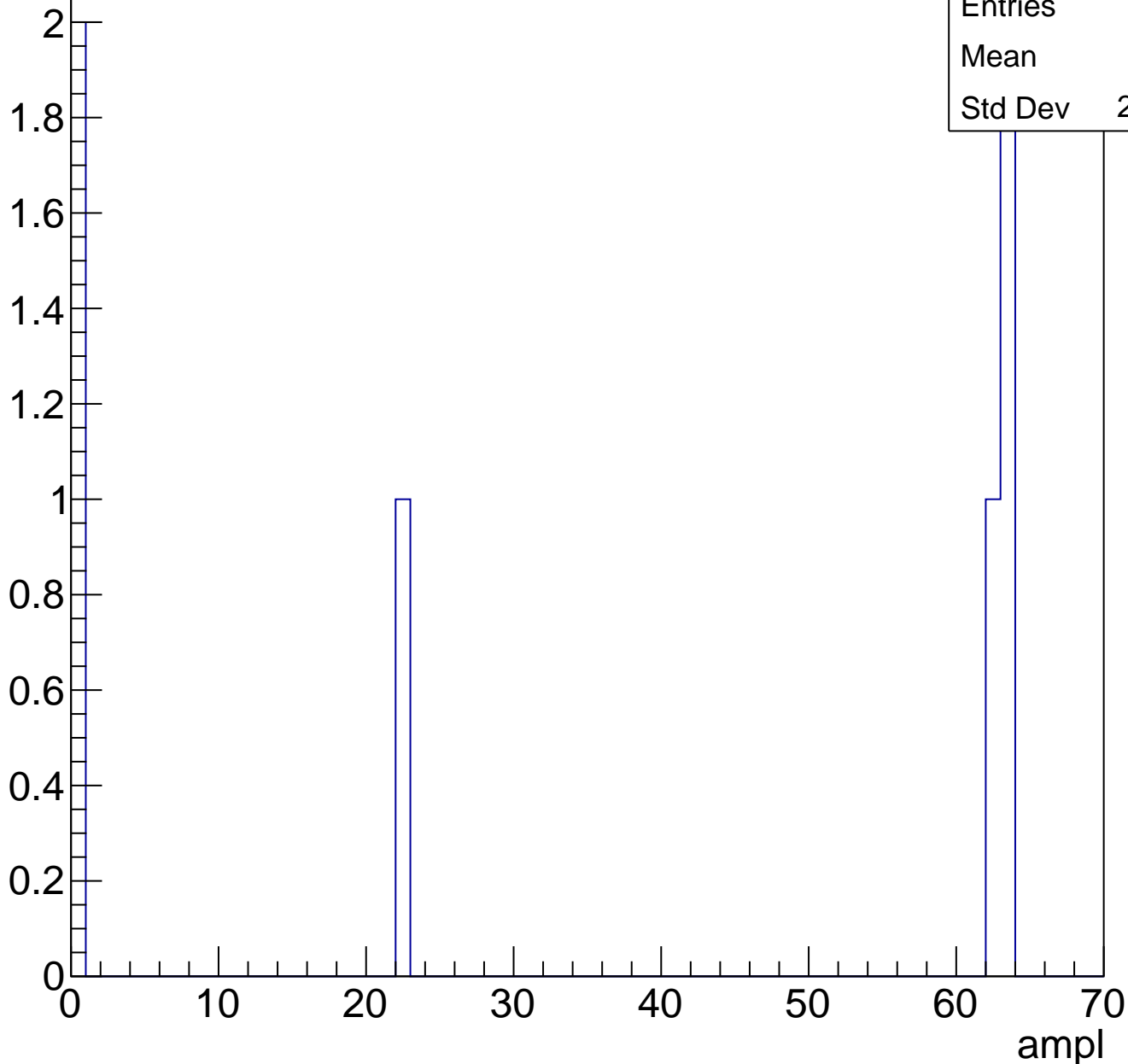




# B1L002S, U3-ch34, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch35, adc0

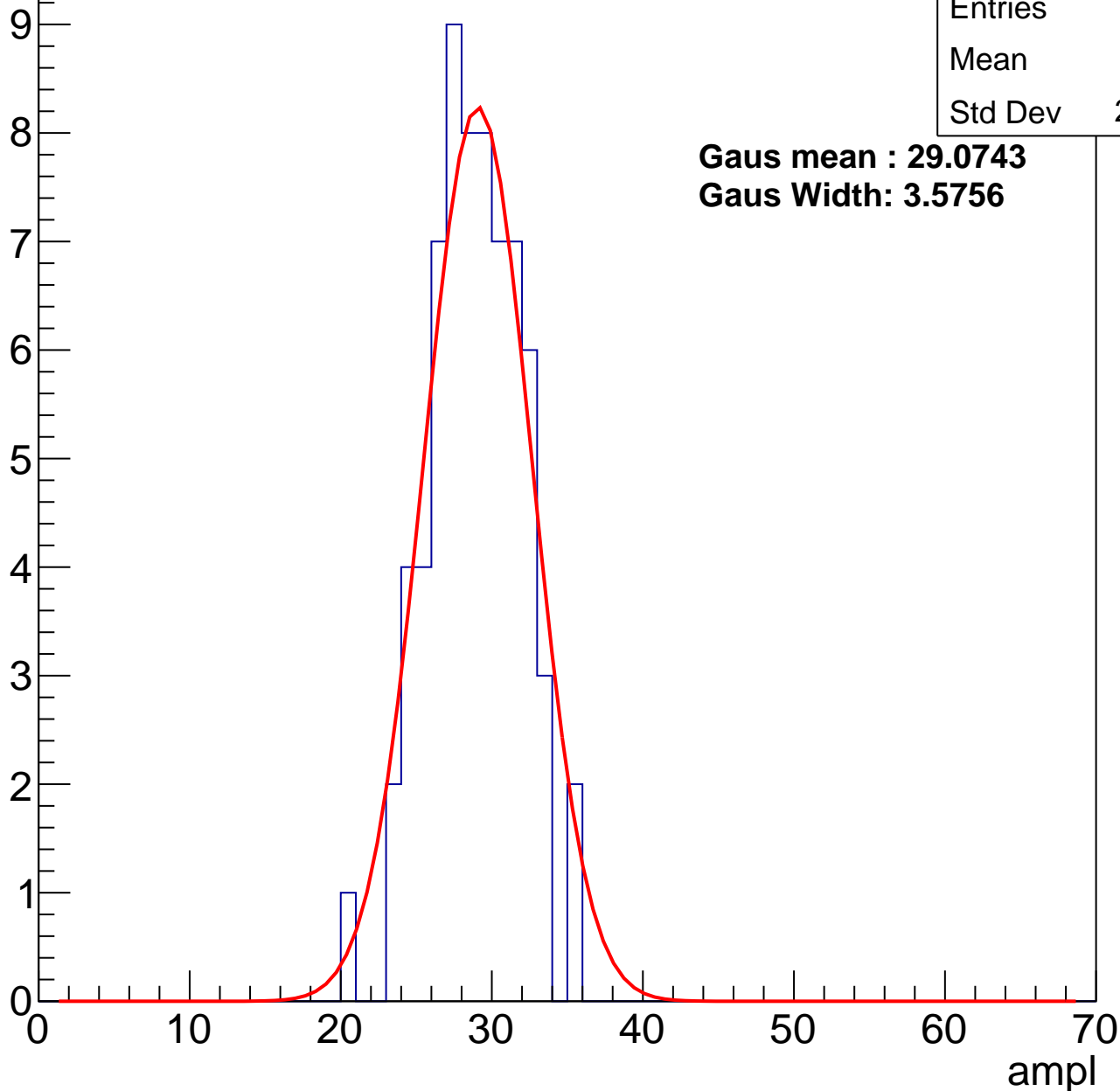
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	28.4
Std Dev	2.991

**Gaus mean : 29.0743**

**Gaus Width: 3.5756**



# B1L002S, U3-ch35, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	35.23
Std Dev	3.345

**Gaus mean : 35.6200**

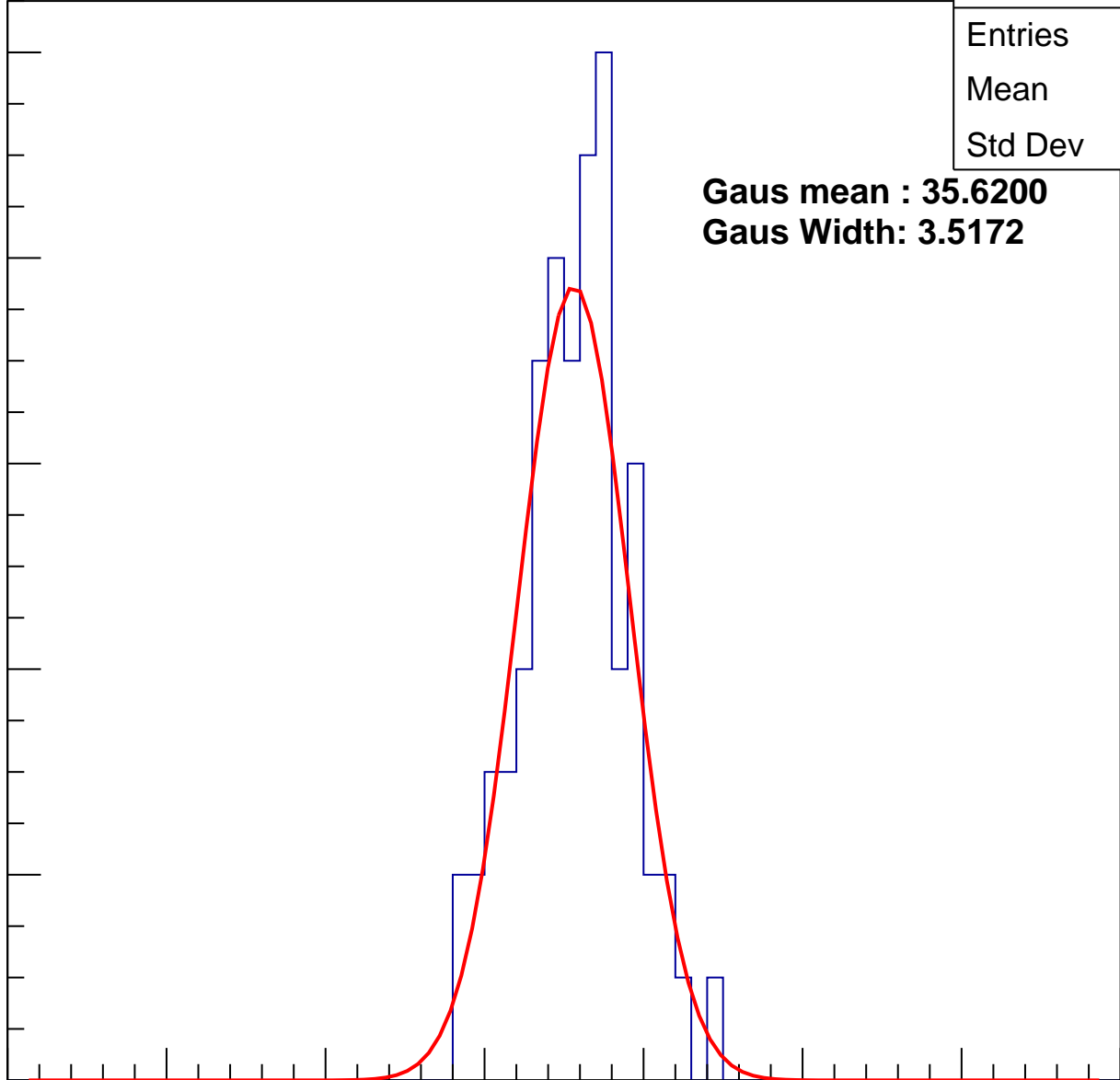
**Gaus Width: 3.5172**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch35, adc2

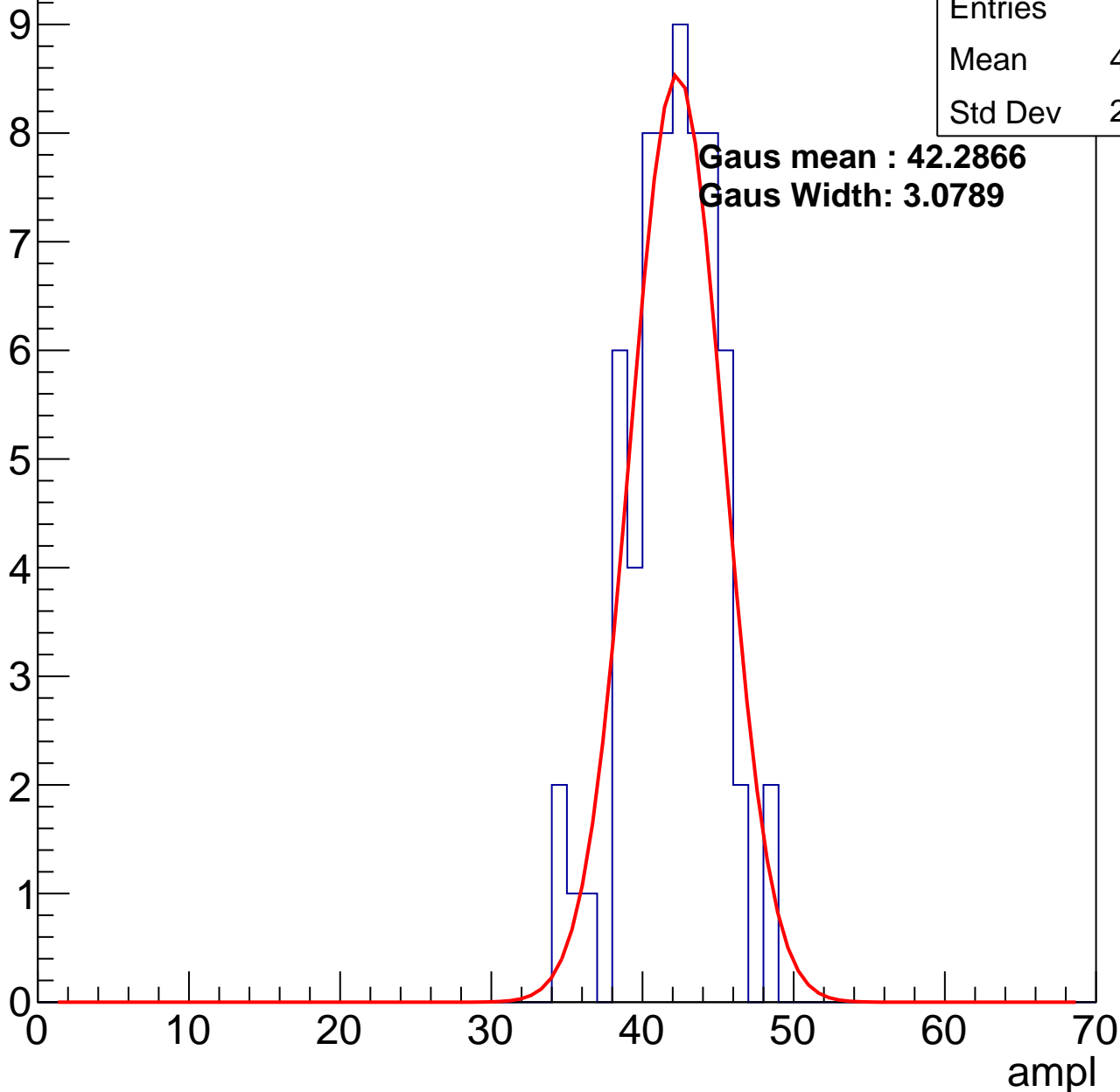
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	41.58
Std Dev	2.966

**Gaus mean : 42.2866**

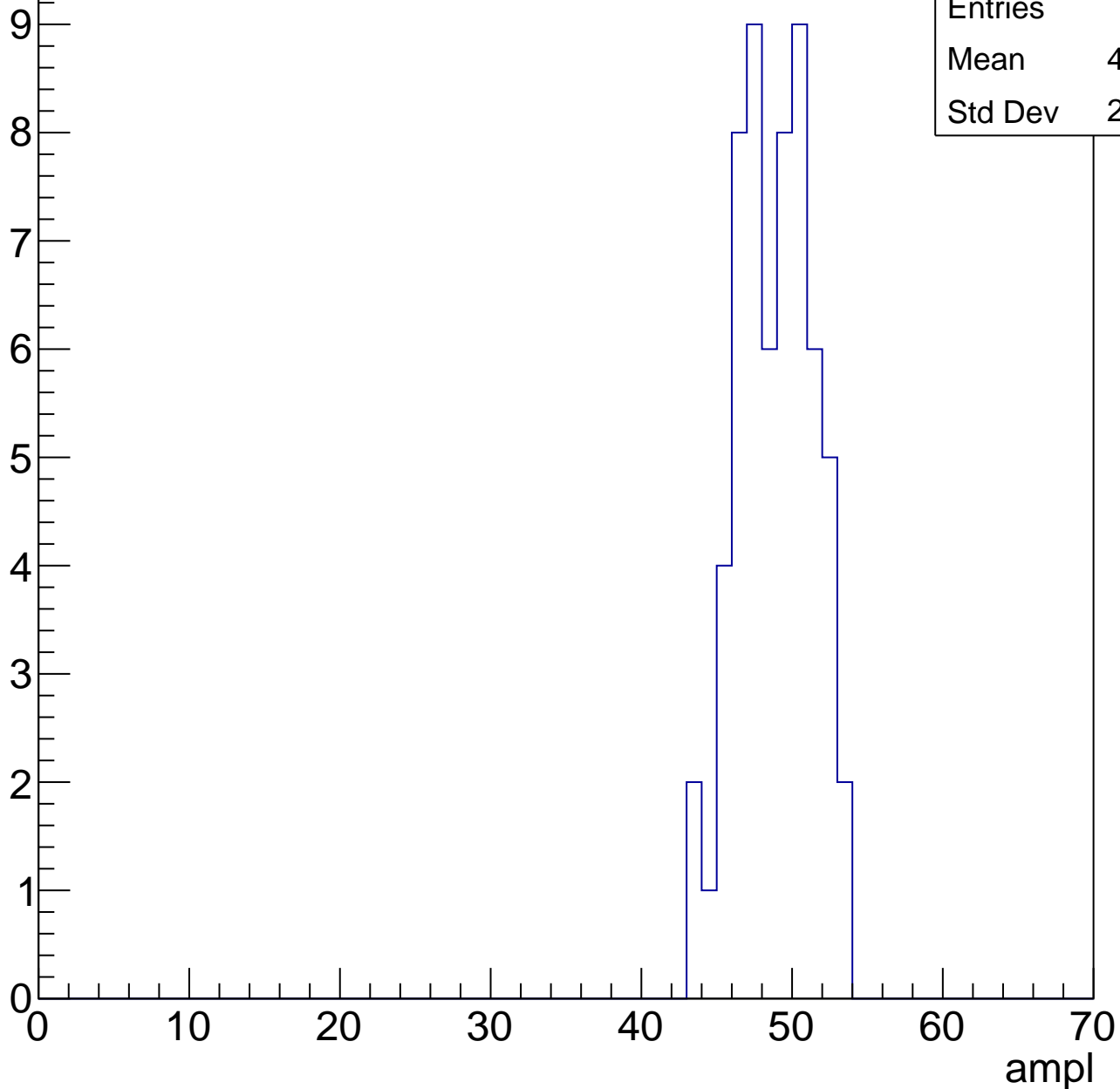
**Gaus Width: 3.0789**



# B1L002S, U3-ch35, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	60
Mean	48.38
Std Dev	2.457

# B1L002S, U3-ch35, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

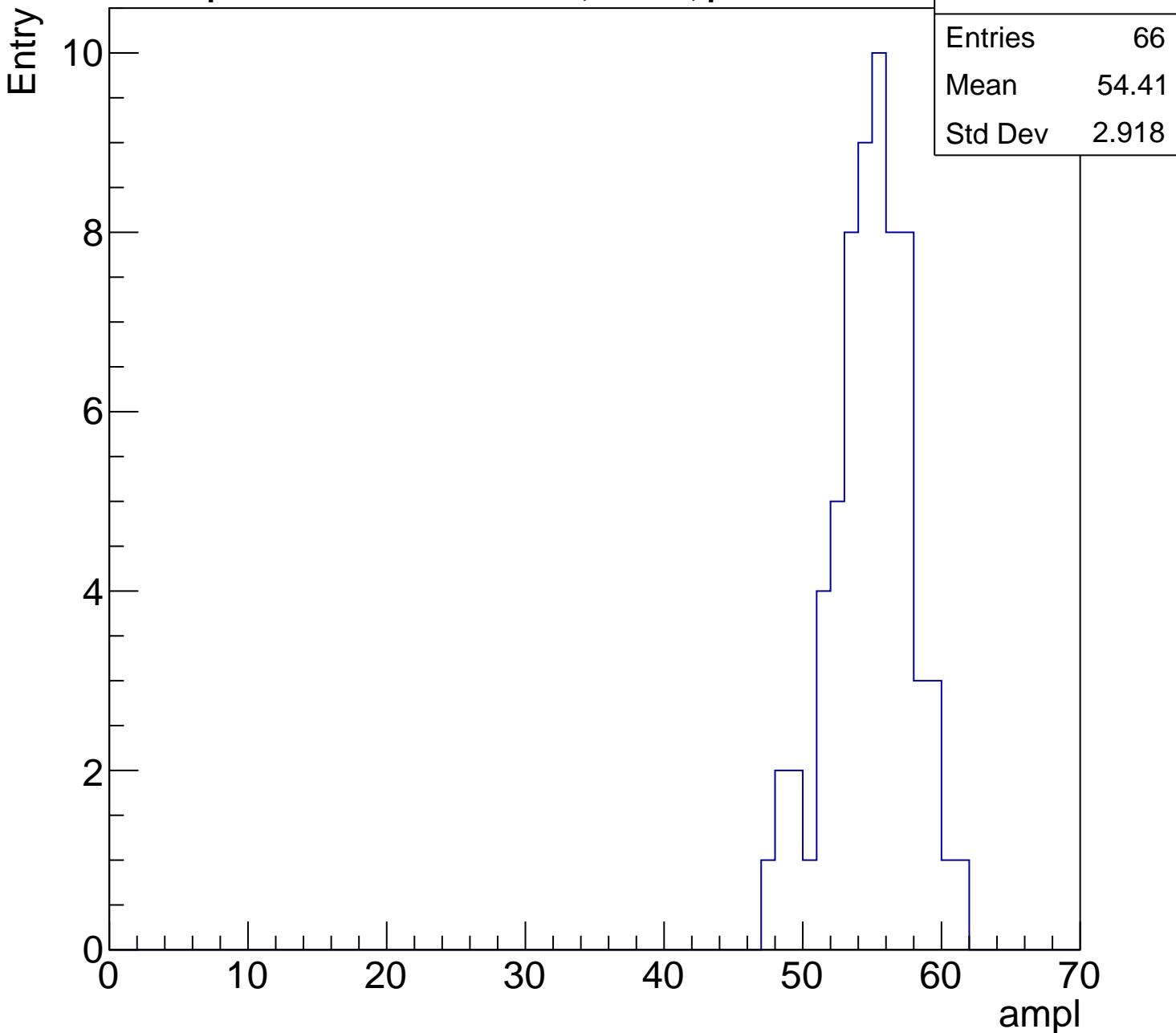
Entries	66
Mean	54.41
Std Dev	2.918

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

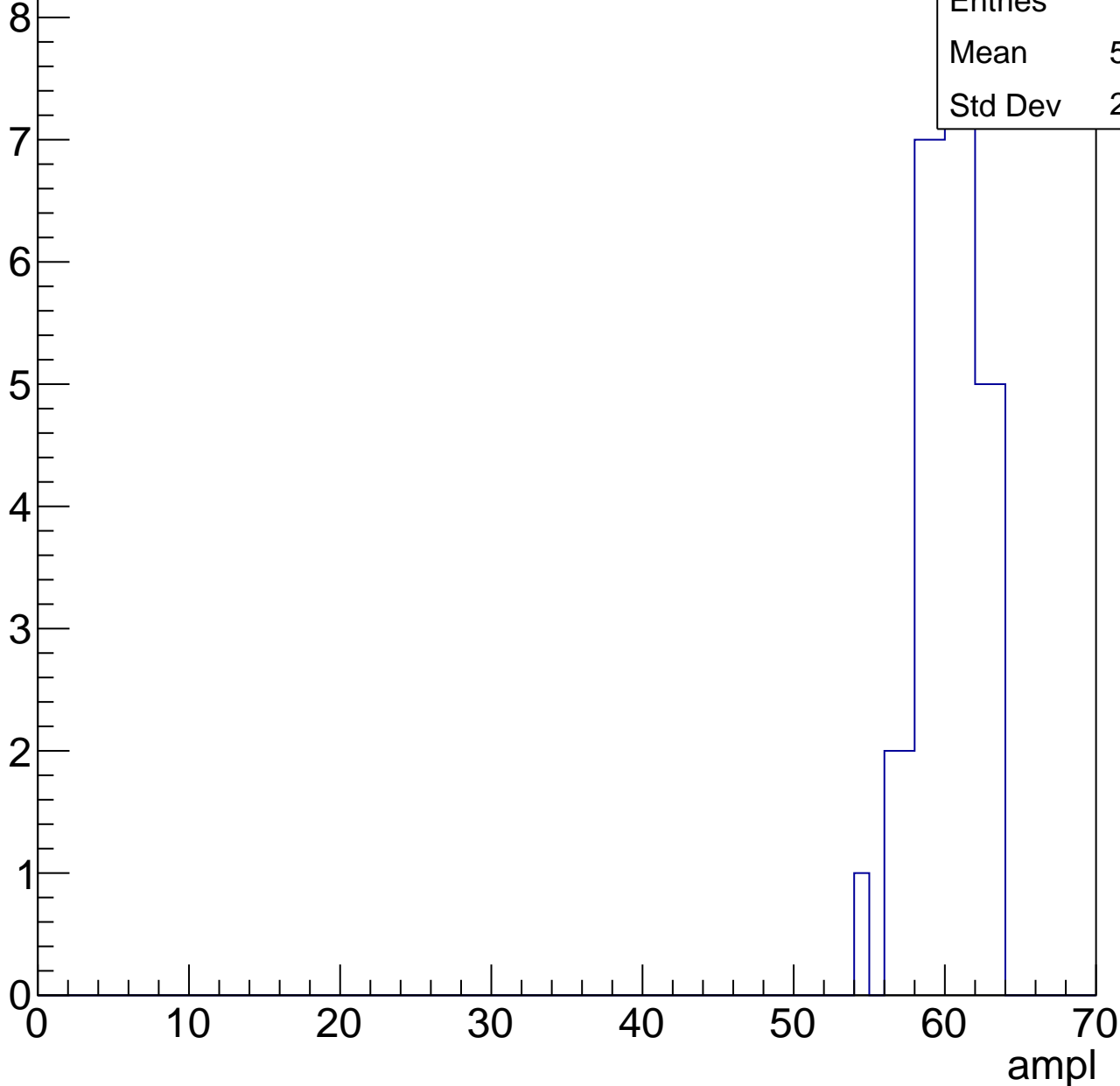


# B1L002S, U3-ch35, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	59.82
Std Dev	2.069

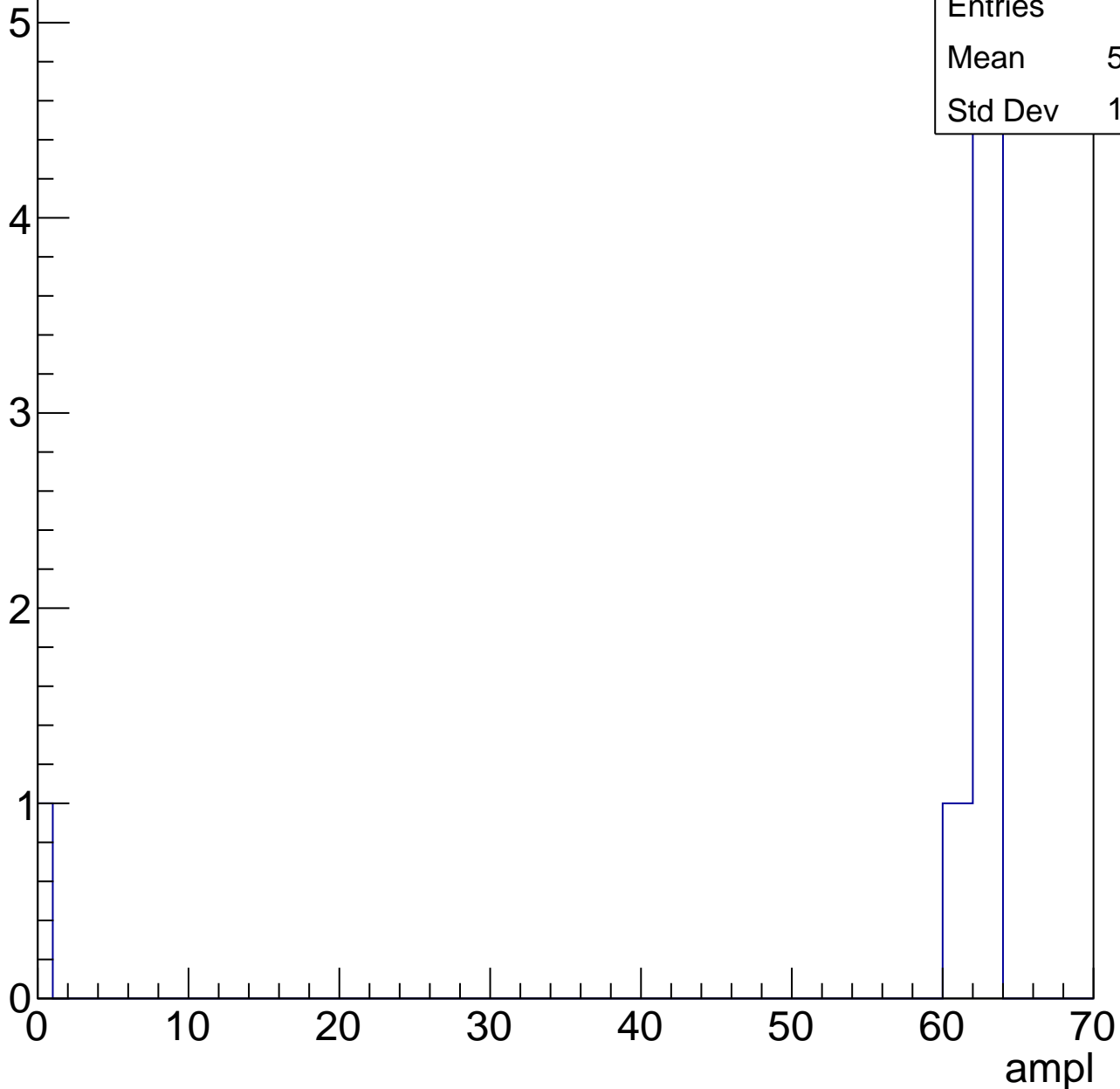


# B1L002S, U3-ch35, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	57.38
Std Dev	16.59





# B1L002S, U3-ch35, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U3-ch36, adc0

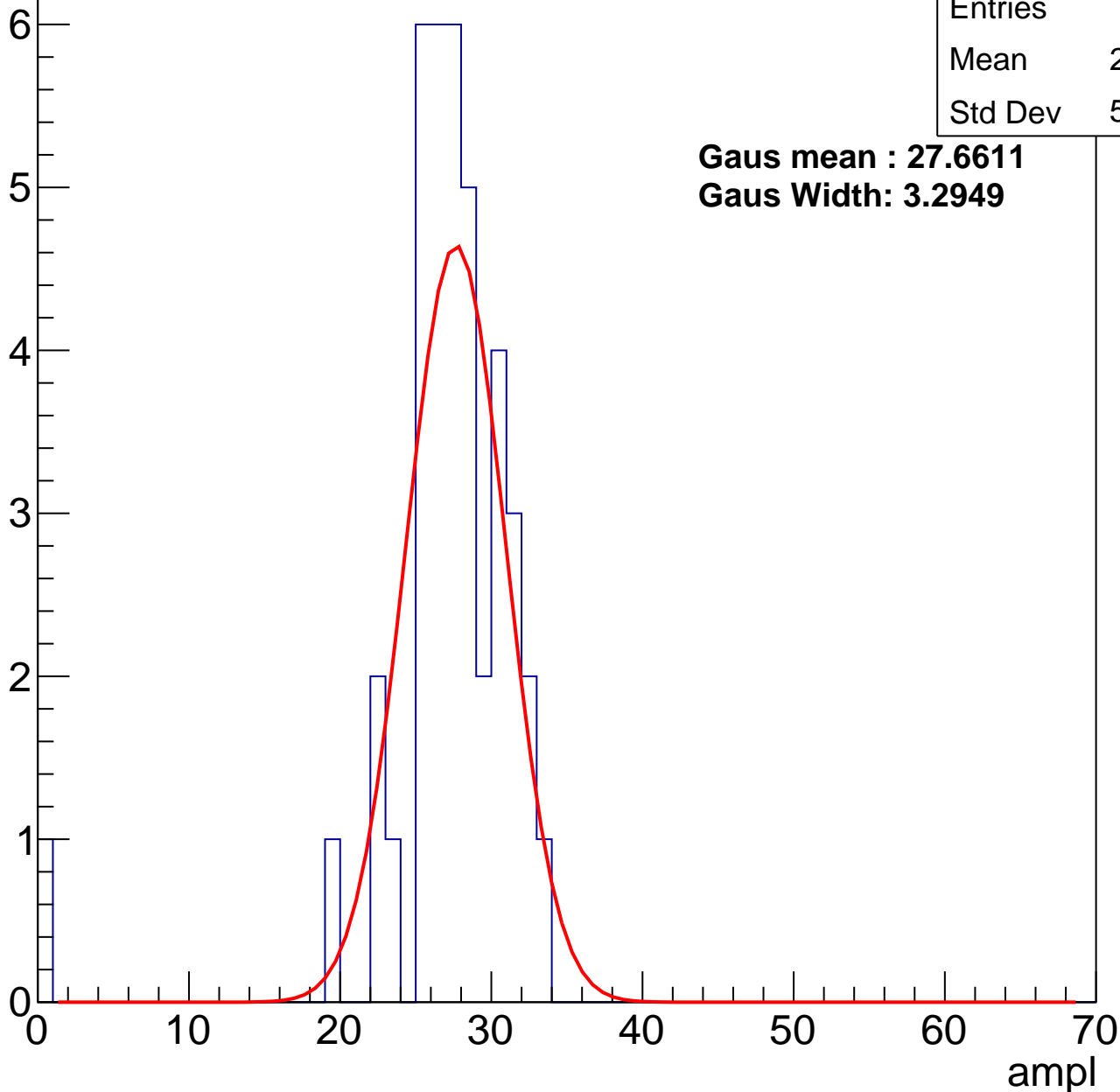
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	26.55
Std Dev	5.157

**Gaus mean : 27.6611**

**Gaus Width: 3.2949**



# B1L002S, U3-ch36, adc1

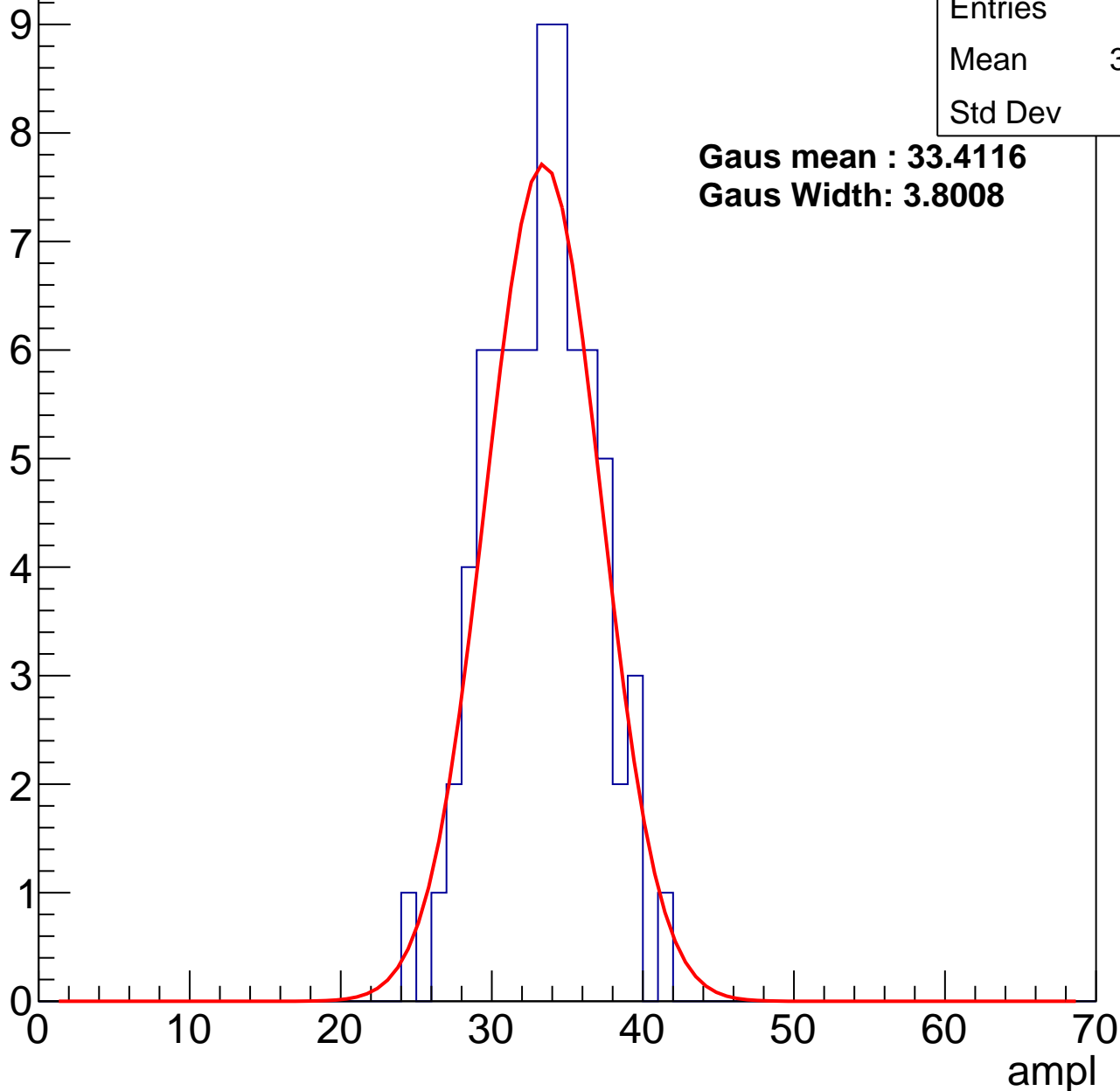
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	32.82
Std Dev	3.45

**Gaus mean : 33.4116**

**Gaus Width: 3.8008**



# B1L002S, U3-ch36, adc2

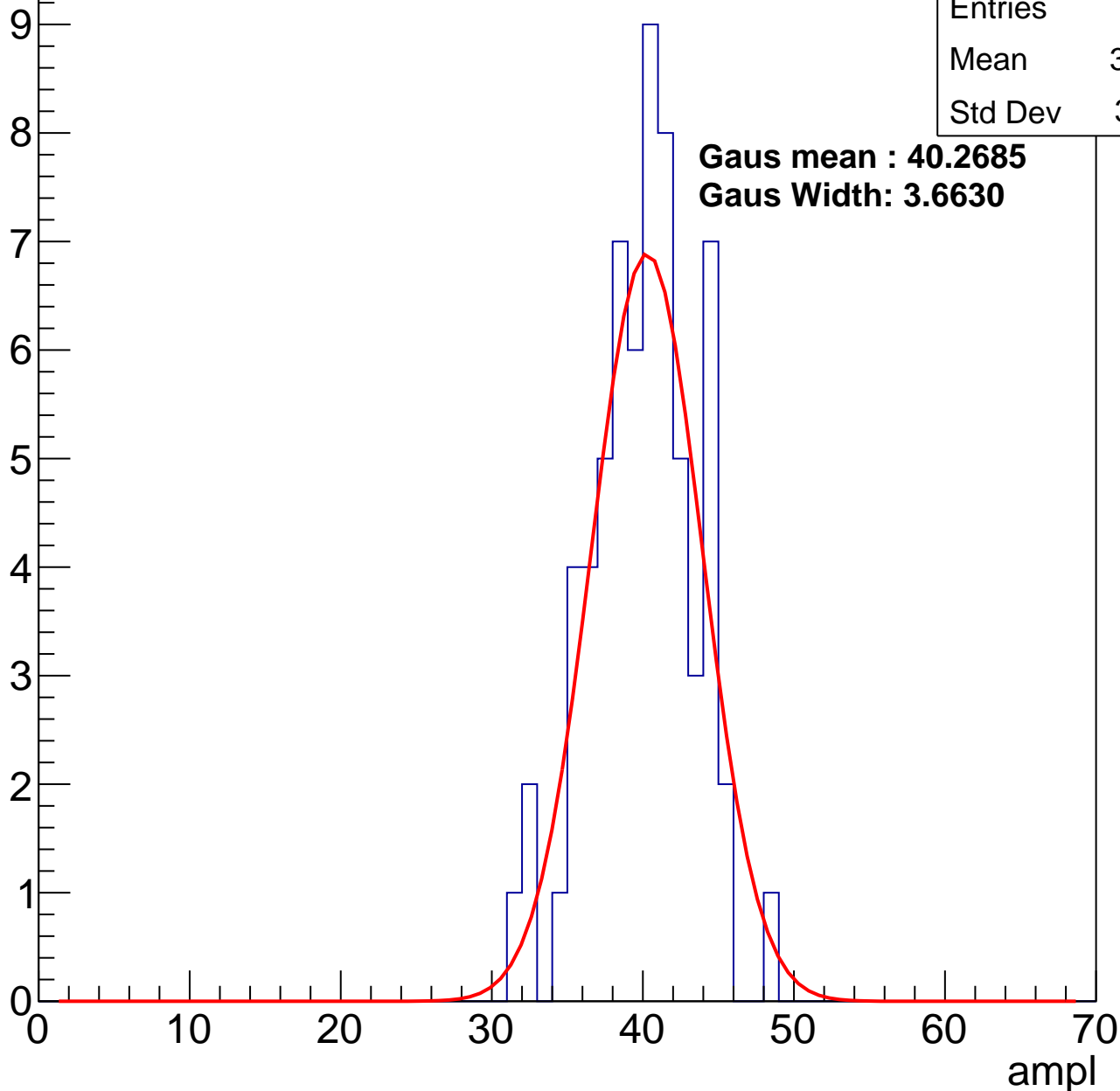
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.55
Std Dev	3.411

**Gaus mean : 40.2685**

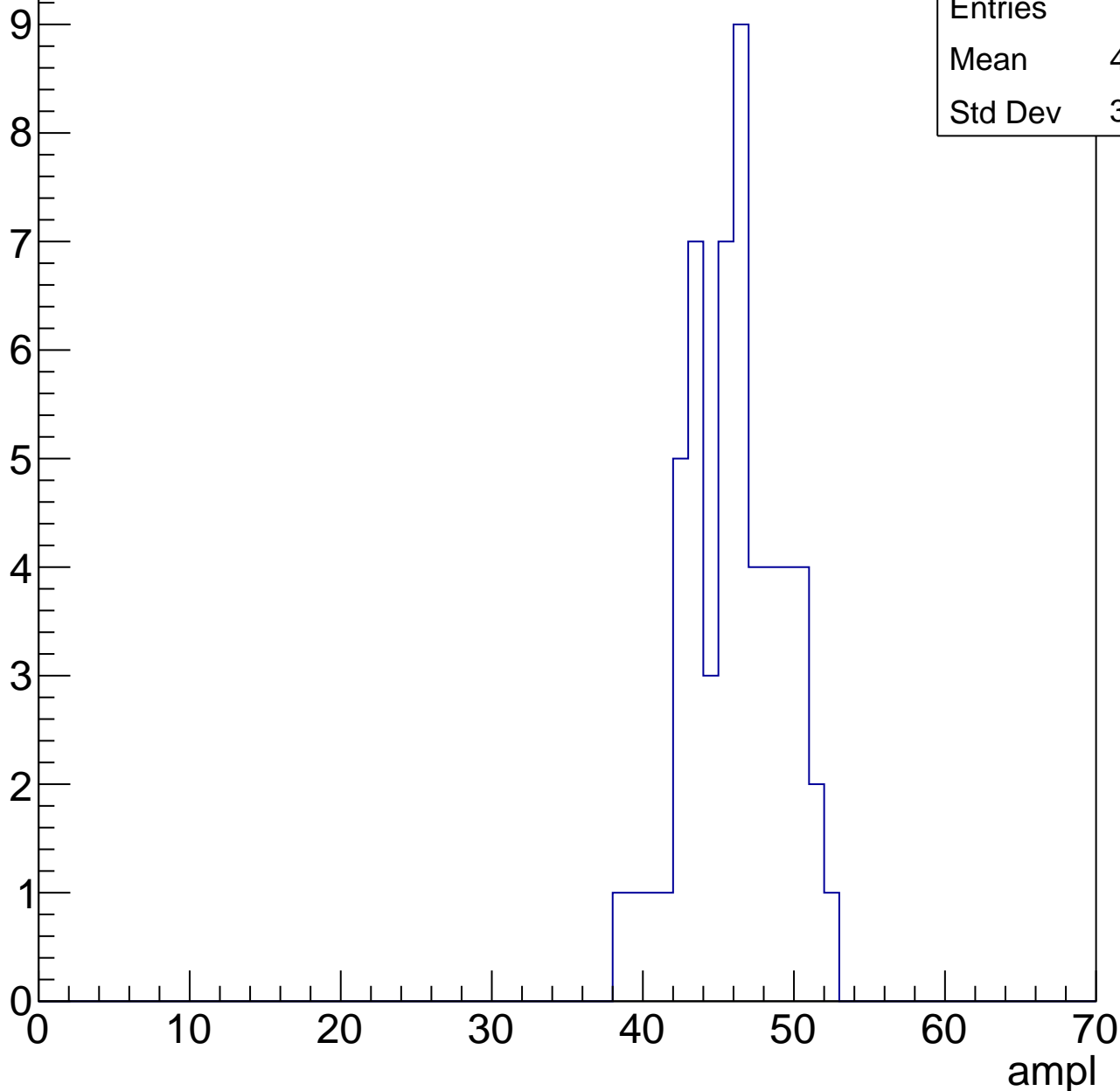
**Gaus Width: 3.6630**



# B1L002S, U3-ch36, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

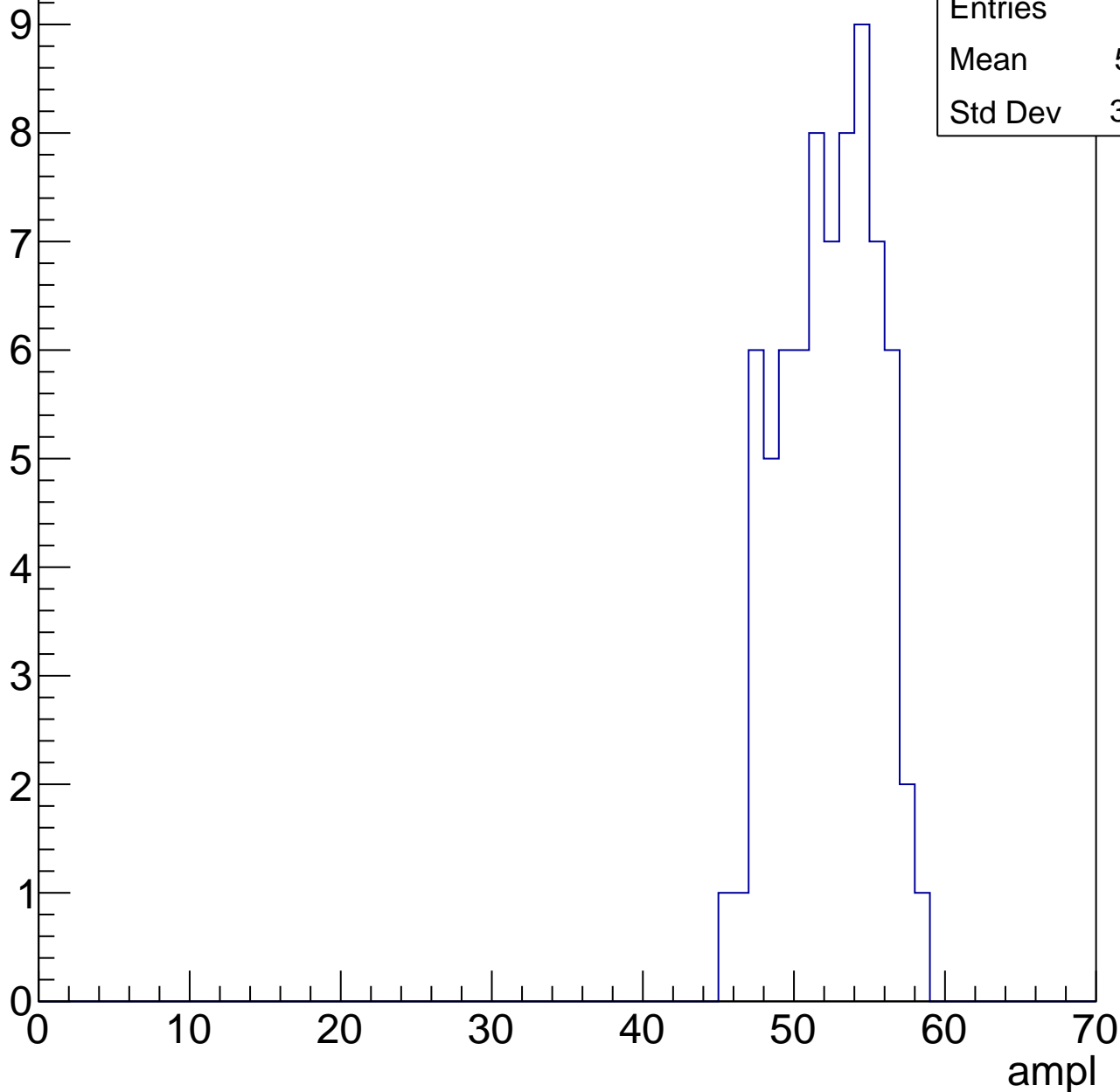


Entries	54
Mean	45.56
Std Dev	3.137

# B1L002S, U3-ch36, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

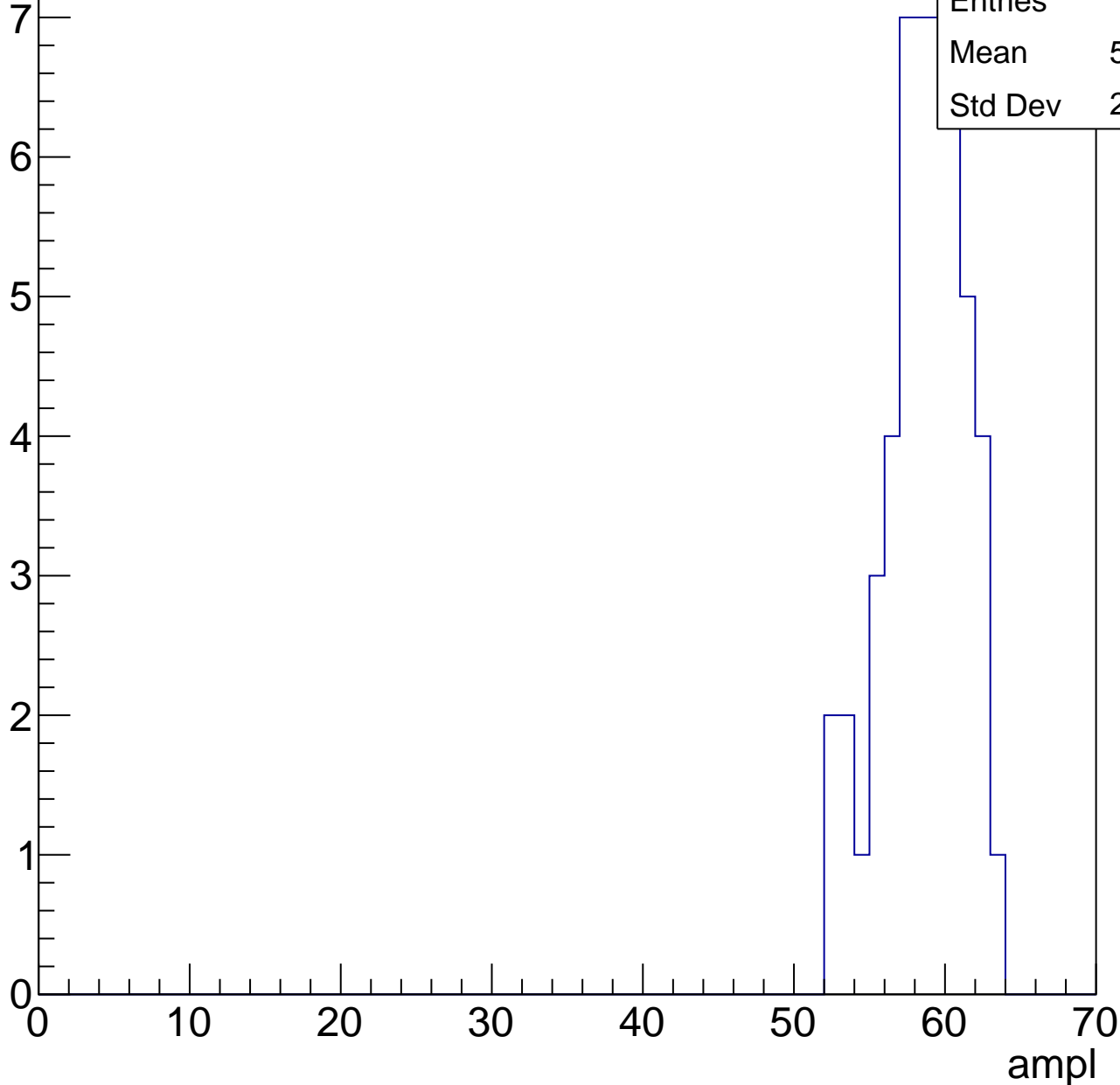
Entry



# B1L002S, U3-ch36, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

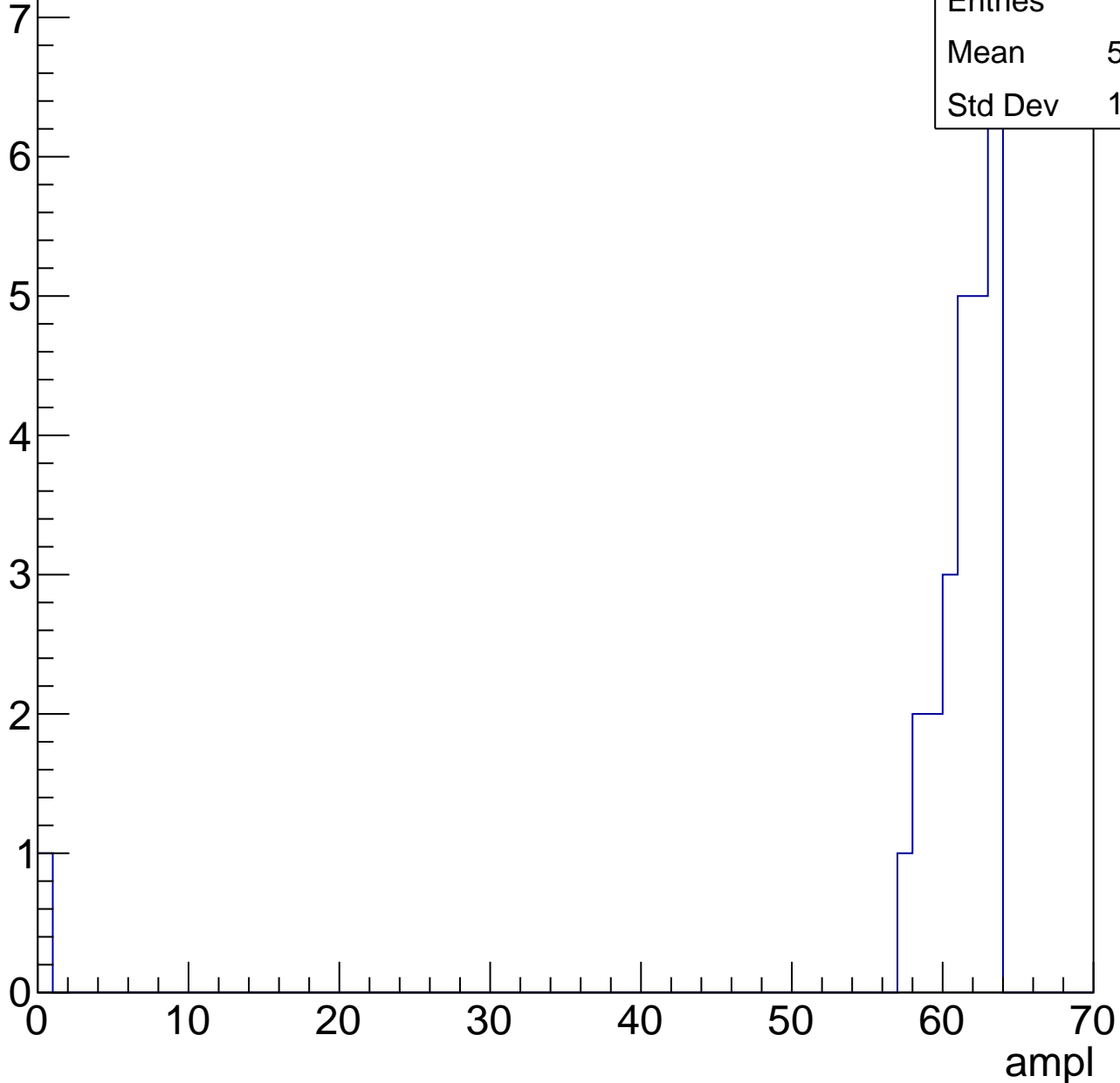


# B1L002S, U3-ch36, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	58.73
Std Dev	11.87





# B1L002S, U3-ch36, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

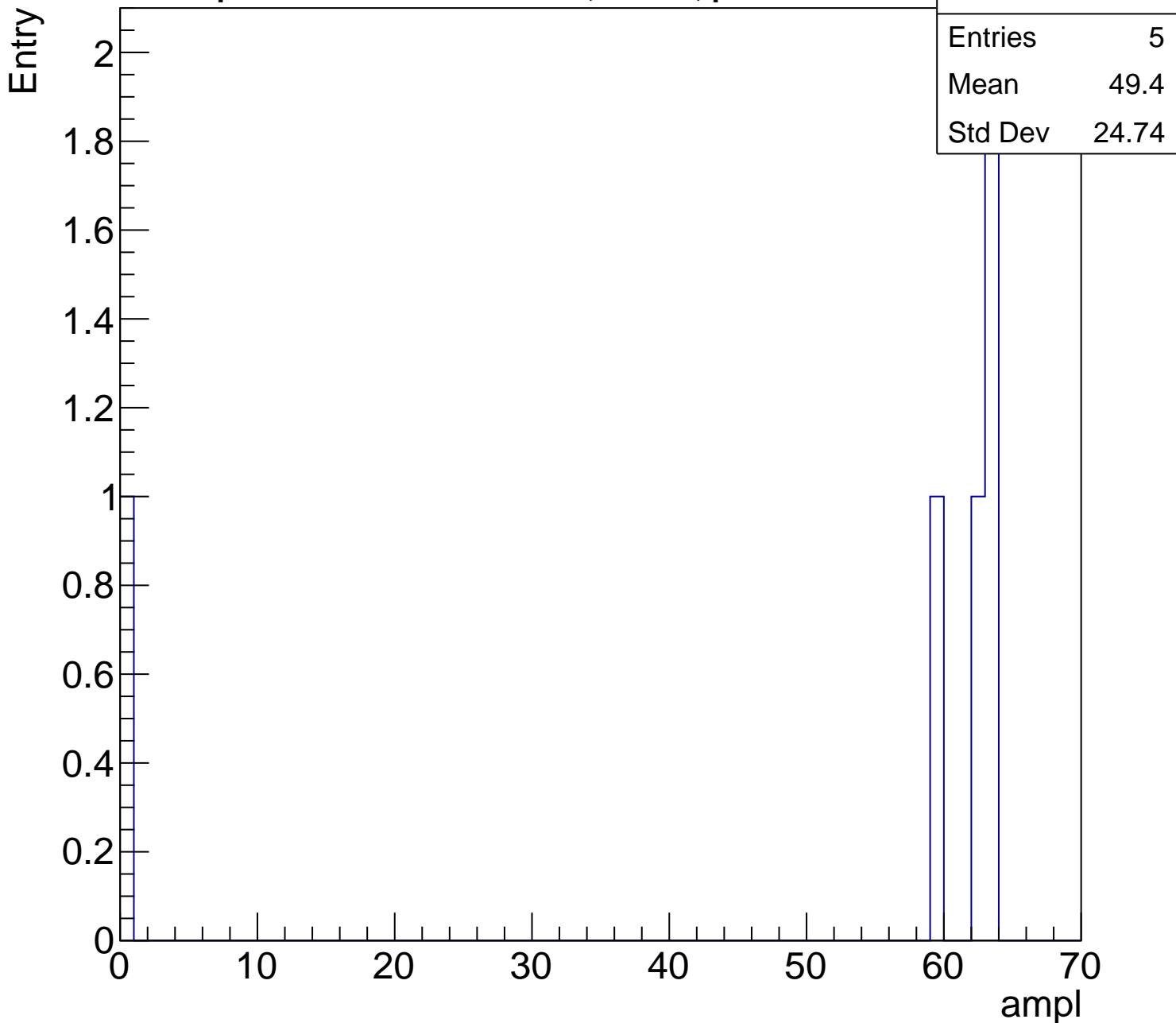
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.4
Std Dev	24.74

0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch37, adc0

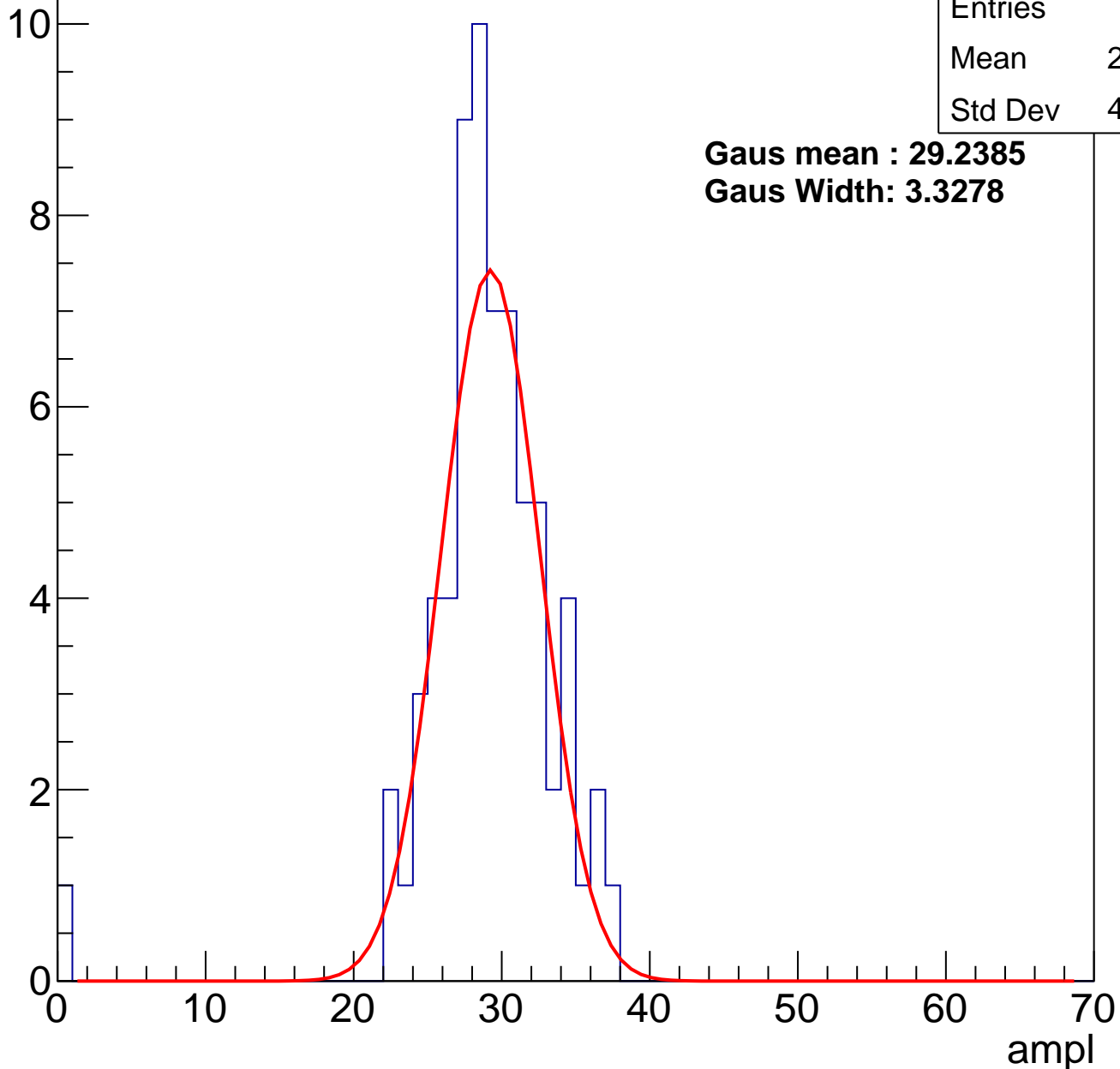
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	68
Mean	28.53
Std Dev	4.825

**Gaus mean : 29.2385**

**Gaus Width: 3.3278**

Entry



# B1L002S, U3-ch37, adc1

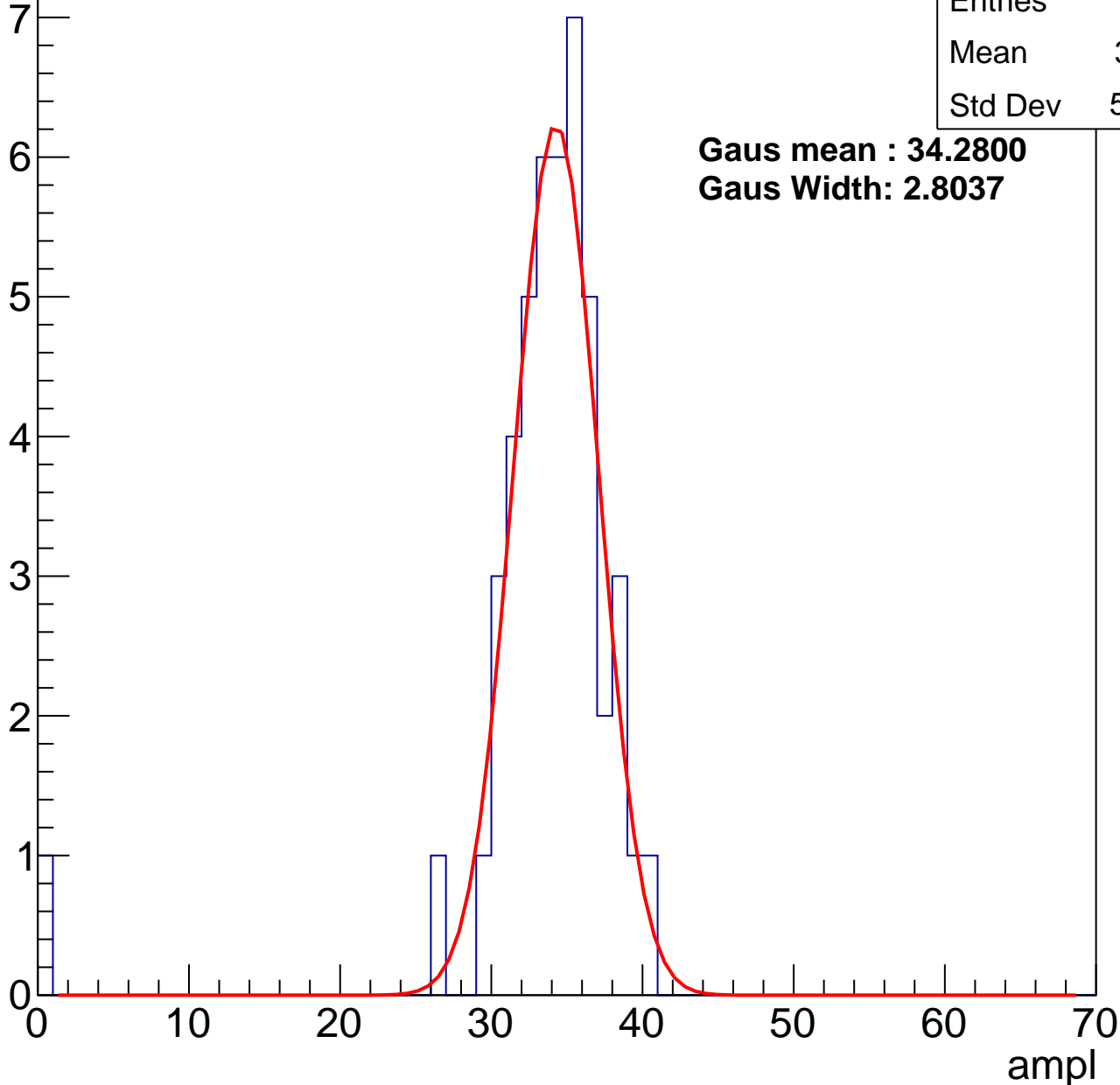
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	33.11
Std Dev	5.658

**Gaus mean : 34.2800**

**Gaus Width: 2.8037**



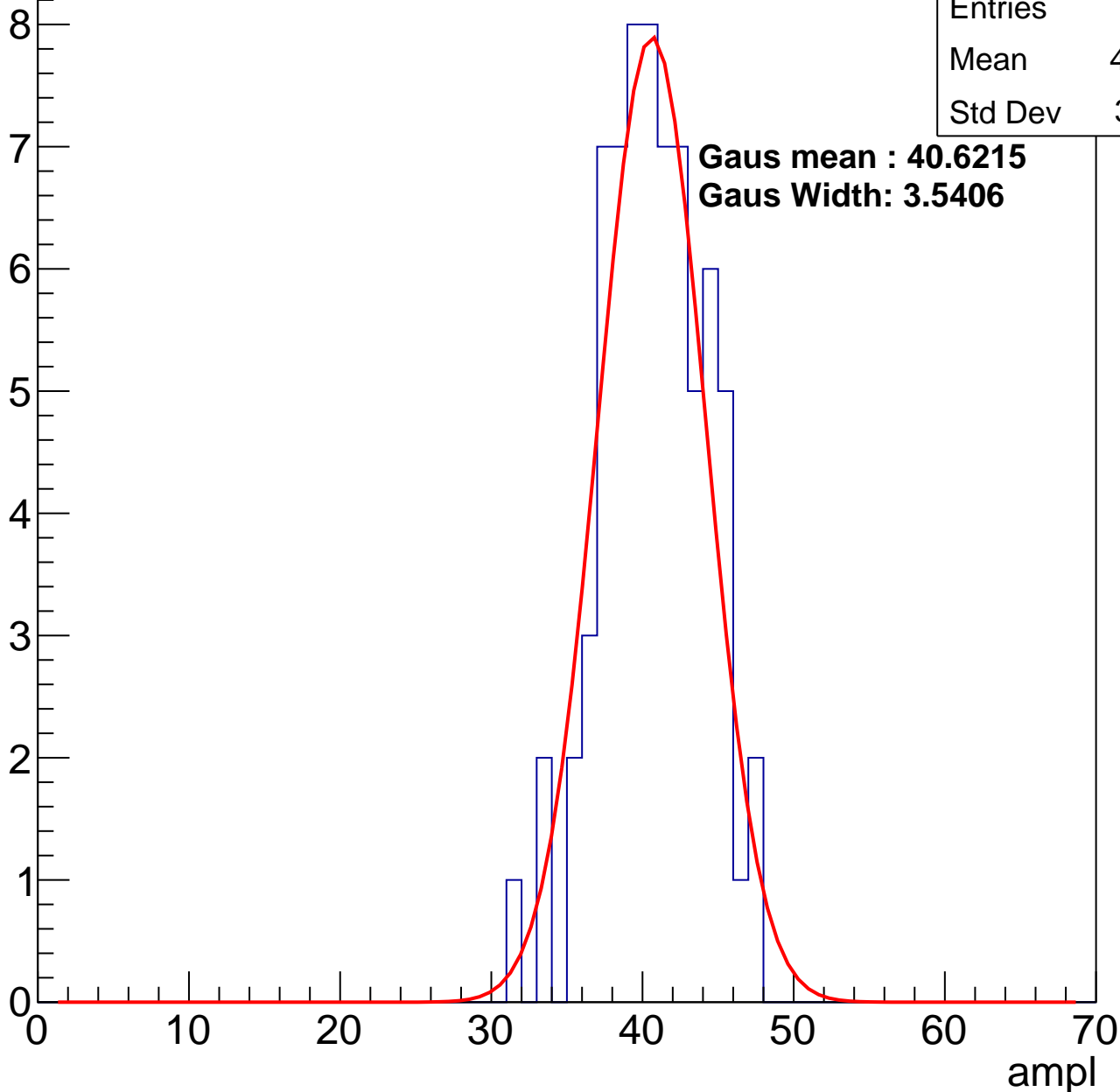
# B1L002S, U3-ch37, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	40.24
Std Dev	3.371

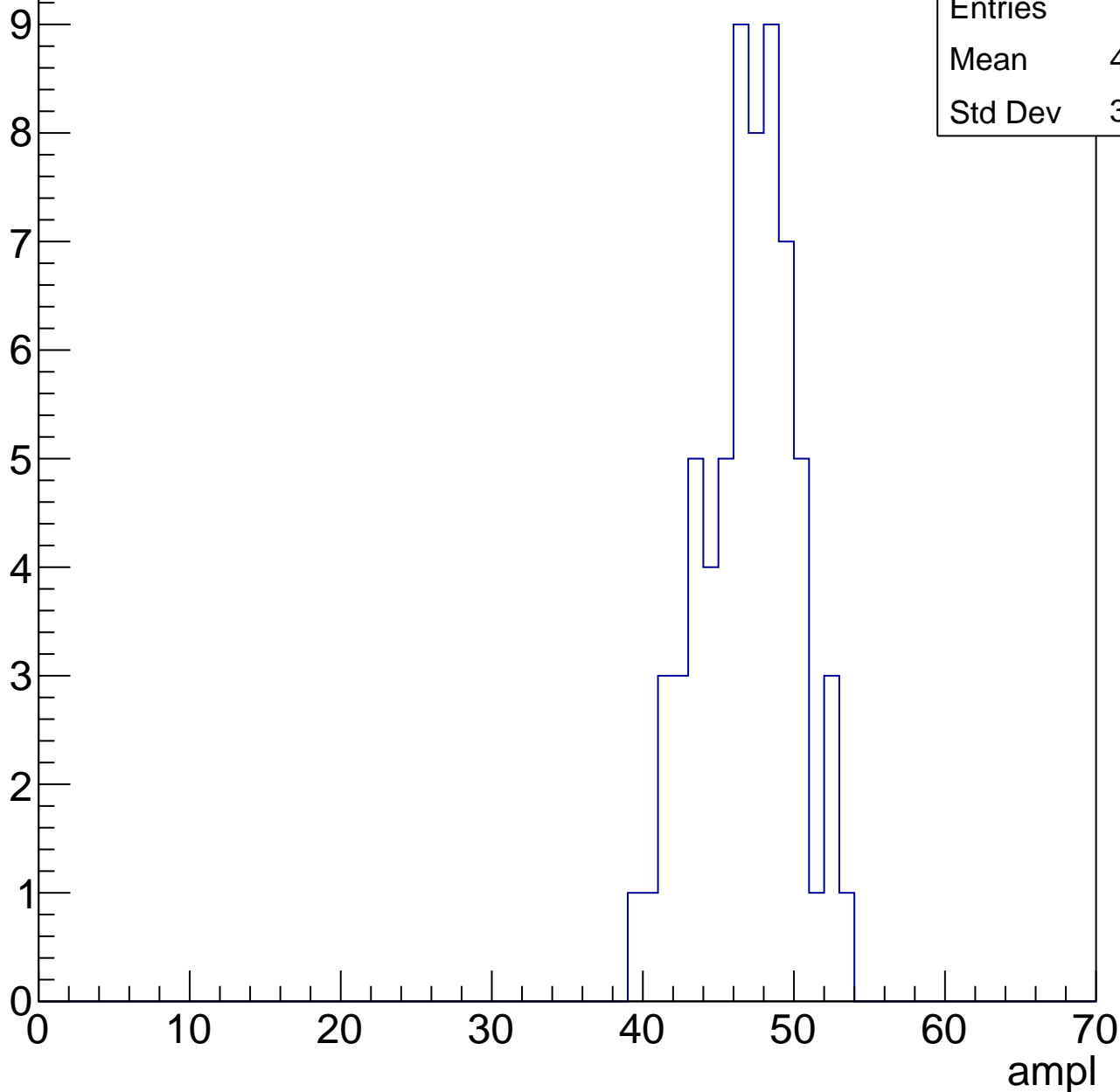
**Gaus mean : 40.6215**  
**Gaus Width: 3.5406**



# B1L002S, U3-ch37, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

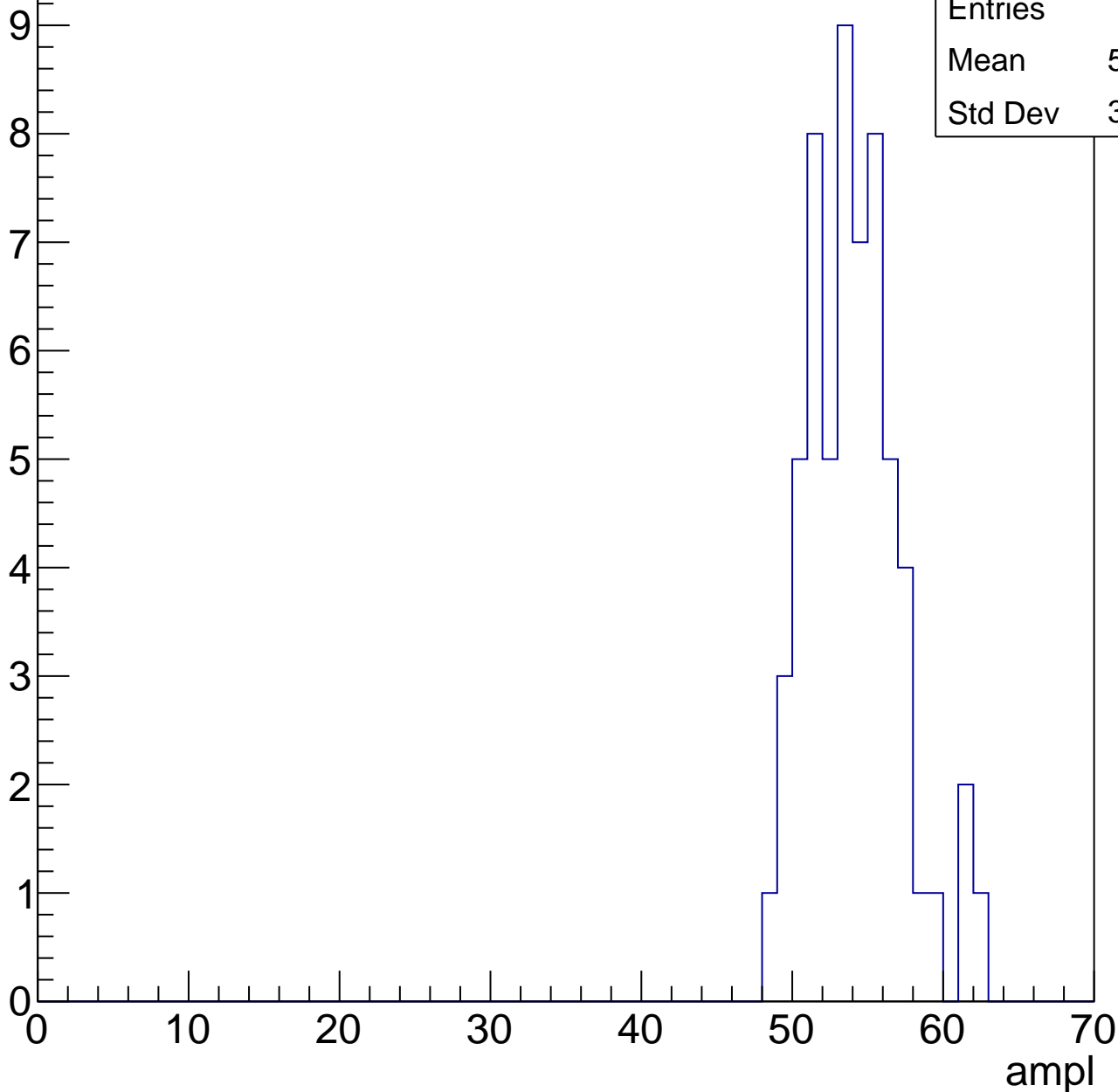


# B1L002S, U3-ch37, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	53.62
Std Dev	3.028

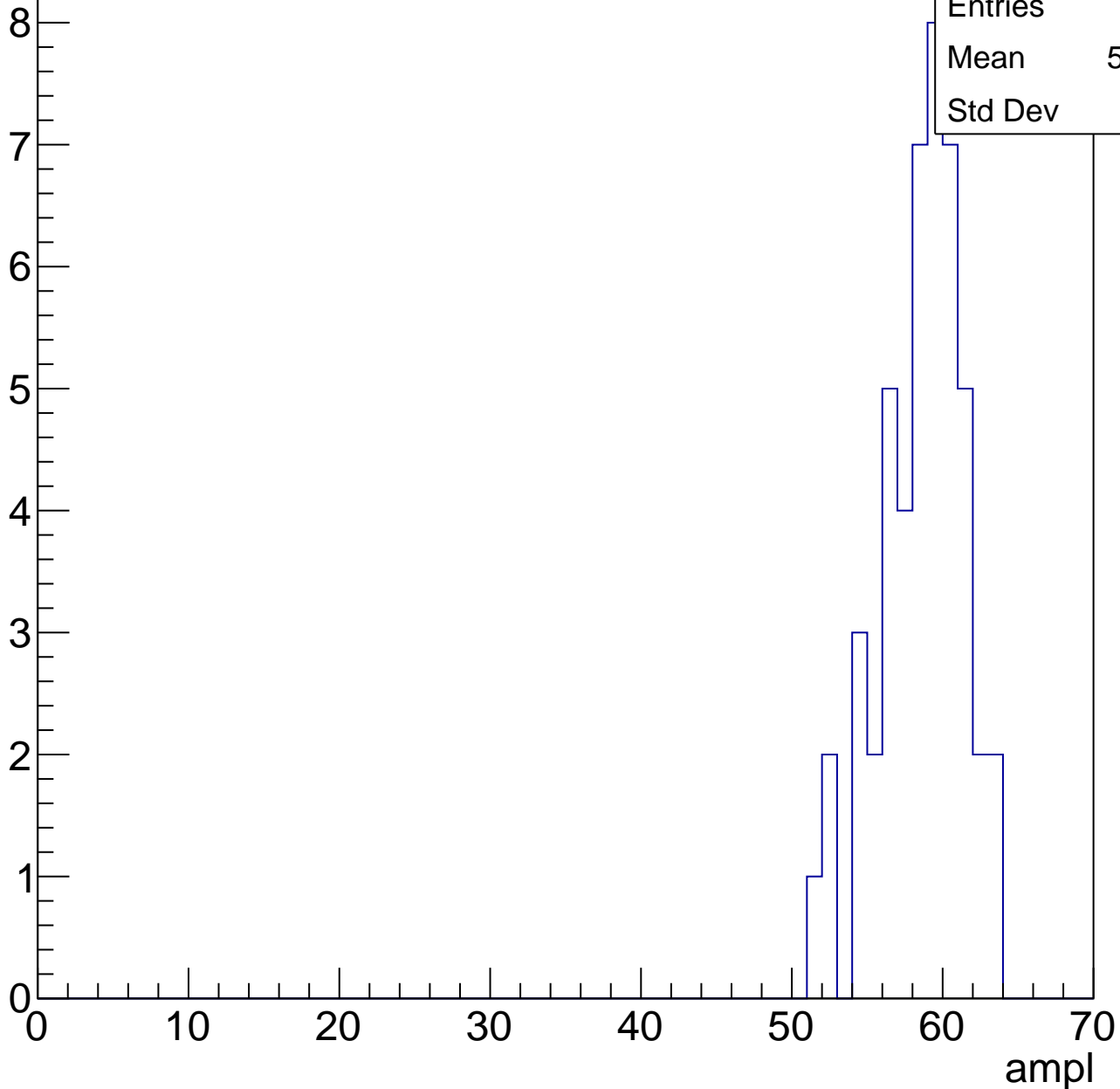


# B1L002S, U3-ch37, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.08
Std Dev	2.79

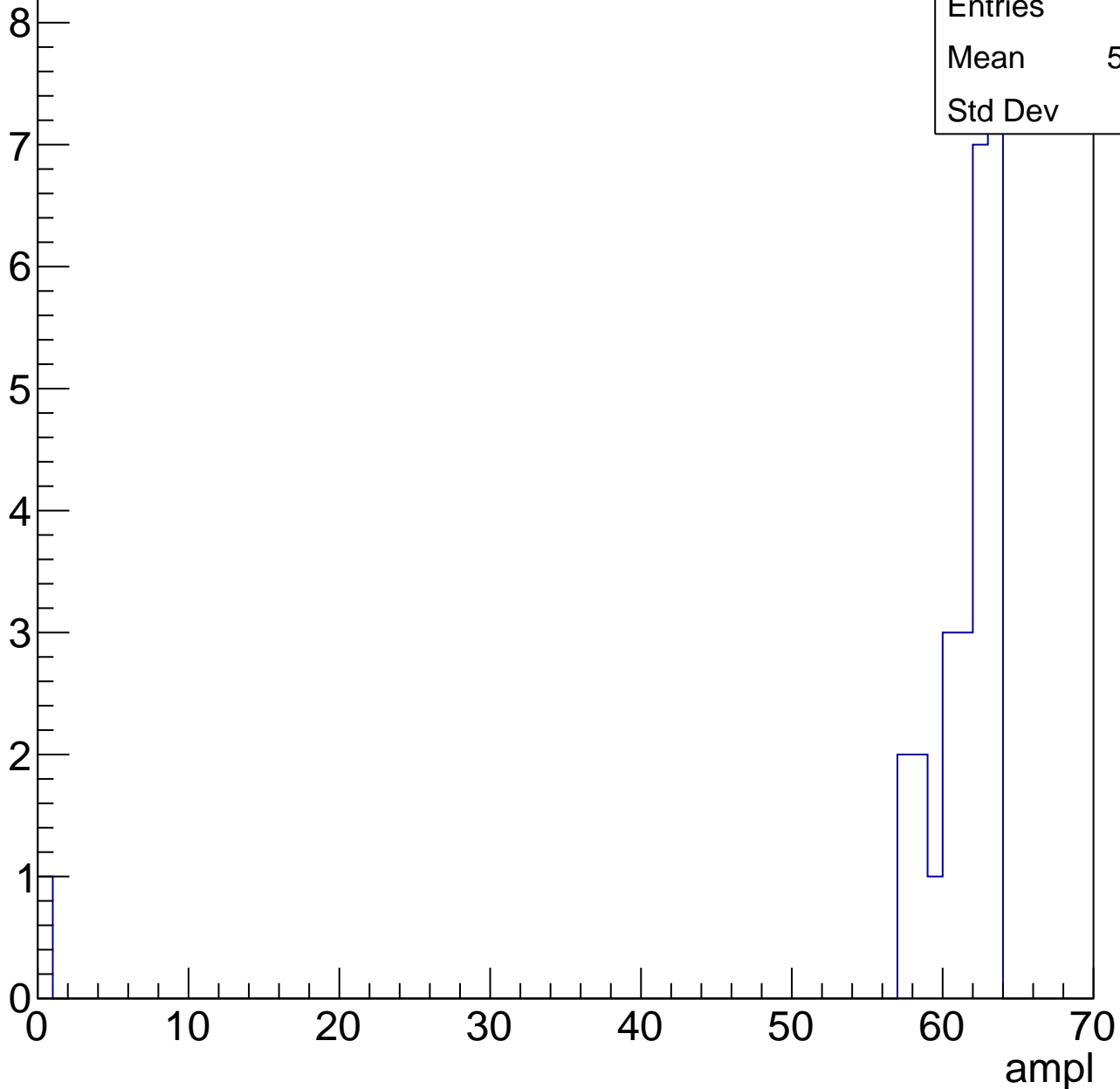


# B1L002S, U3-ch37, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.89
Std Dev	11.7





# B1L002S, U3-ch37, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch38, adc0

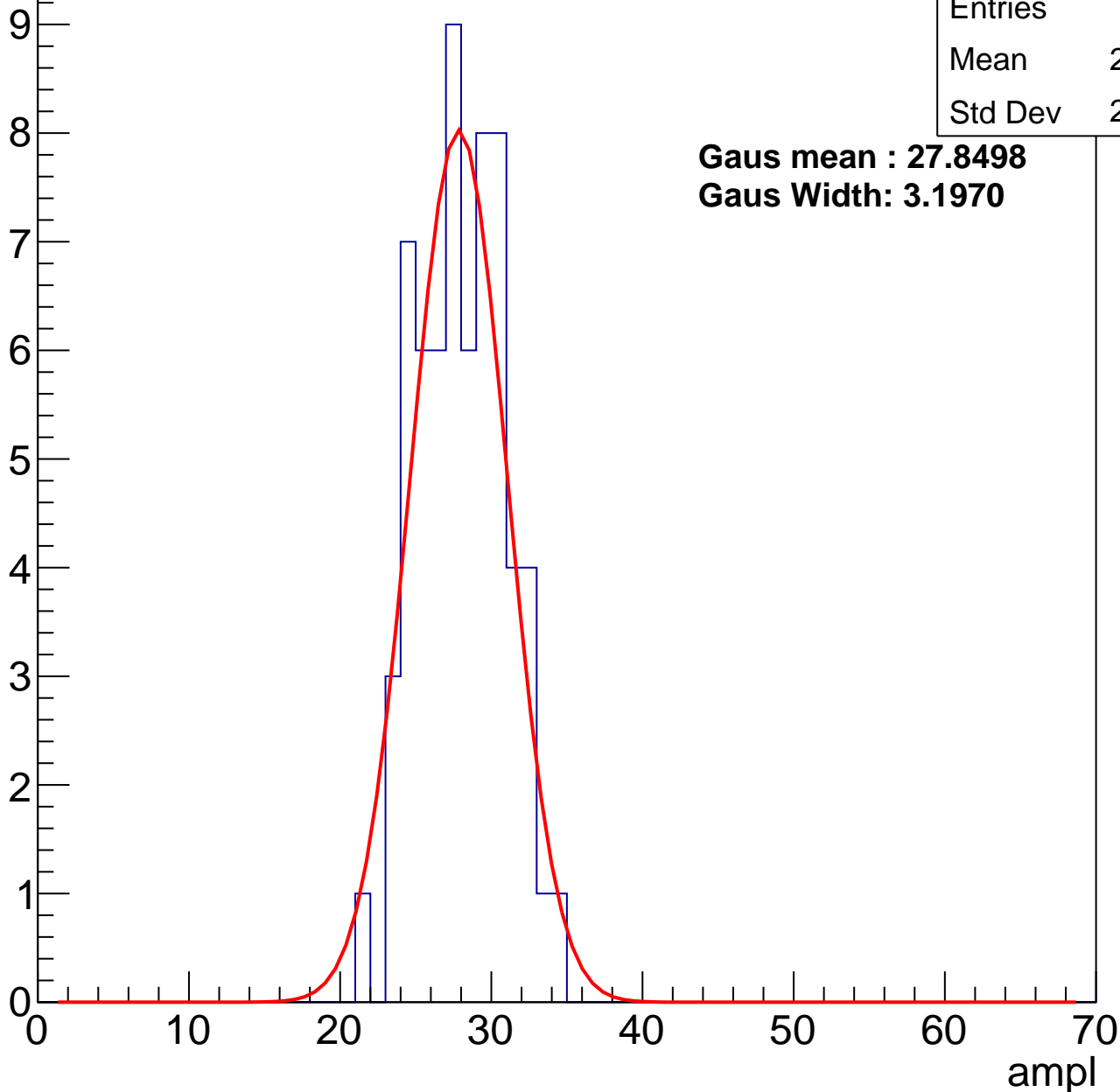
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.59
Std Dev	2.832

**Gaus mean : 27.8498**

**Gaus Width: 3.1970**



# B1L002S, U3-ch38, adc1

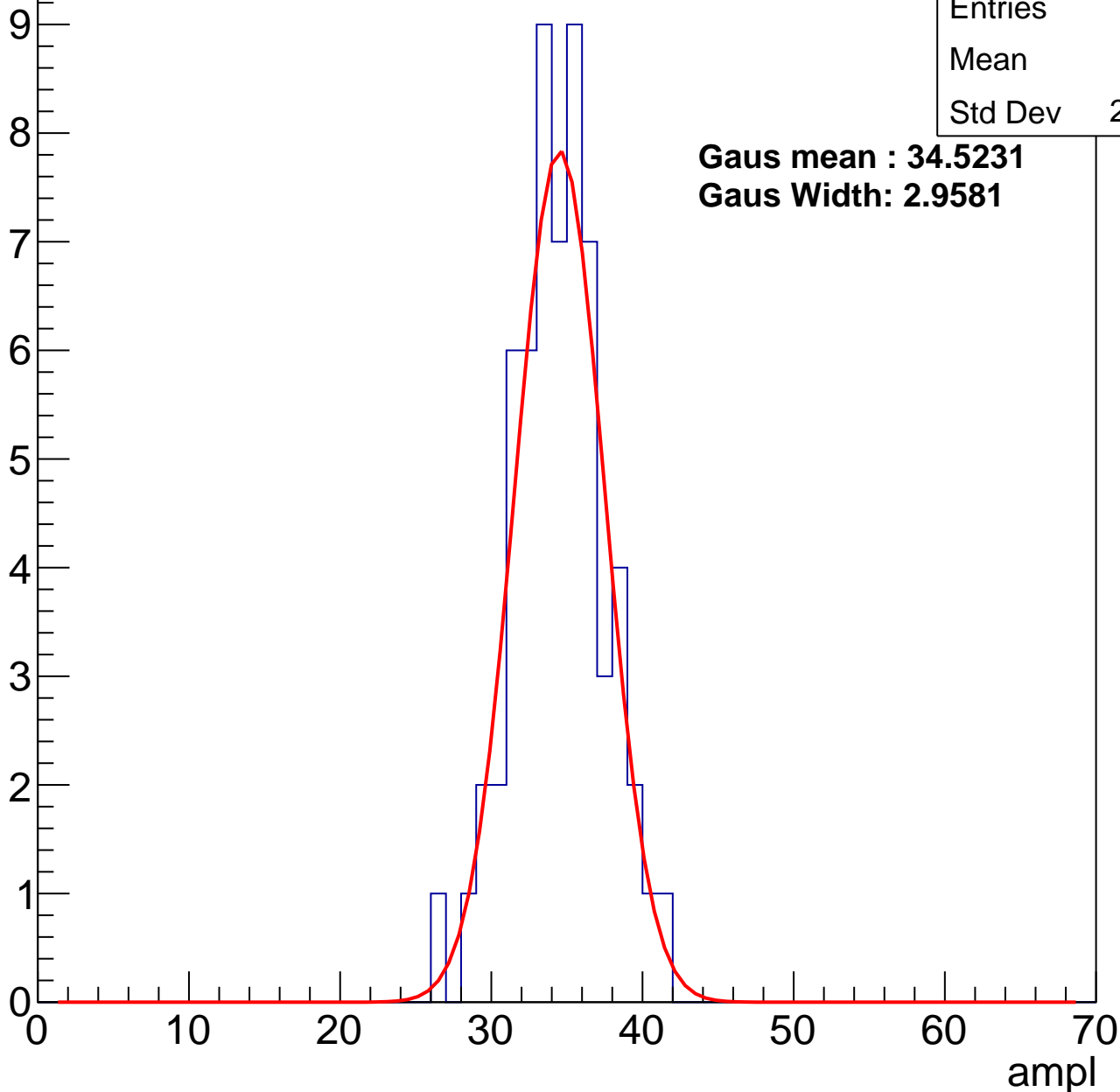
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	34
Std Dev	2.953

**Gaus mean : 34.5231**

**Gaus Width: 2.9581**



# B1L002S, U3-ch38, adc2

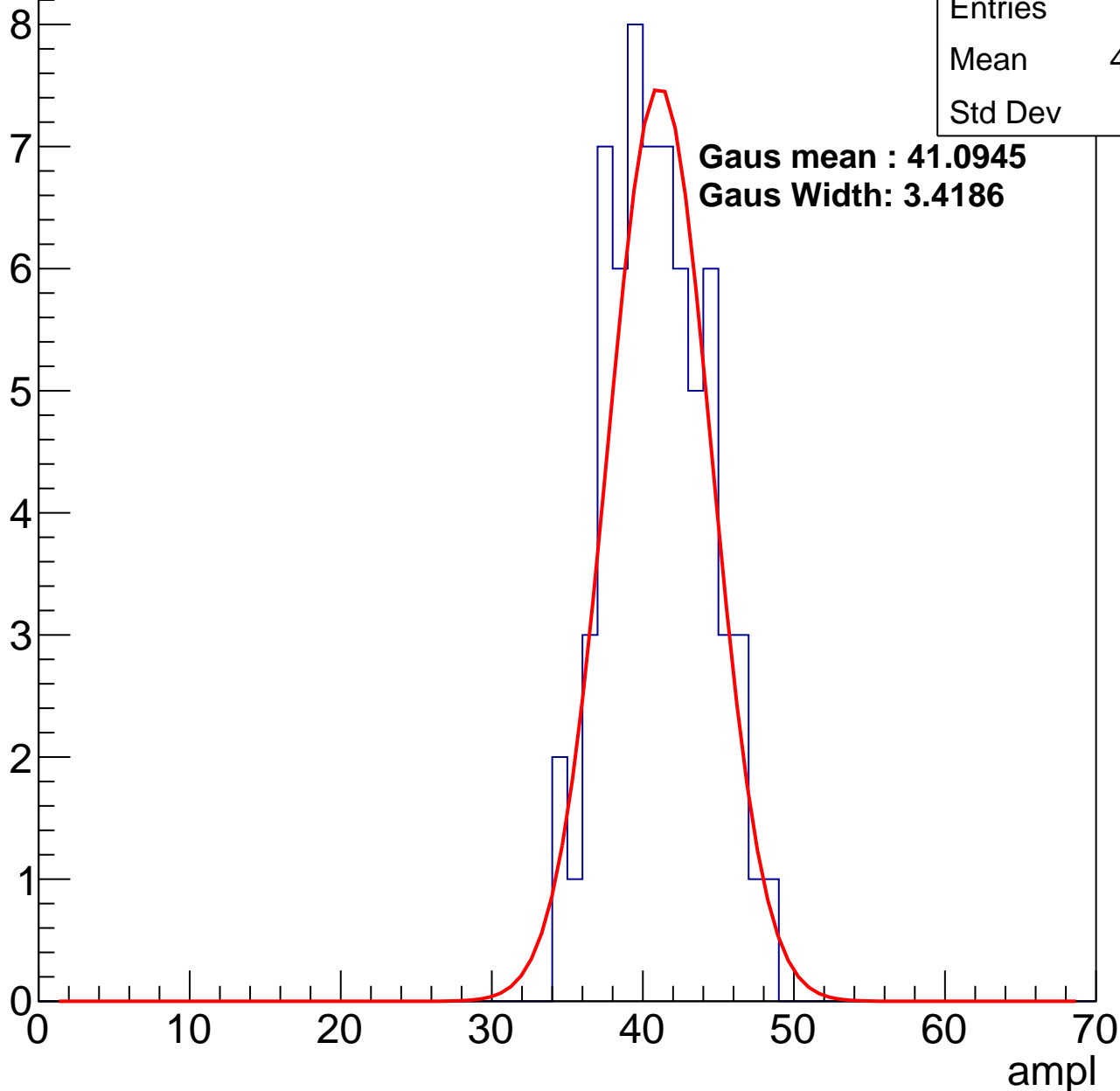
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.55
Std Dev	3.23

**Gaus mean : 41.0945**

**Gaus Width: 3.4186**

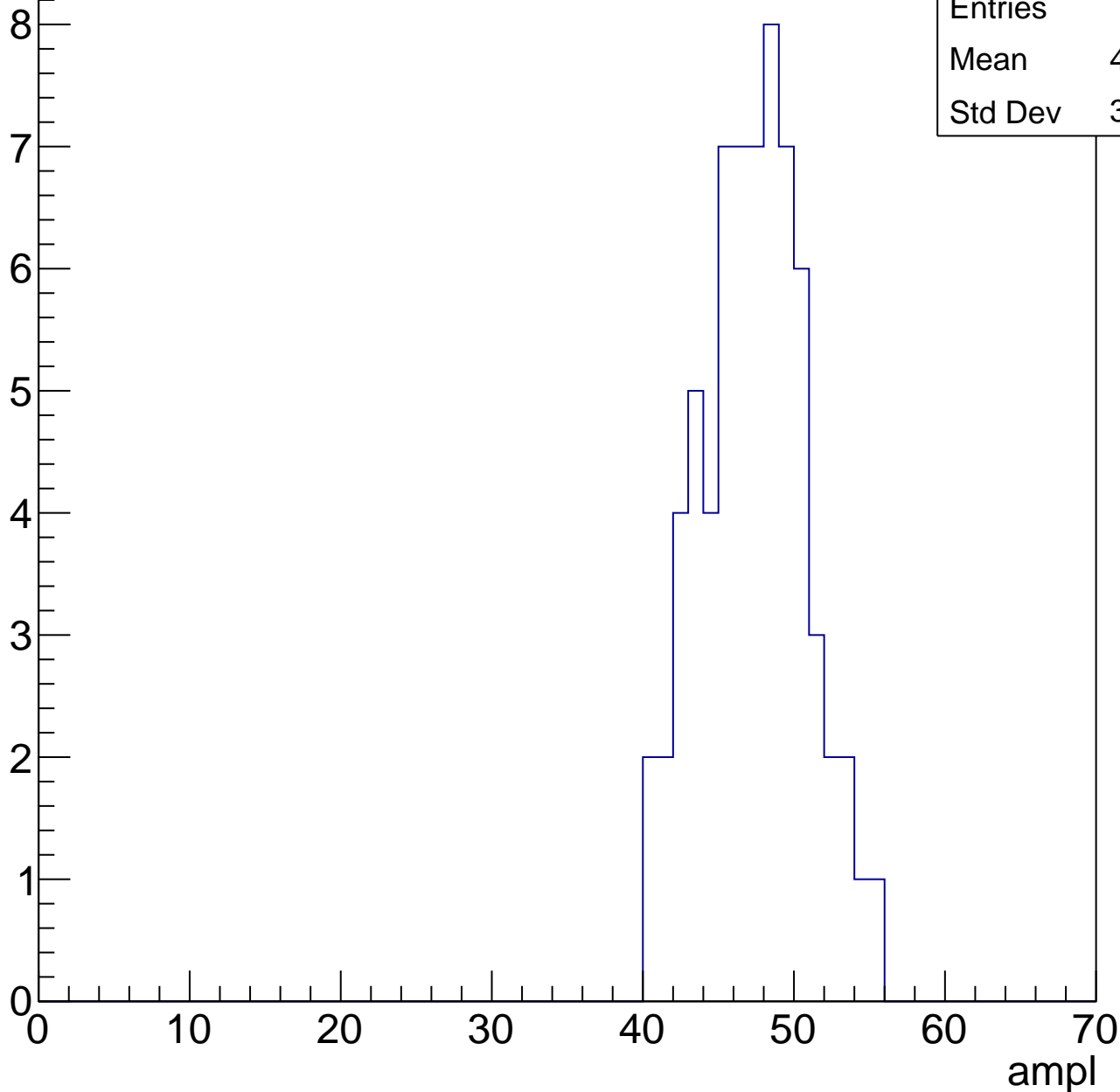


# B1L002S, U3-ch38, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	46.85
Std Dev	3.414

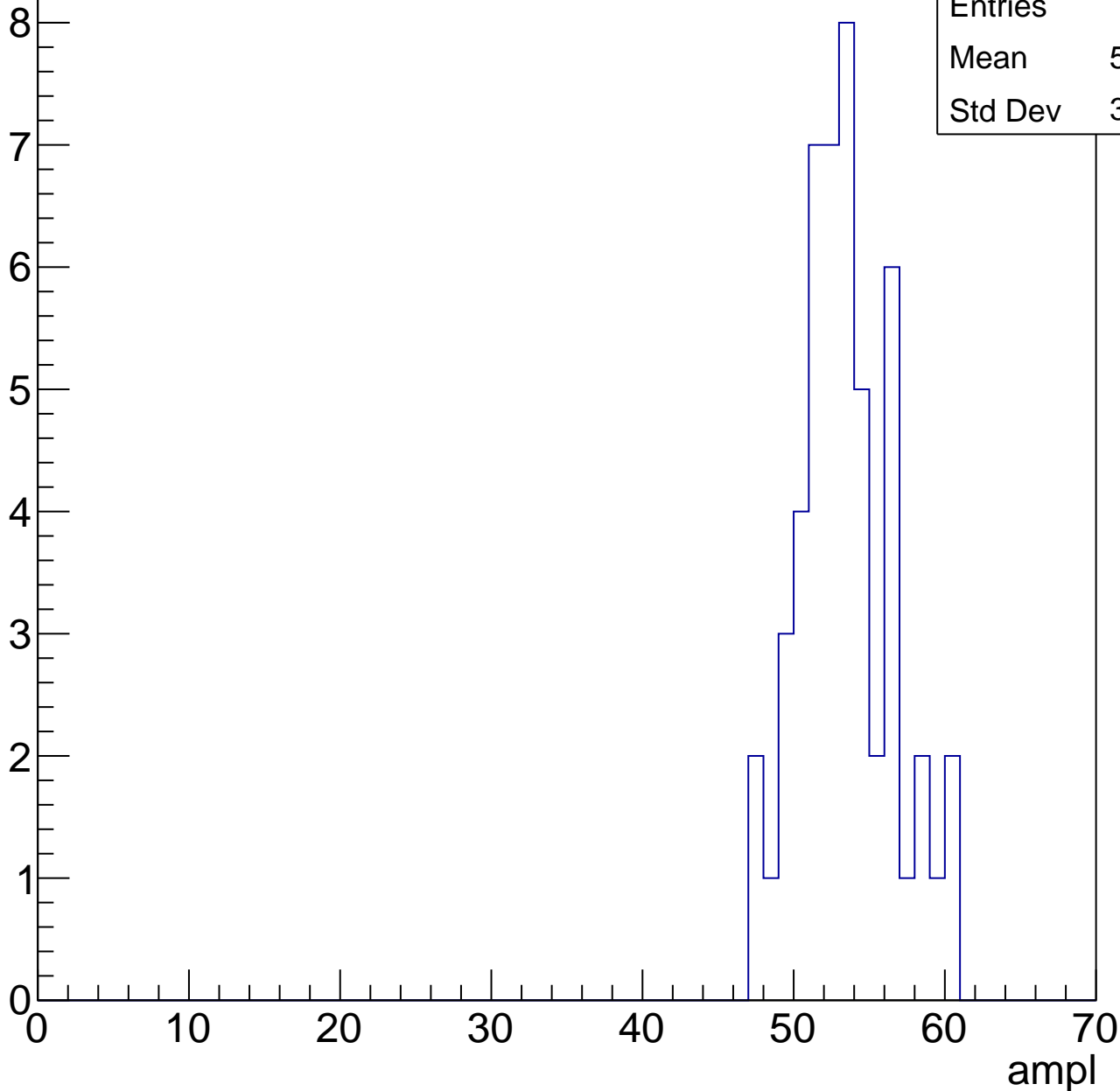


# B1L002S, U3-ch38, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	52.98
Std Dev	3.077

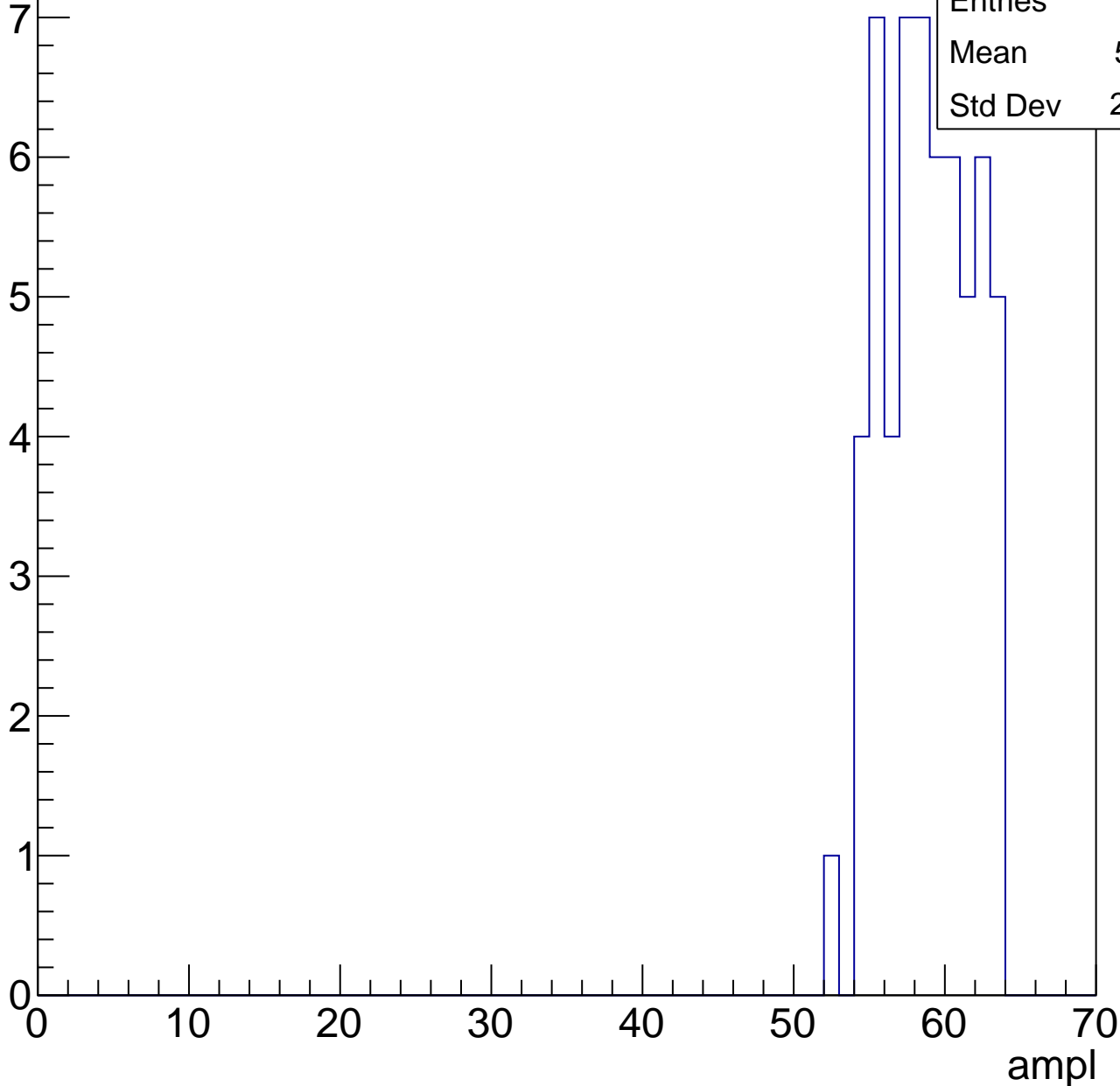


# B1L002S, U3-ch38, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	58.41
Std Dev	2.853

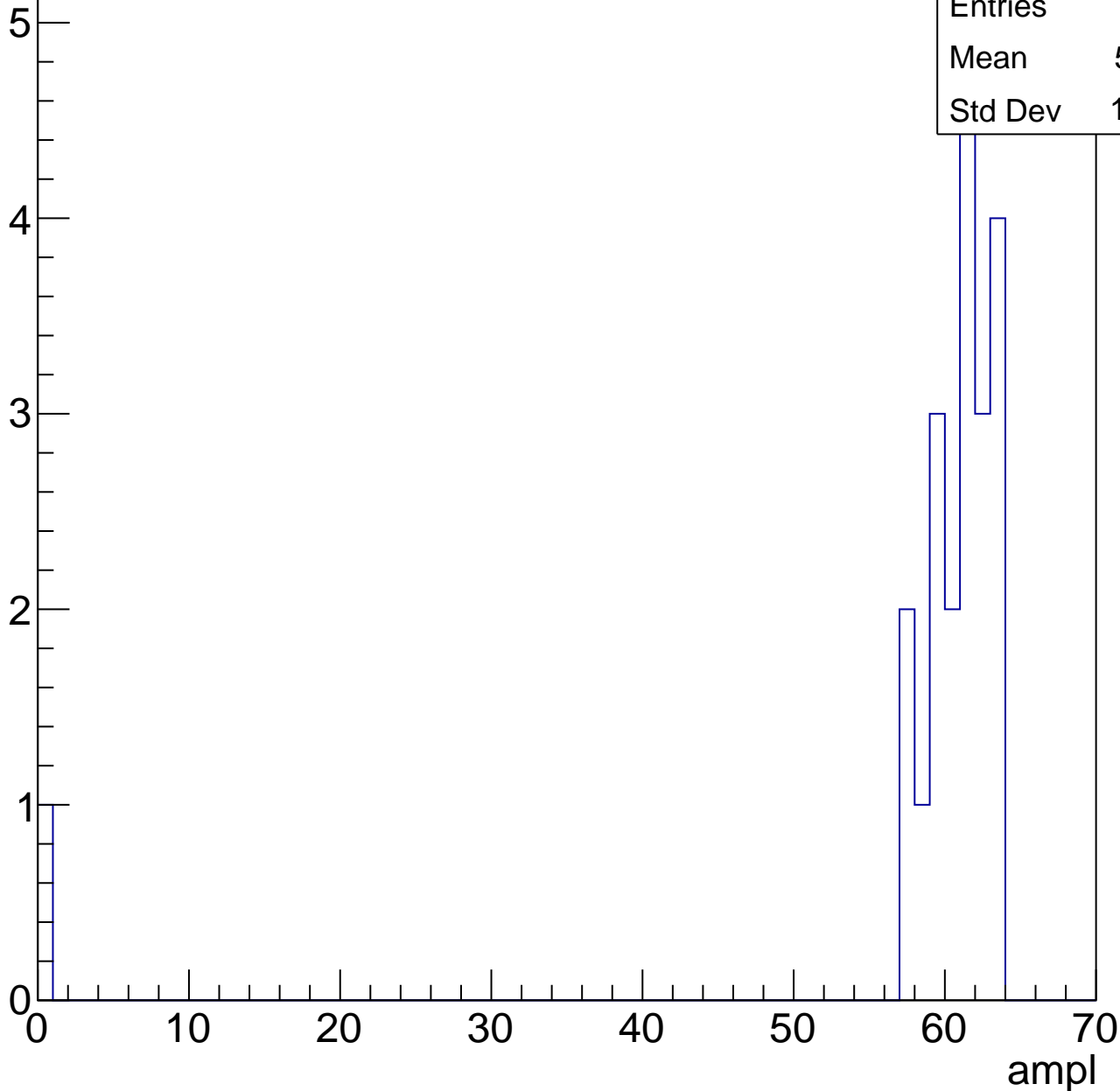


# B1L002S, U3-ch38, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	57.71
Std Dev	13.04





# B1L002S, U3-ch38, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch39, adc0

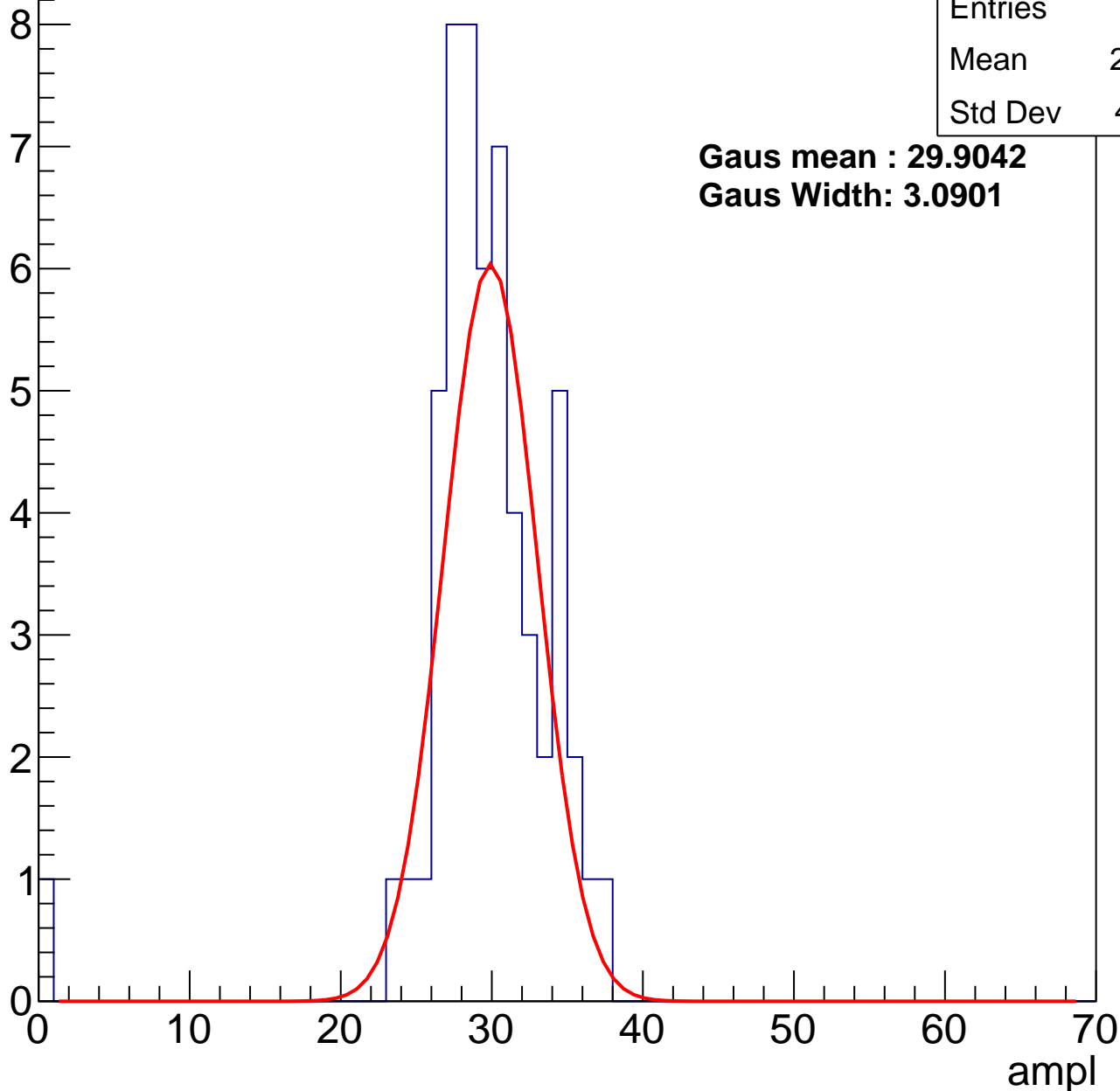
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	29.02
Std Dev	4.991

**Gaus mean : 29.9042**

**Gaus Width: 3.0901**



# B1L002S, U3-ch39, adc1

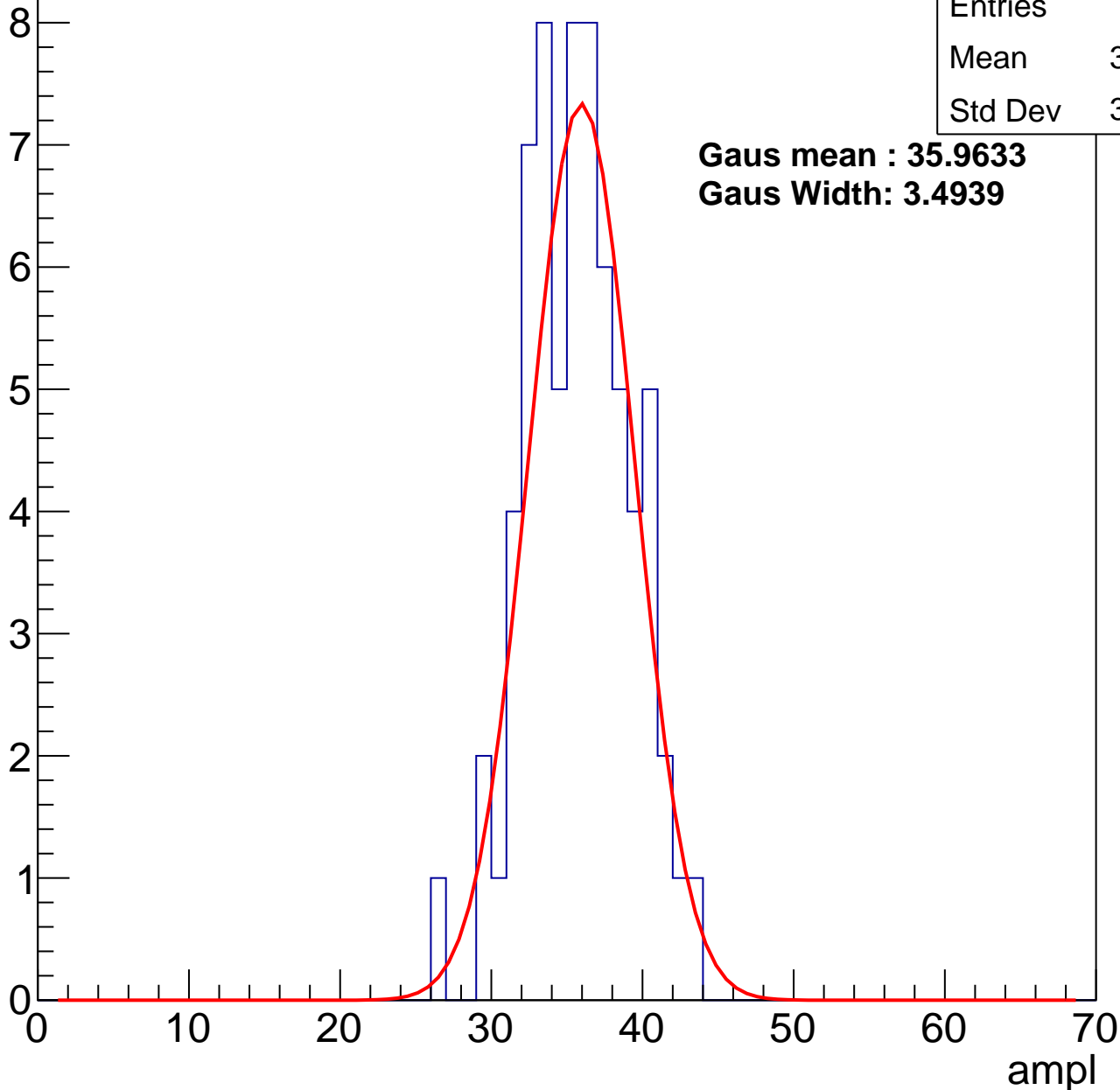
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.28
Std Dev	3.403

**Gaus mean : 35.9633**

**Gaus Width: 3.4939**



# B1L002S, U3-ch39, adc2

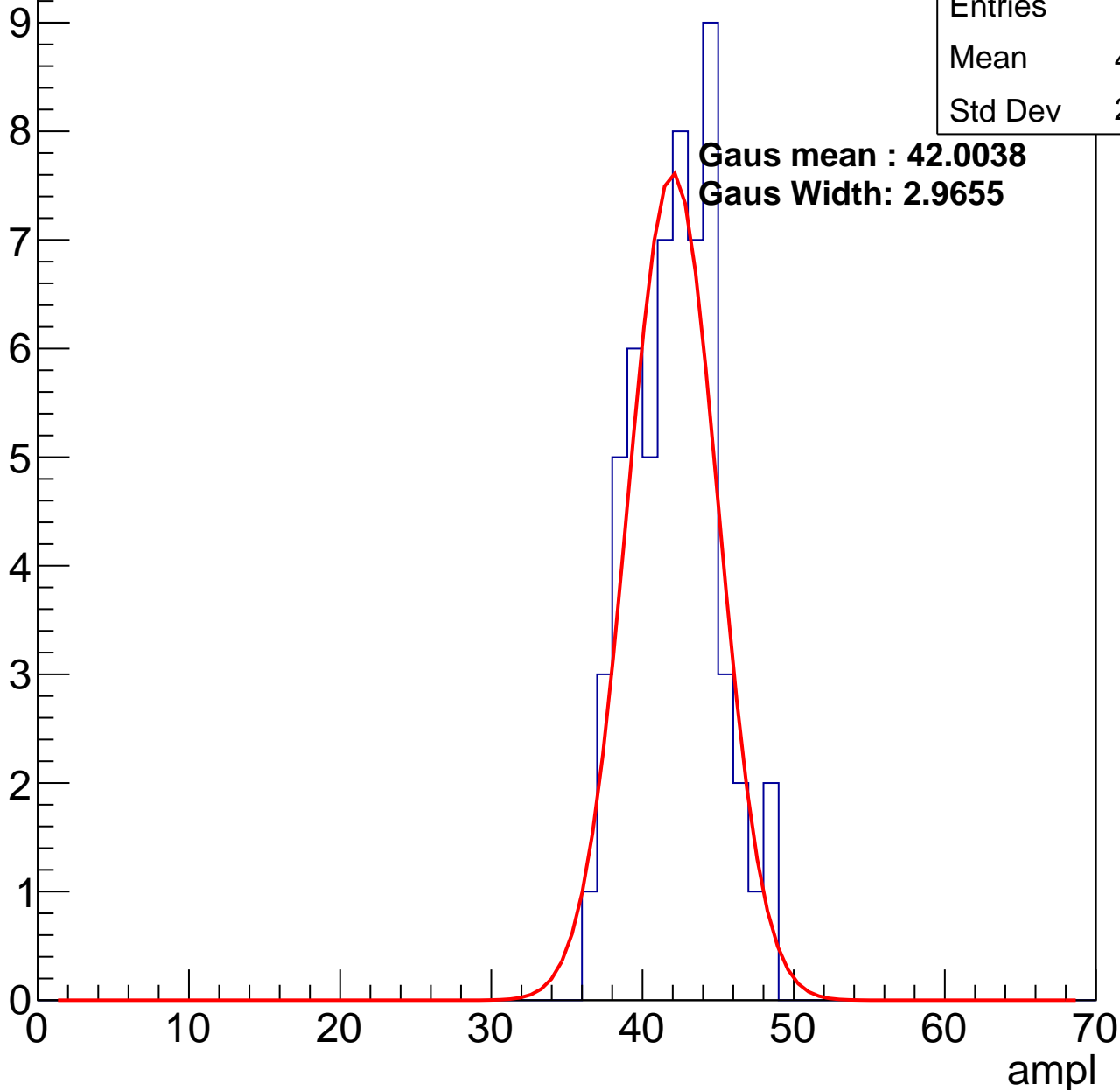
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.71
Std Dev	2.811

**Gaus mean : 42.0038**

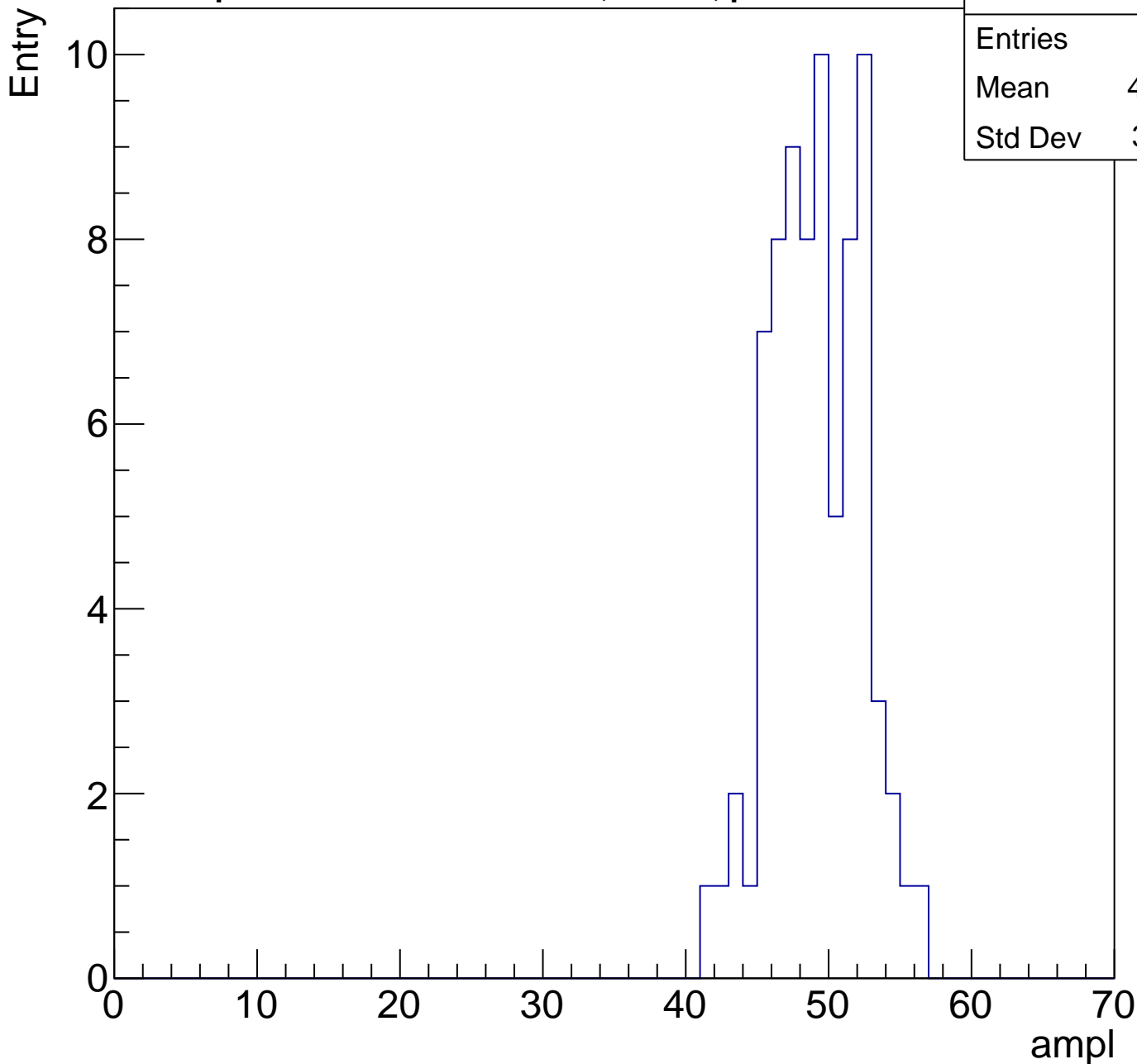
**Gaus Width: 2.9655**



# B1L002S, U3-ch39, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	48.69
Std Dev	3.101

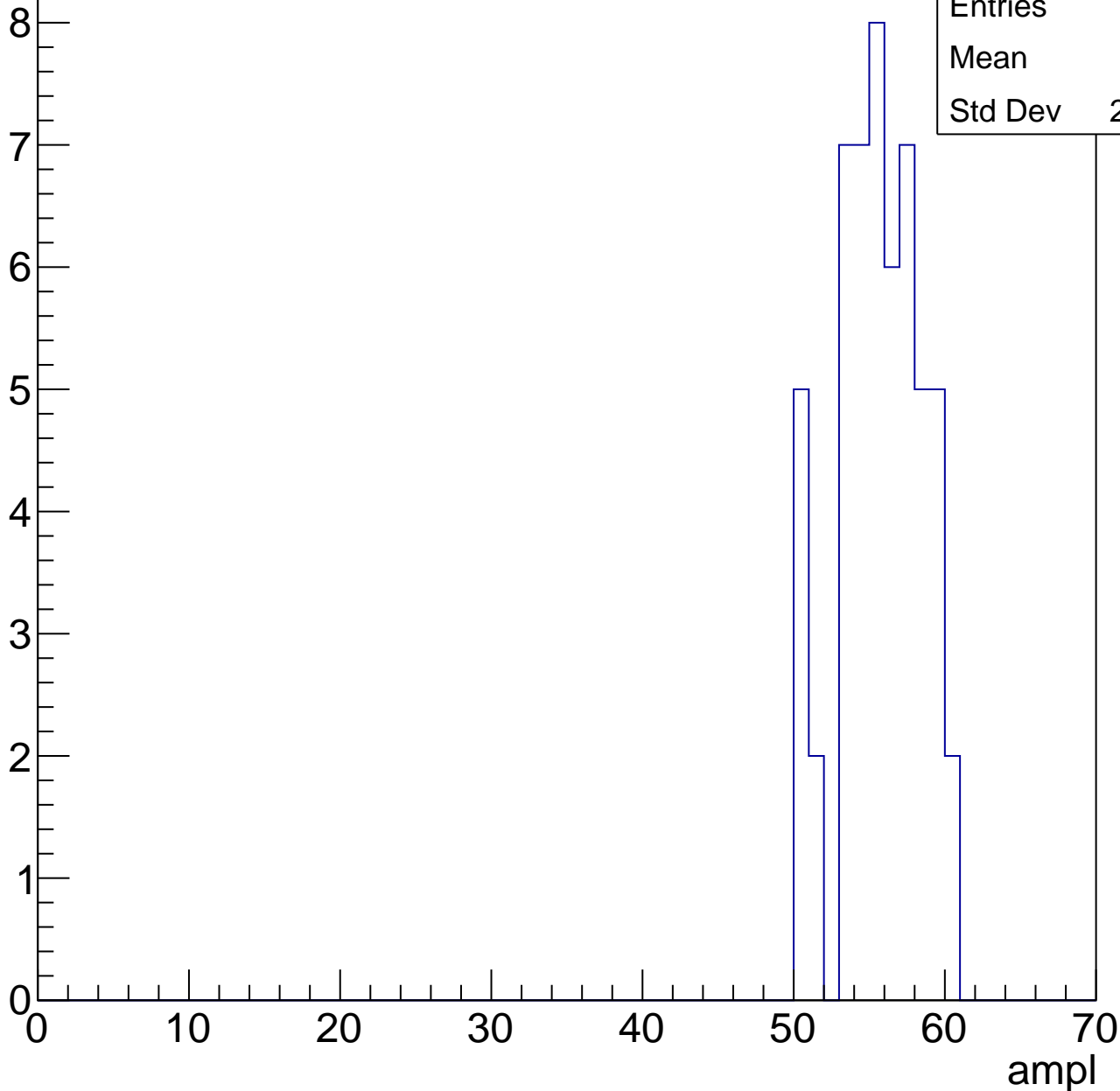


# B1L002S, U3-ch39, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	55.2
Std Dev	2.717



# B1L002S, U3-ch39, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7

6

5

4

3

2

1

0

Entries	41
Mean	59.73
Std Dev	2.338

ampl

0

10

20

30

40

50

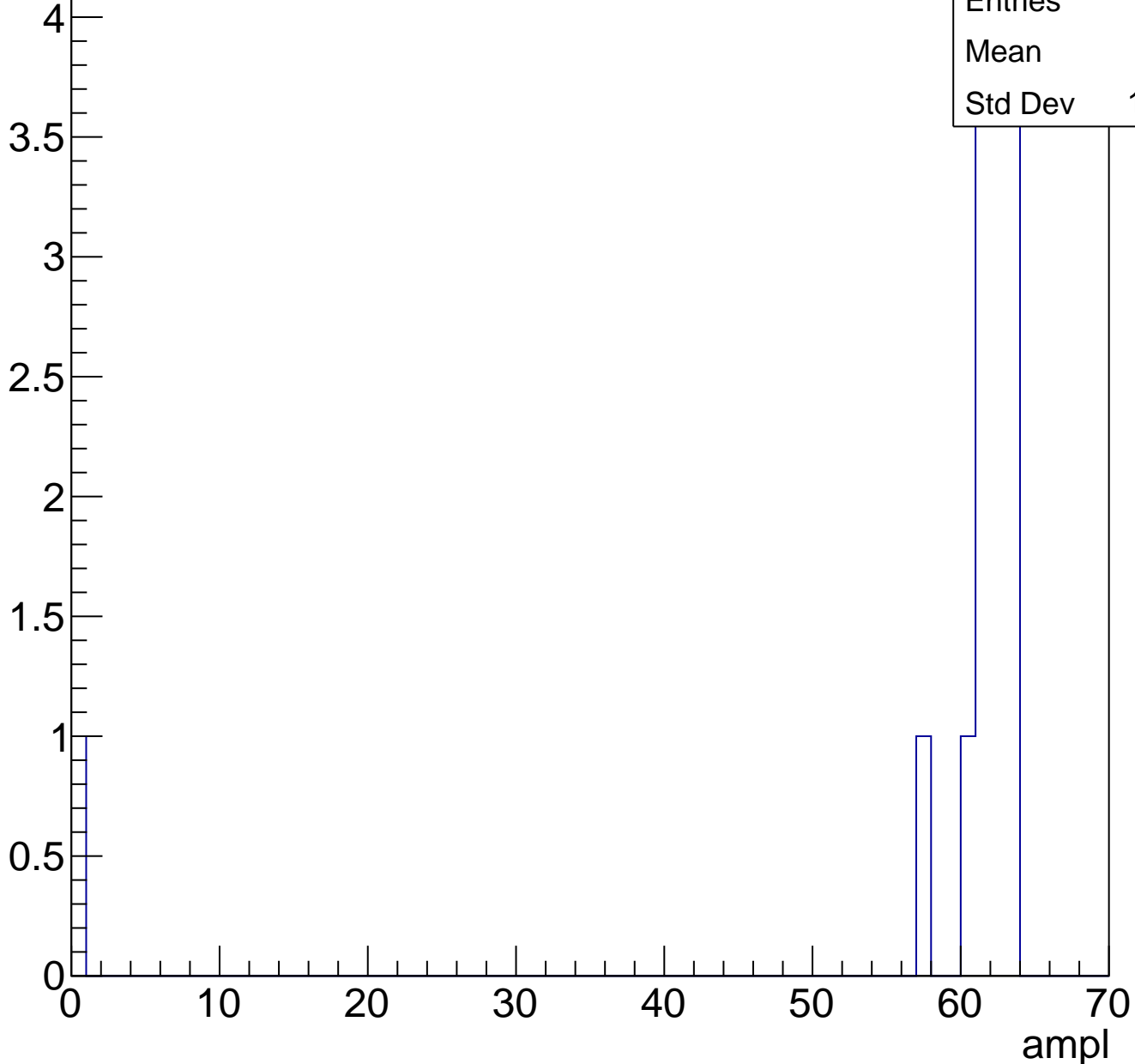
60

70

# B1L002S, U3-ch39, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch39, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch40, adc0

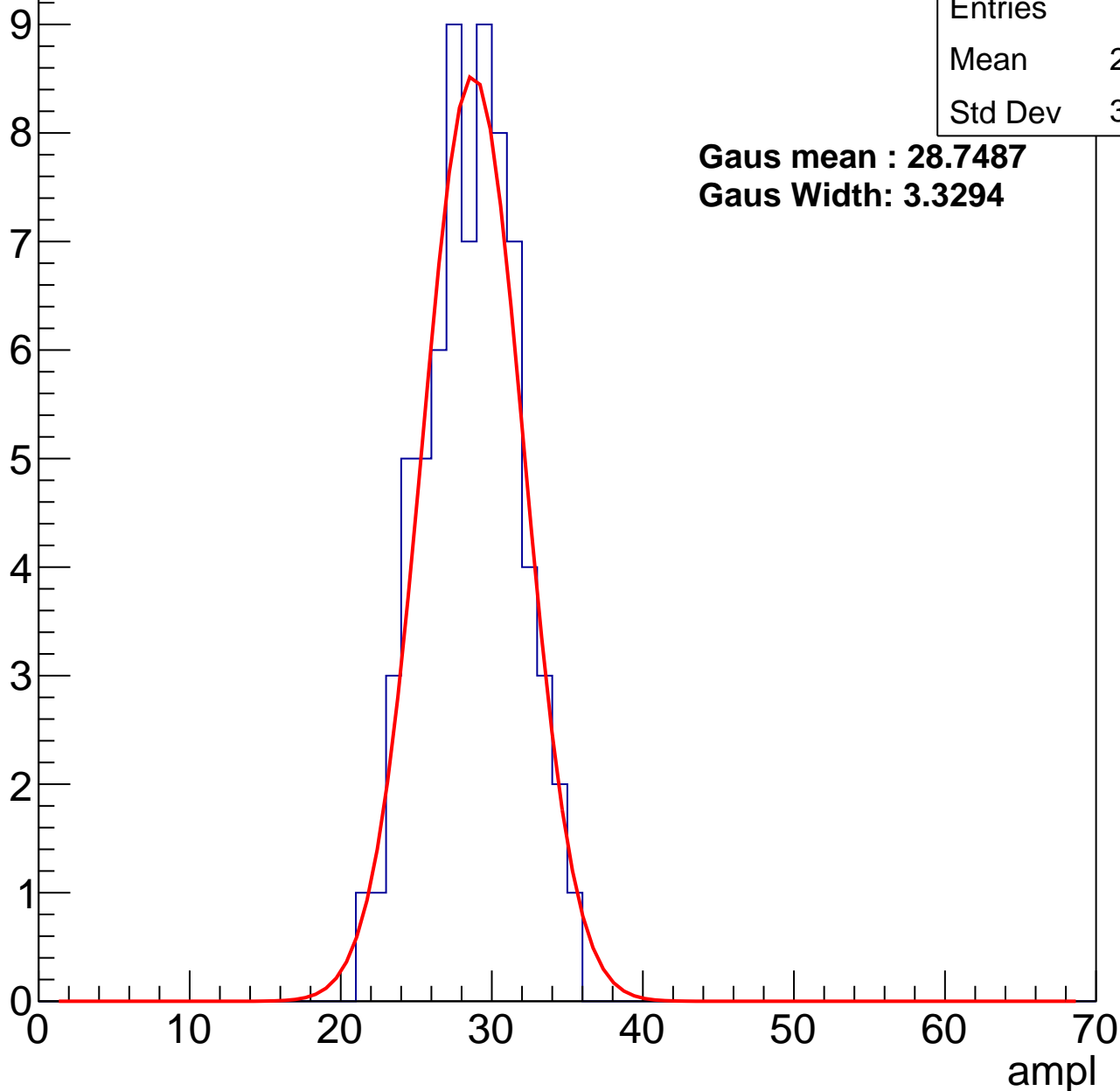
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.17
Std Dev	3.095

**Gaus mean : 28.7487**

**Gaus Width: 3.3294**



# B1L002S, U3-ch40, adc1

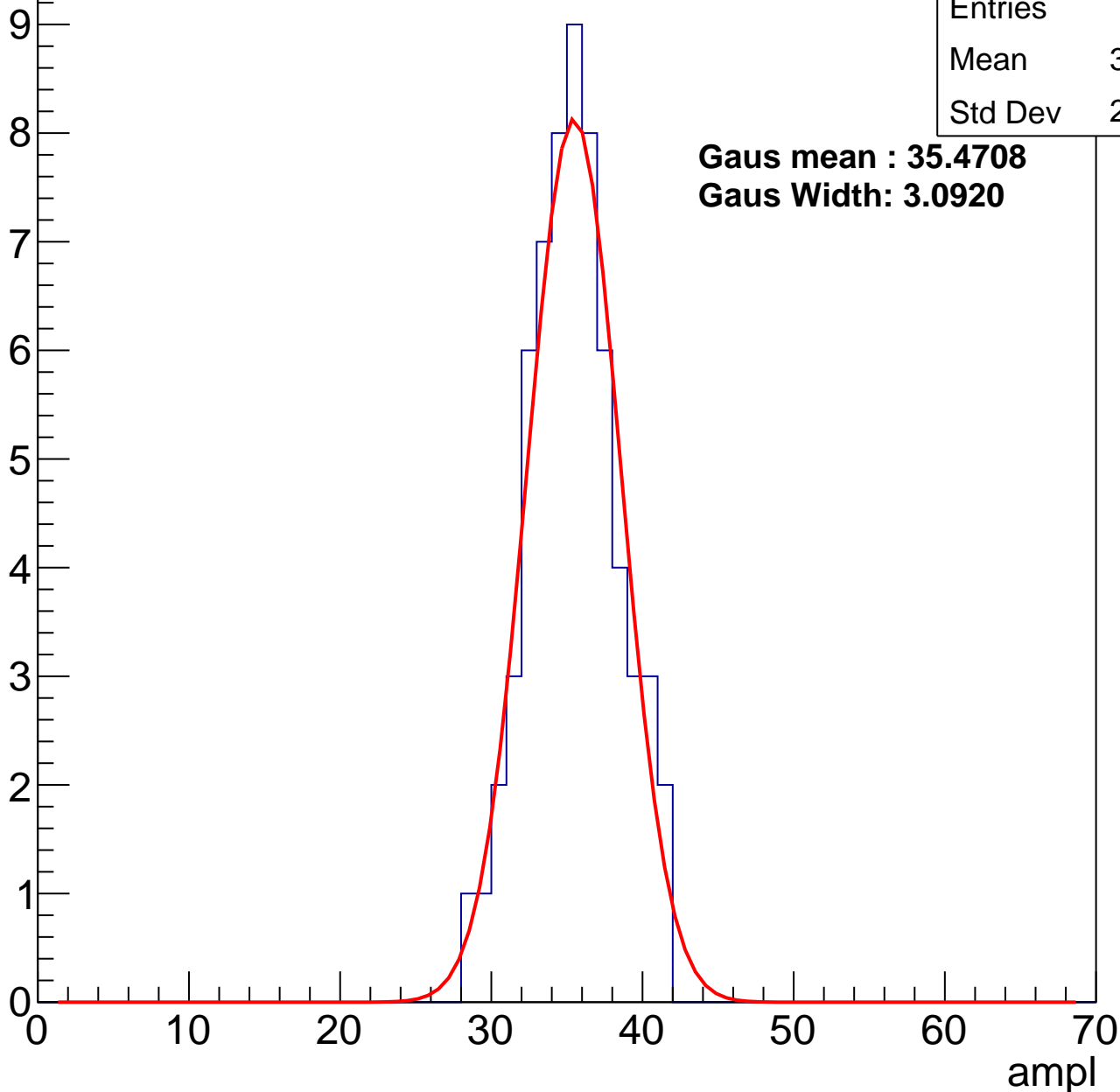
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.94
Std Dev	2.916

**Gaus mean : 35.4708**

**Gaus Width: 3.0920**



# B1L002S, U3-ch40, adc2

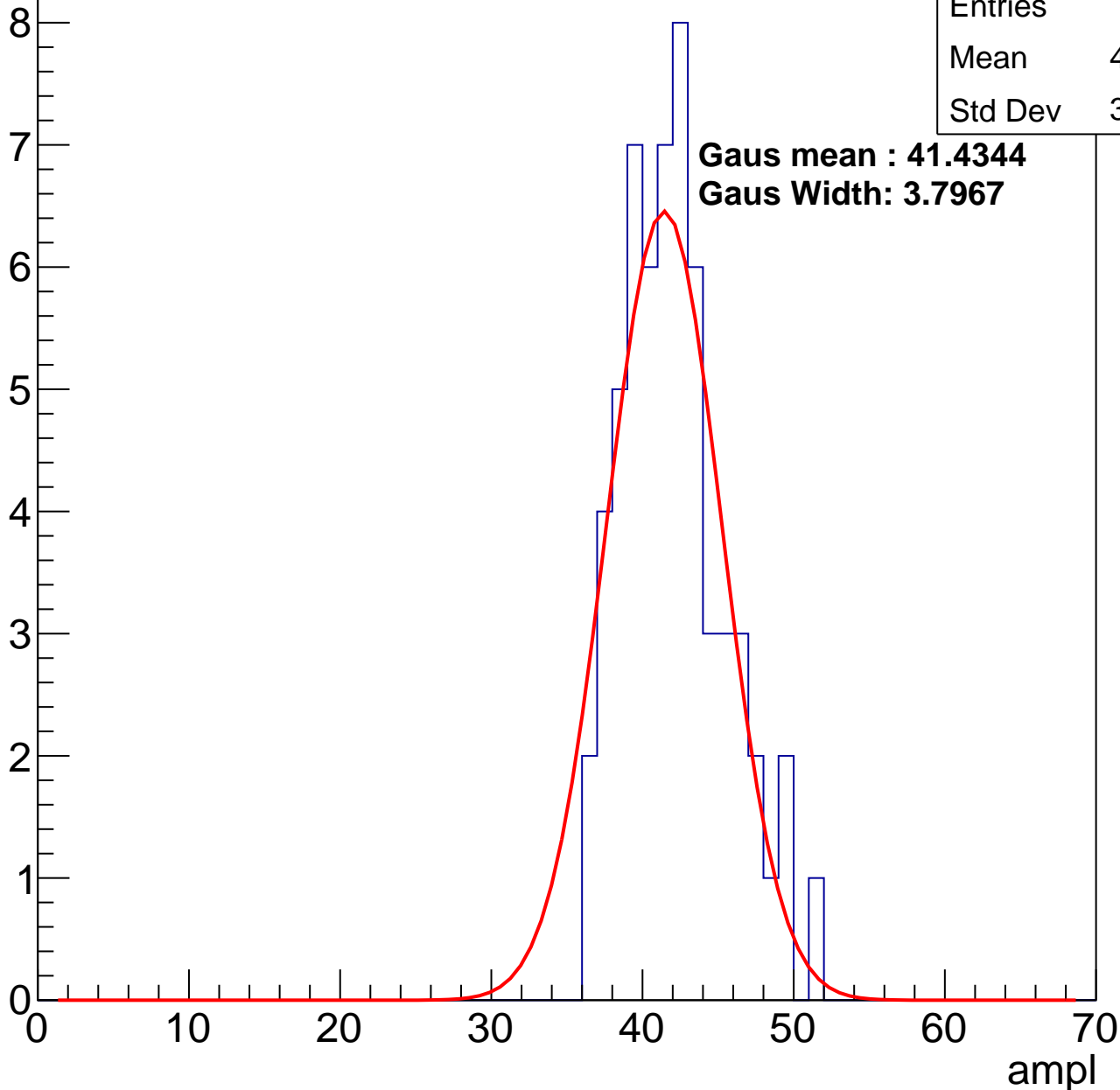
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.67
Std Dev	3.409

**Gaus mean : 41.4344**

**Gaus Width: 3.7967**

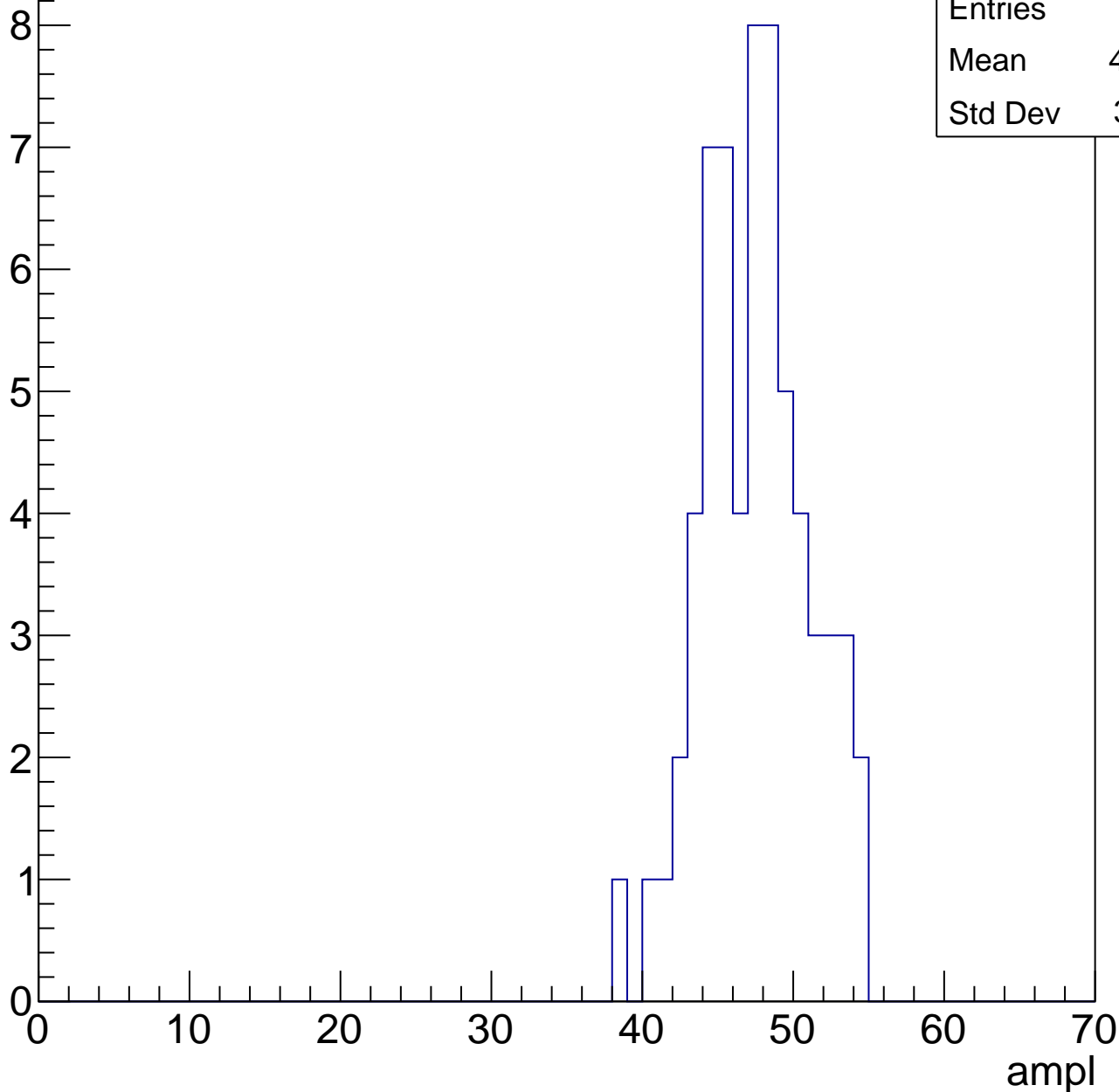


# B1L002S, U3-ch40, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	47.03
Std Dev	3.491

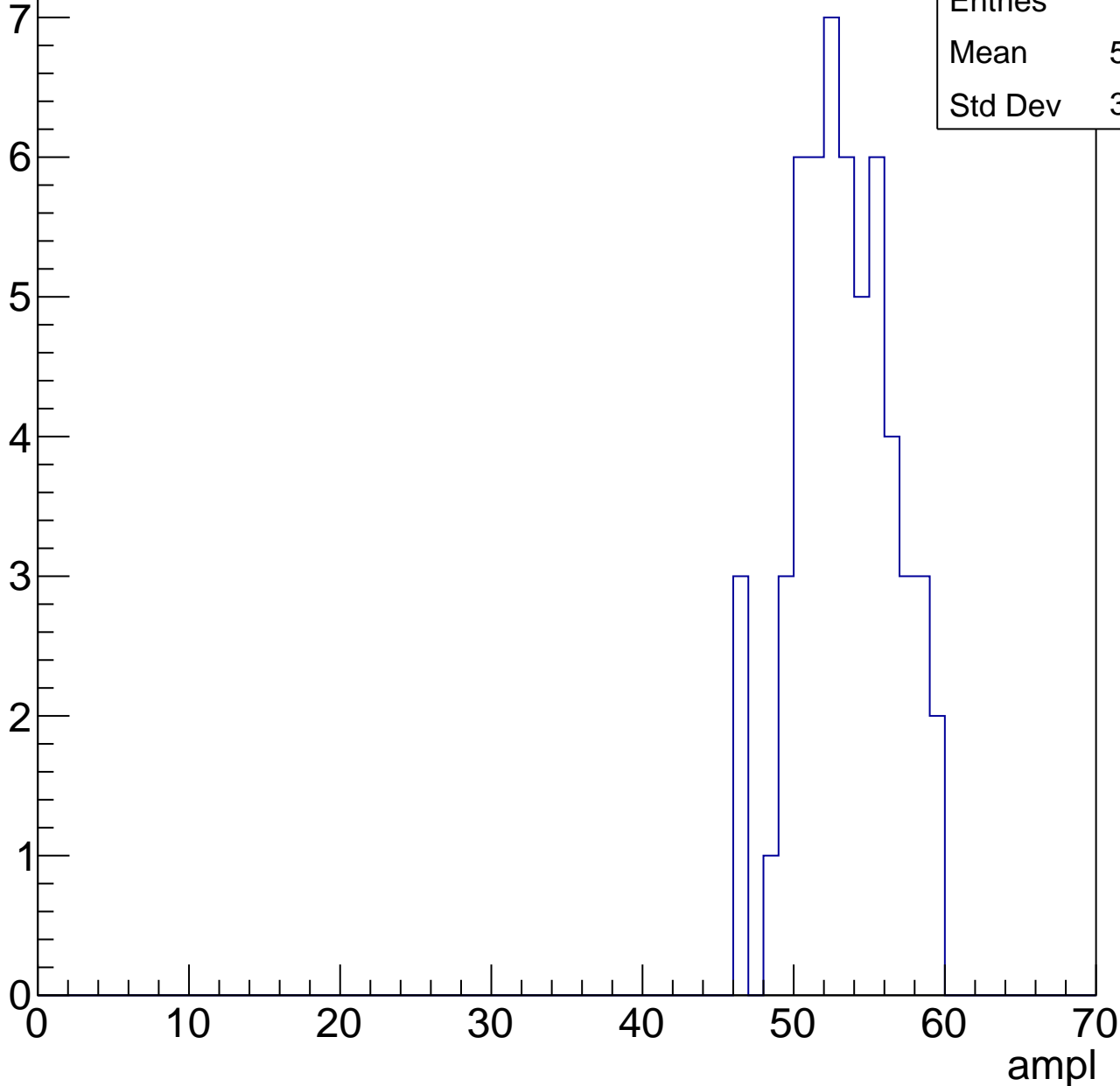


# B1L002S, U3-ch40, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

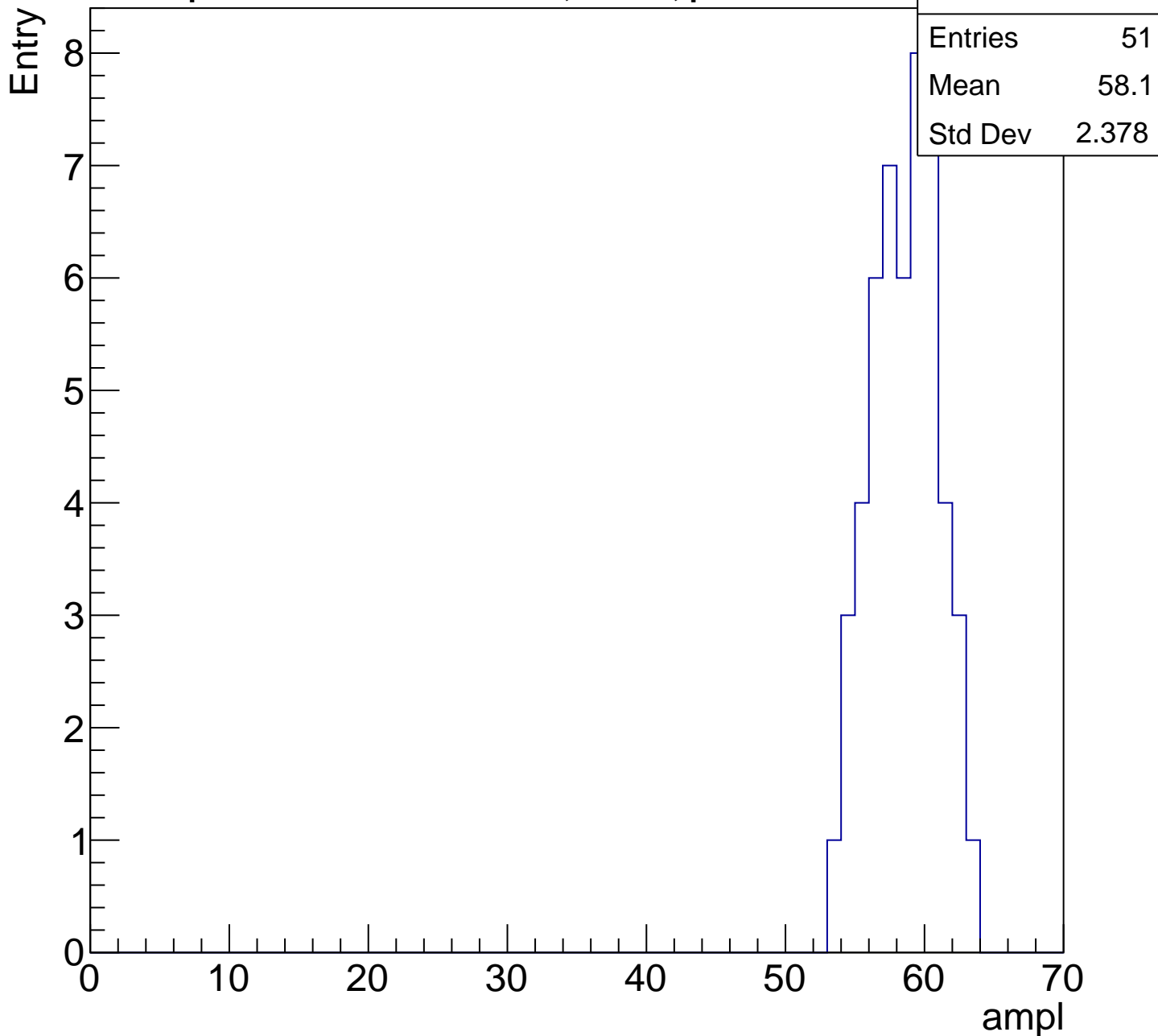
Entry

Entries	55
Mean	52.87
Std Dev	3.203



# B1L002S, U3-ch40, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

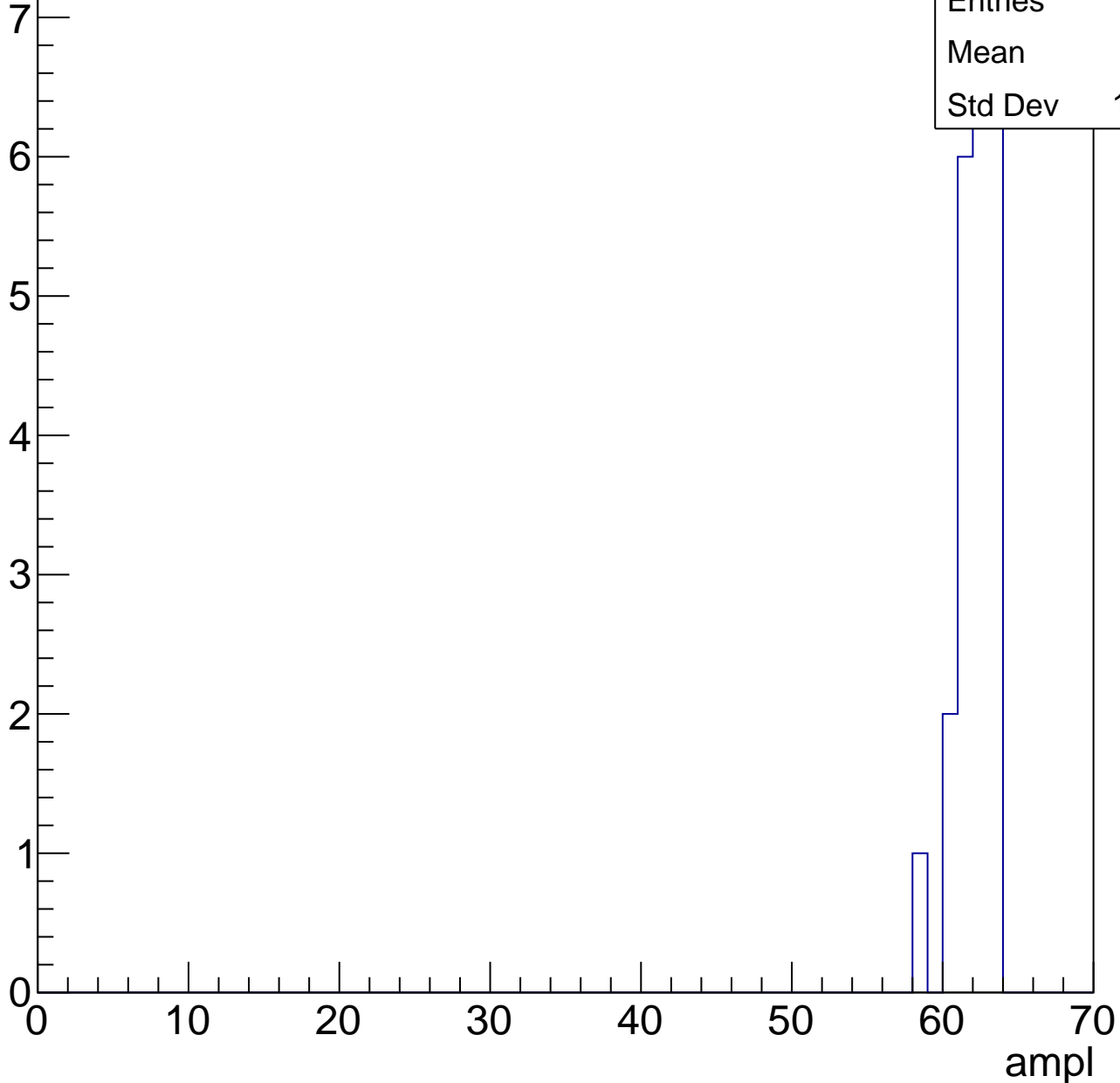


# B1L002S, U3-ch40, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.7
Std Dev	1.231

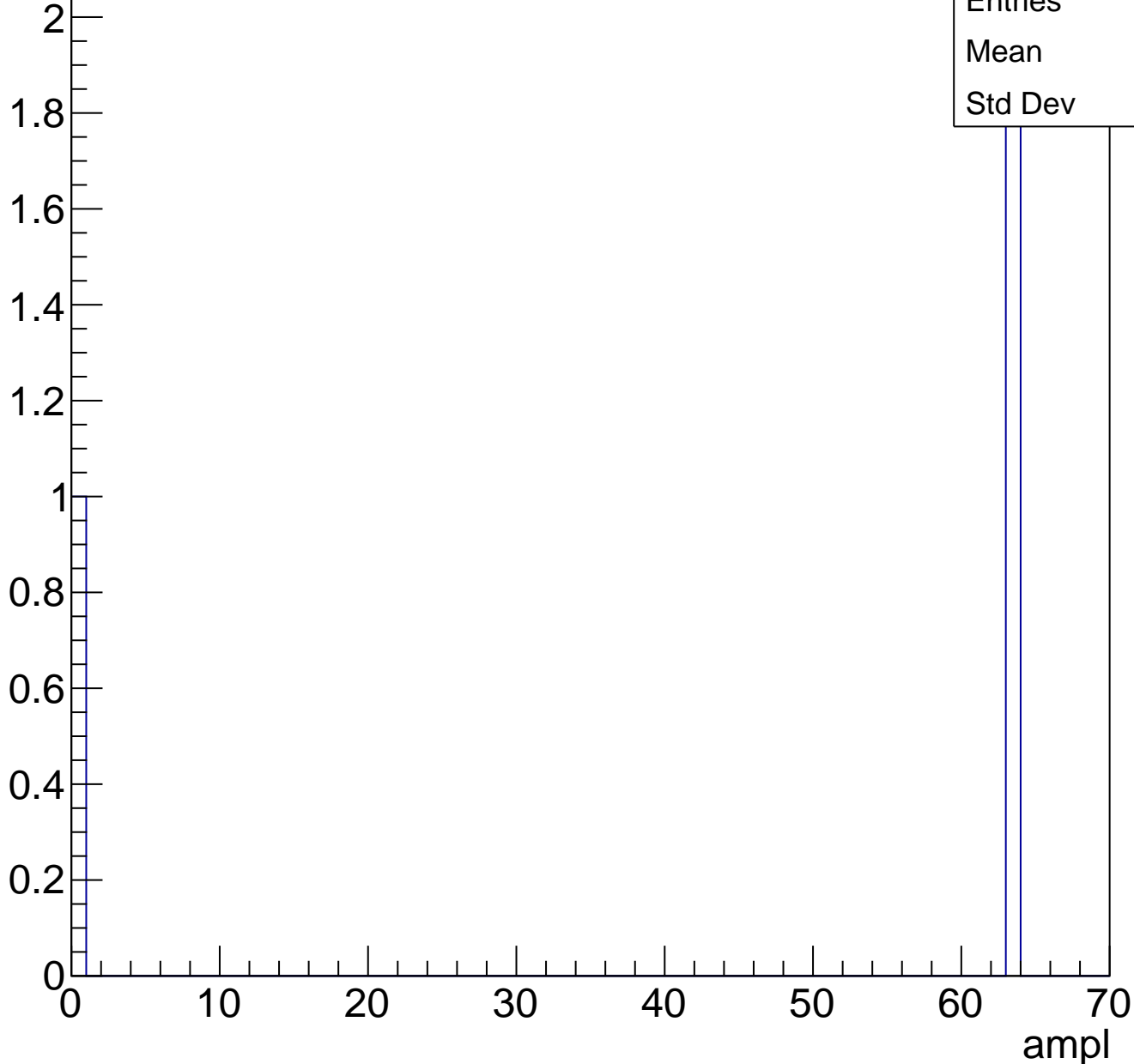




# B1L002S, U3-ch40, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch41, adc0

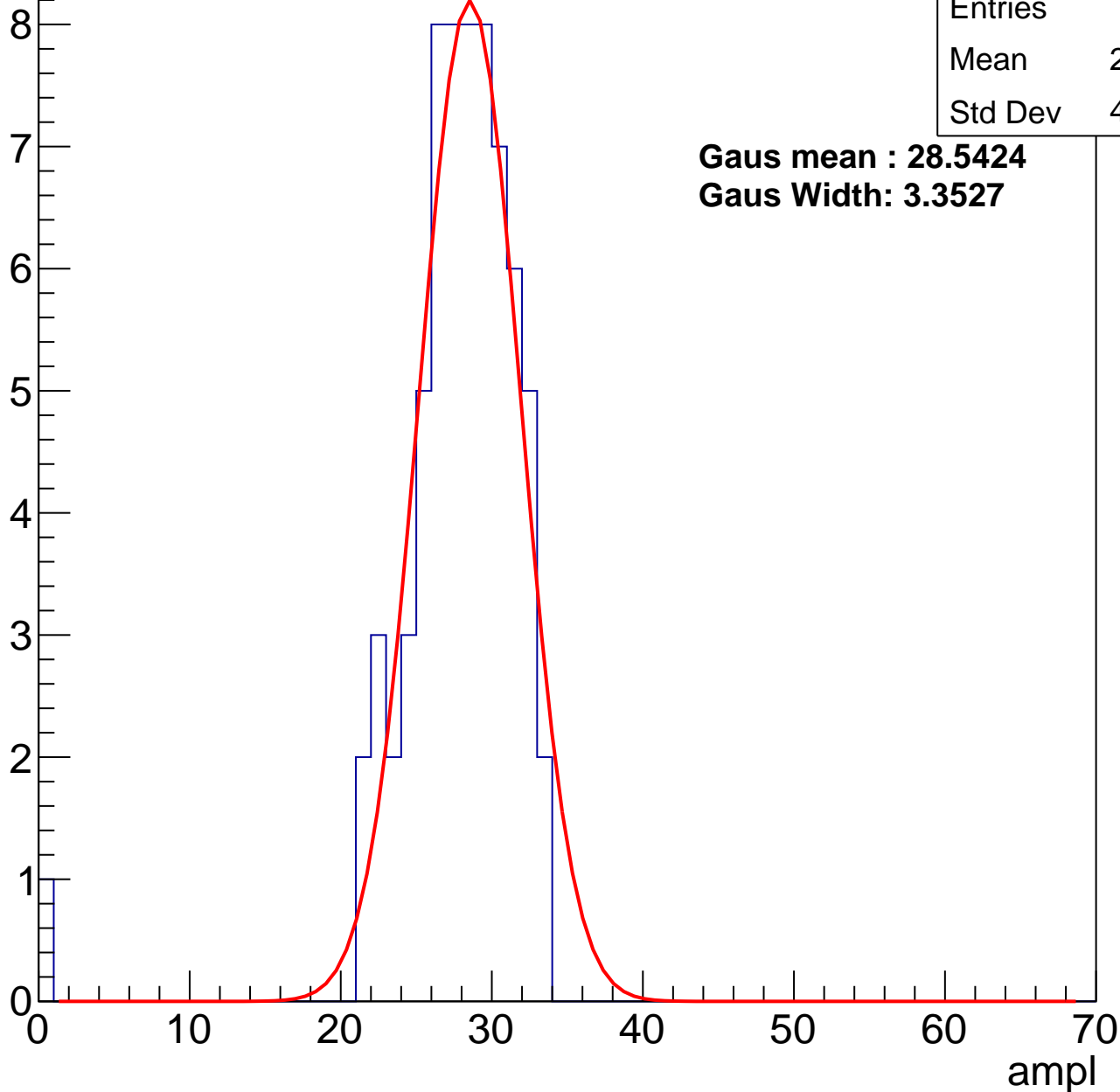
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.25
Std Dev	4.463

**Gaus mean : 28.5424**

**Gaus Width: 3.3527**



# B1L002S, U3-ch41, adc1

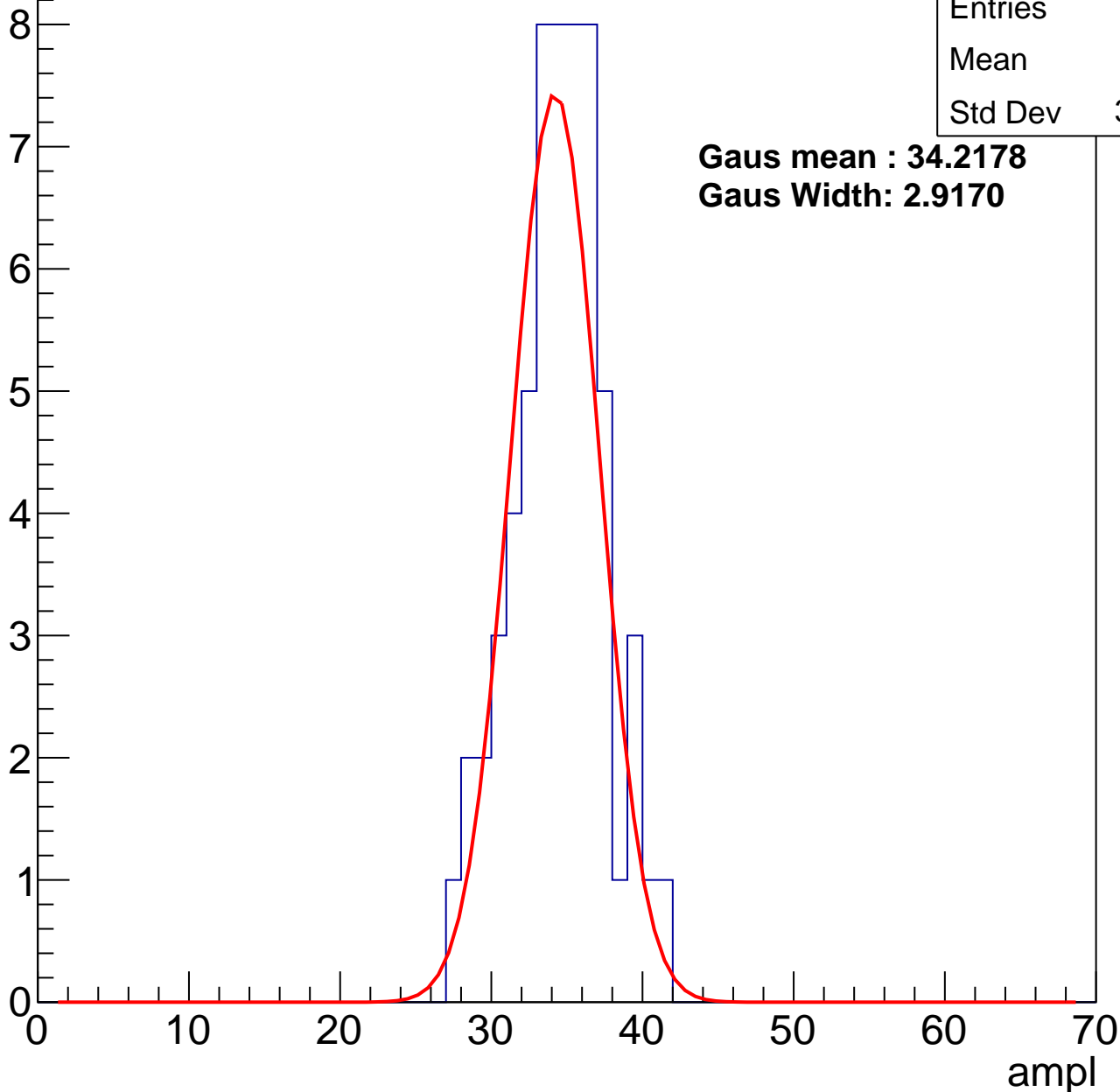
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	34
Std Dev	3.011

**Gaus mean : 34.2178**

**Gaus Width: 2.9170**



# B1L002S, U3-ch41, adc2

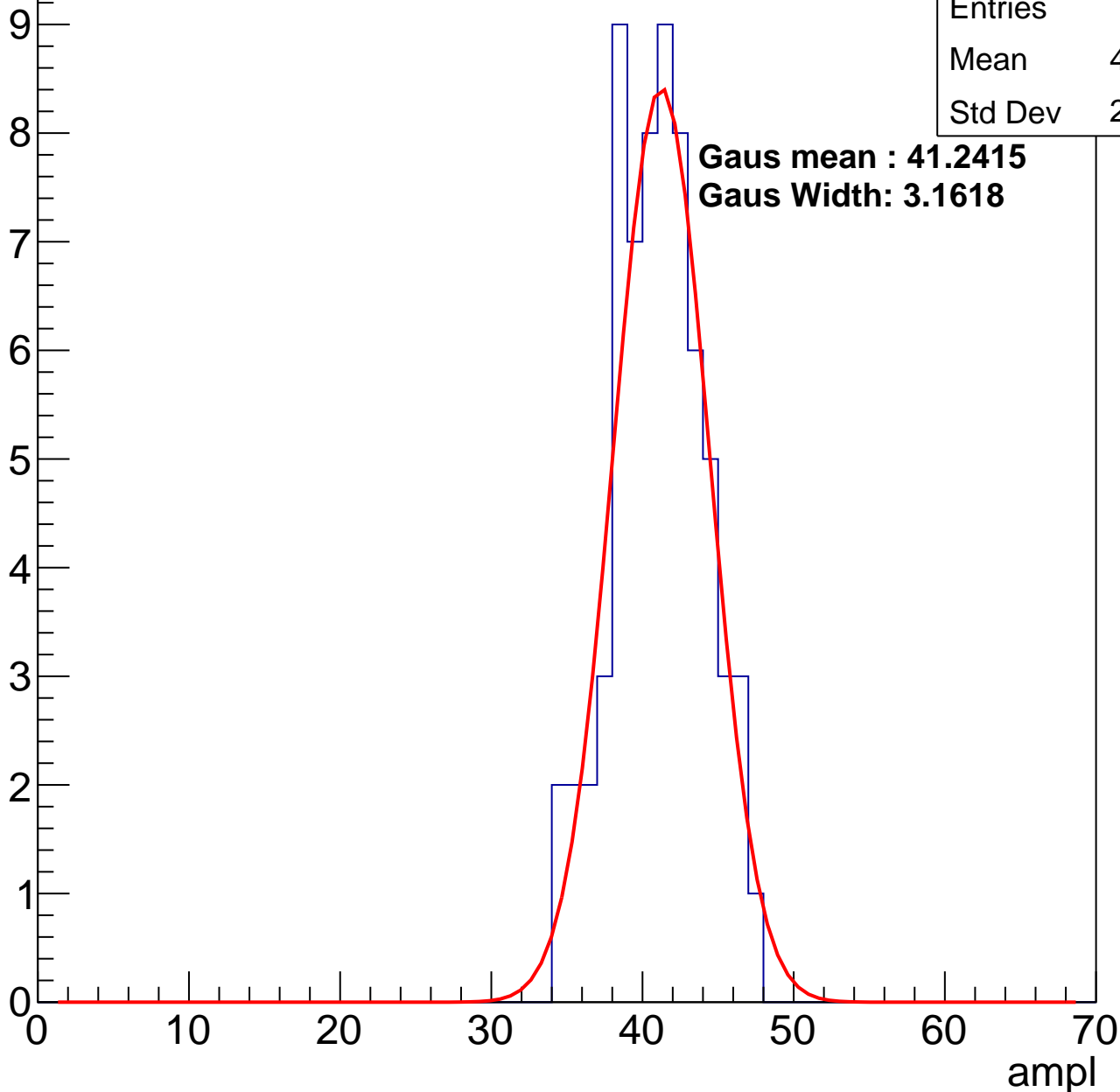
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.57
Std Dev	2.992

**Gaus mean : 41.2415**

**Gaus Width: 3.1618**



# B1L002S, U3-ch41, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

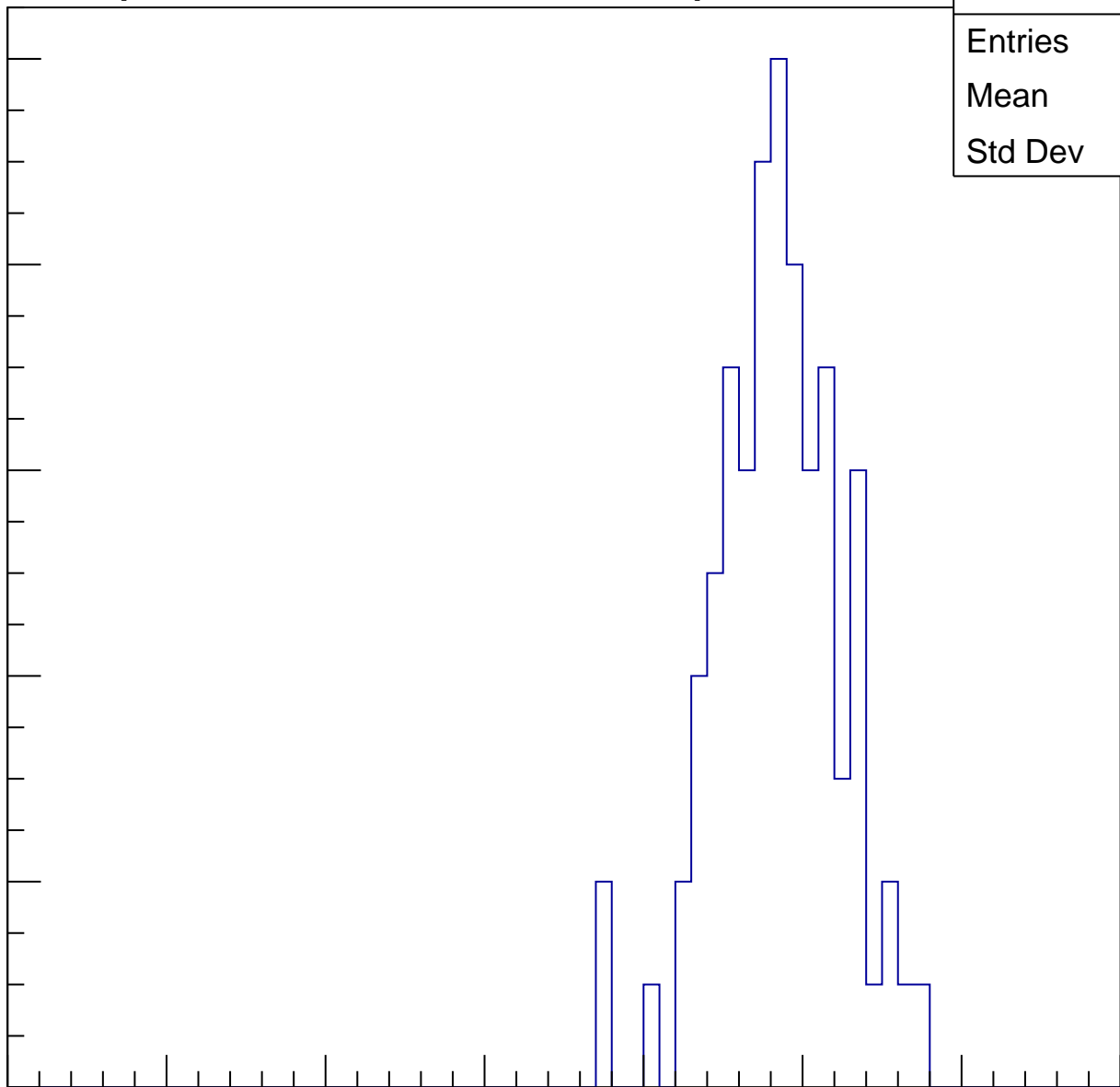
Entries	81
Mean	47.95
Std Dev	3.89

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

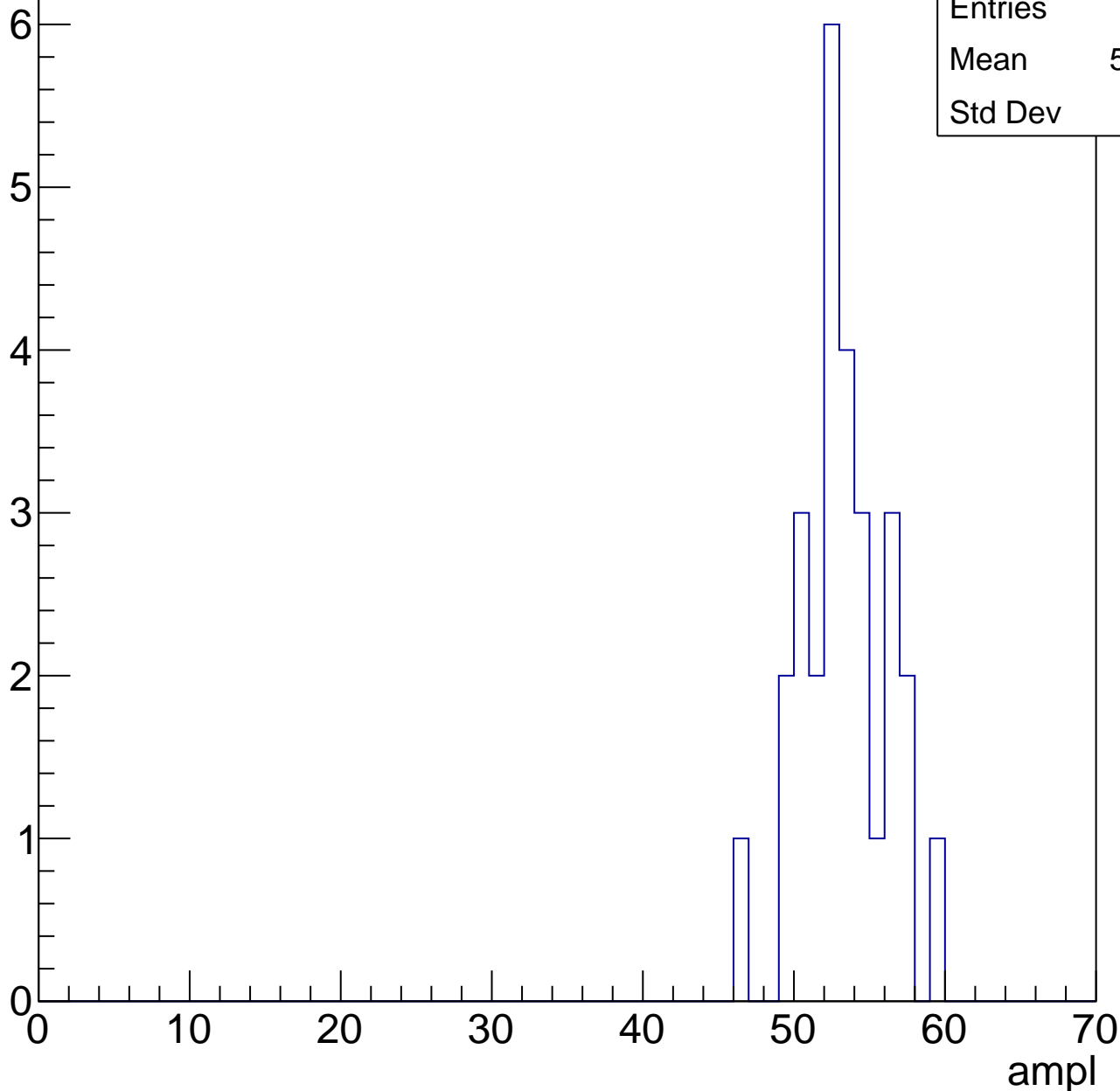


# B1L002S, U3-ch41, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	52.79
Std Dev	2.82



# B1L002S, U3-ch41, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	56.52
Std Dev	7.827

Entry

10

8

6

4

2

0

0

10

20

30

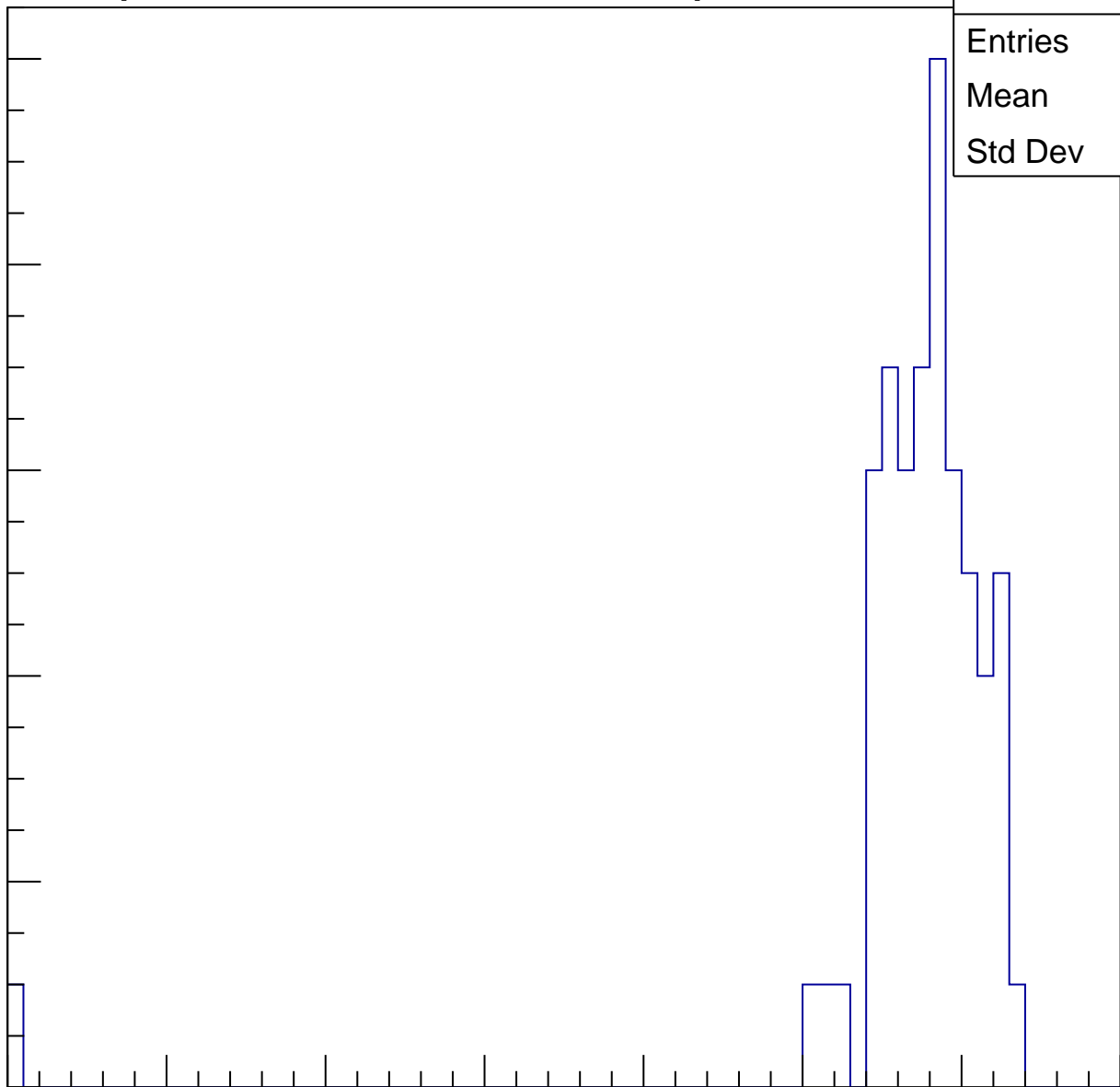
40

50

60

70

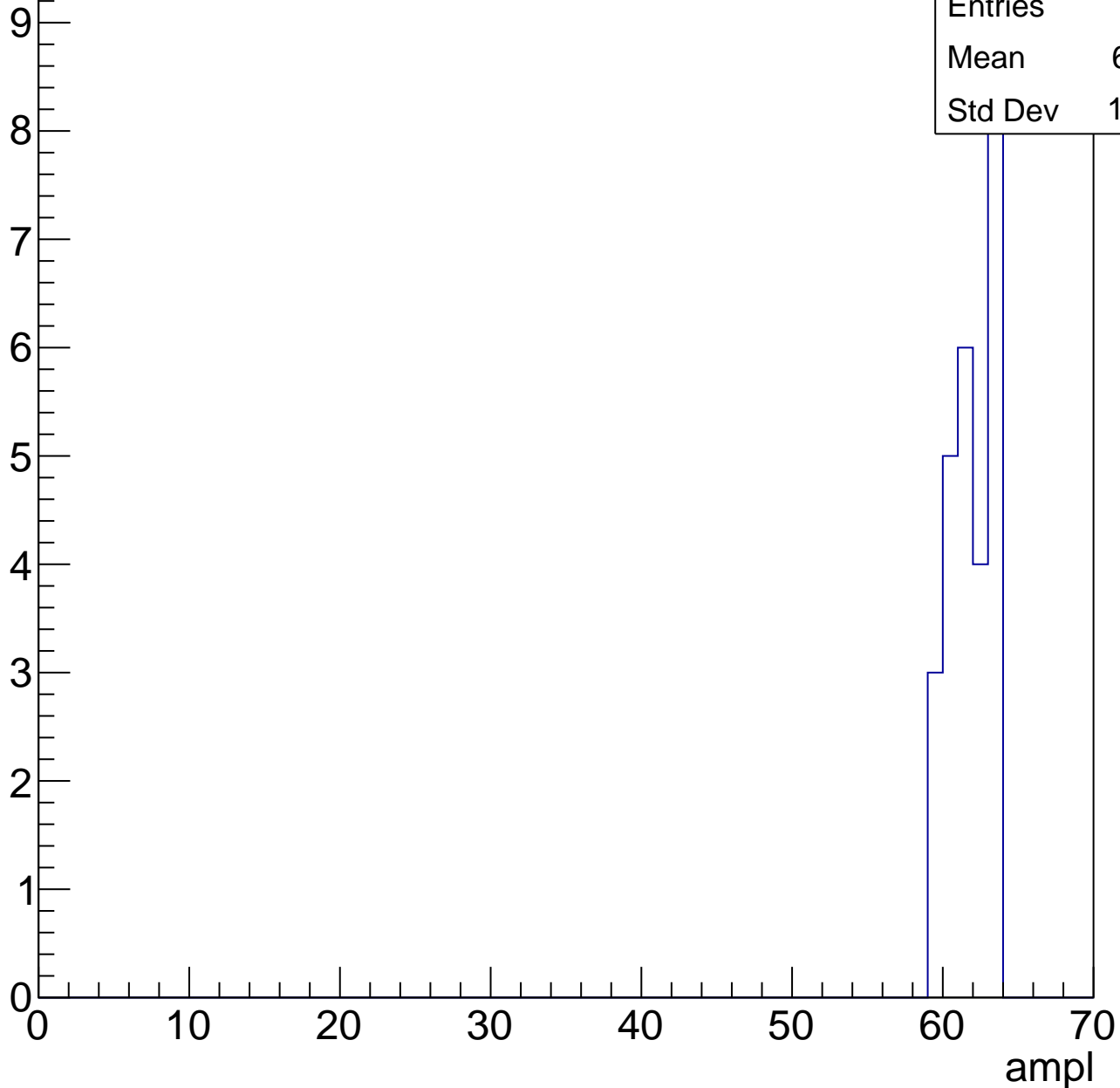
ampl



# B1L002S, U3-ch41, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch41, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch42, adc0

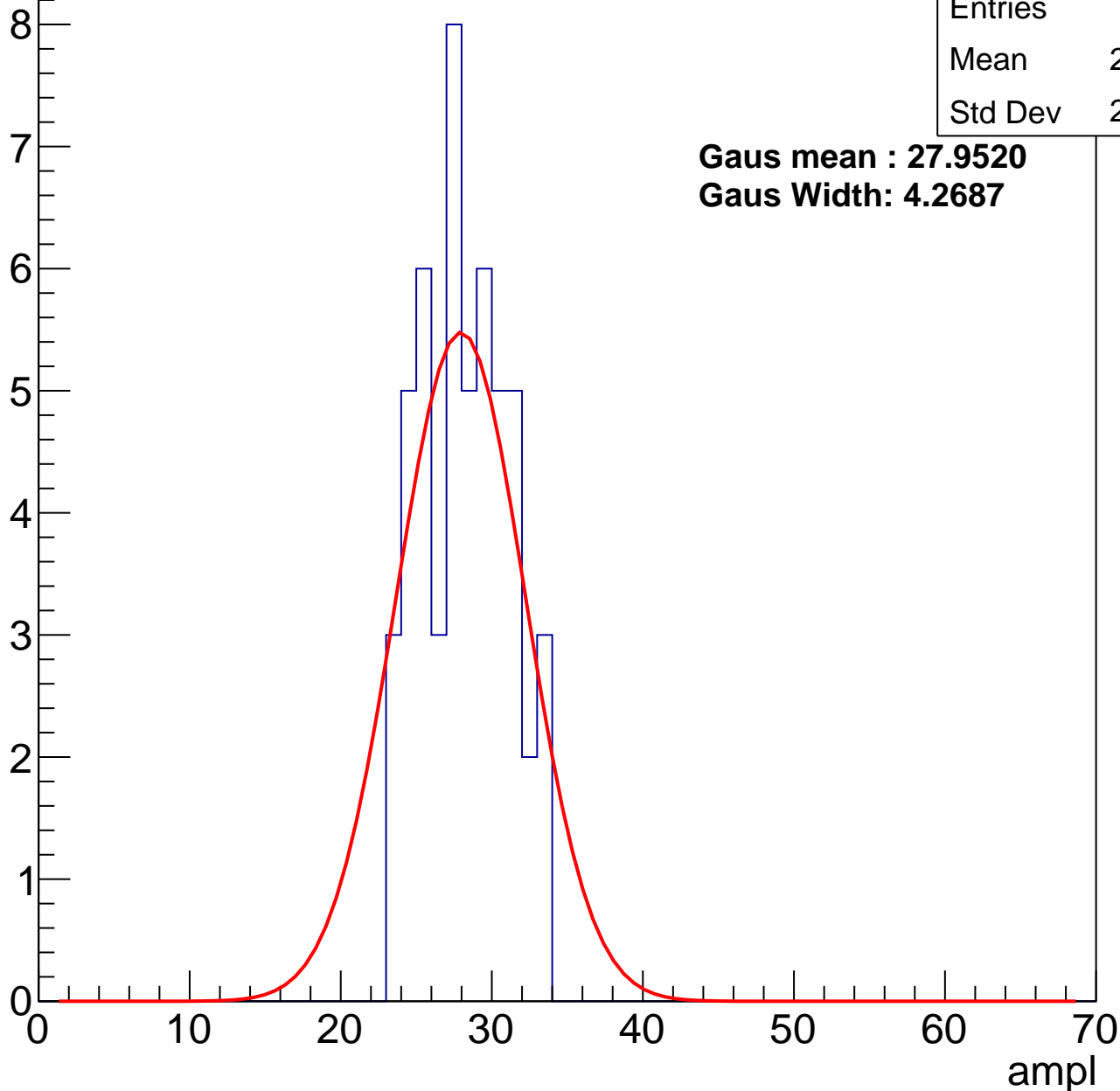
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	27.75
Std Dev	2.813

**Gaus mean : 27.9520**

**Gaus Width: 4.2687**



# B1L002S, U3-ch42, adc1

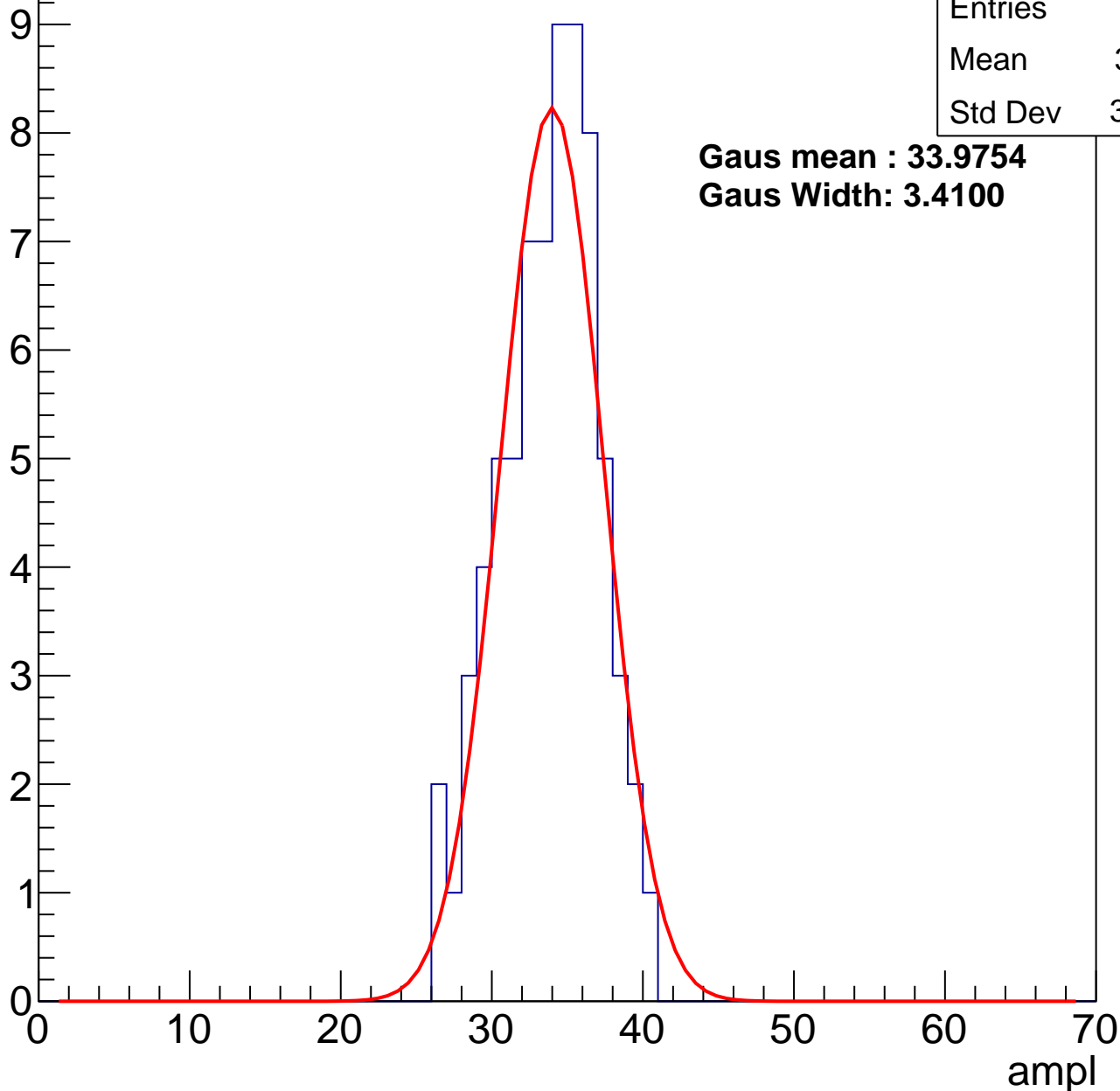
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	33.31
Std Dev	3.209

**Gaus mean : 33.9754**

**Gaus Width: 3.4100**



# B1L002S, U3-ch42, adc2

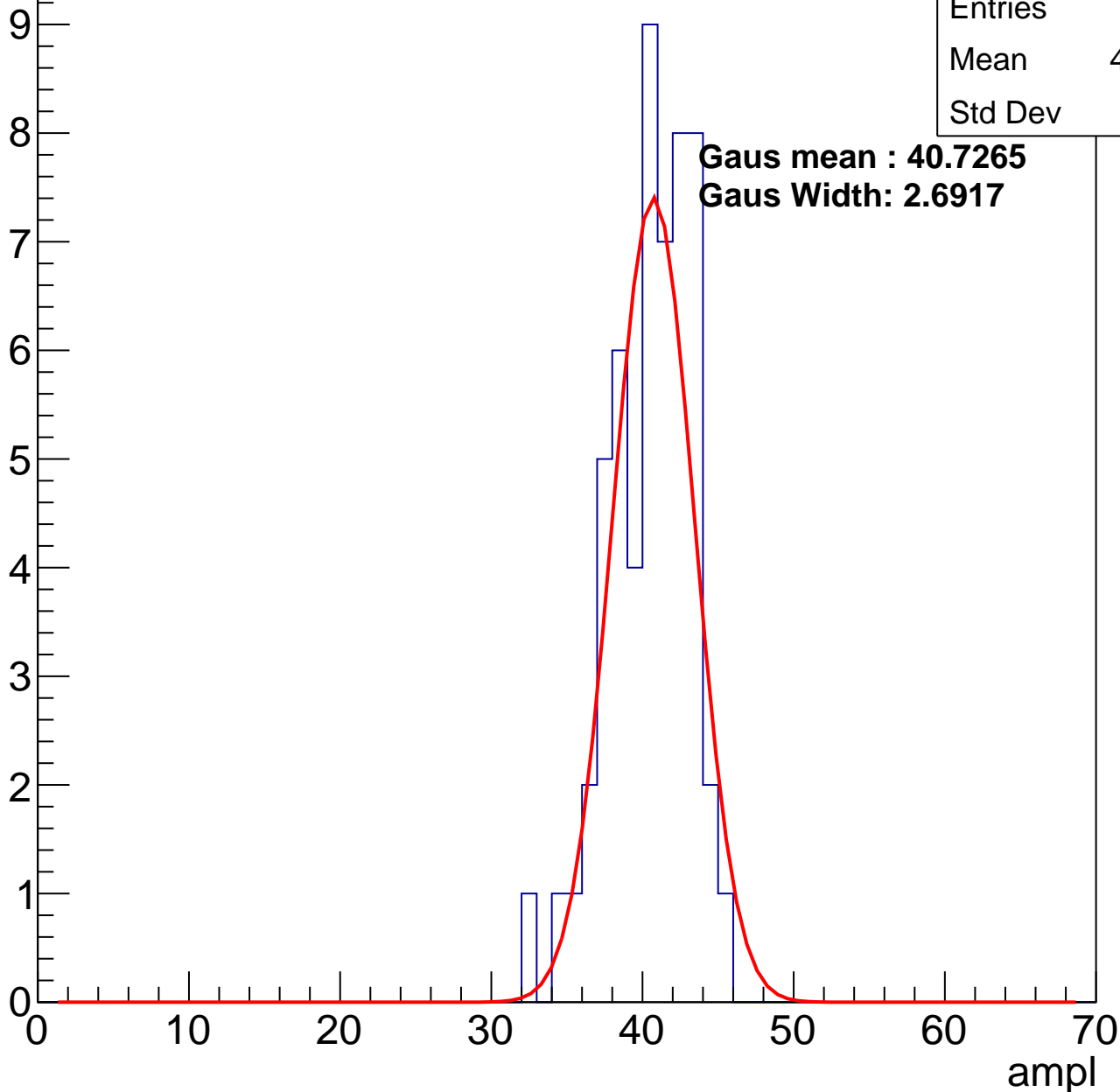
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	40.04
Std Dev	2.69

**Gaus mean : 40.7265**

**Gaus Width: 2.6917**

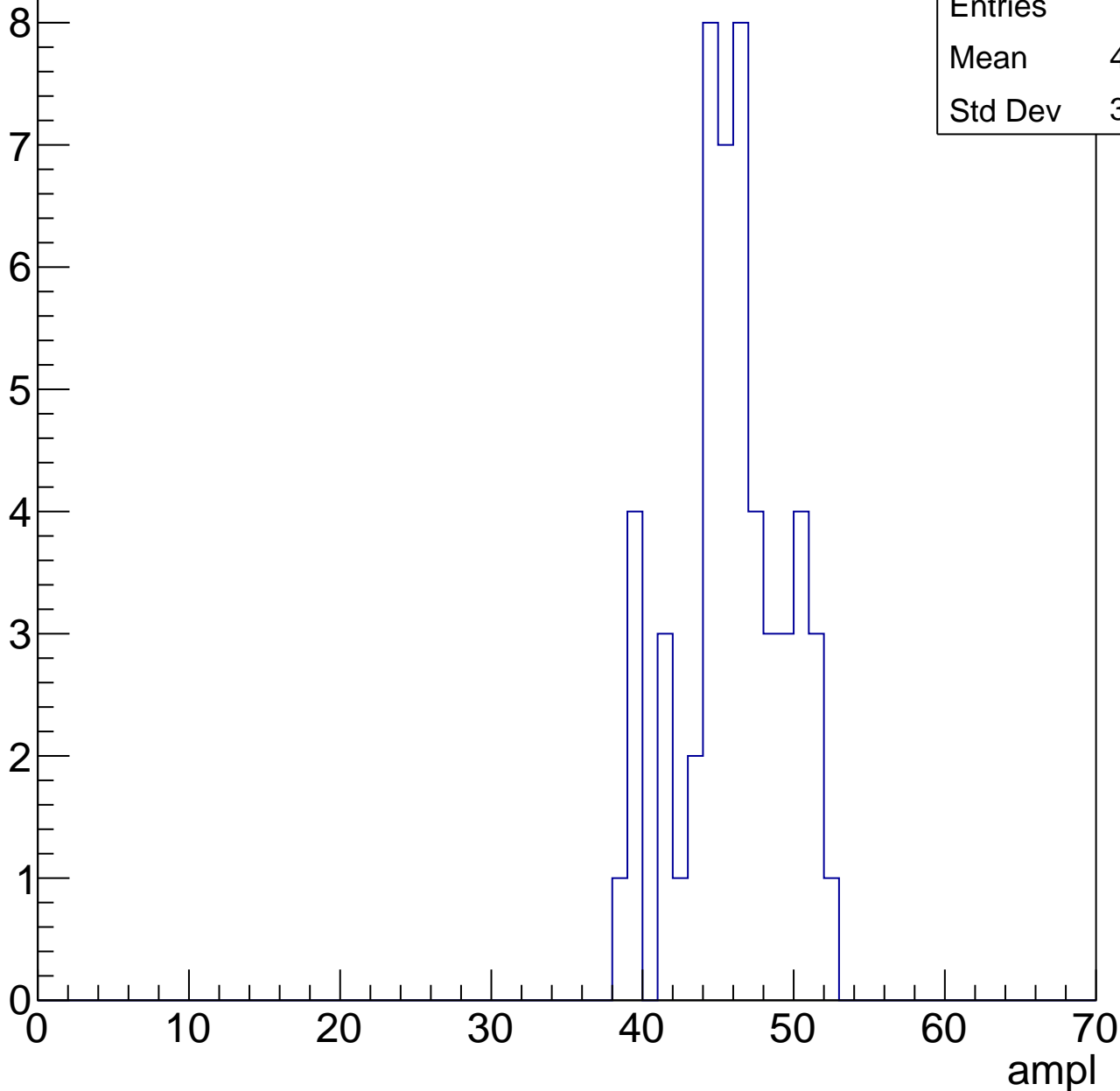


# B1L002S, U3-ch42, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

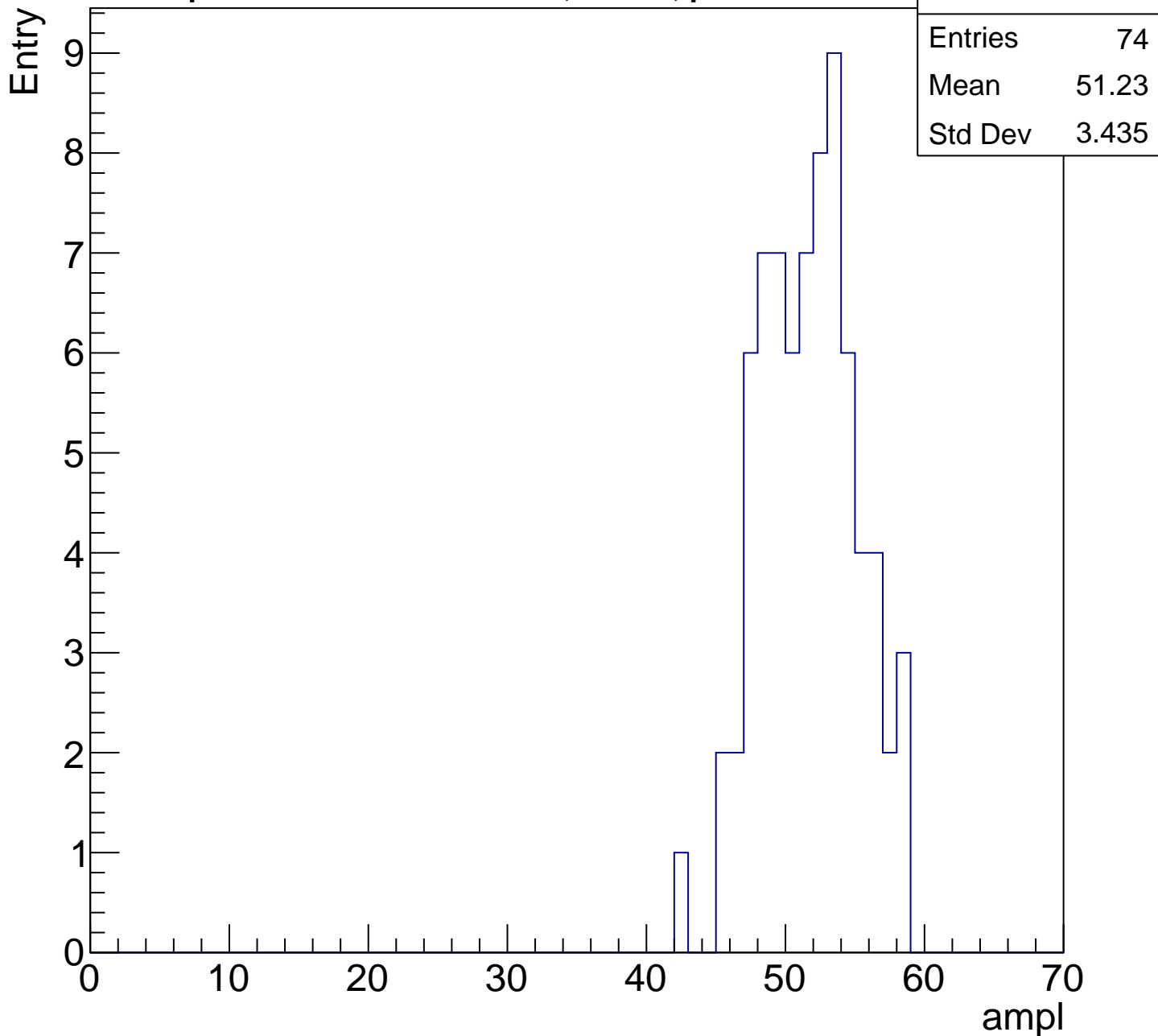
Entry

Entries	52
Mean	45.46
Std Dev	3.428



# B1L002S, U3-ch42, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

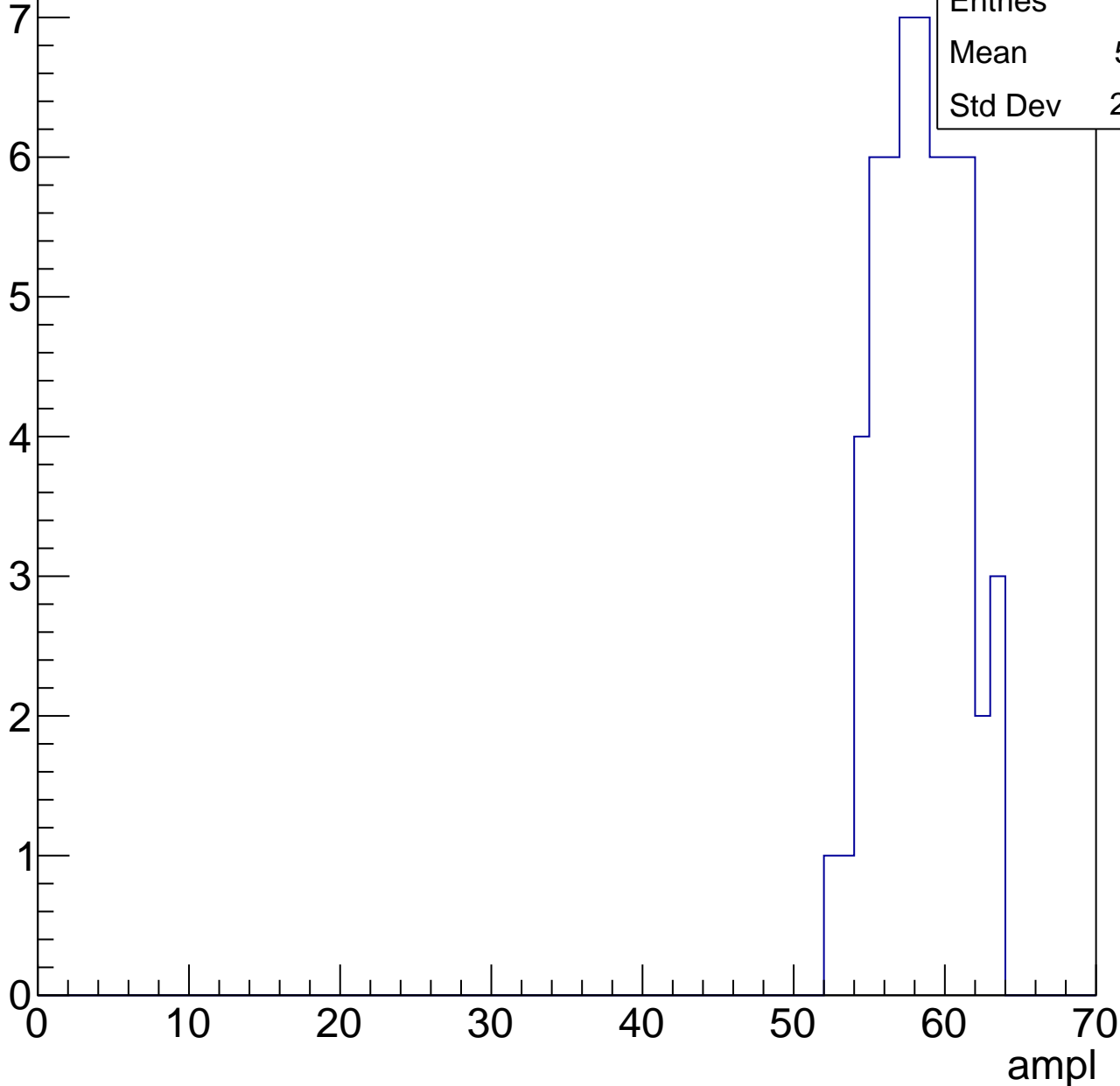


# B1L002S, U3-ch42, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	57.91
Std Dev	2.699

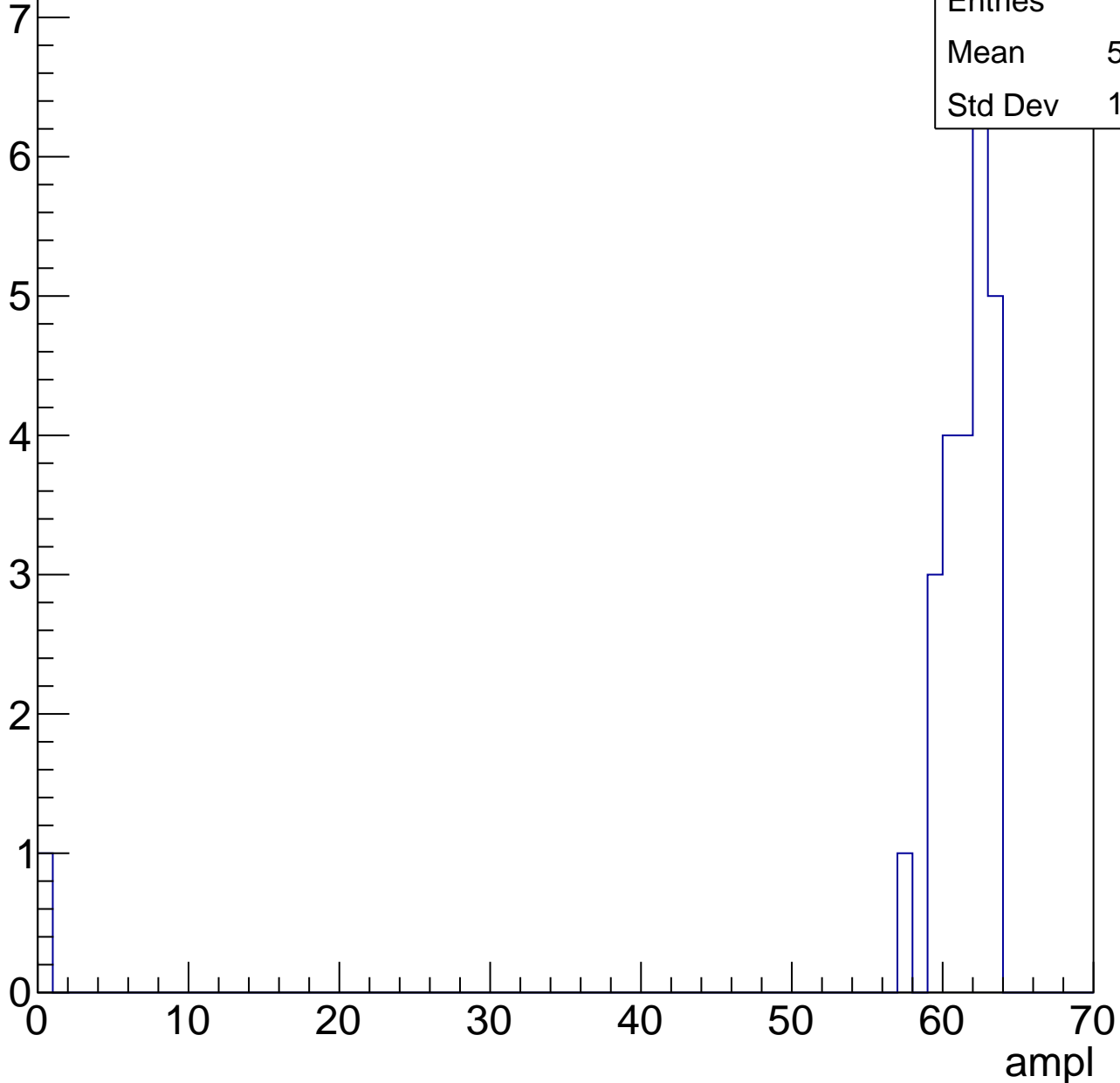


# B1L002S, U3-ch42, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.68
Std Dev	12.08





# B1L002S, U3-ch42, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

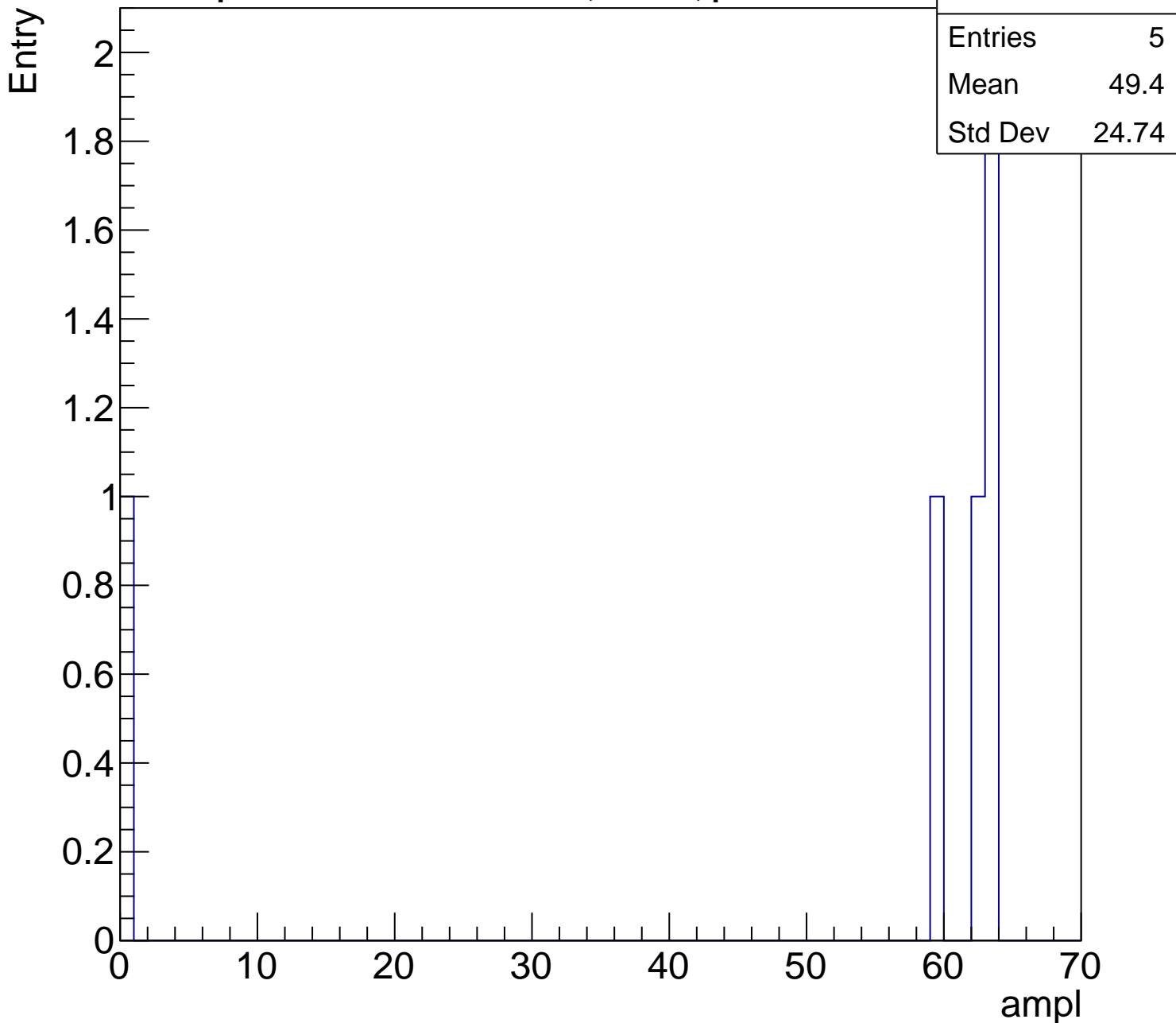
Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	5
Mean	49.4
Std Dev	24.74

0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch43, adc0

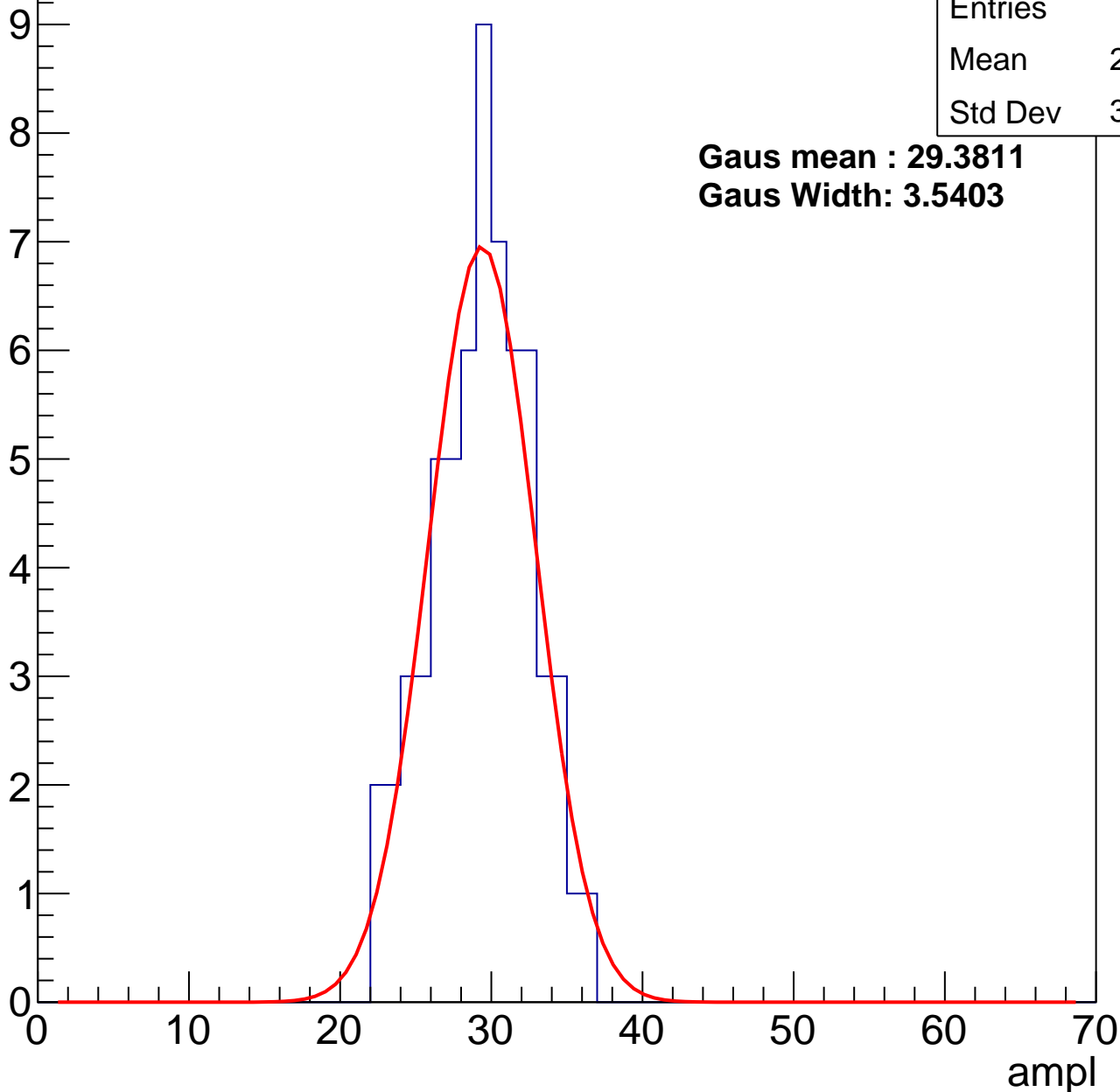
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.89
Std Dev	3.253

**Gaus mean : 29.3811**

**Gaus Width: 3.5403**



# B1L002S, U3-ch43, adc1

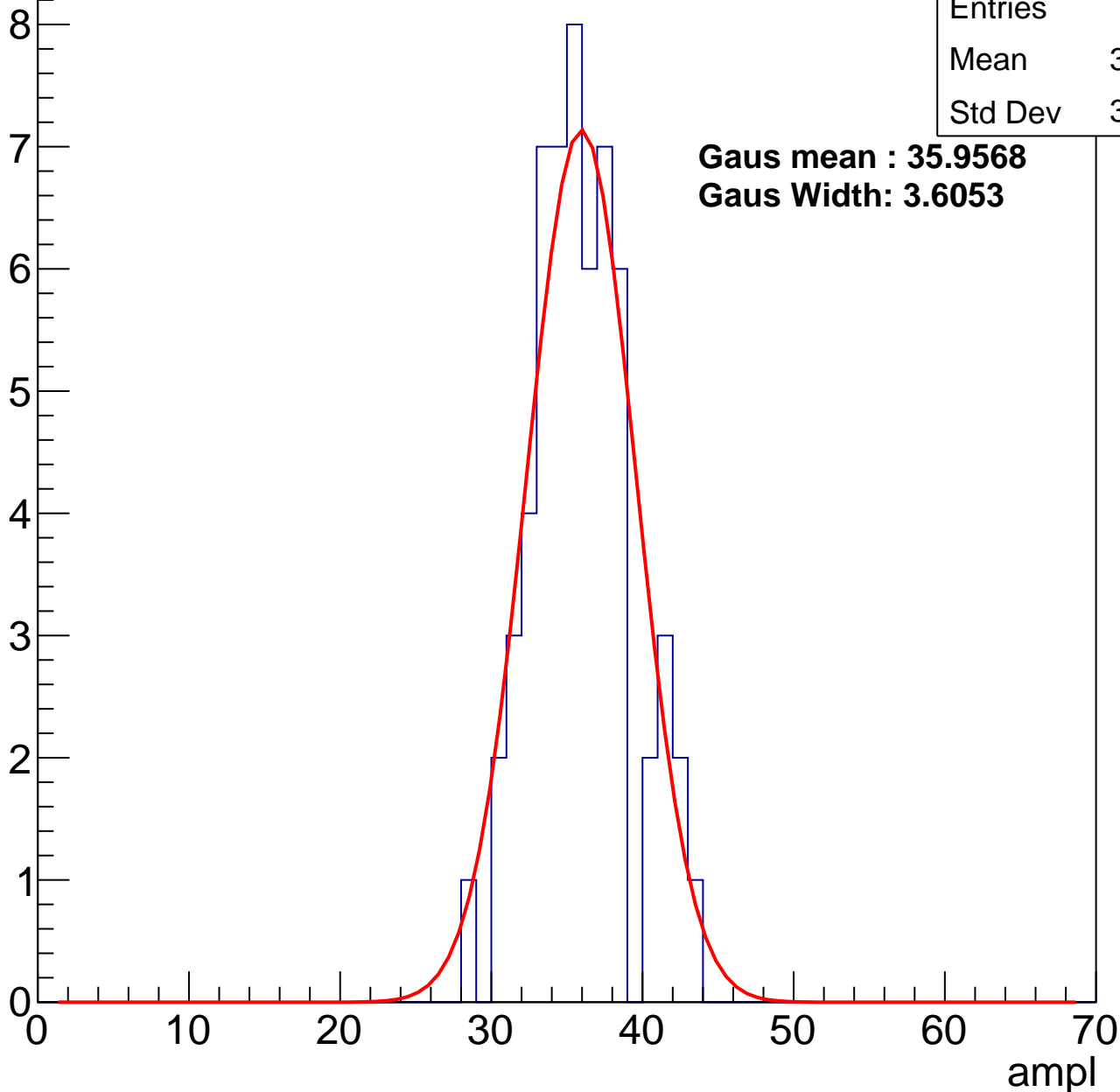
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	35.44
Std Dev	3.227

**Gaus mean : 35.9568**

**Gaus Width: 3.6053**



# B1L002S, U3-ch43, adc2

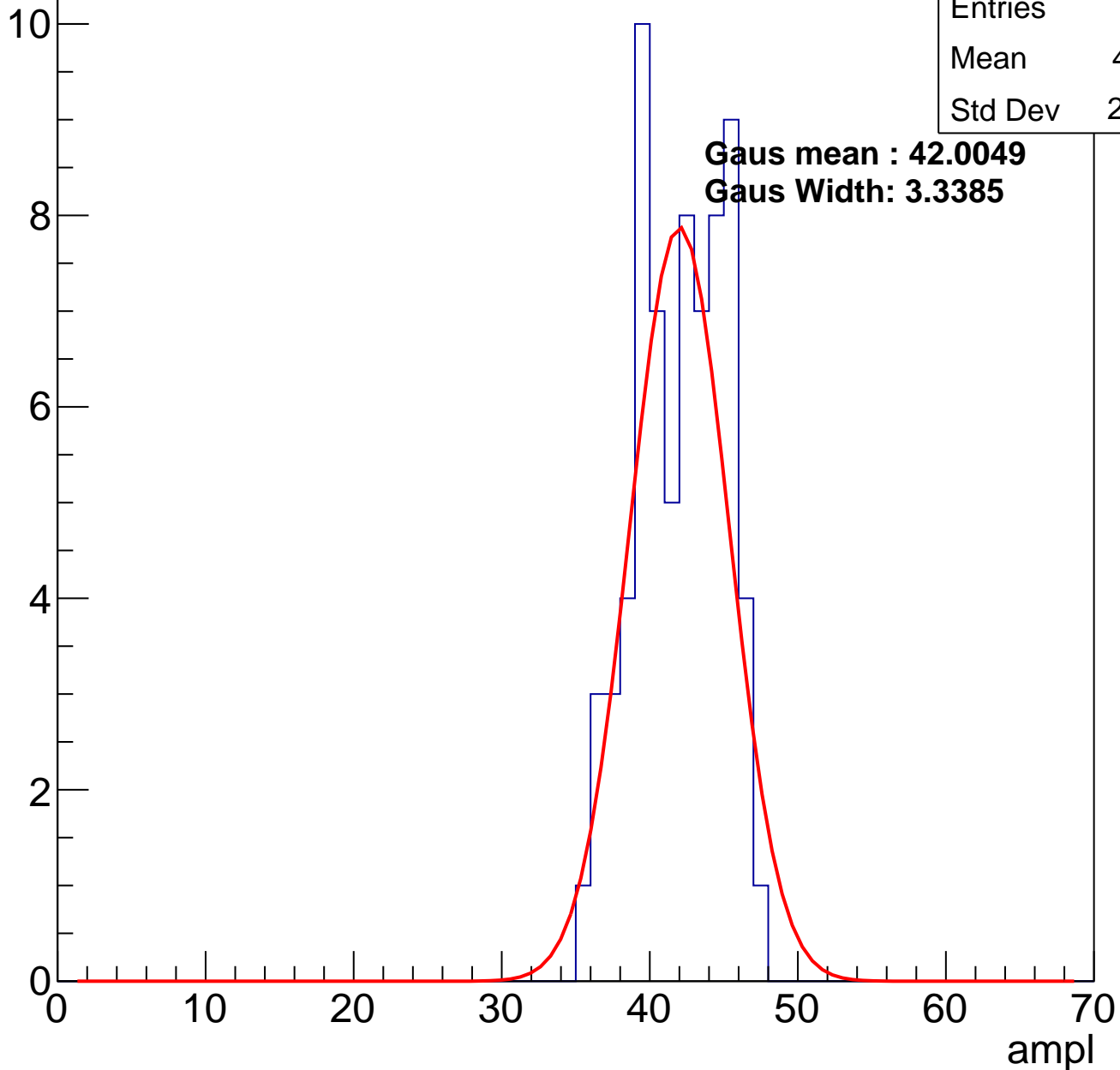
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	41.51
Std Dev	2.956

**Gaus mean : 42.0049**

**Gaus Width: 3.3385**

Entry

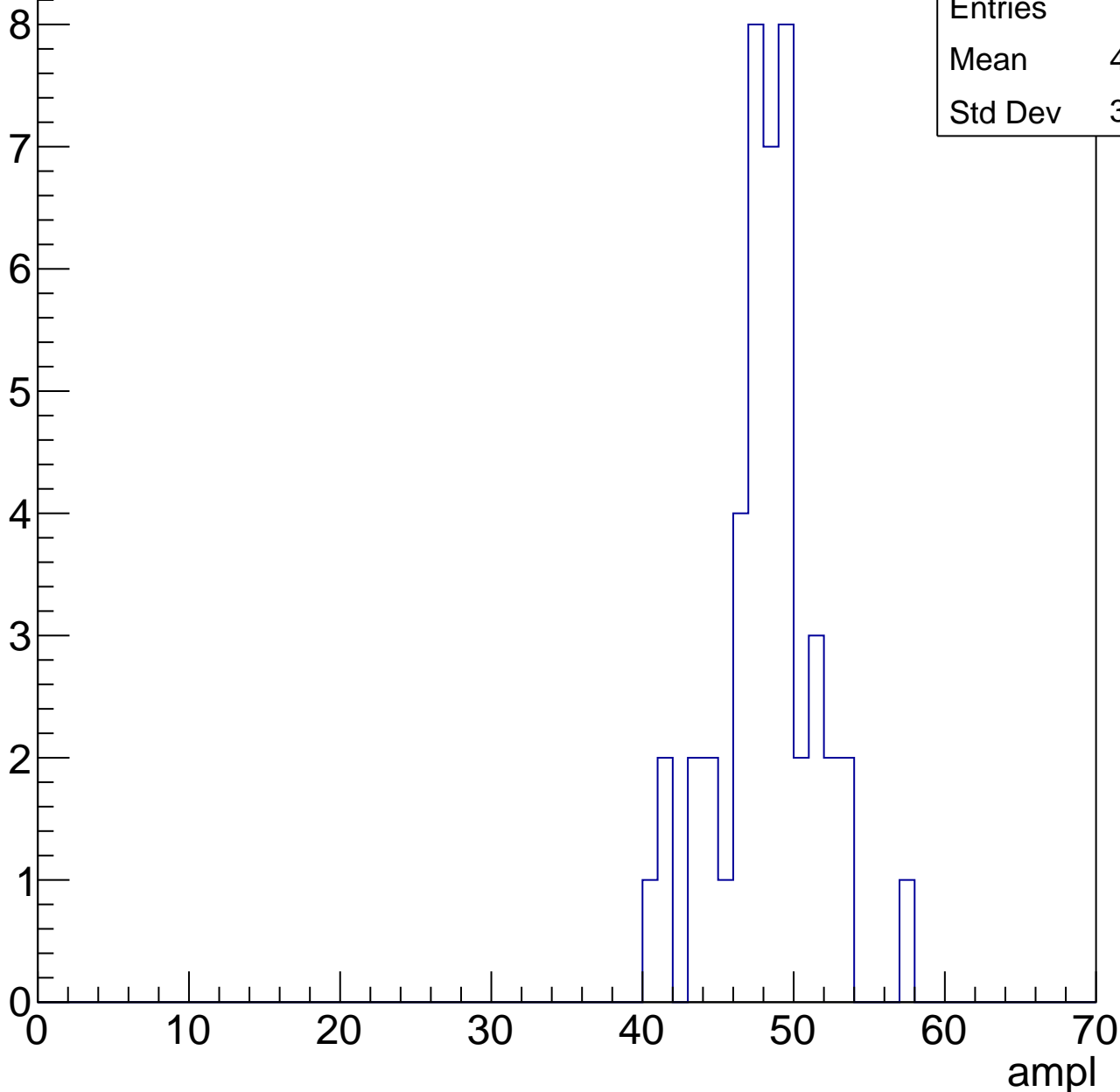


# B1L002S, U3-ch43, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	47.76
Std Dev	3.267

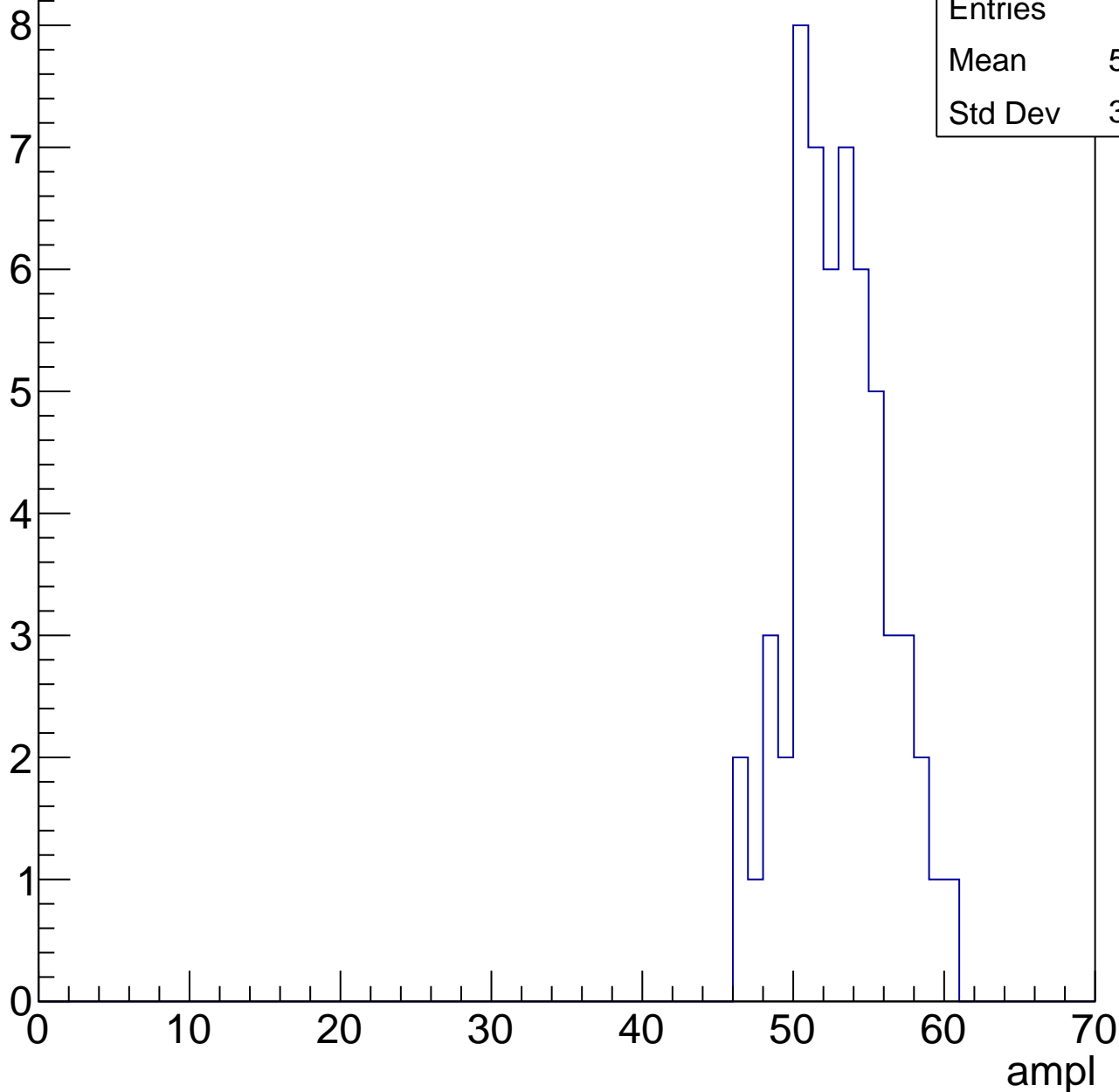


# B1L002S, U3-ch43, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

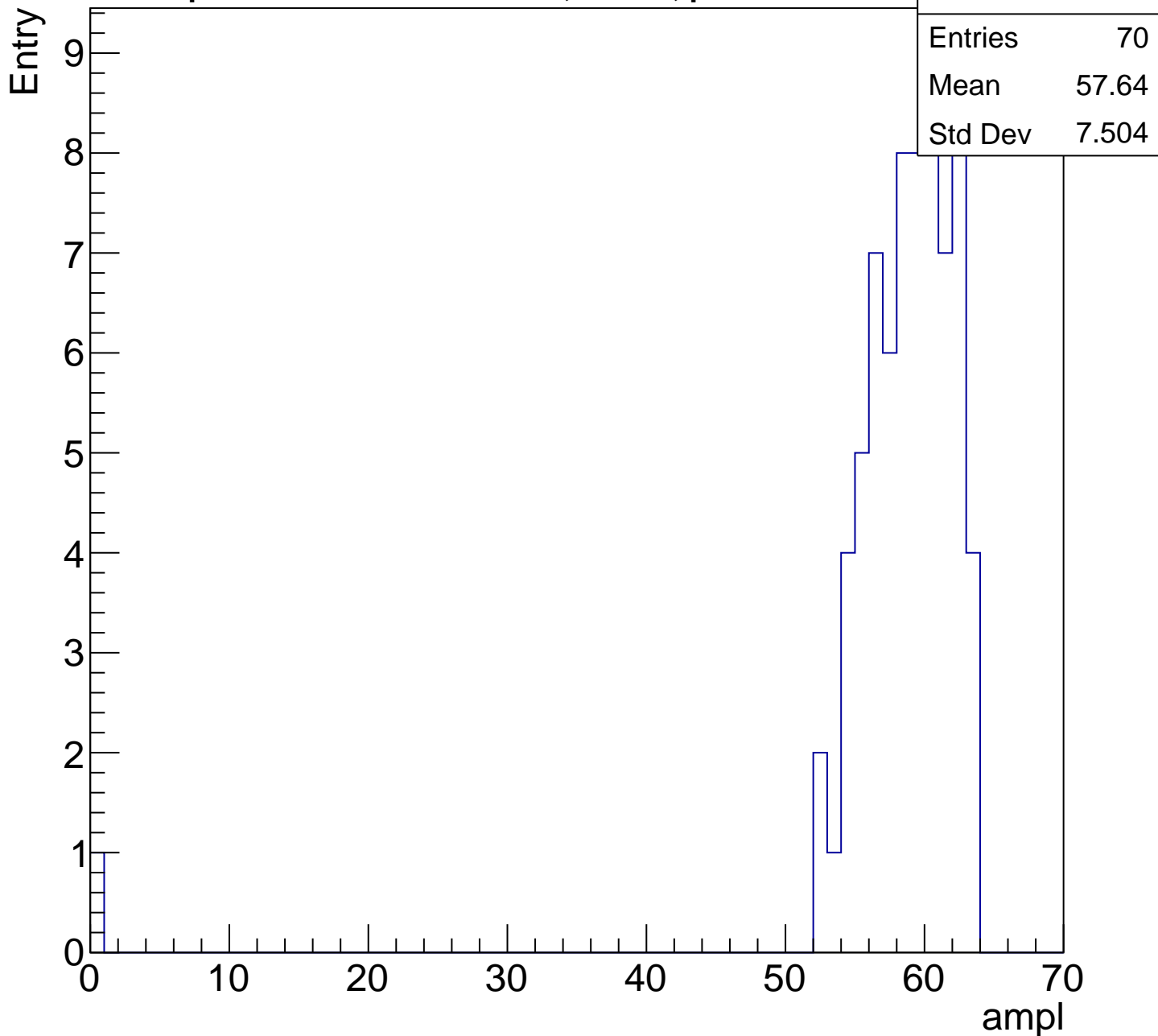
Entry

Entries	57
Mean	52.53
Std Dev	3.163



# B1L002S, U3-ch43, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

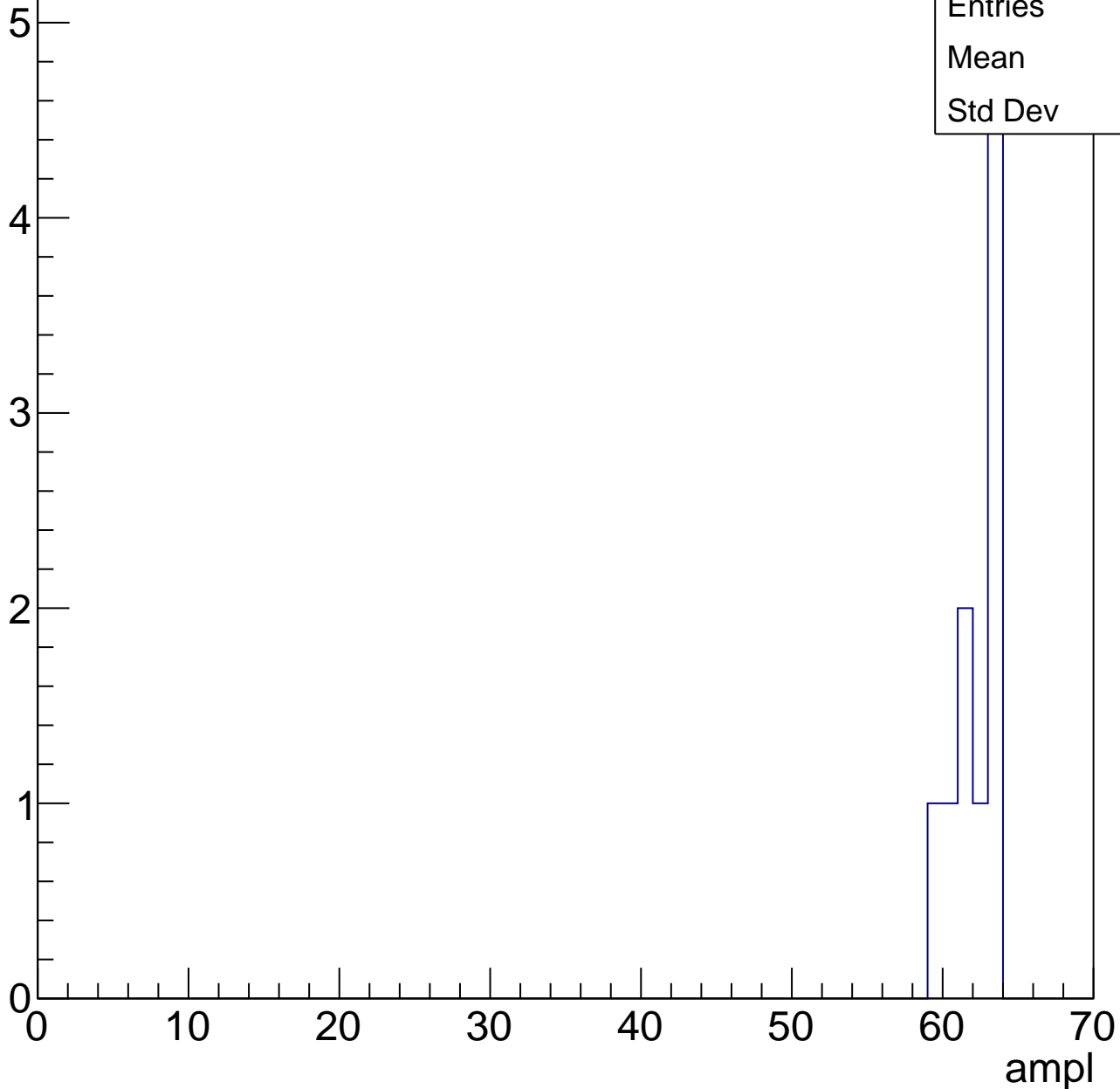


# B1L002S, U3-ch43, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	10
Mean	61.8
Std Dev	1.4





# B1L002S, U3-ch43, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch44, adc0

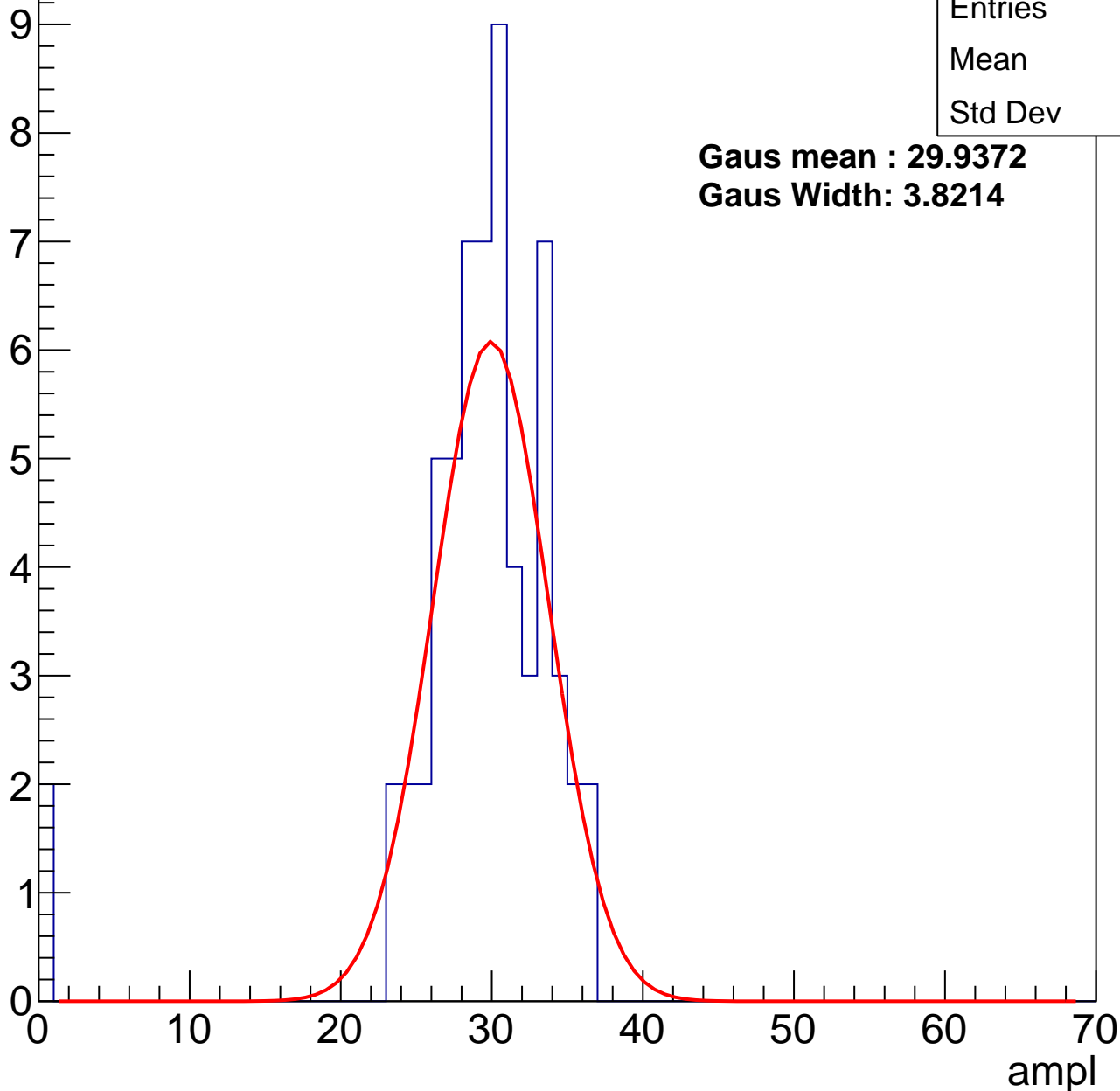
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.6
Std Dev	6.1

**Gaus mean : 29.9372**

**Gaus Width: 3.8214**



# B1L002S, U3-ch44, adc1

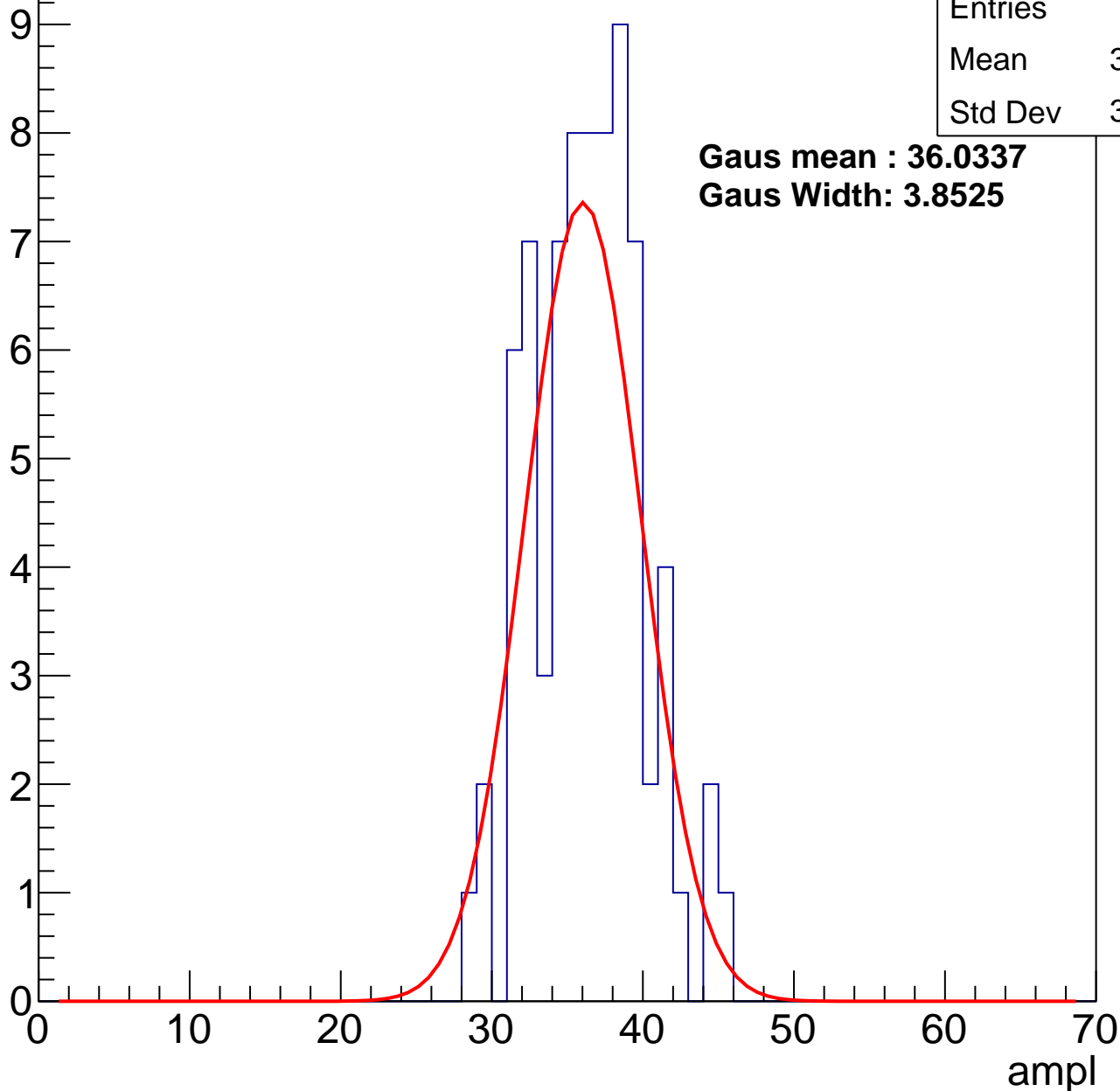
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	35.93
Std Dev	3.574

**Gaus mean : 36.0337**

**Gaus Width: 3.8525**



# B1L002S, U3-ch44, adc2

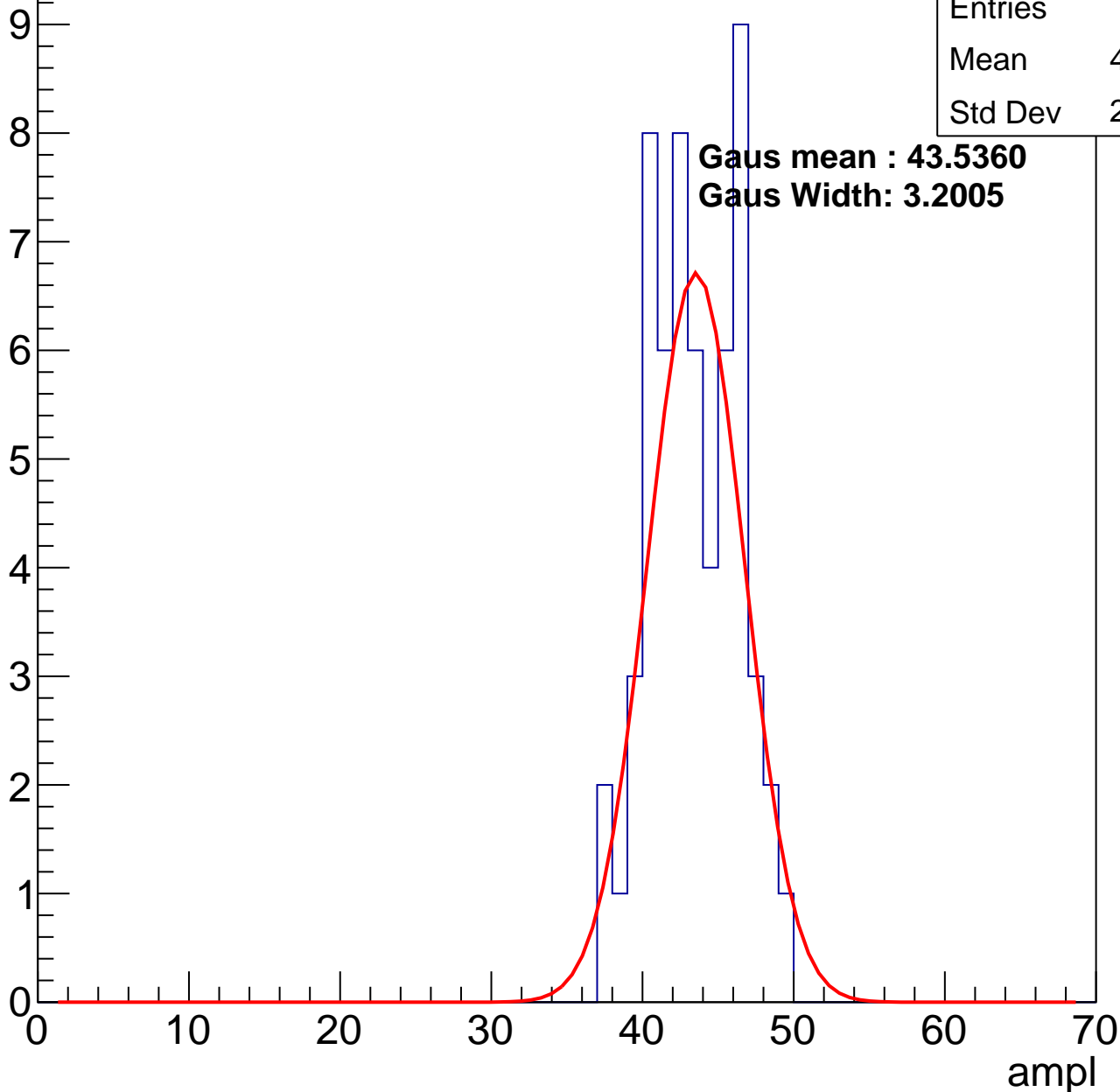
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	42.97
Std Dev	2.888

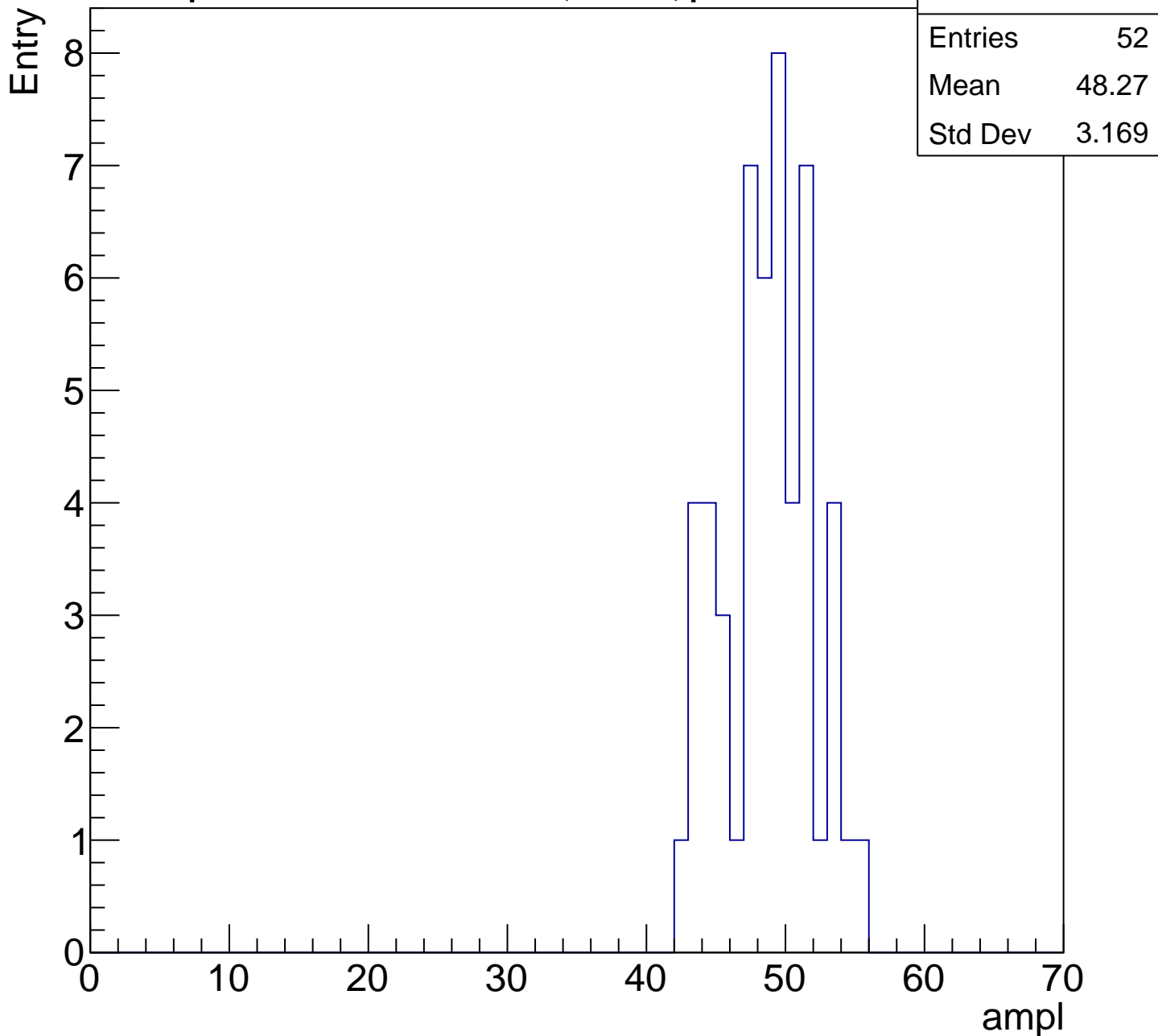
**Gaus mean : 43.5360**

**Gaus Width: 3.2005**



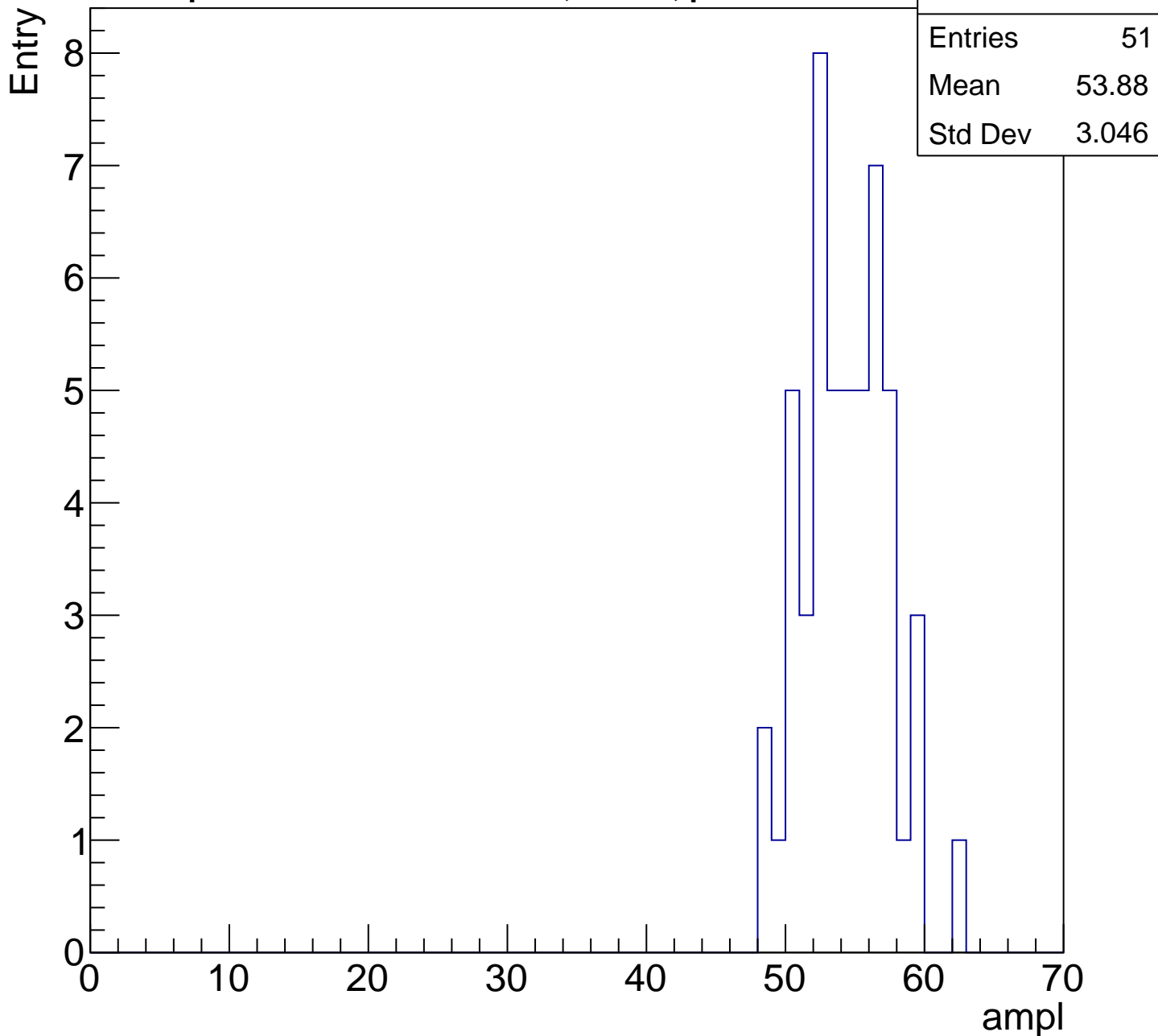
# B1L002S, U3-ch44, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch44, adc4

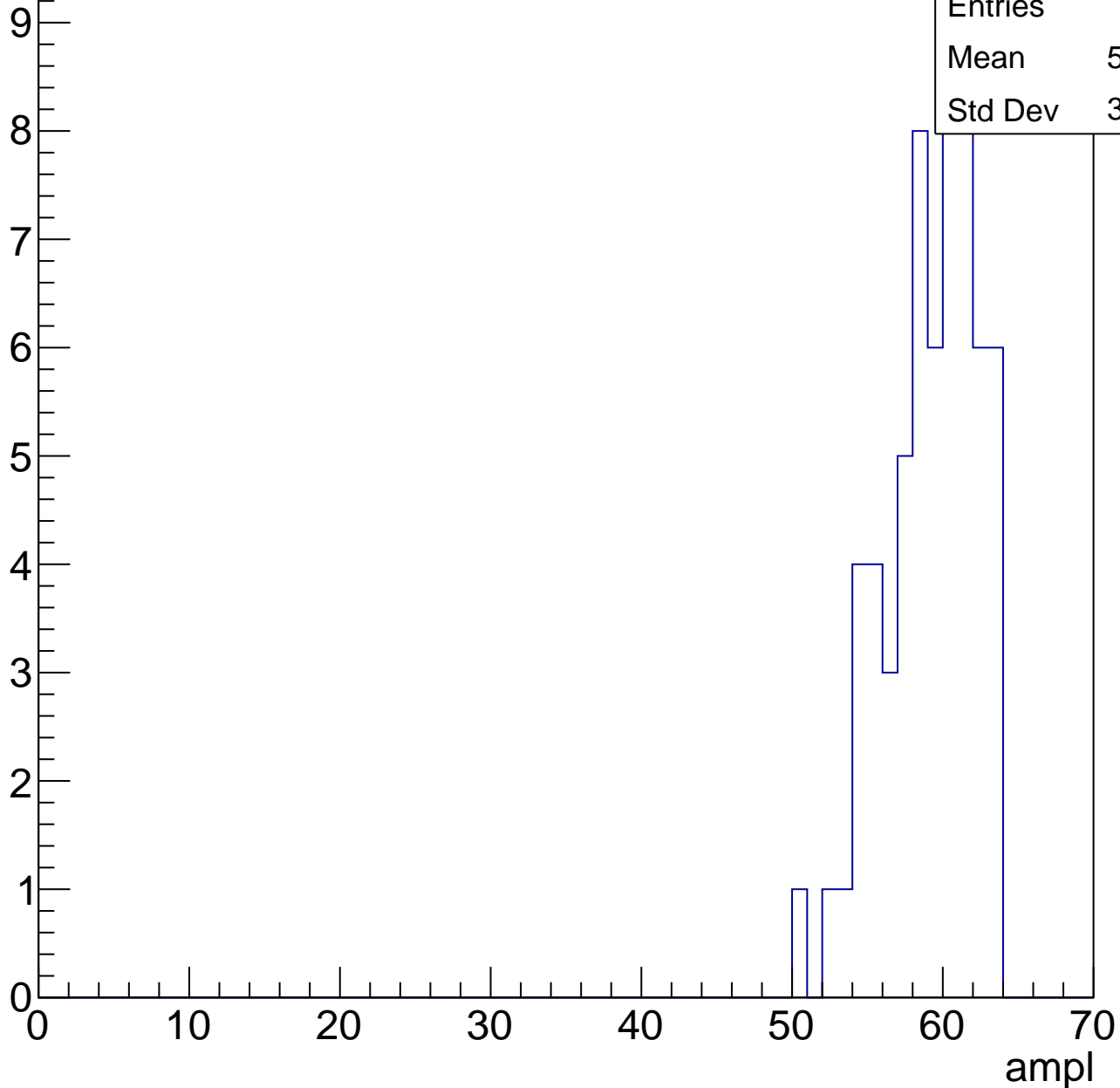
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch44, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

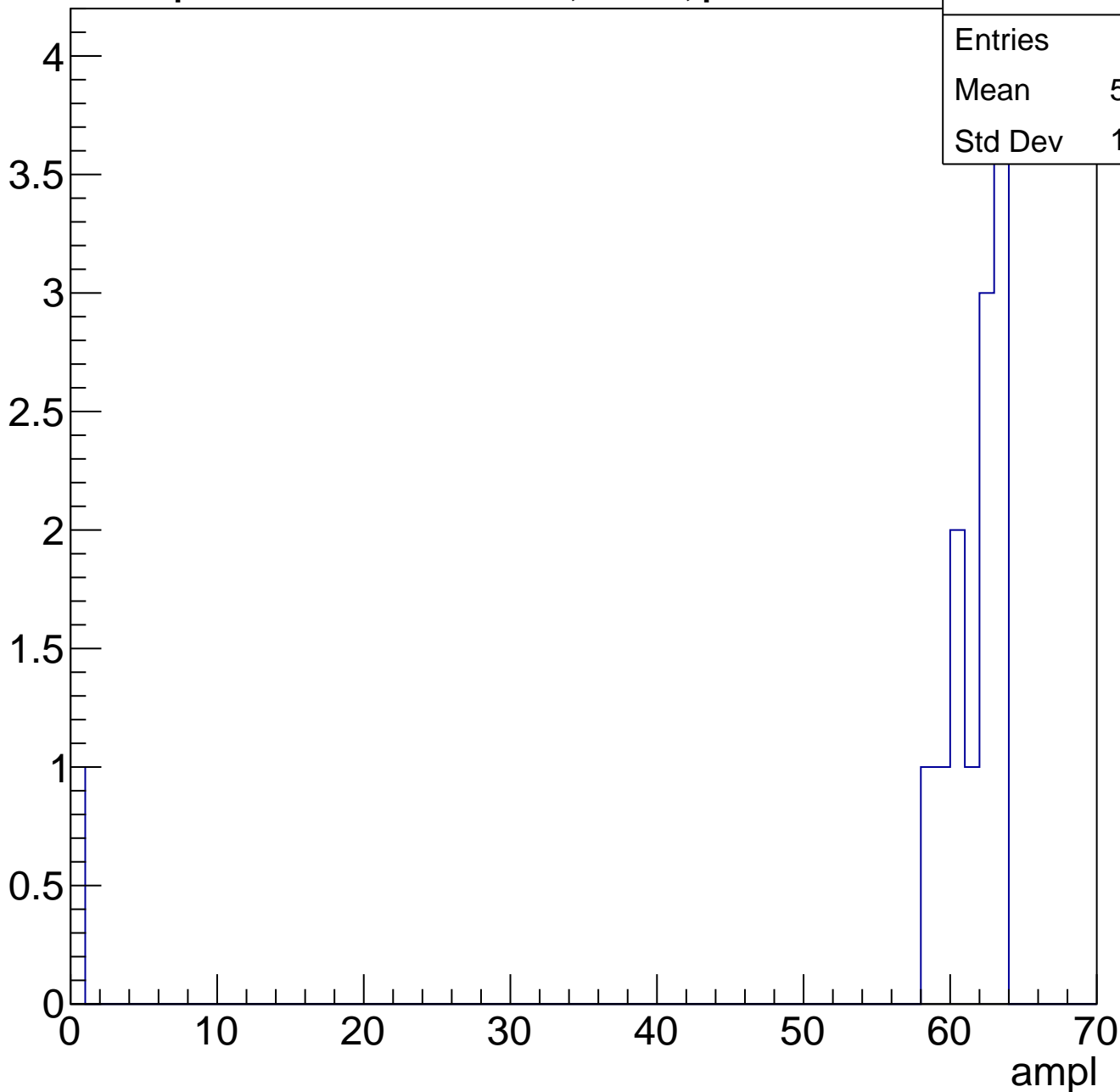
Entry



# B1L002S, U3-ch44, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch44, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U3-ch45, adc0

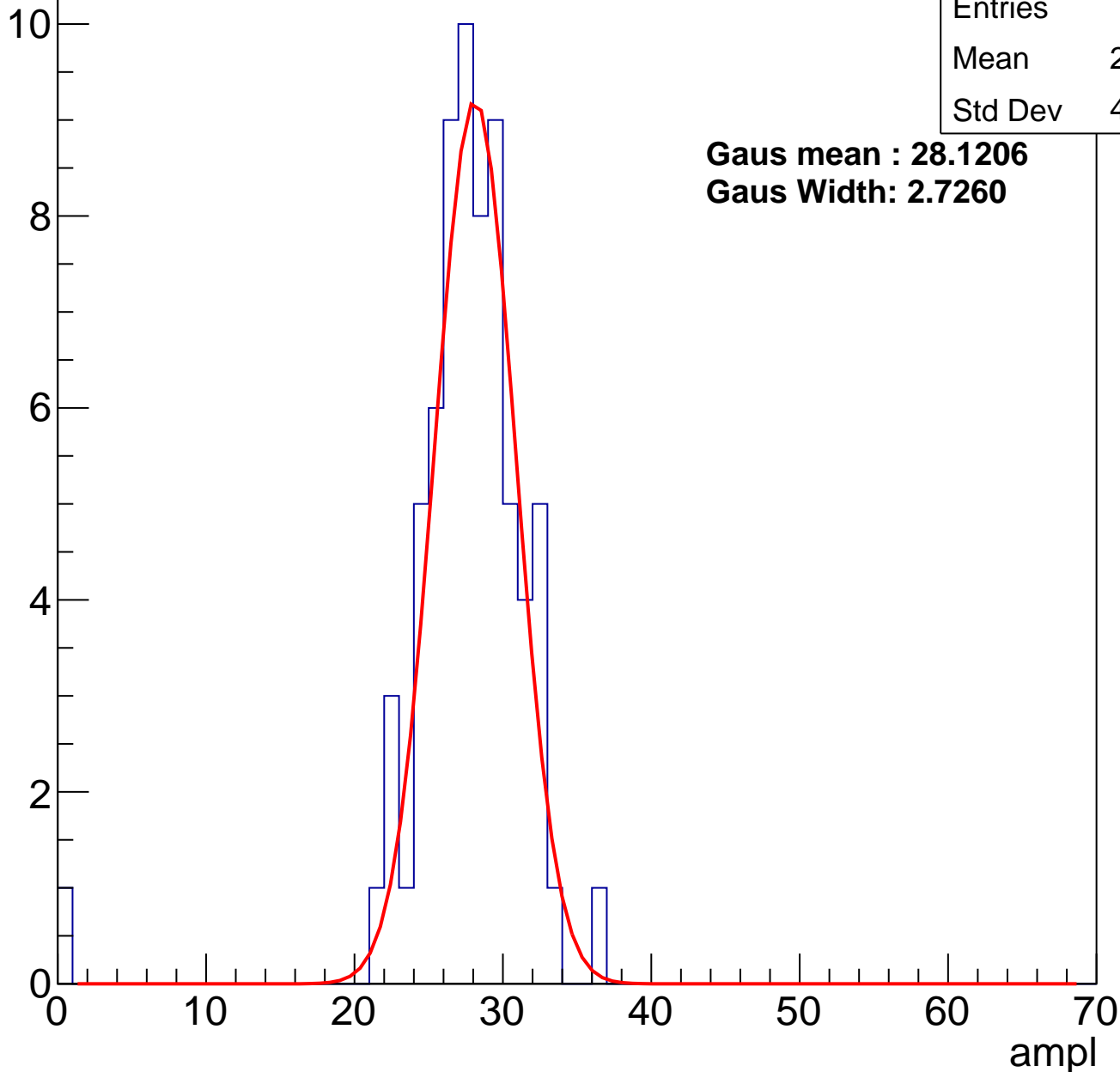
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	27.13
Std Dev	4.387

**Gaus mean : 28.1206**

**Gaus Width: 2.7260**

Entry



# B1L002S, U3-ch45, adc1

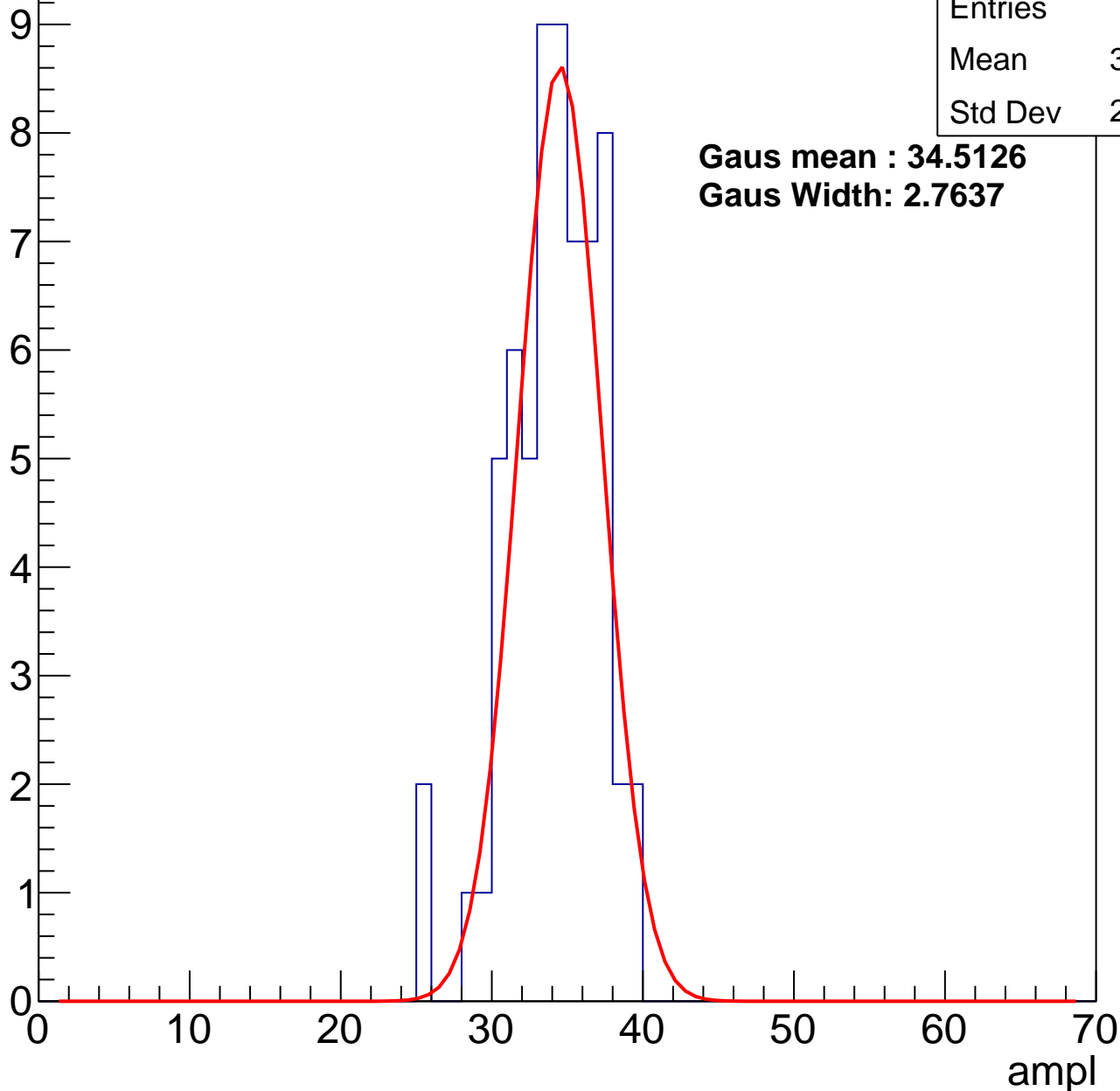
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	33.64
Std Dev	2.976

**Gaus mean : 34.5126**

**Gaus Width: 2.7637**



# B1L002S, U3-ch45, adc2

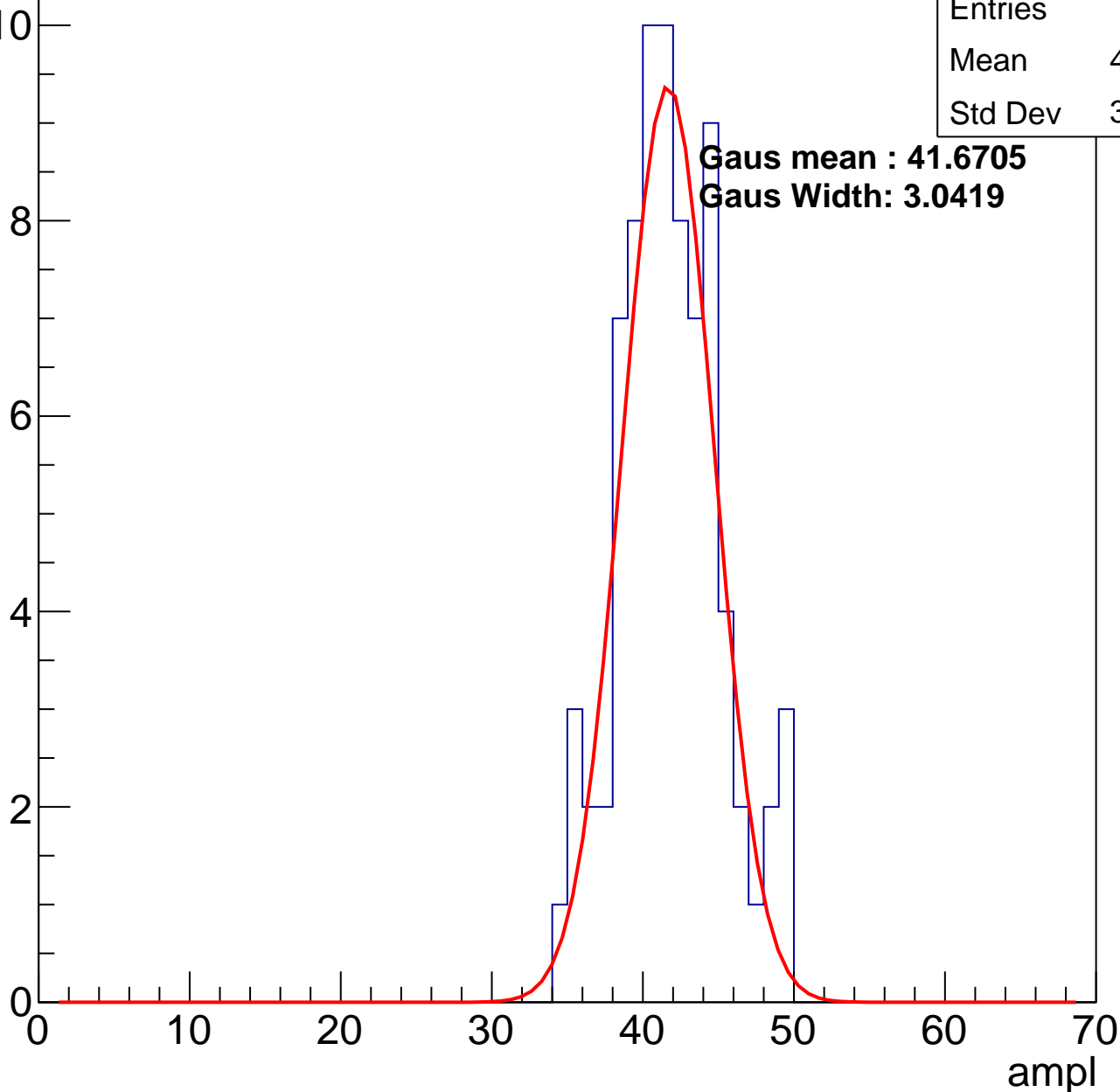
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	79
Mean	41.37
Std Dev	3.357

**Gaus mean : 41.6705**

**Gaus Width: 3.0419**

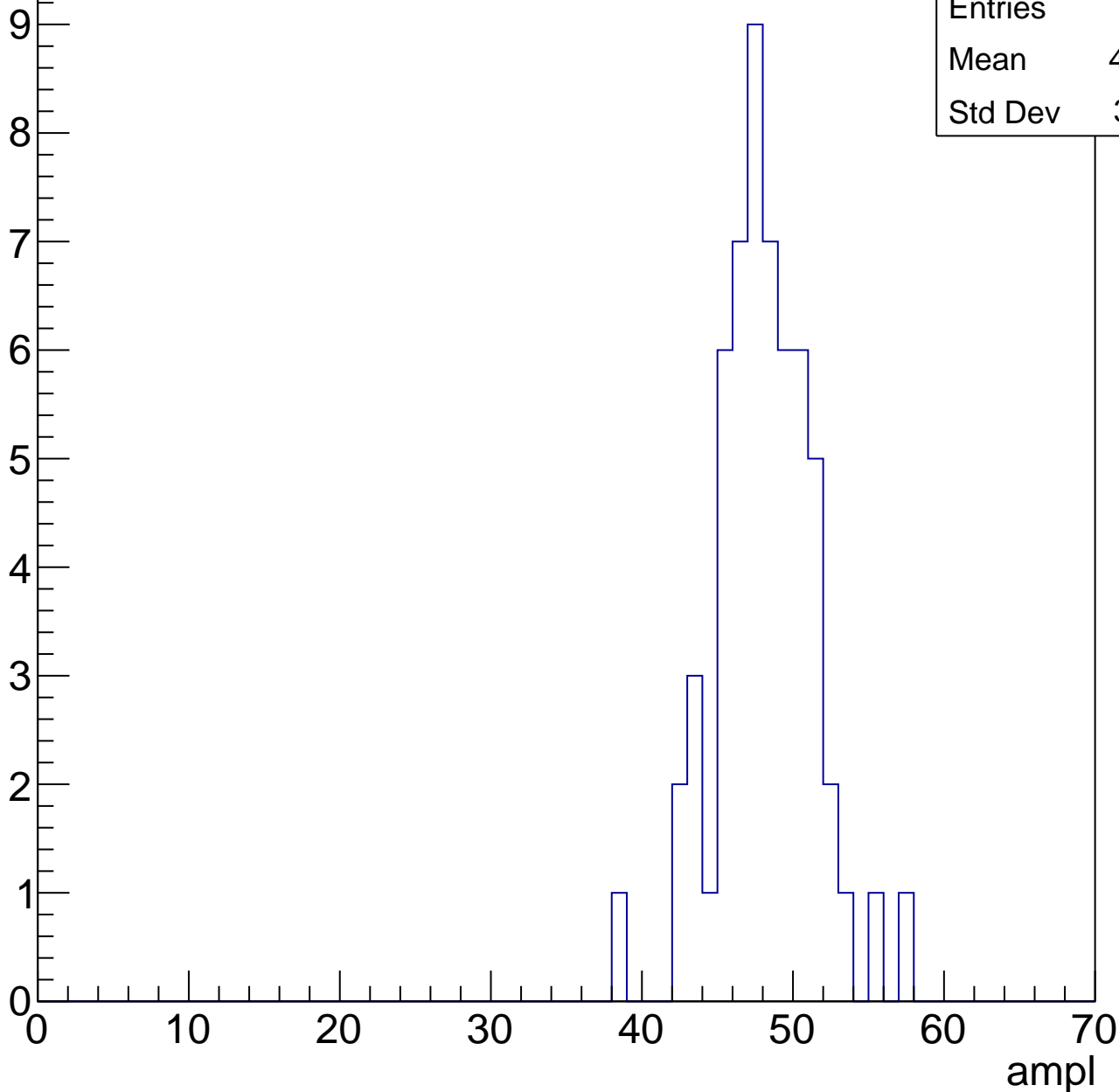


# B1L002S, U3-ch45, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

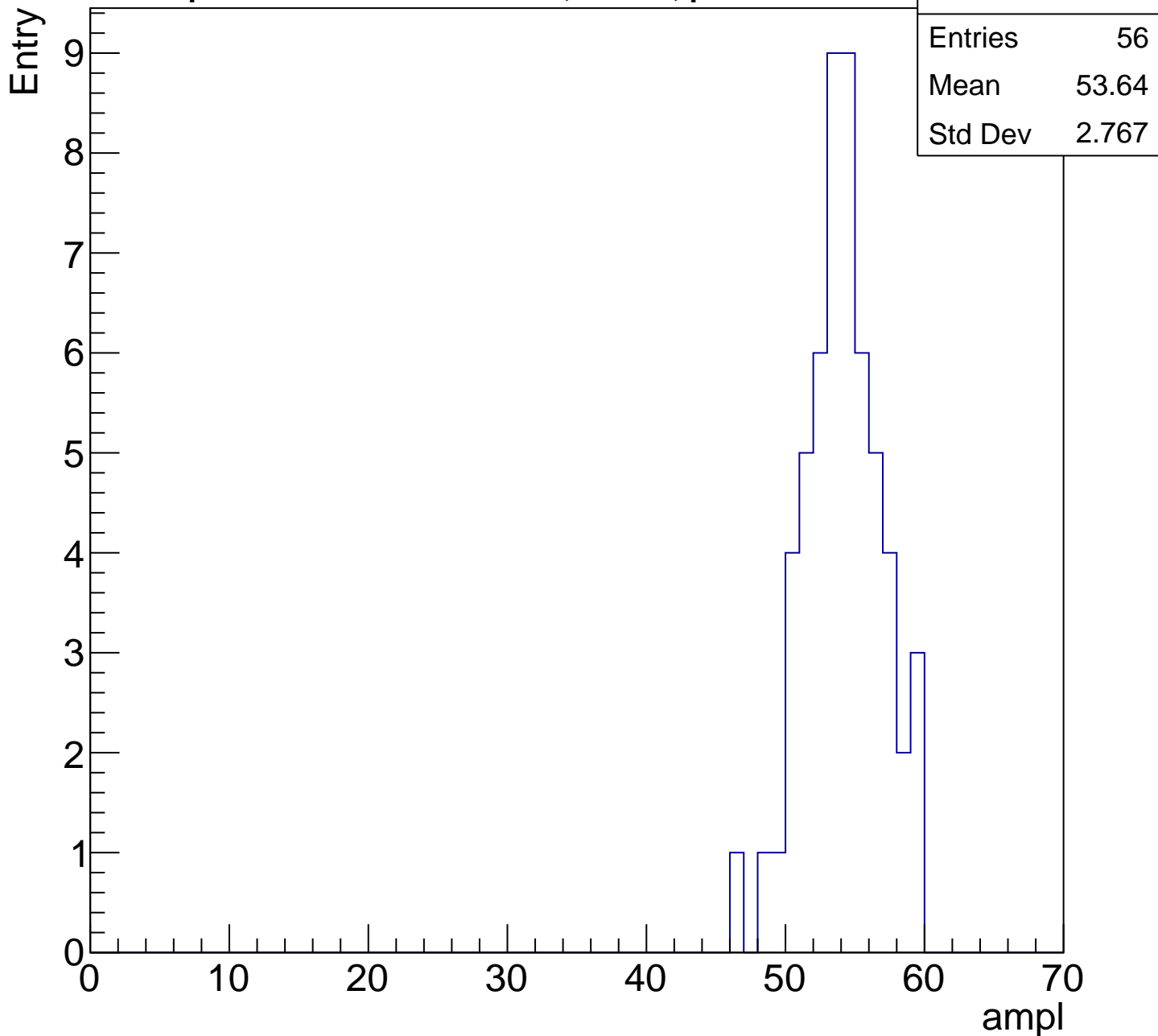
Entry

Entries	58
Mean	47.66
Std Dev	3.241



# B1L002S, U3-ch45, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch45, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

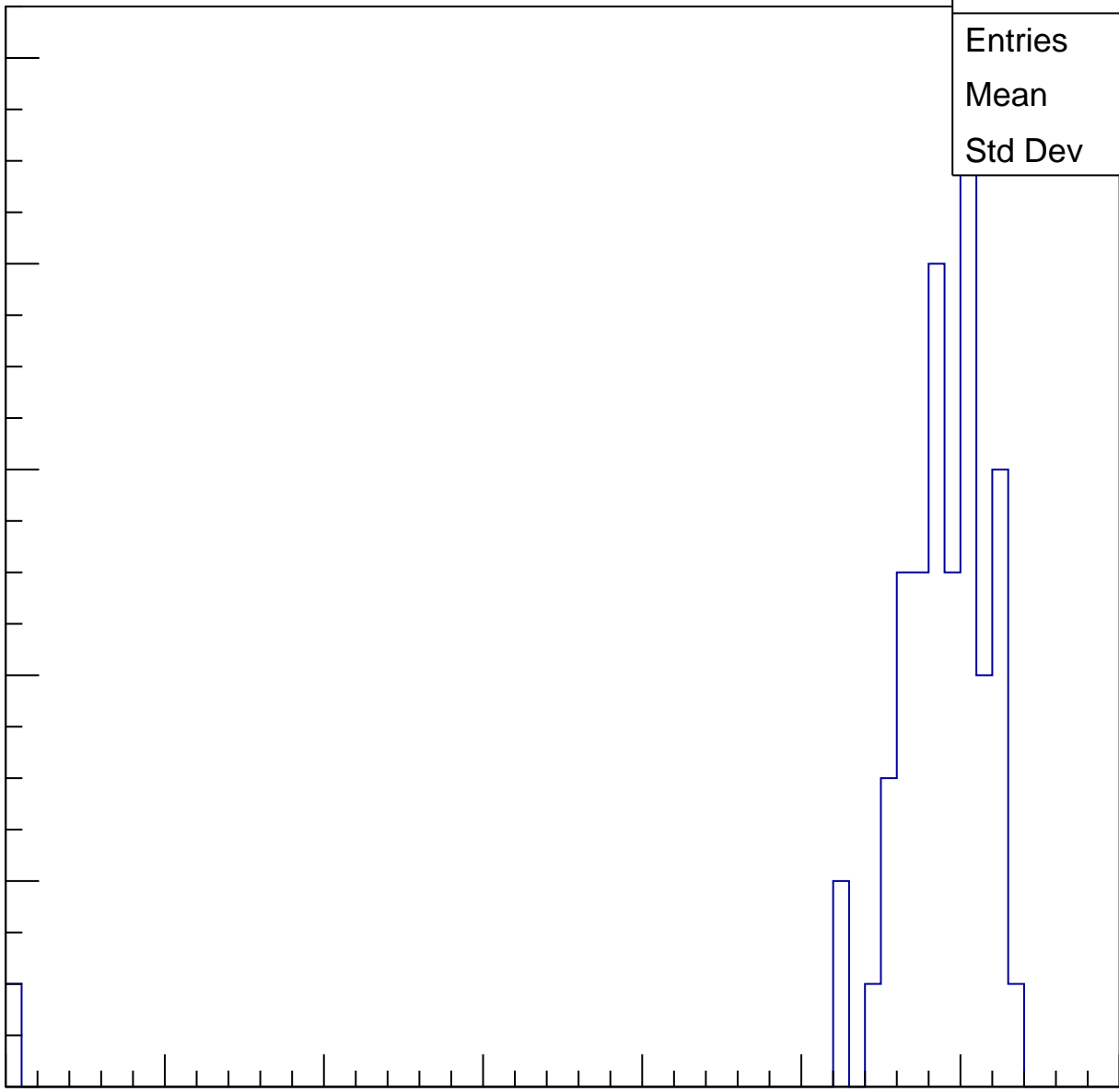
Entries	51
Mean	57.37
Std Dev	8.501

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

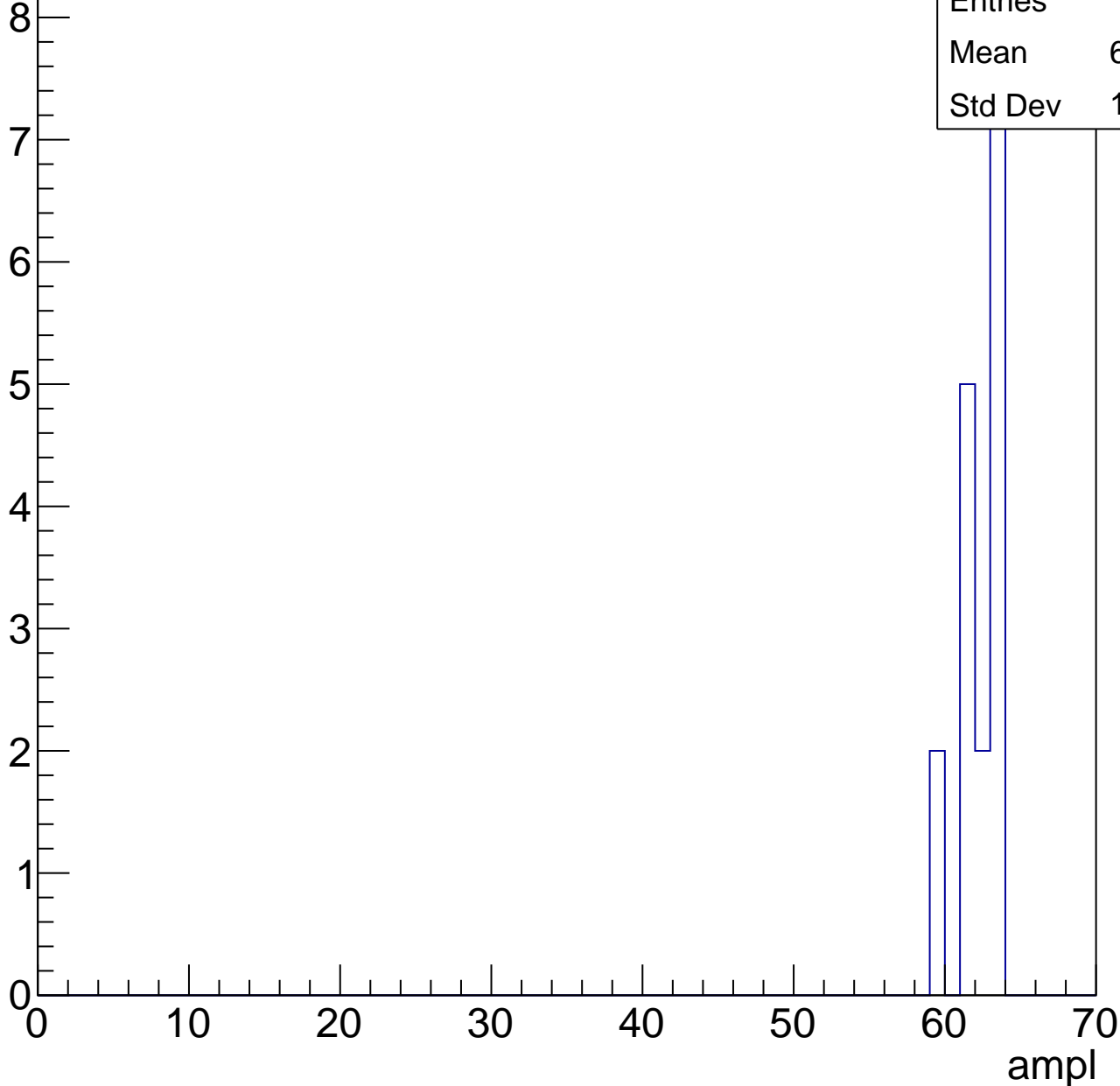


# B1L002S, U3-ch45, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.82
Std Dev	1.339





# B1L002S, U3-ch45, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	4
Mean	62
Std Dev	0.7071

ampl

# B1L002S, U3-ch46, adc0

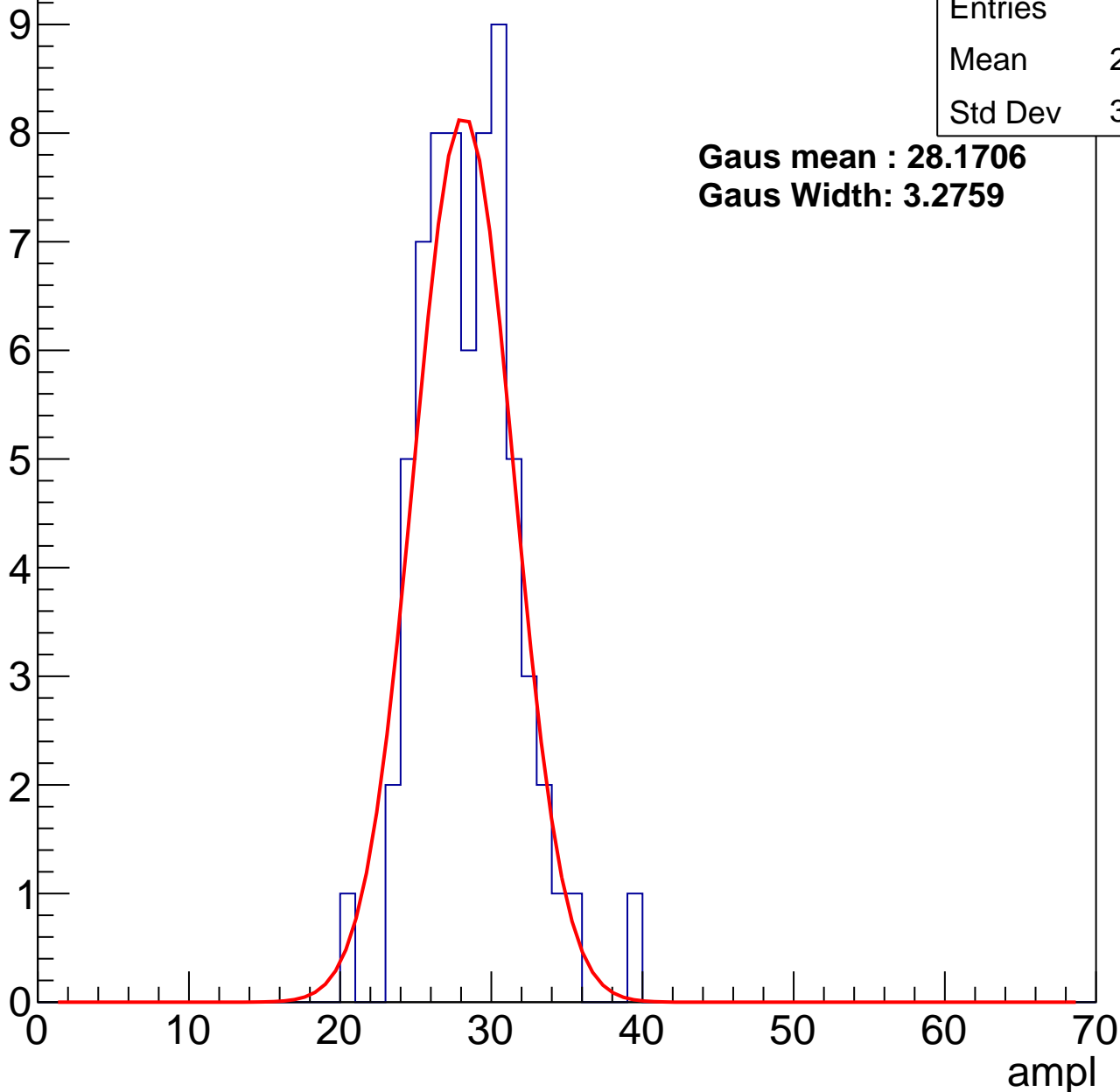
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.06
Std Dev	3.209

**Gaus mean : 28.1706**

**Gaus Width: 3.2759**



# B1L002S, U3-ch46, adc1

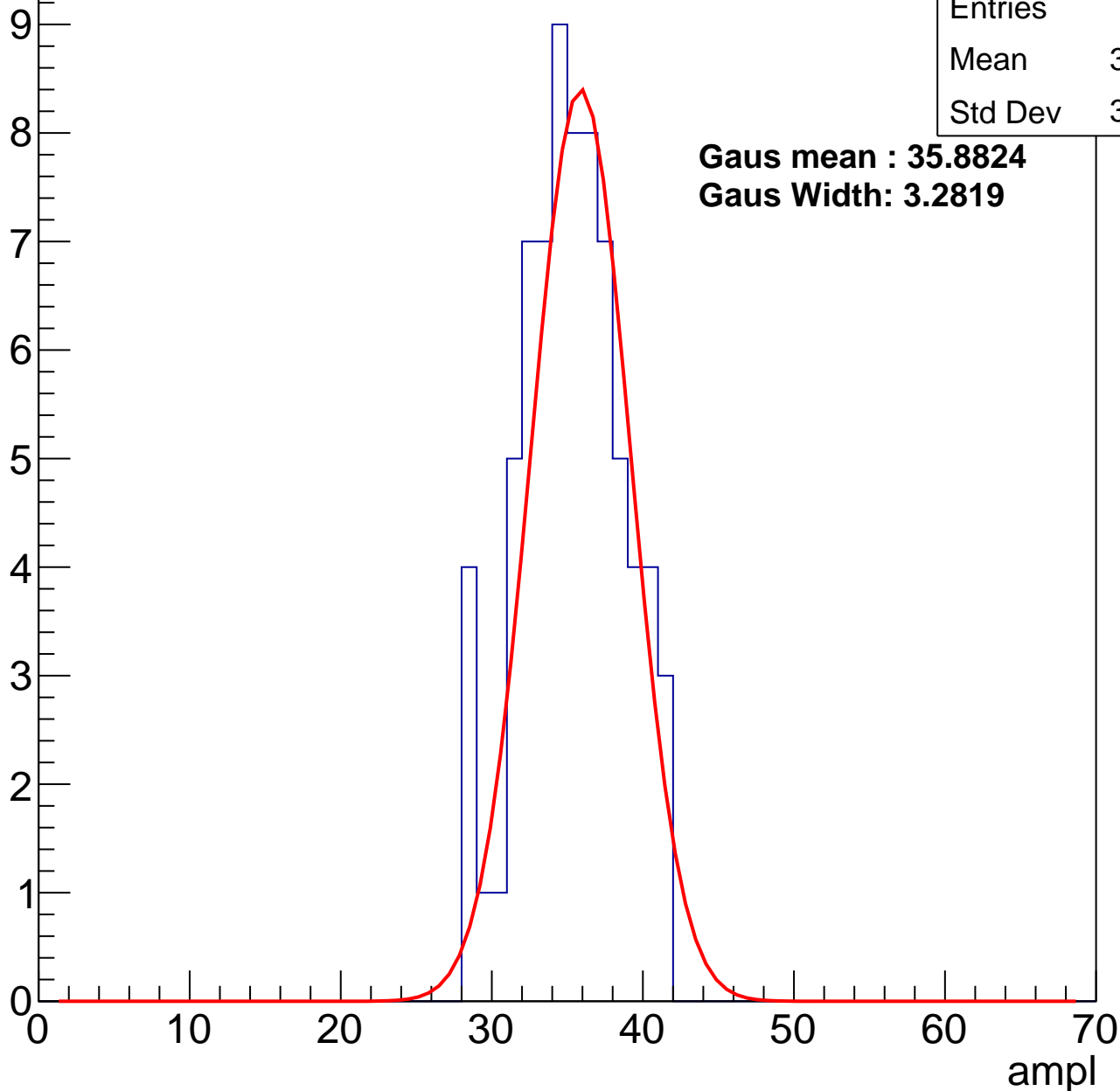
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34.84
Std Dev	3.286

**Gaus mean : 35.8824**

**Gaus Width: 3.2819**



# B1L002S, U3-ch46, adc2

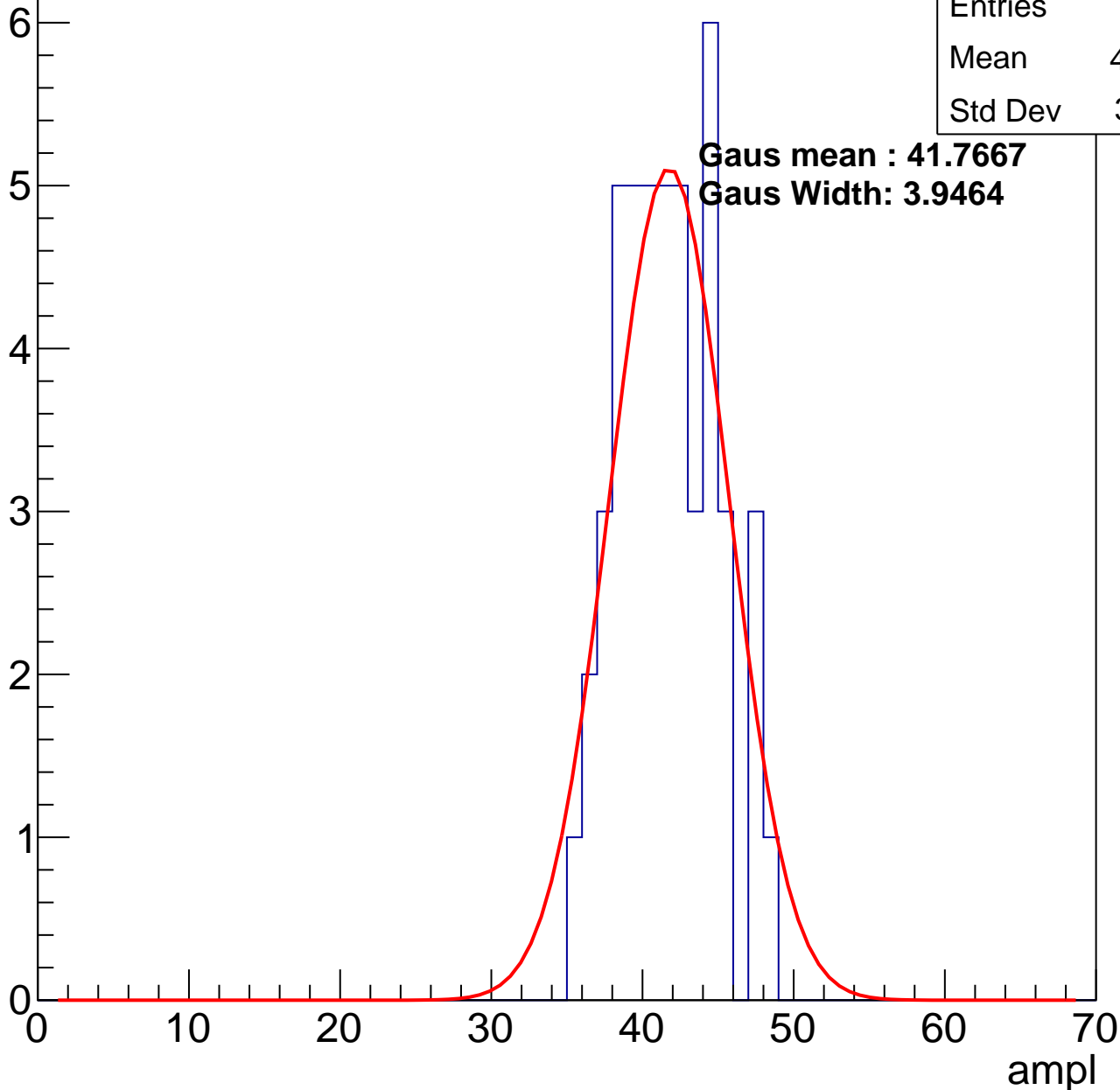
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	41.17
Std Dev	3.191

**Gaus mean : 41.7667**

**Gaus Width: 3.9464**



# B1L002S, U3-ch46, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	46.97
Std Dev	3.464

Entry

10

8

6

4

2

0

0

10

20

30

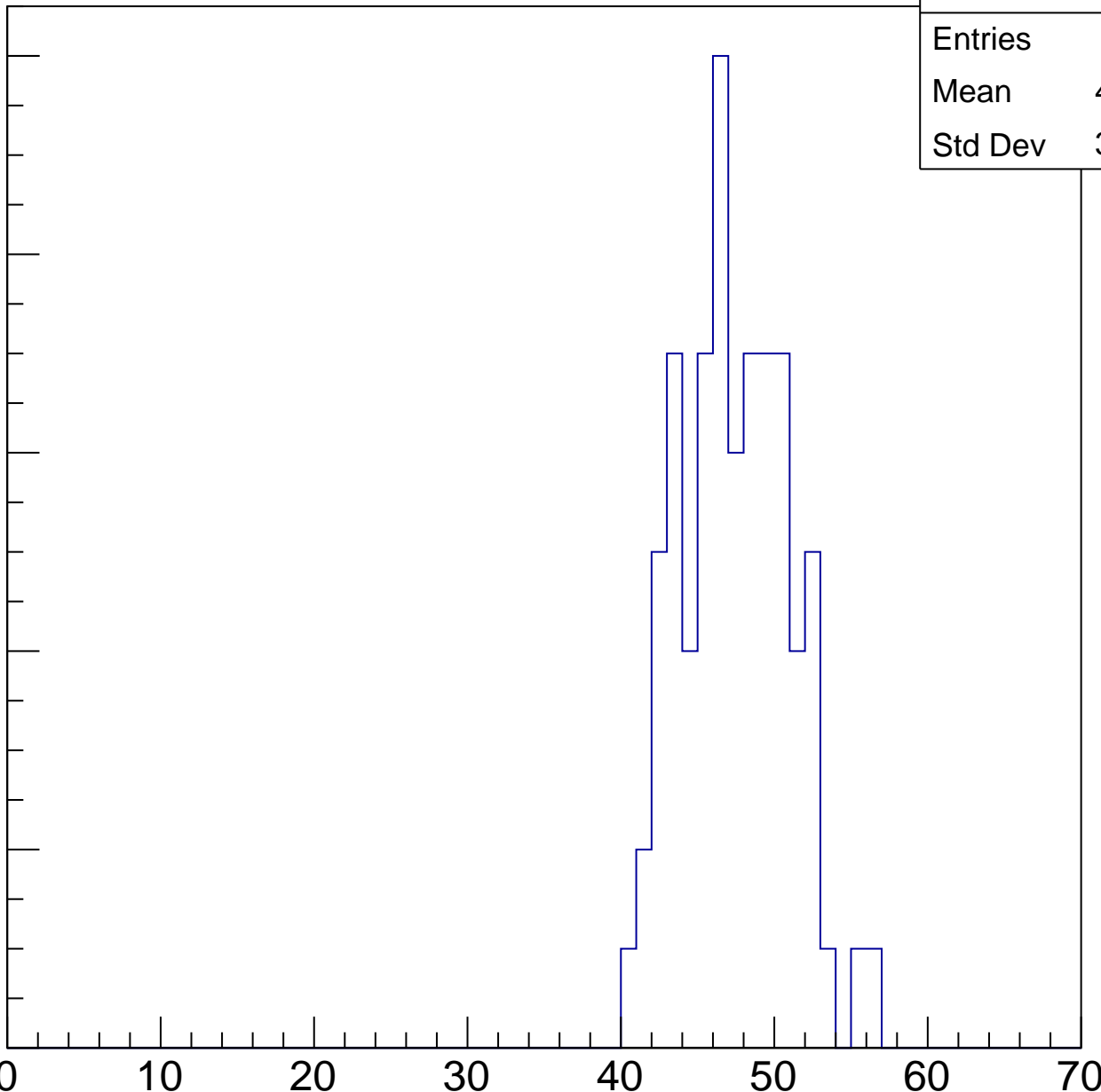
40

50

60

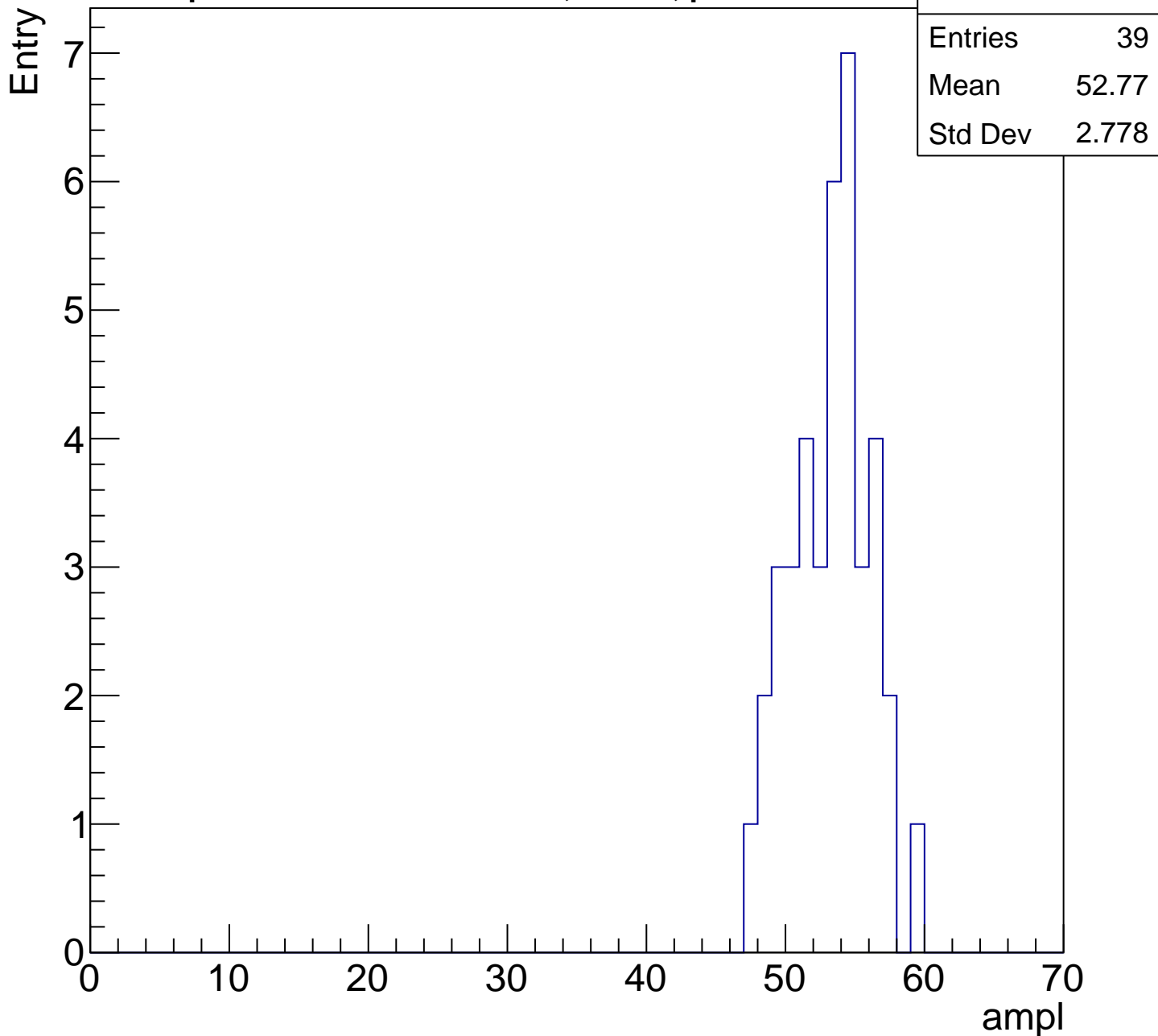
70

ampl



# B1L002S, U3-ch46, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

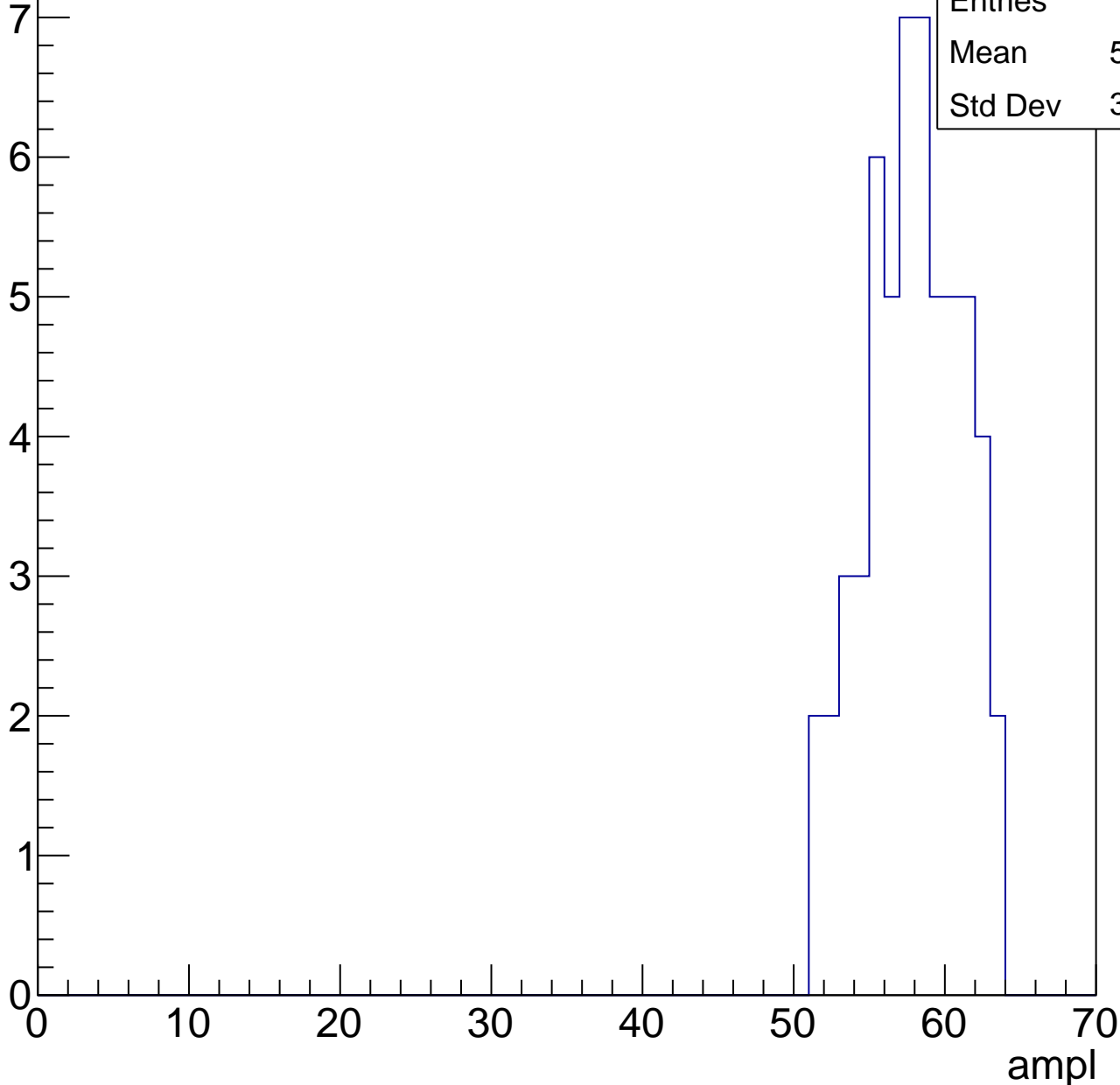


# B1L002S, U3-ch46, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	57.43
Std Dev	3.104

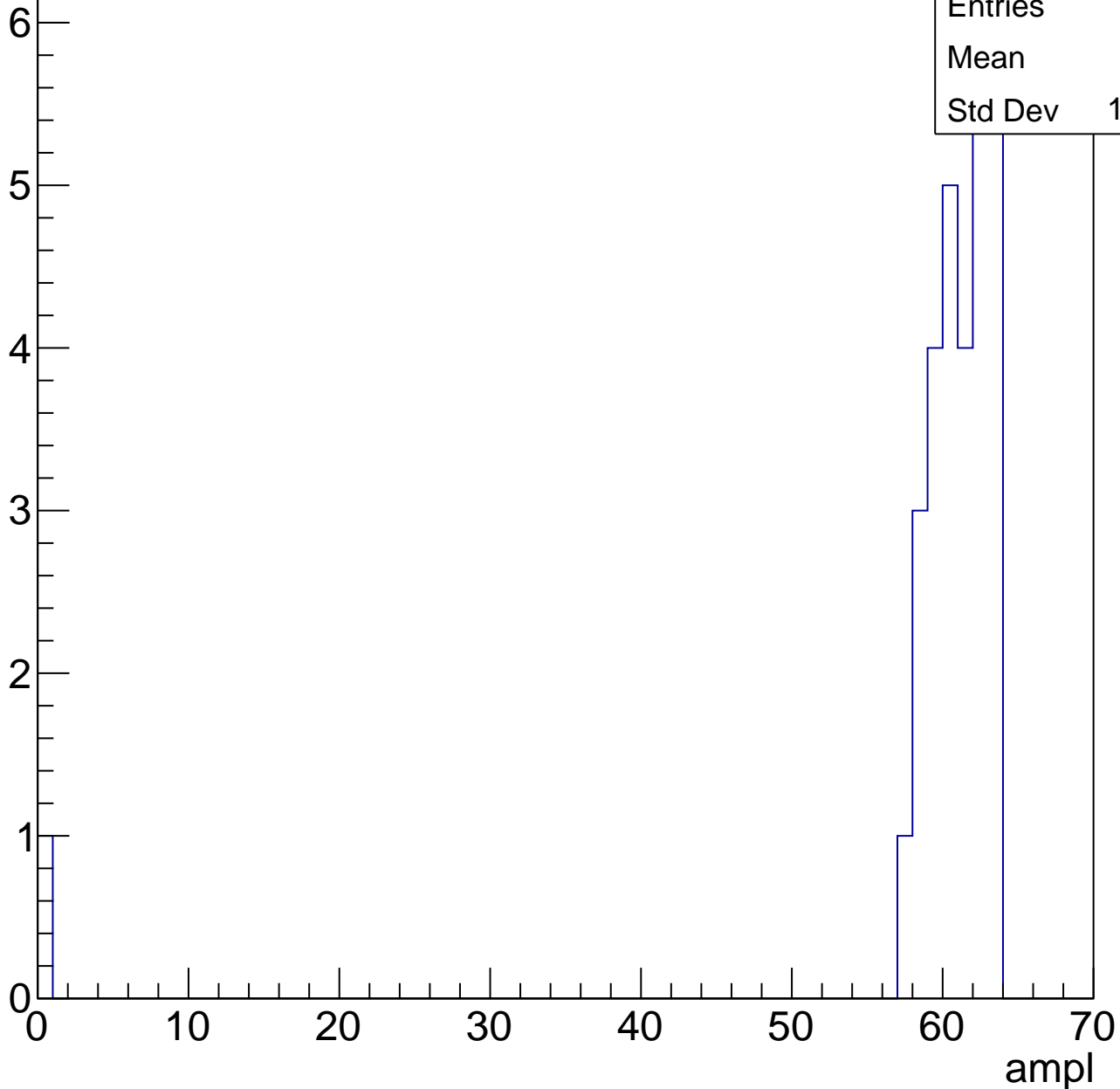


# B1L002S, U3-ch46, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.7
Std Dev	11.04





# B1L002S, U3-ch46, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch47, adc0

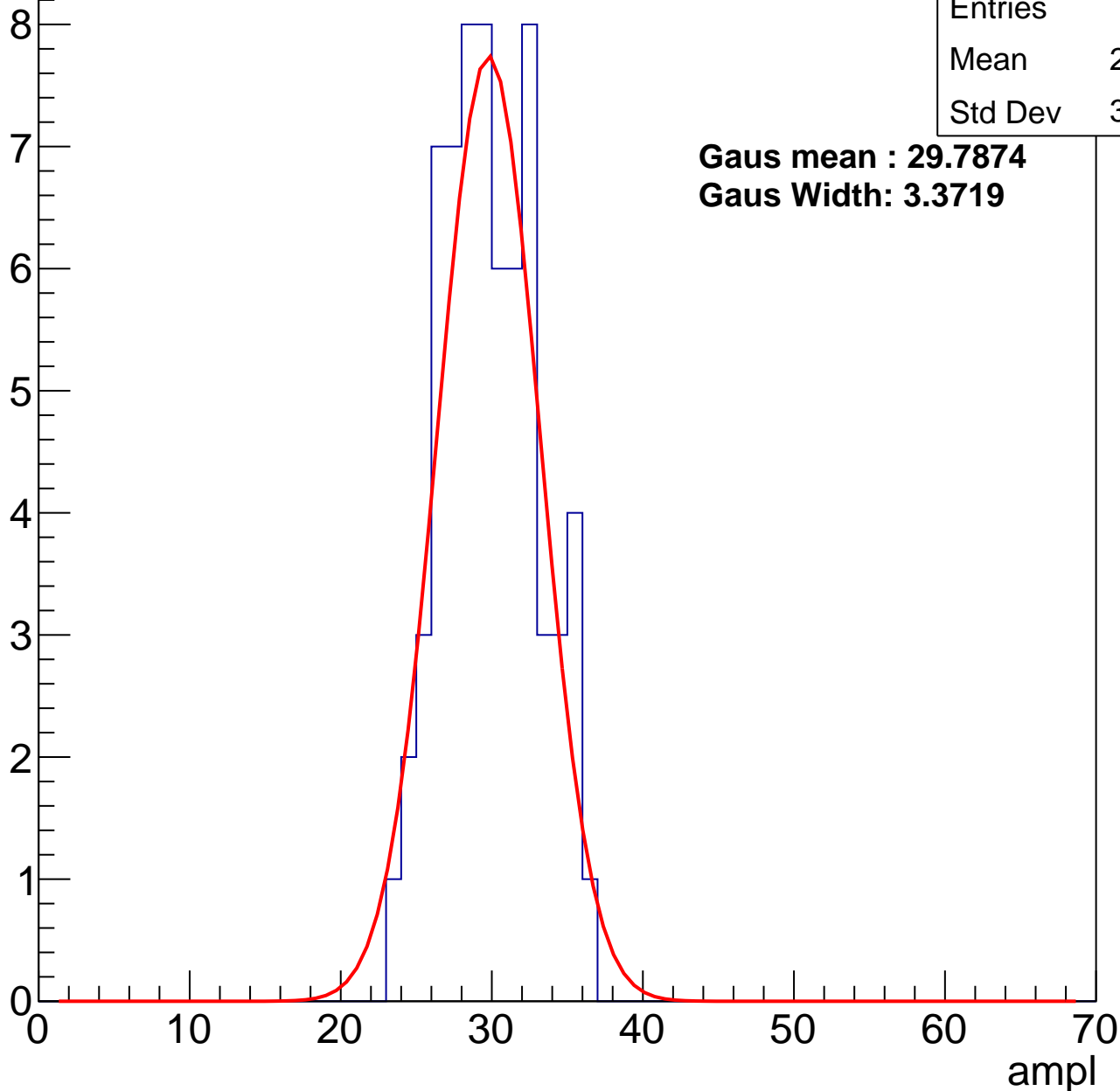
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	29.43
Std Dev	3.087

**Gaus mean : 29.7874**

**Gaus Width: 3.3719**



# B1L002S, U3-ch47, adc1

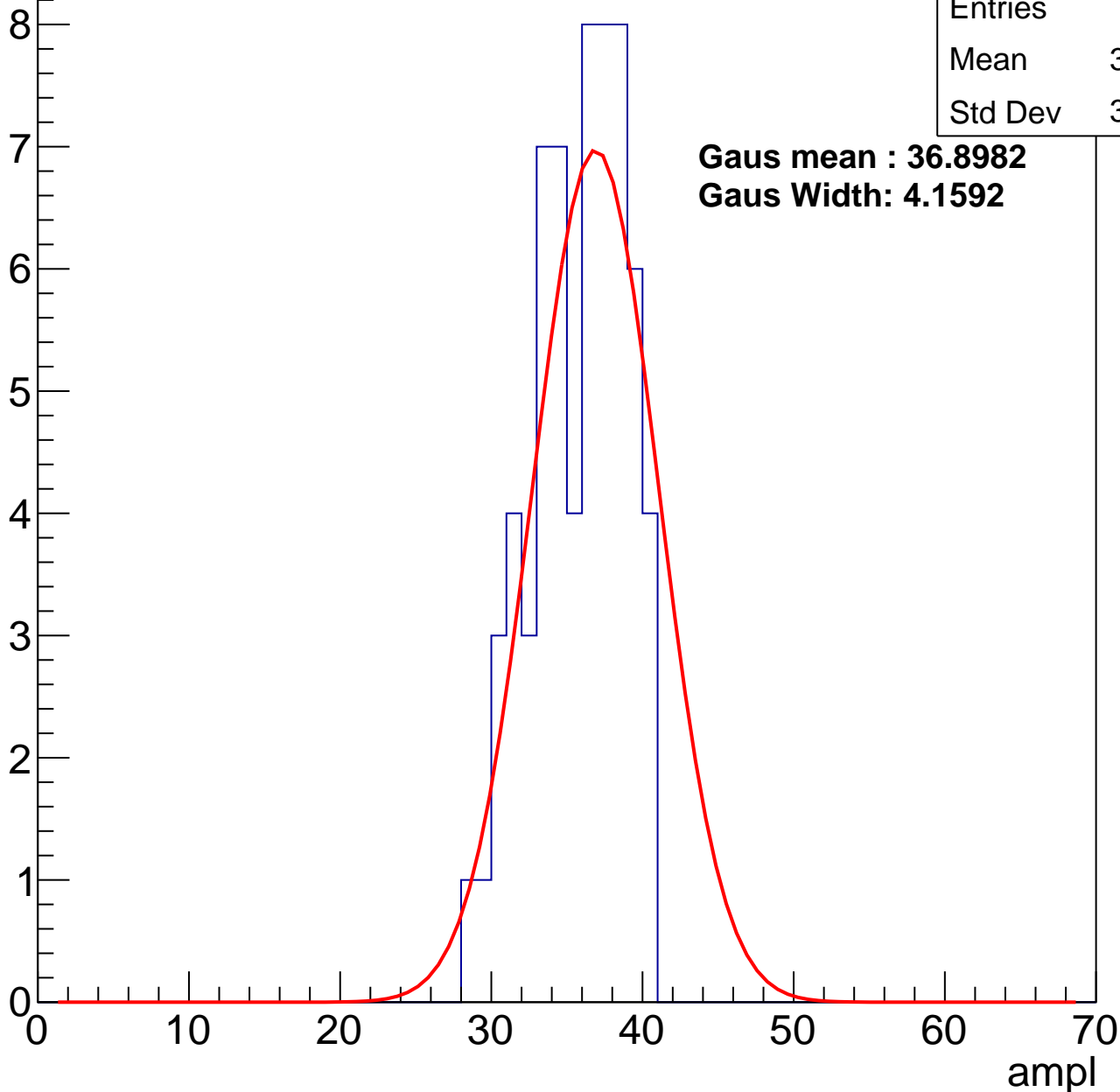
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	35.28
Std Dev	3.034

**Gaus mean : 36.8982**

**Gaus Width: 4.1592**



# B1L002S, U3-ch47, adc2

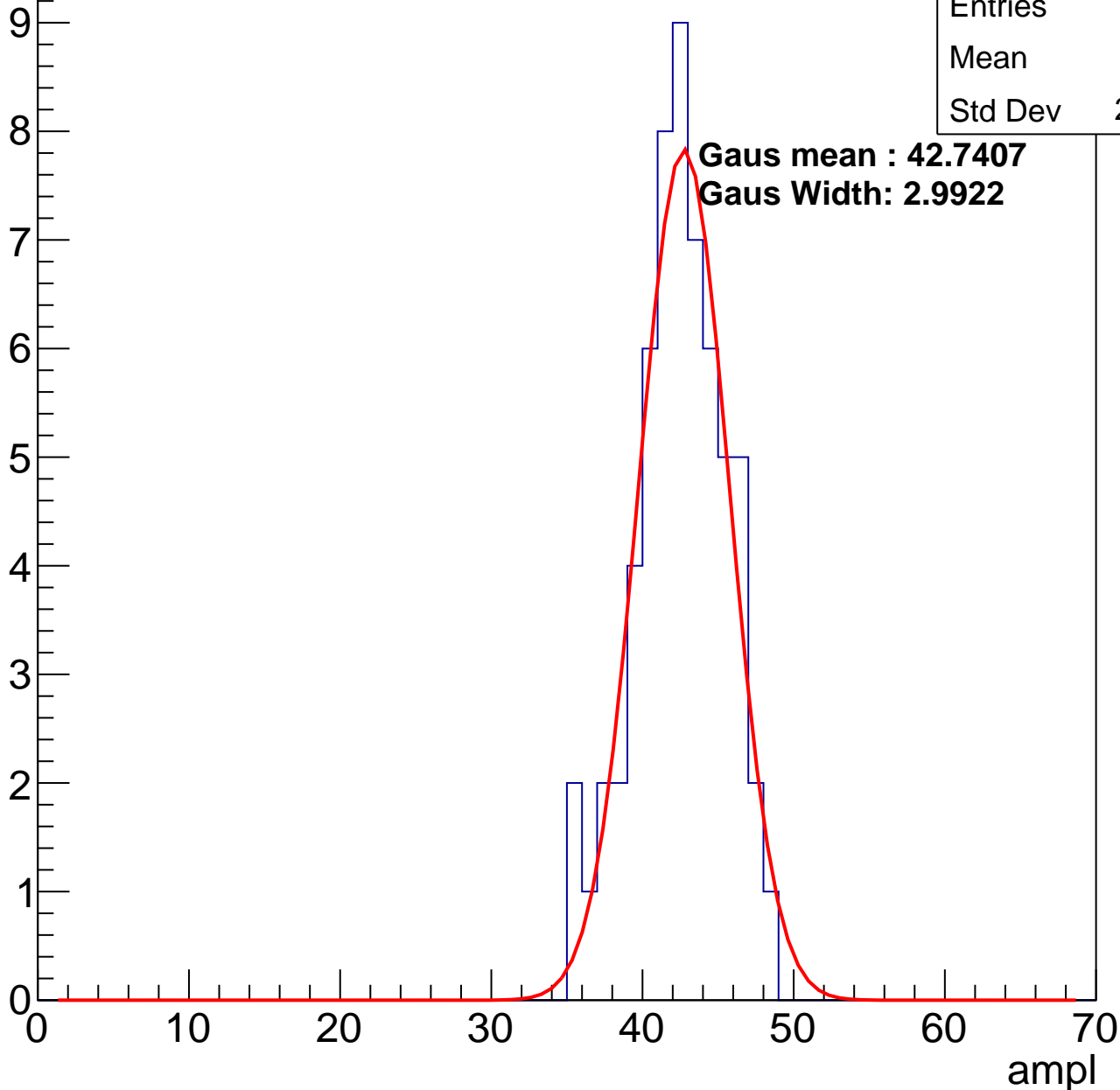
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	42
Std Dev	2.961

**Gaus mean : 42.7407**

**Gaus Width: 2.9922**

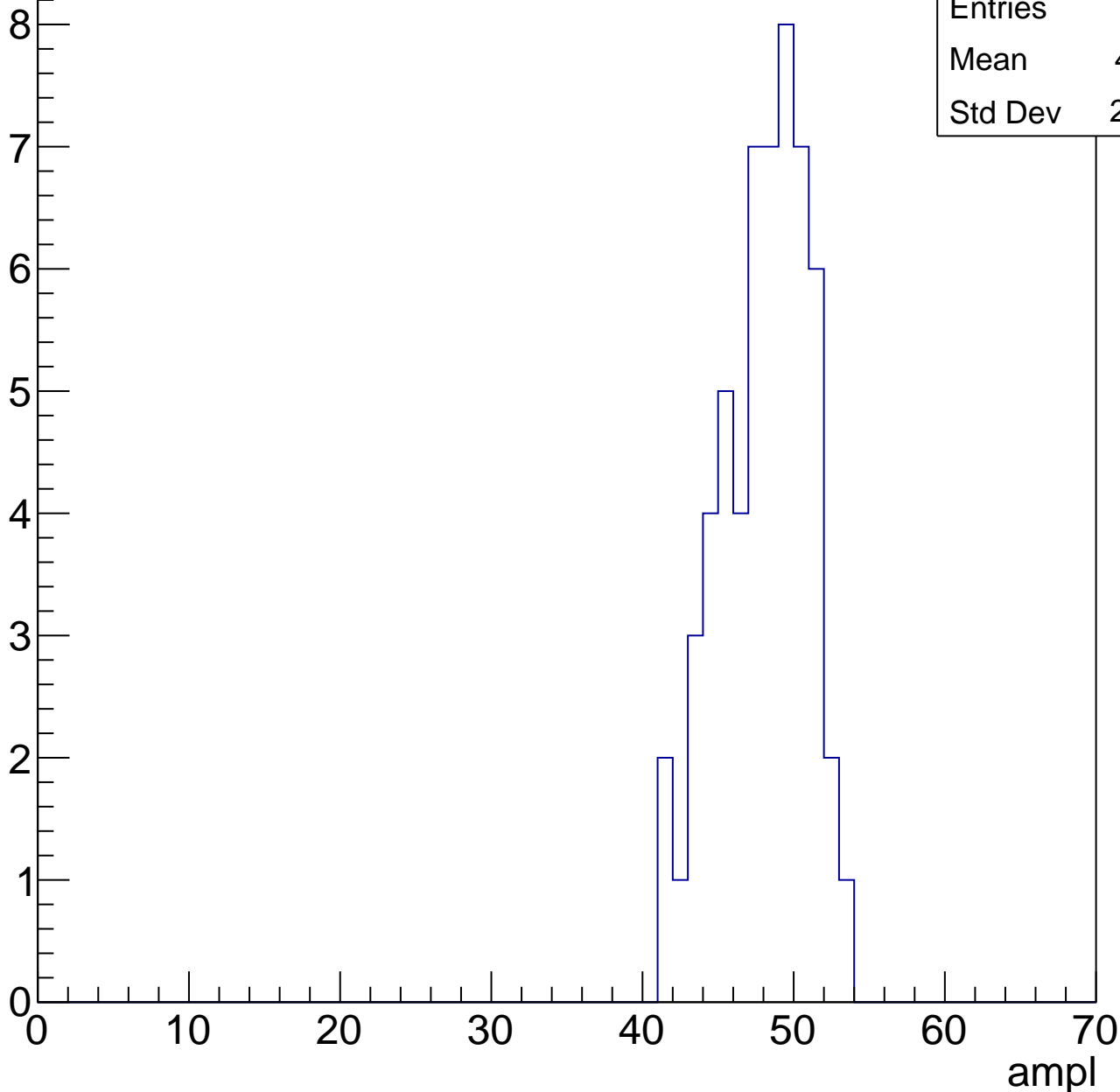


# B1L002S, U3-ch47, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	47.51
Std Dev	2.884

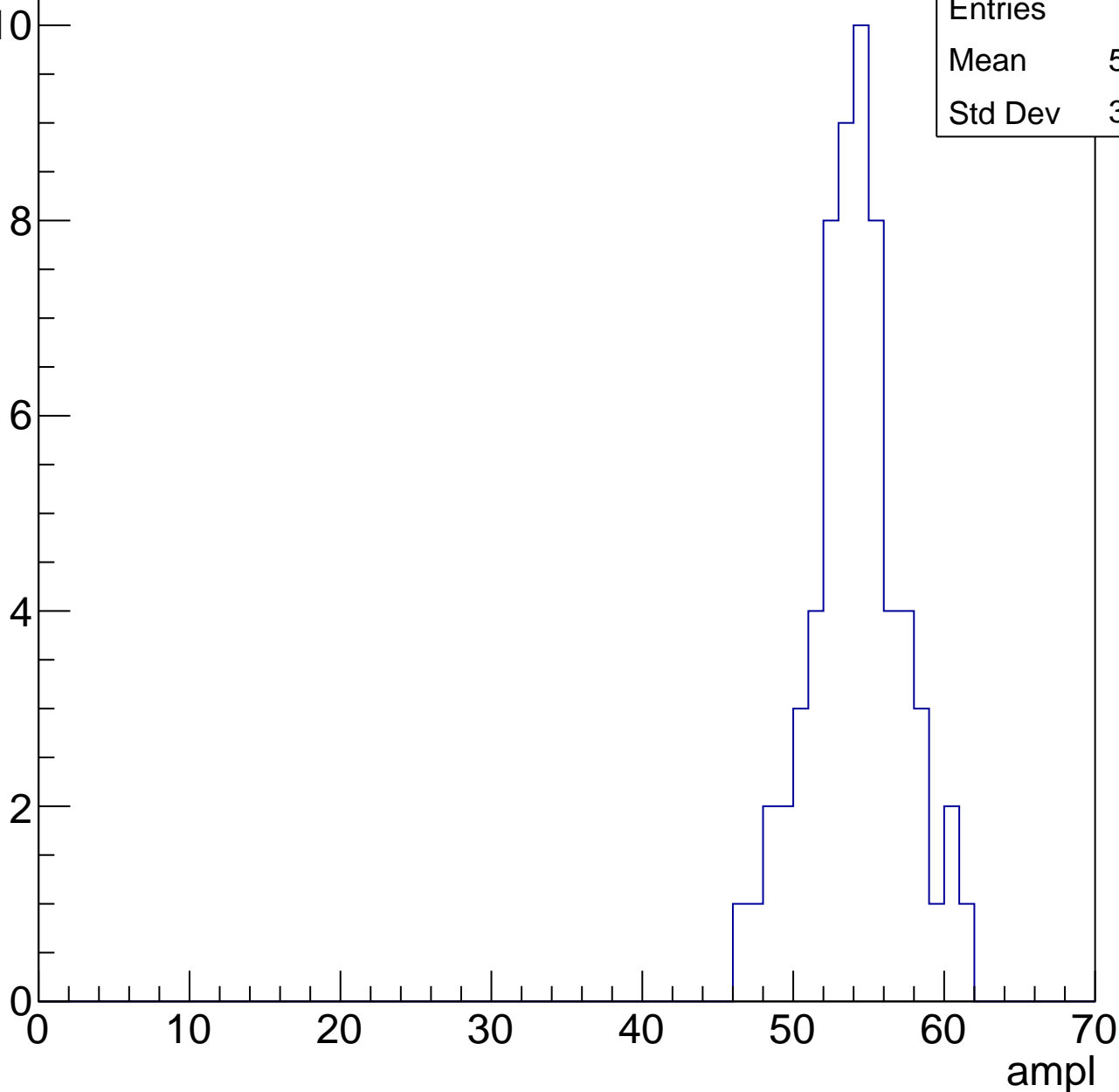


# B1L002S, U3-ch47, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	53.65
Std Dev	3.102



# B1L002S, U3-ch47, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	54
Mean	58.19
Std Dev	8.325

ampl

0

10

20

30

40

50

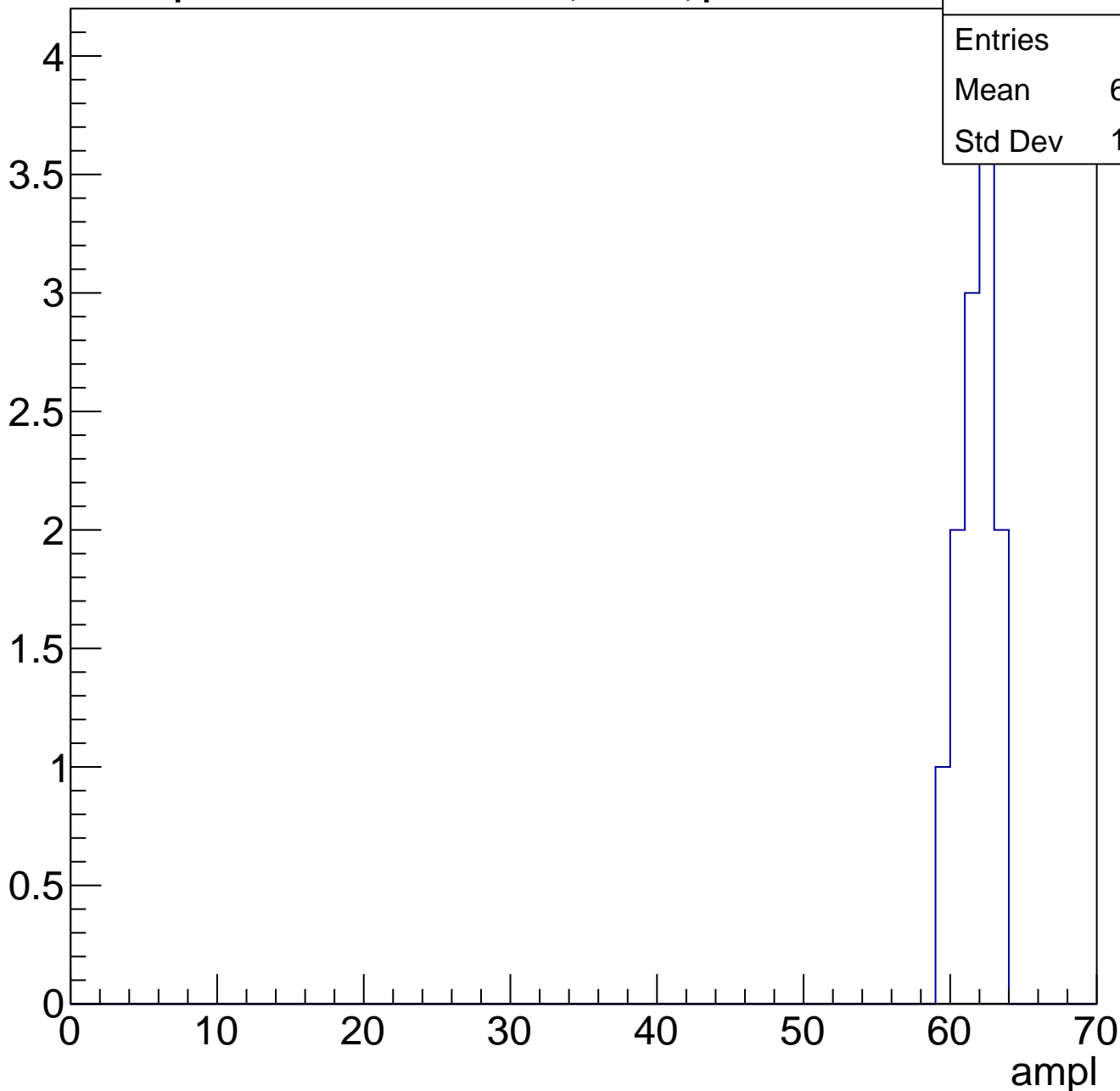
60

70

# B1L002S, U3-ch47, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

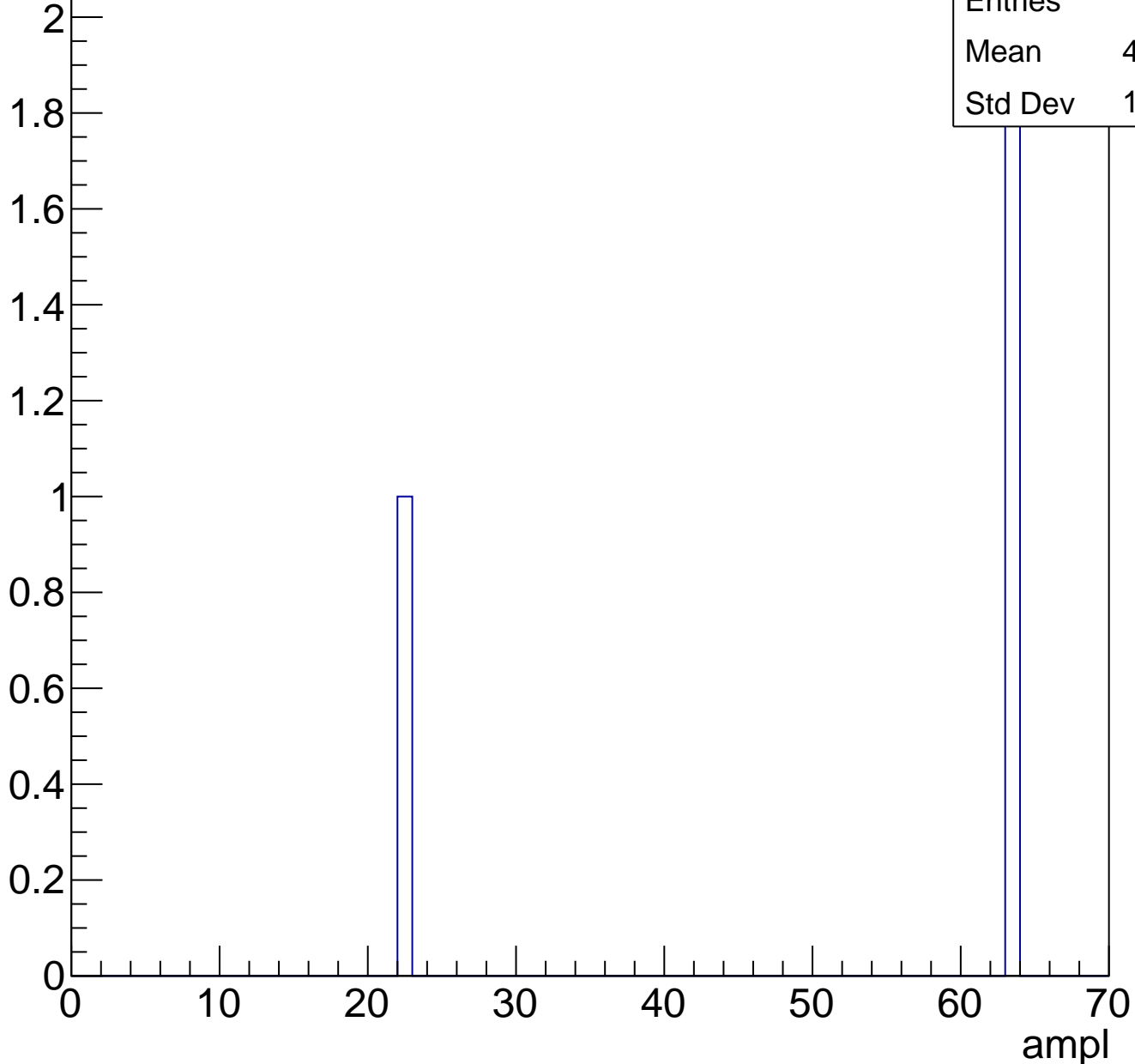




# B1L002S, U3-ch47, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	49.33
Std Dev	19.33

# B1L002S, U3-ch48, adc0

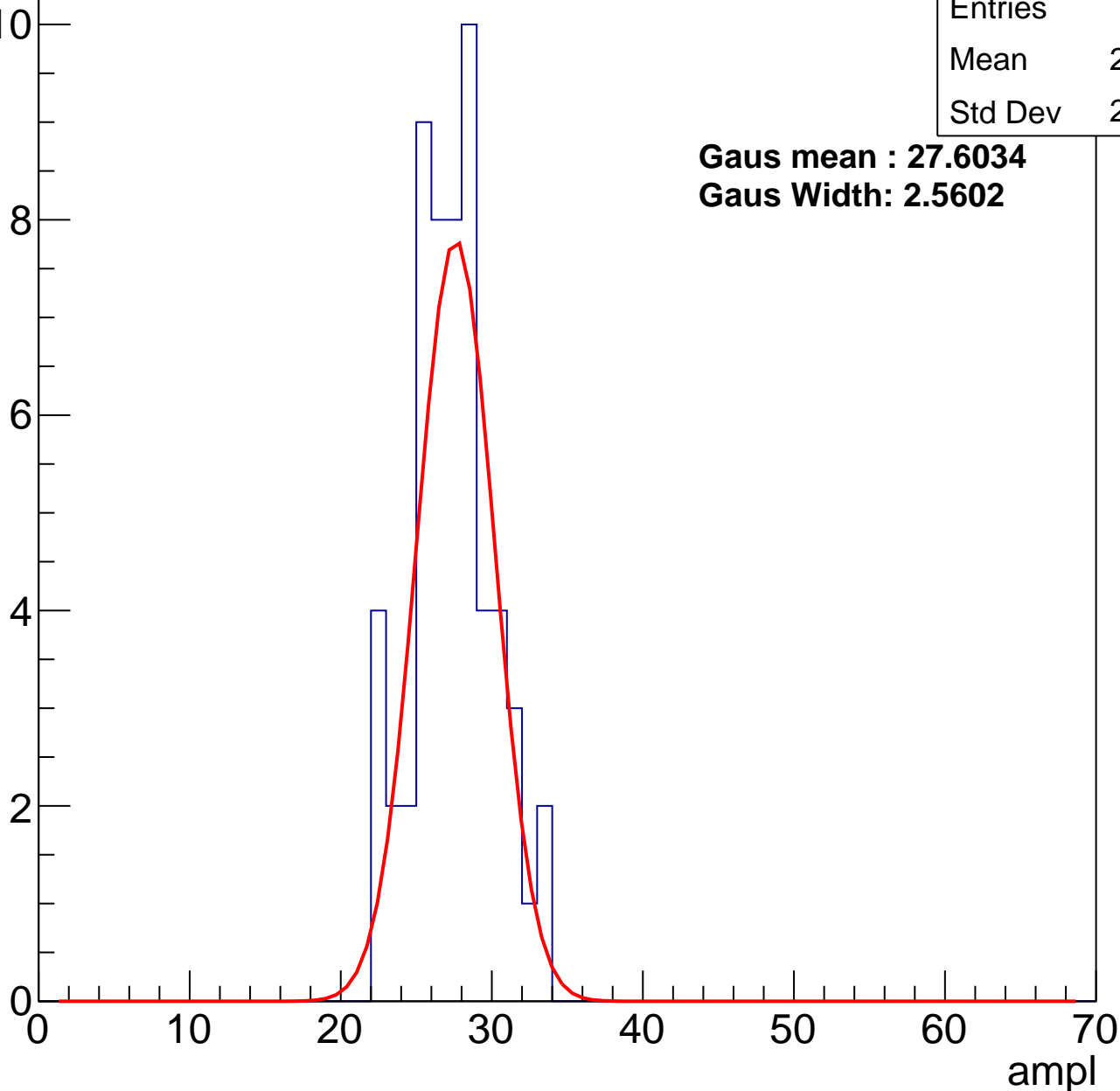
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	26.98
Std Dev	2.652

**Gaus mean : 27.6034**

**Gaus Width: 2.5602**



# B1L002S, U3-ch48, adc1

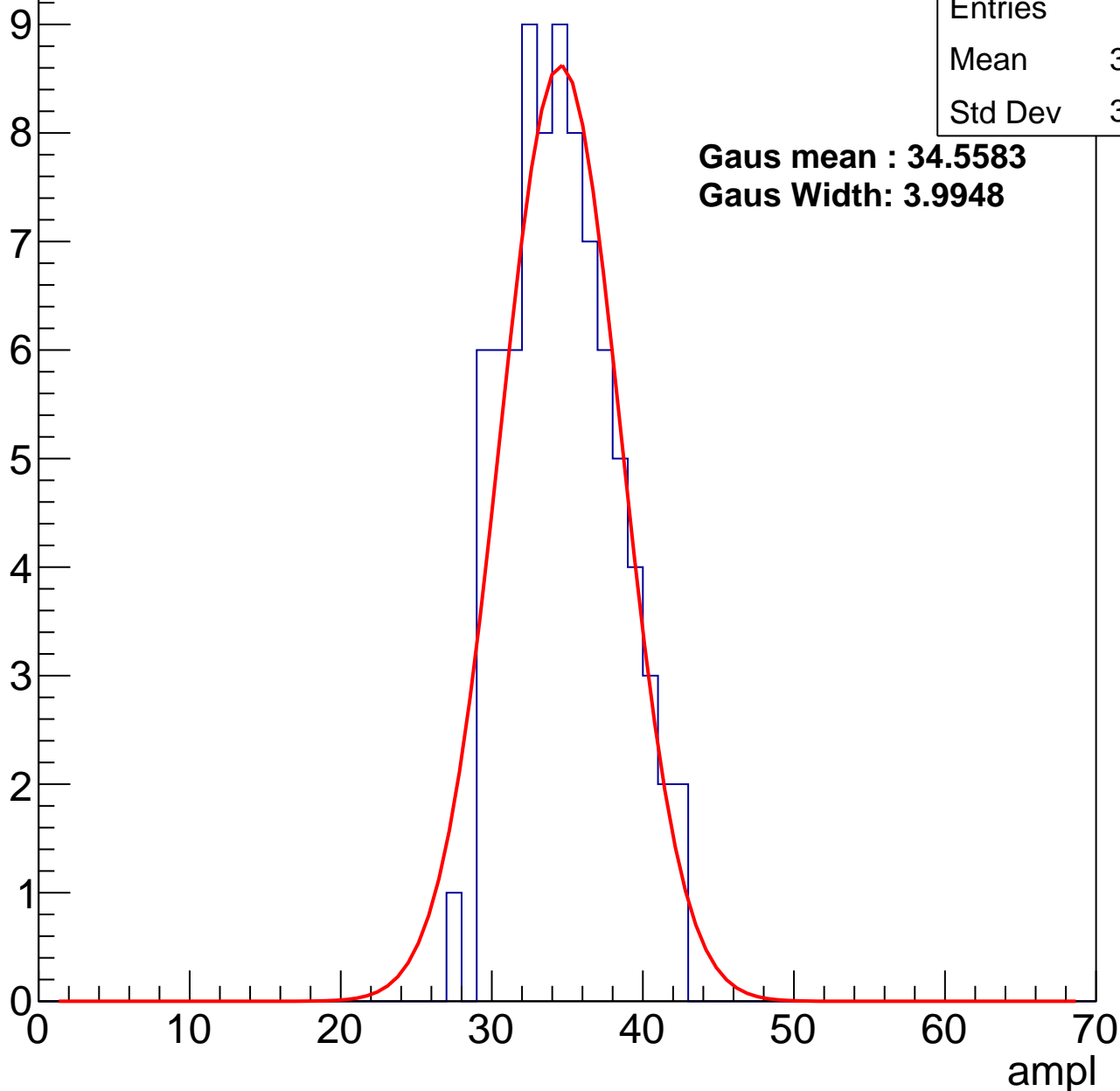
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	34.28
Std Dev	3.476

**Gaus mean : 34.5583**

**Gaus Width: 3.9948**



# B1L002S, U3-ch48, adc2

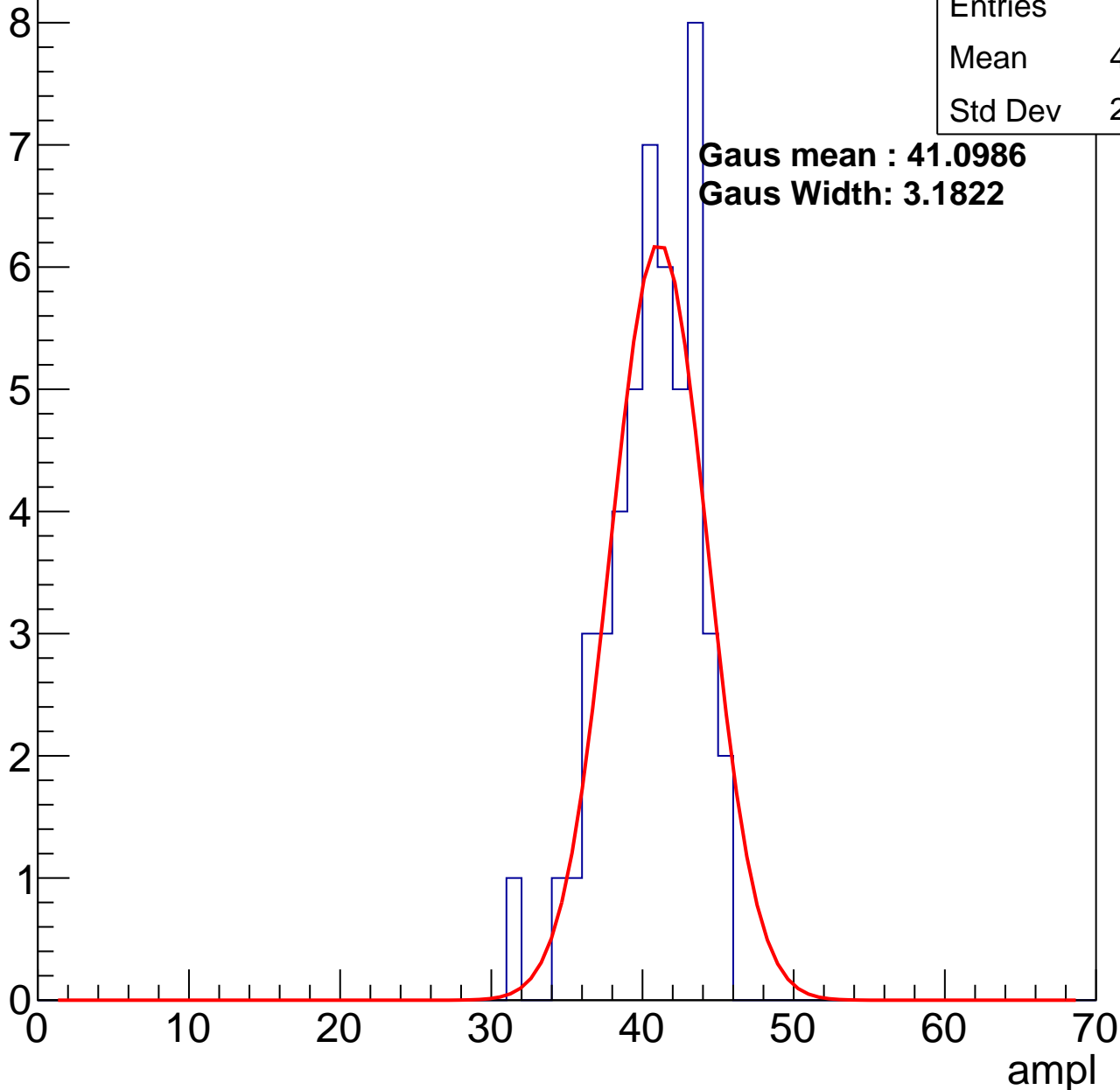
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	40.16
Std Dev	2.972

**Gaus mean : 41.0986**

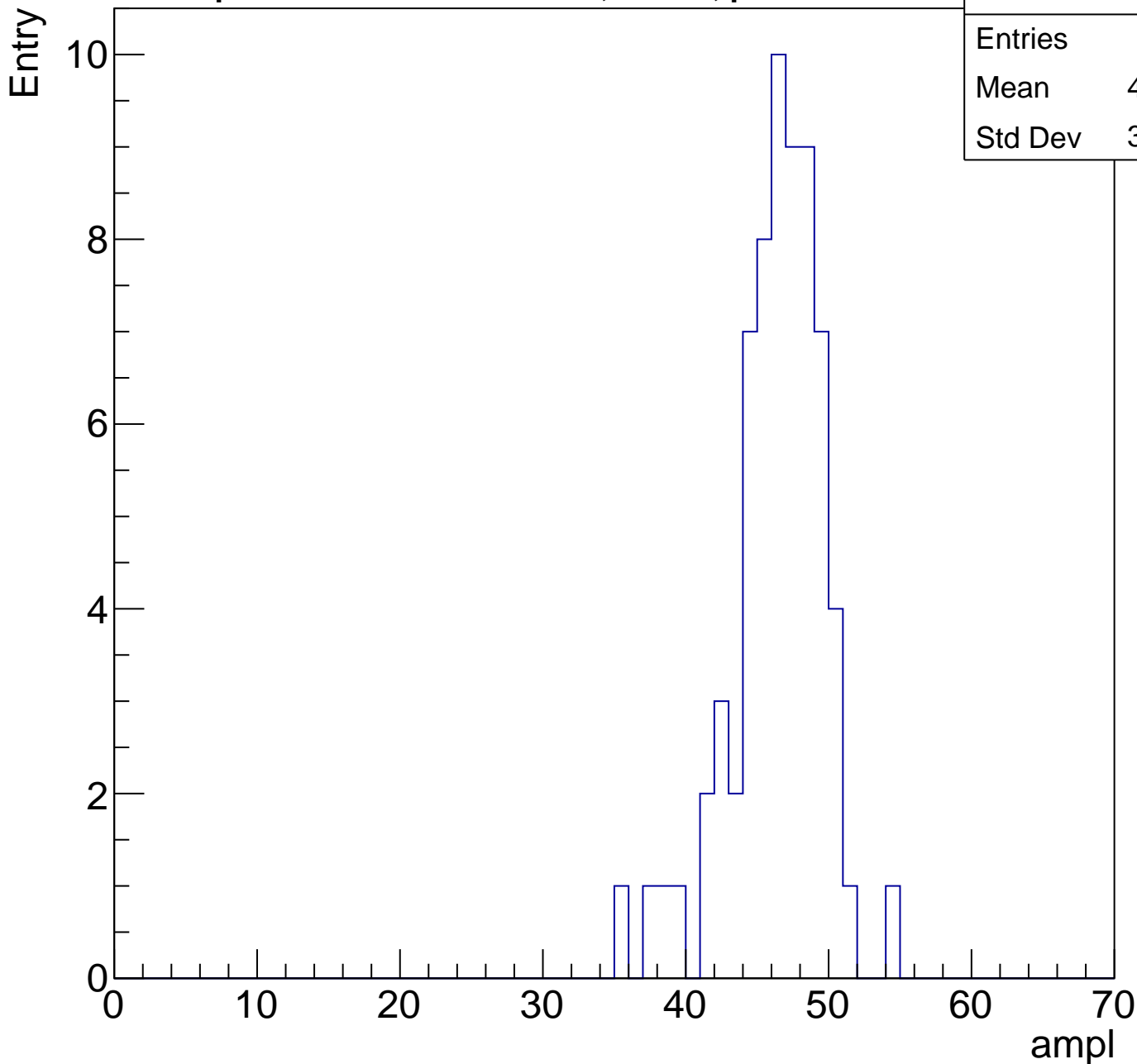
**Gaus Width: 3.1822**



# B1L002S, U3-ch48, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

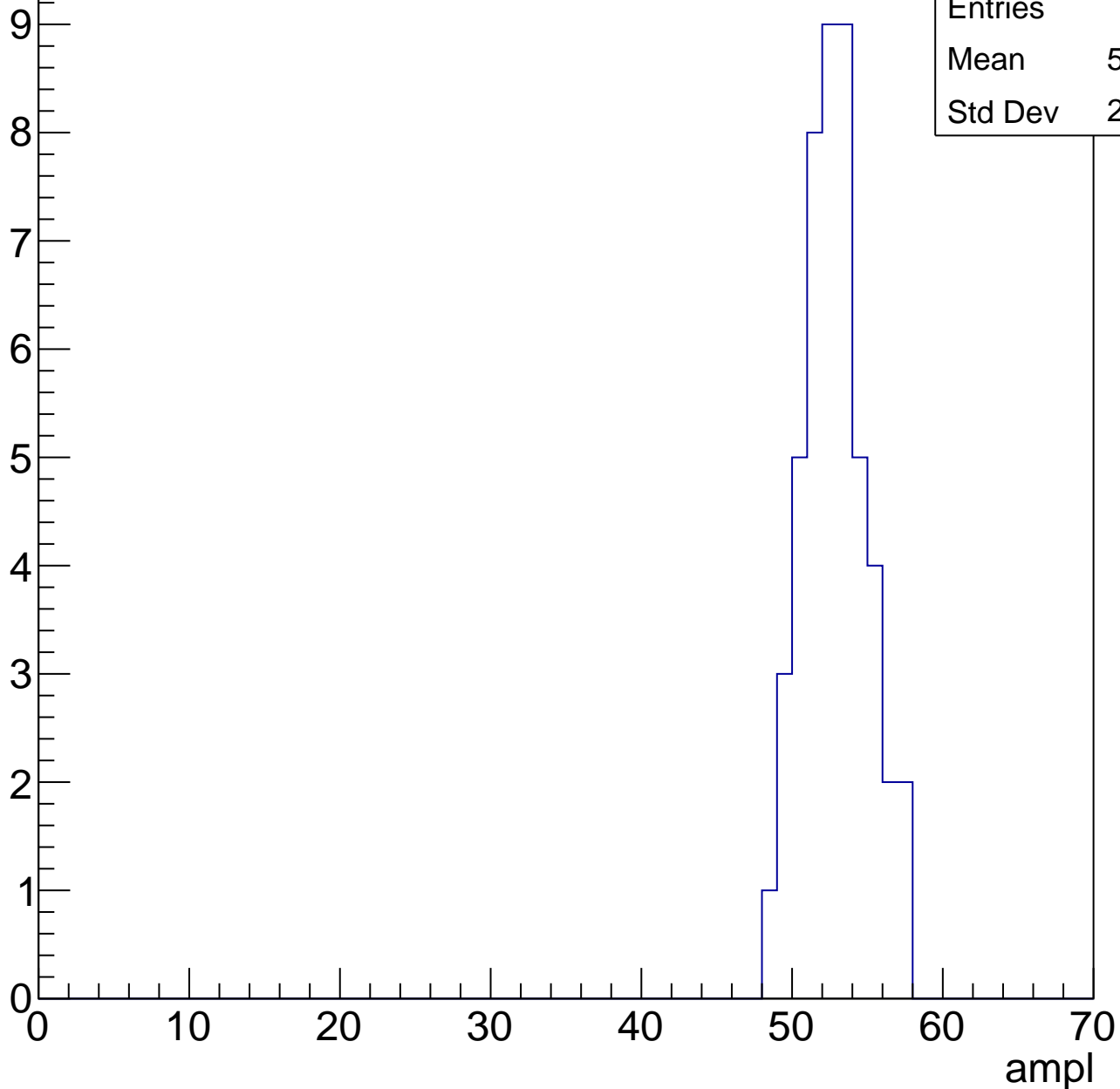
Entries	67
Mean	45.88
Std Dev	3.294



# B1L002S, U3-ch48, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



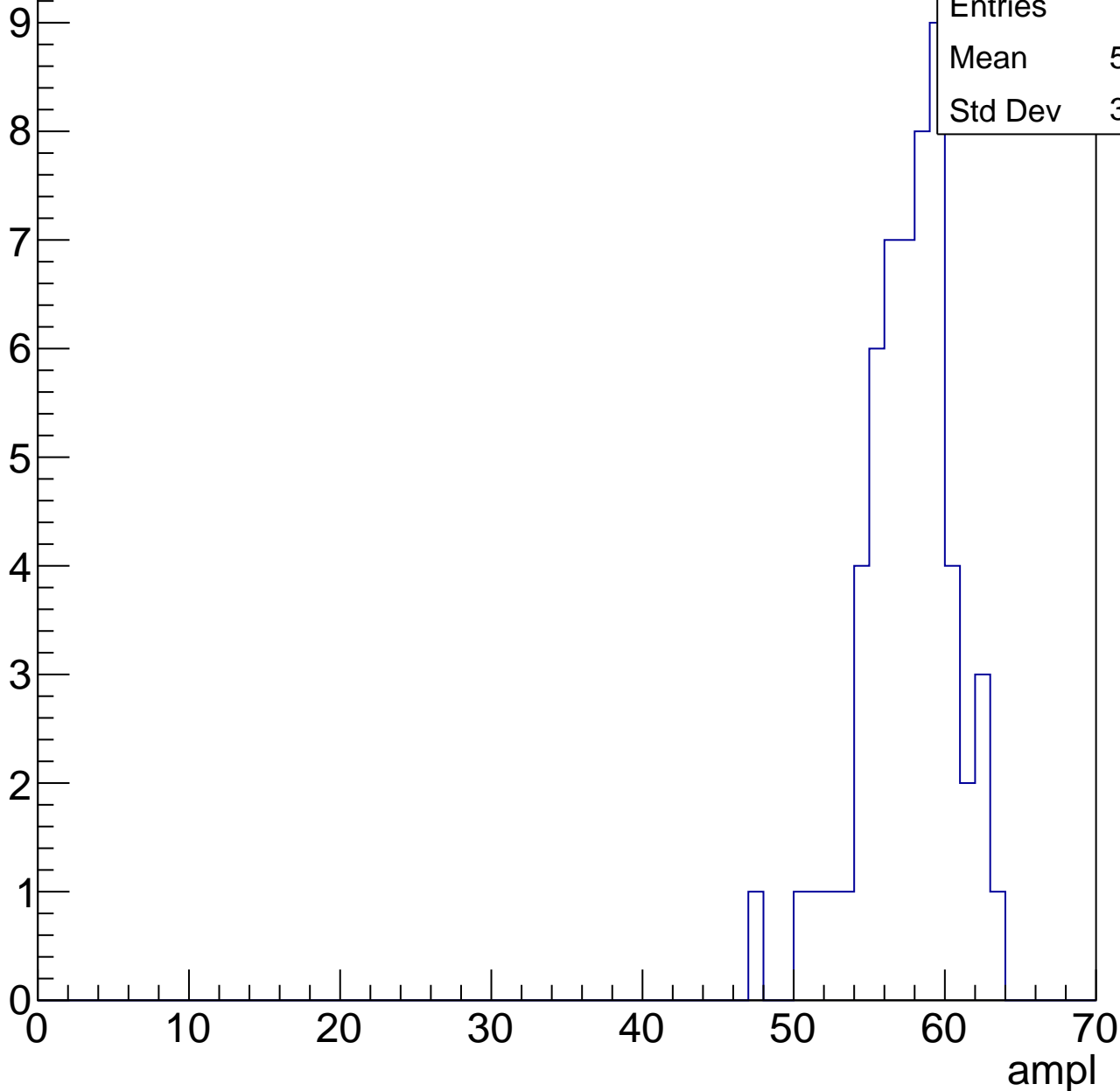
Entries	48
Mean	52.38
Std Dev	2.098

# B1L002S, U3-ch48, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

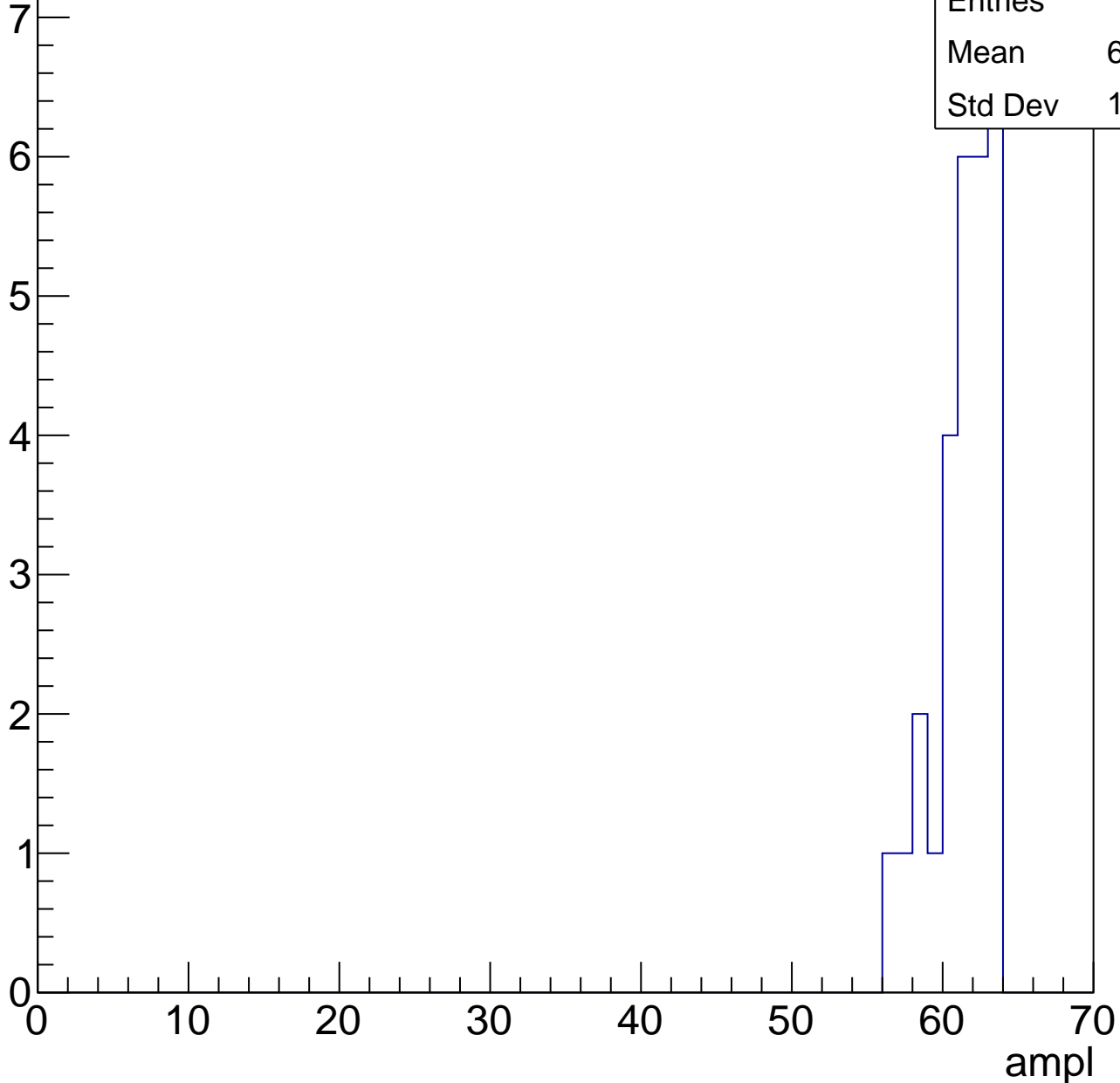
Entries	56
Mean	57.07
Std Dev	3.035



# B1L002S, U3-ch48, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

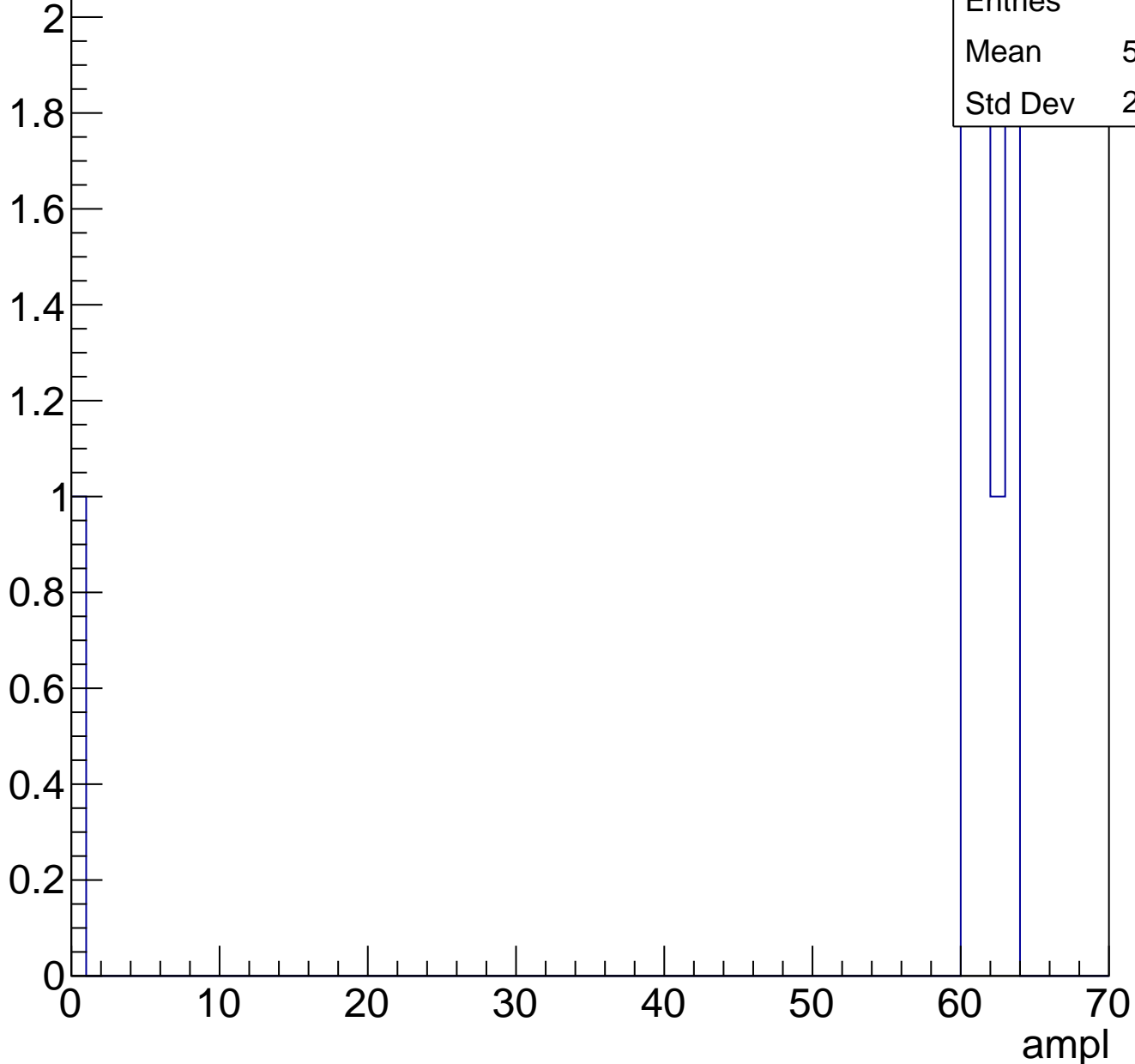




# B1L002S, U3-ch48, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	8
Mean	53.75
Std Dev	20.35

# B1L002S, U3-ch49, adc0

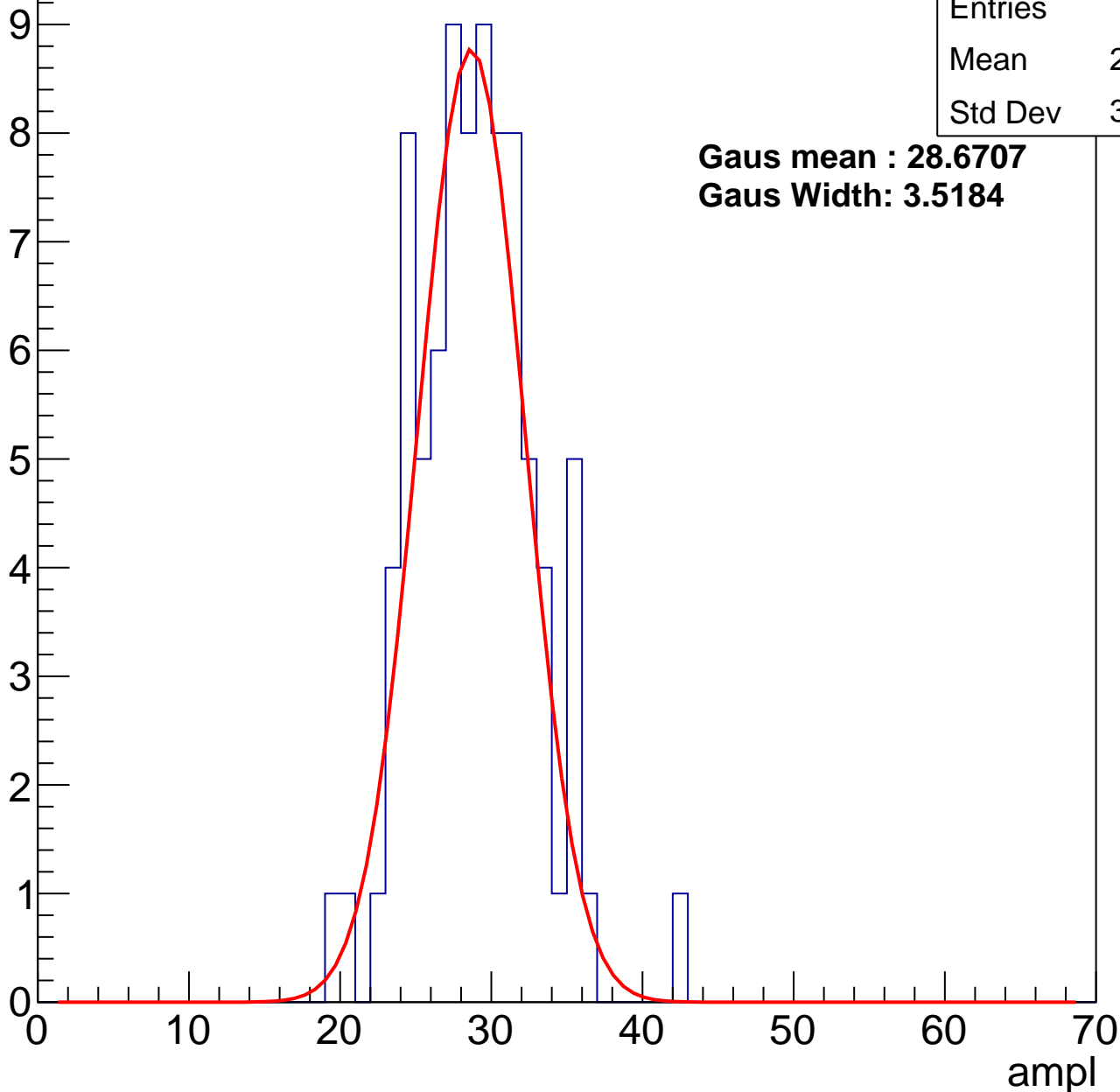
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	85
Mean	28.48
Std Dev	3.913

**Gaus mean : 28.6707**

**Gaus Width: 3.5184**



# B1L002S, U3-ch49, adc1

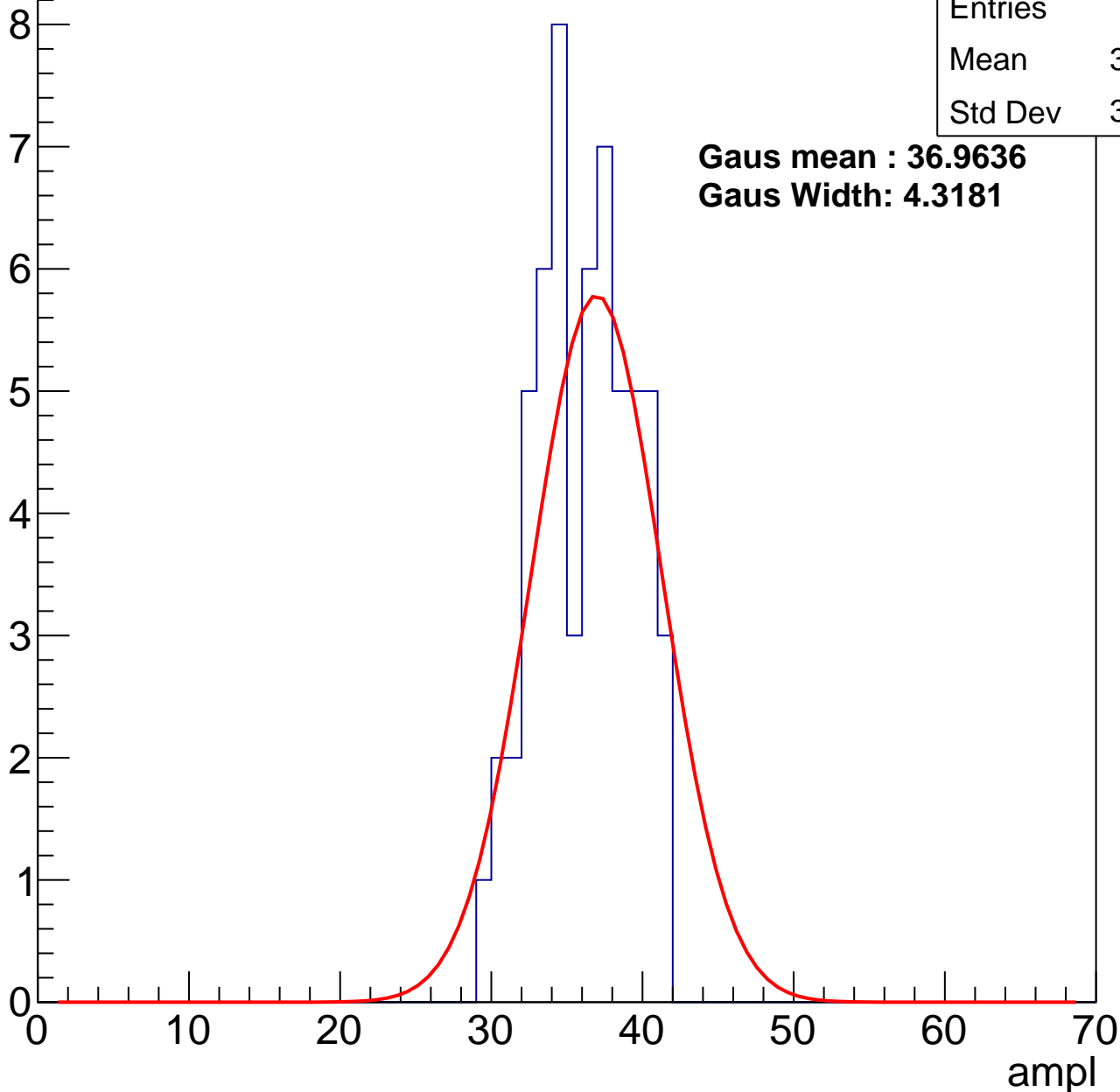
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.67
Std Dev	3.109

**Gaus mean : 36.9636**

**Gaus Width: 4.3181**

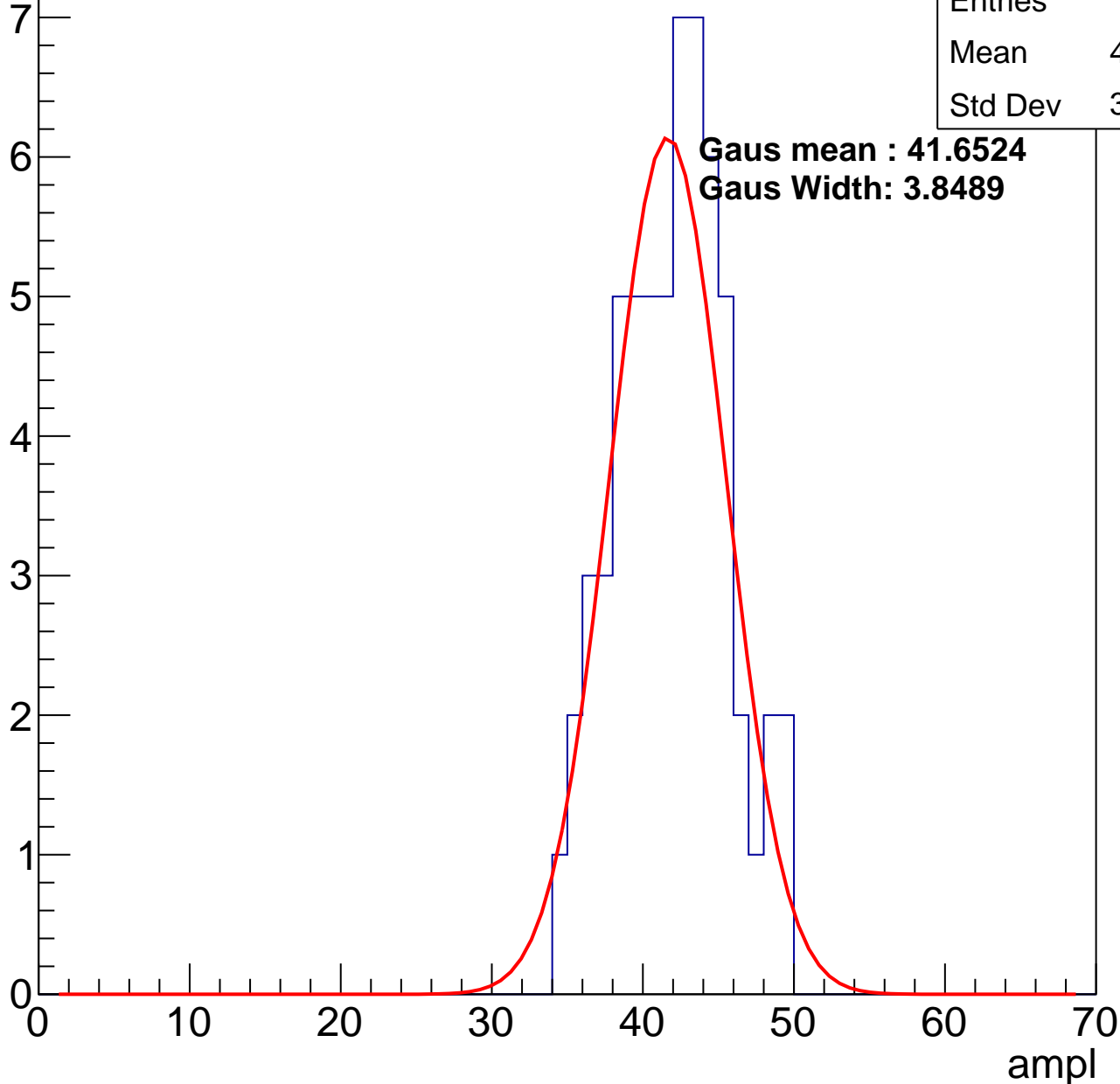


# B1L002S, U3-ch49, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

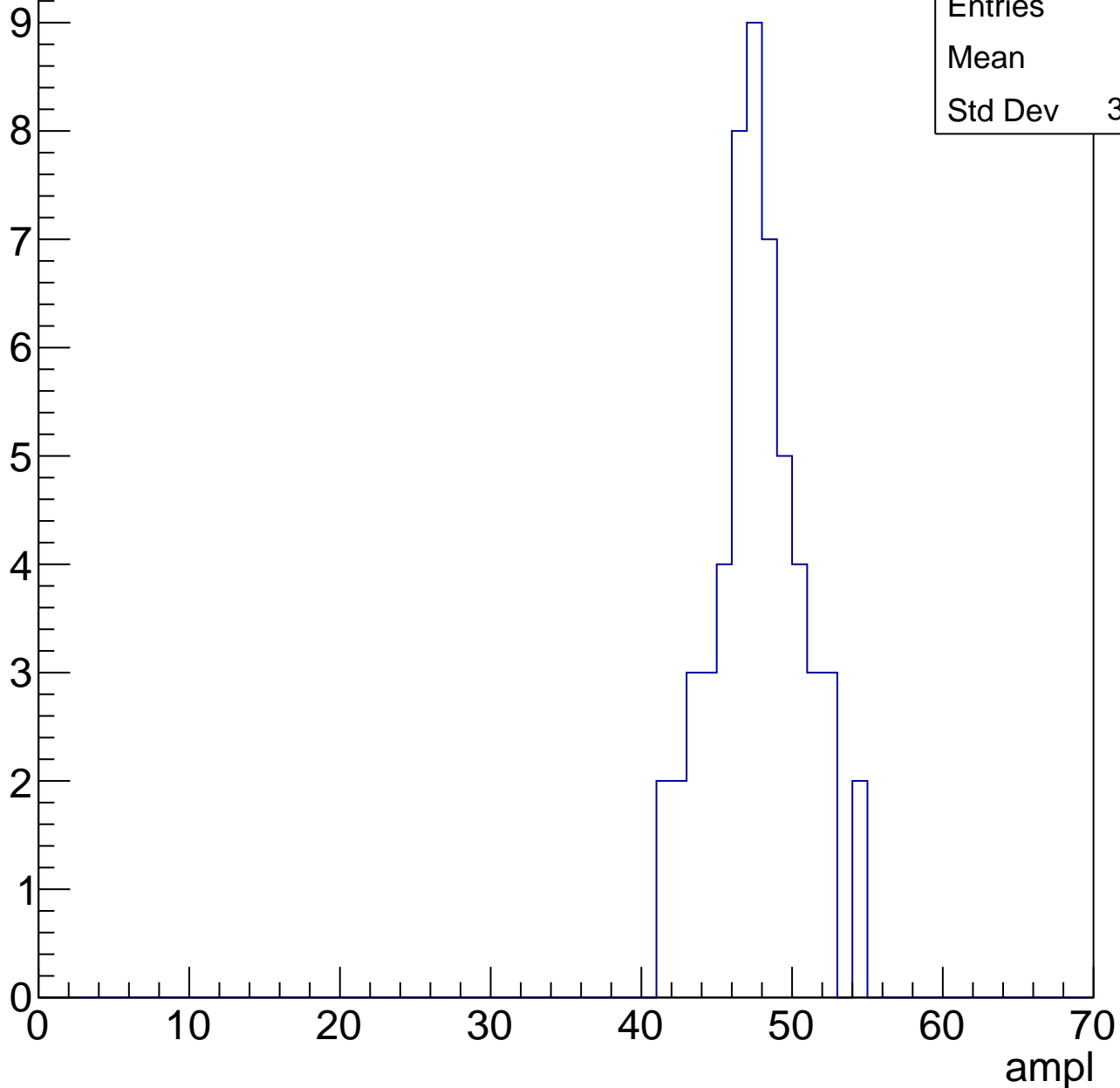
Entries	61
Mean	41.48
Std Dev	3.579



# B1L002S, U3-ch49, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



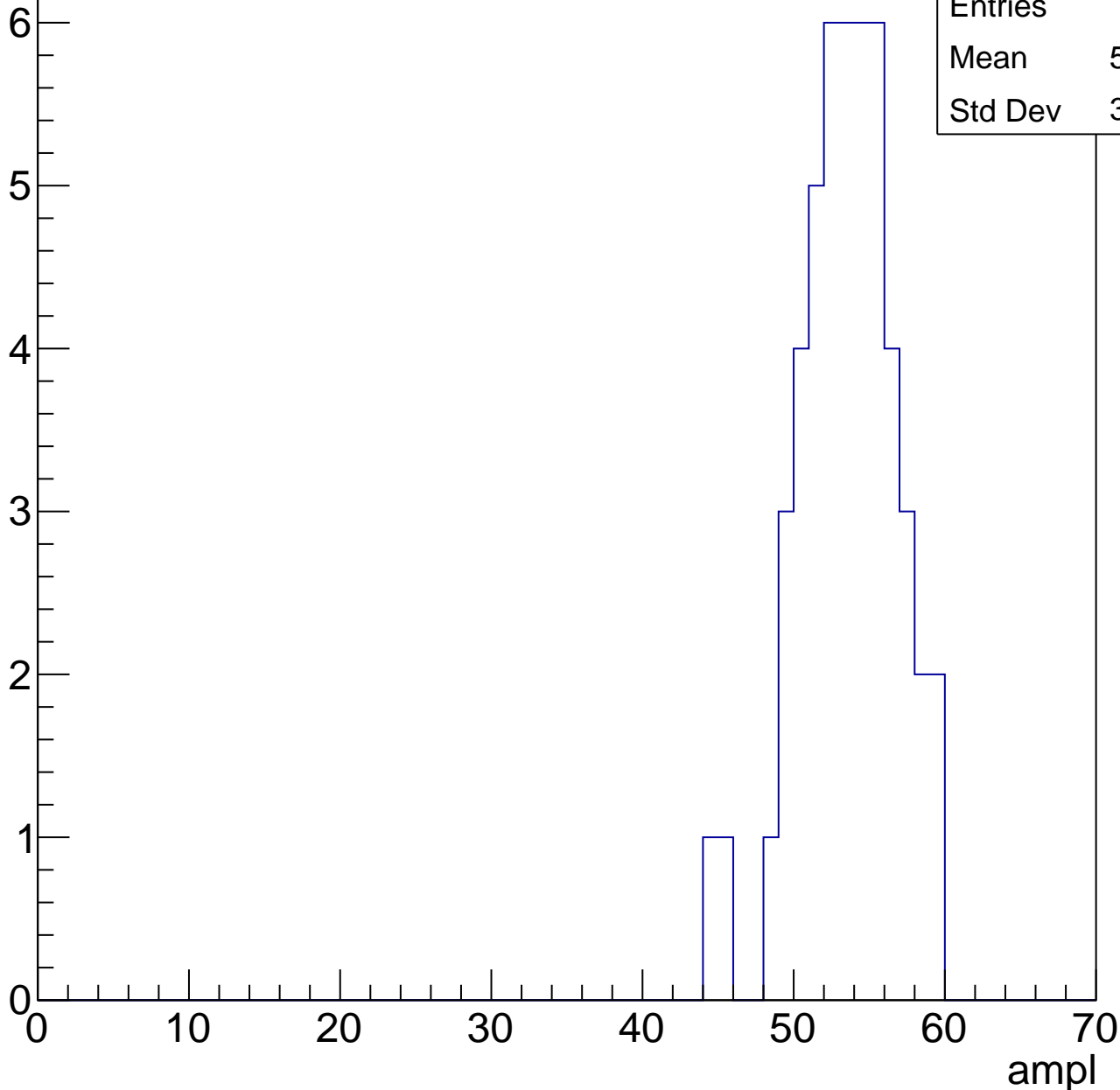
Entries	55
Mean	47.2
Std Dev	3.024

# B1L002S, U3-ch49, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	53.04
Std Dev	3.212

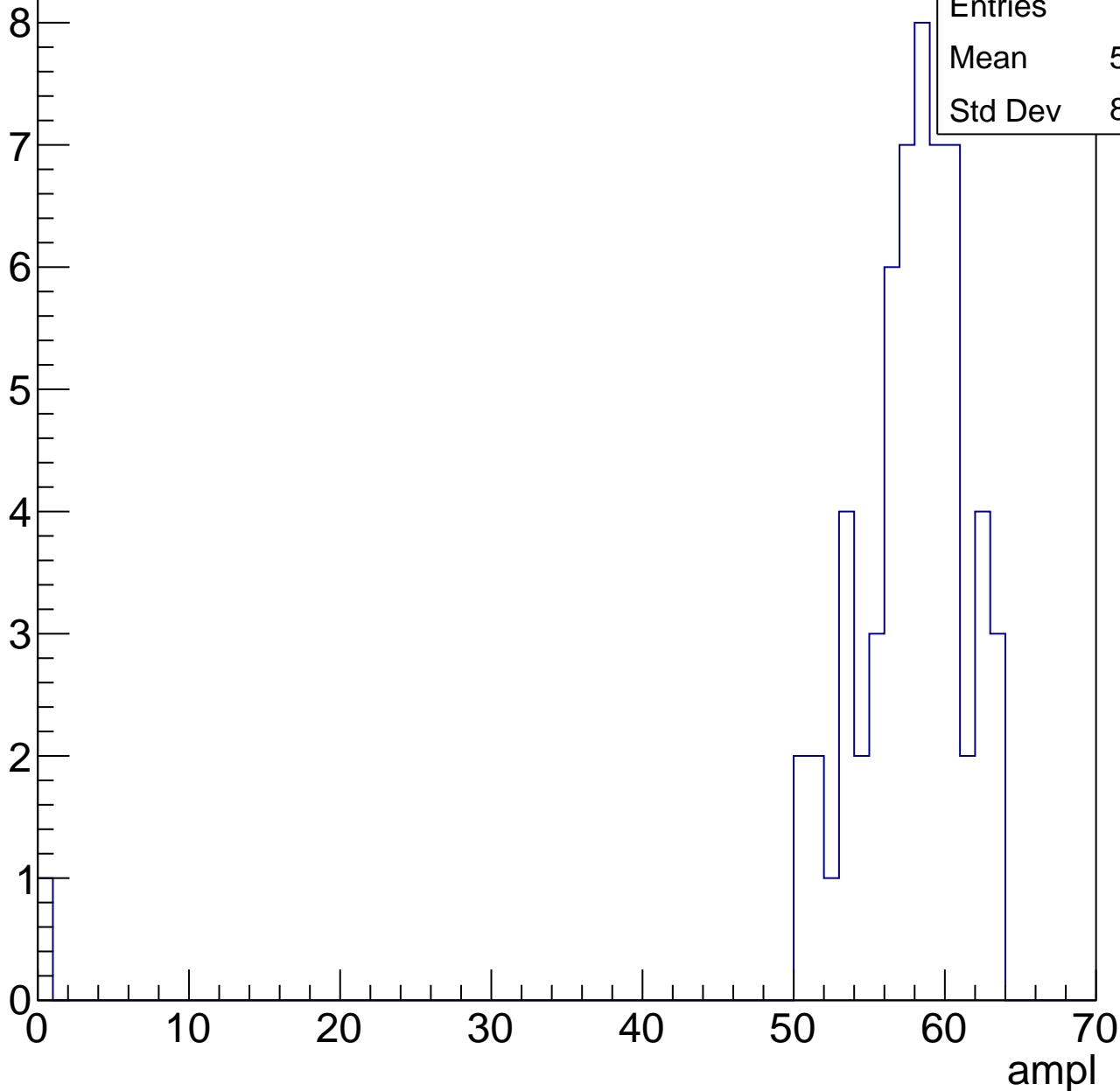


# B1L002S, U3-ch49, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	56.44
Std Dev	8.093

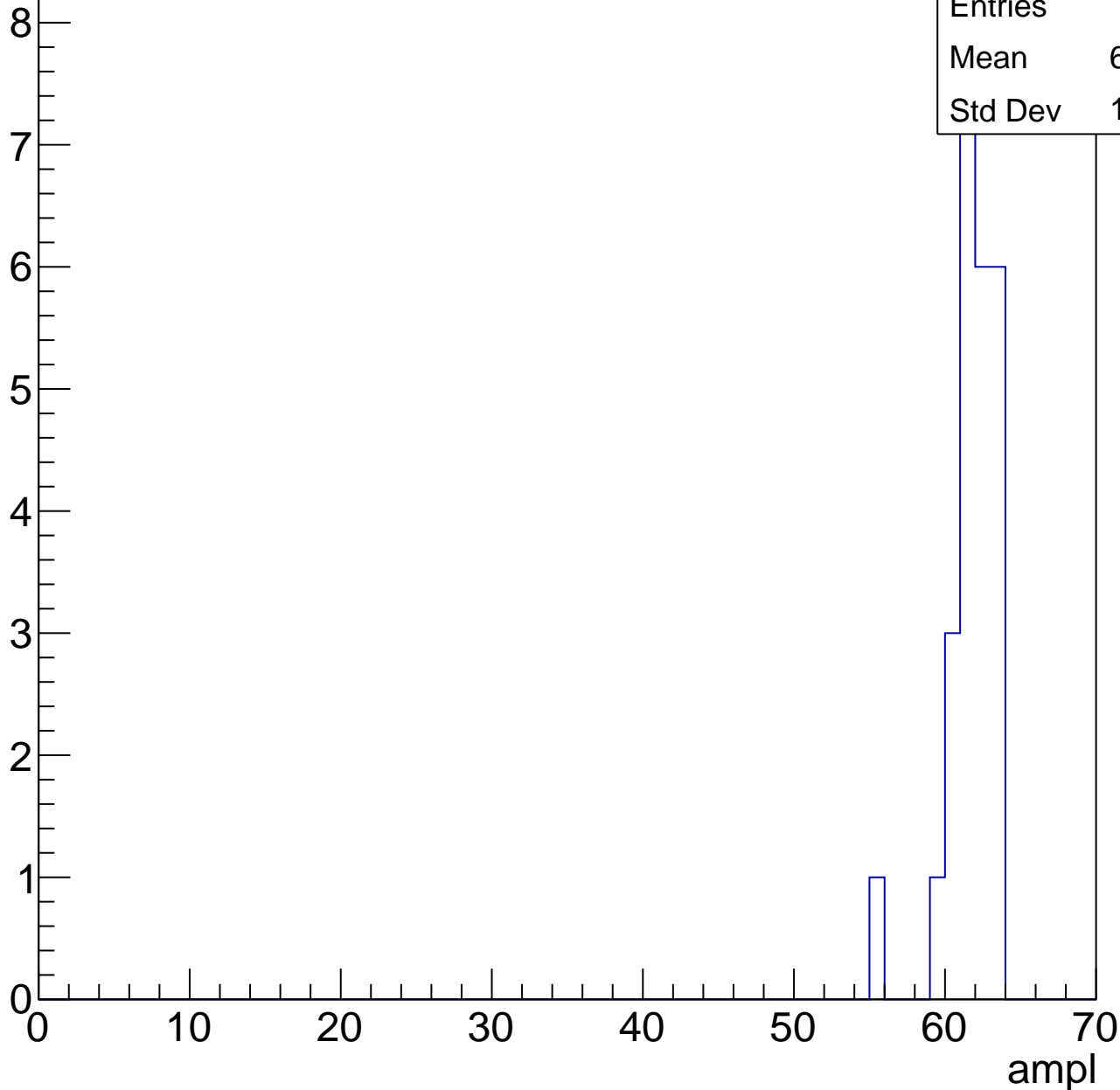


# B1L002S, U3-ch49, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.28
Std Dev	1.686





# B1L002S, U3-ch49, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	21
Std Dev	29.7

# B1L002S, U3-ch50, adc0

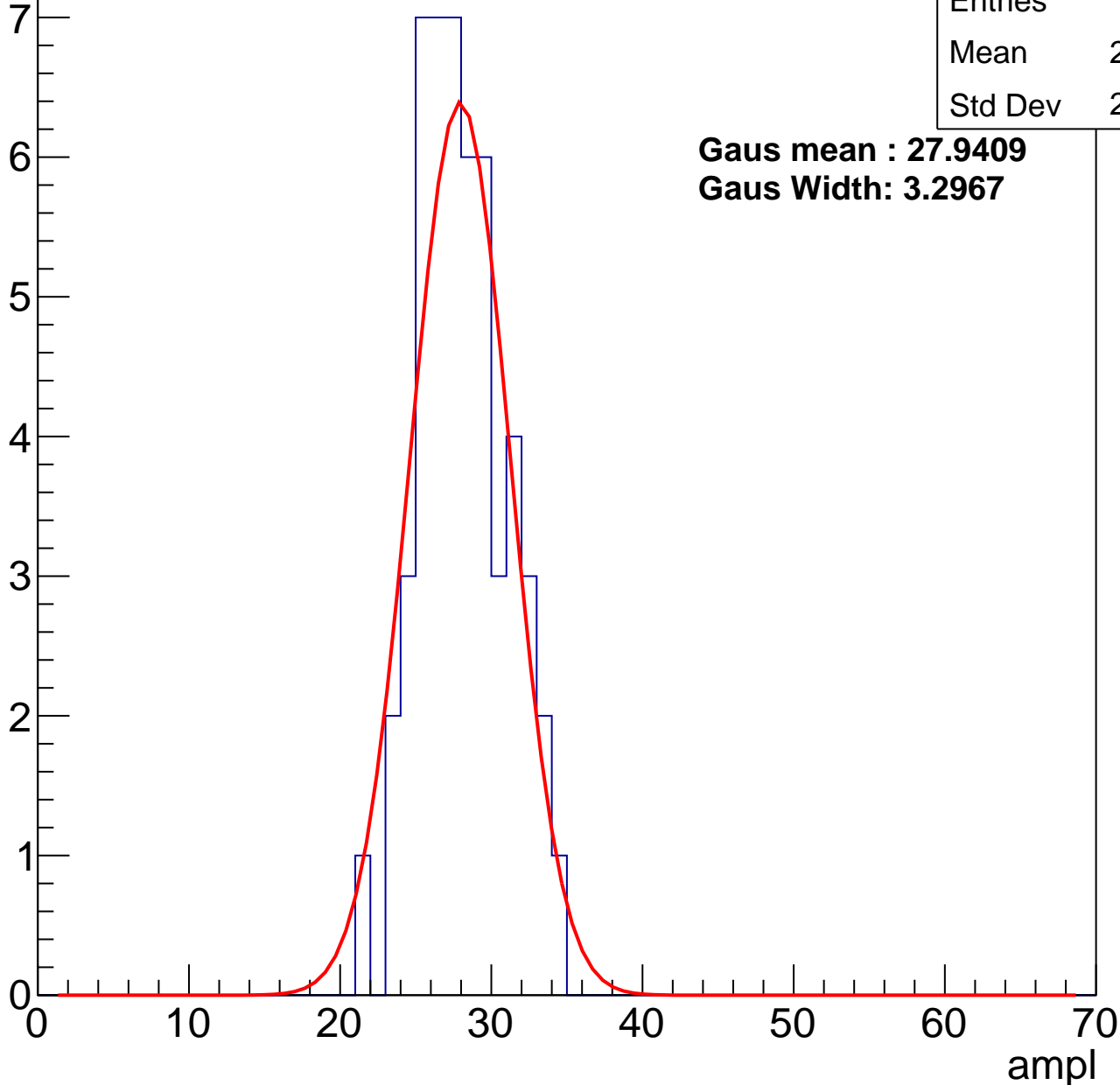
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.63
Std Dev	2.862

**Gaus mean : 27.9409**

**Gaus Width: 3.2967**



# B1L002S, U3-ch50, adc1

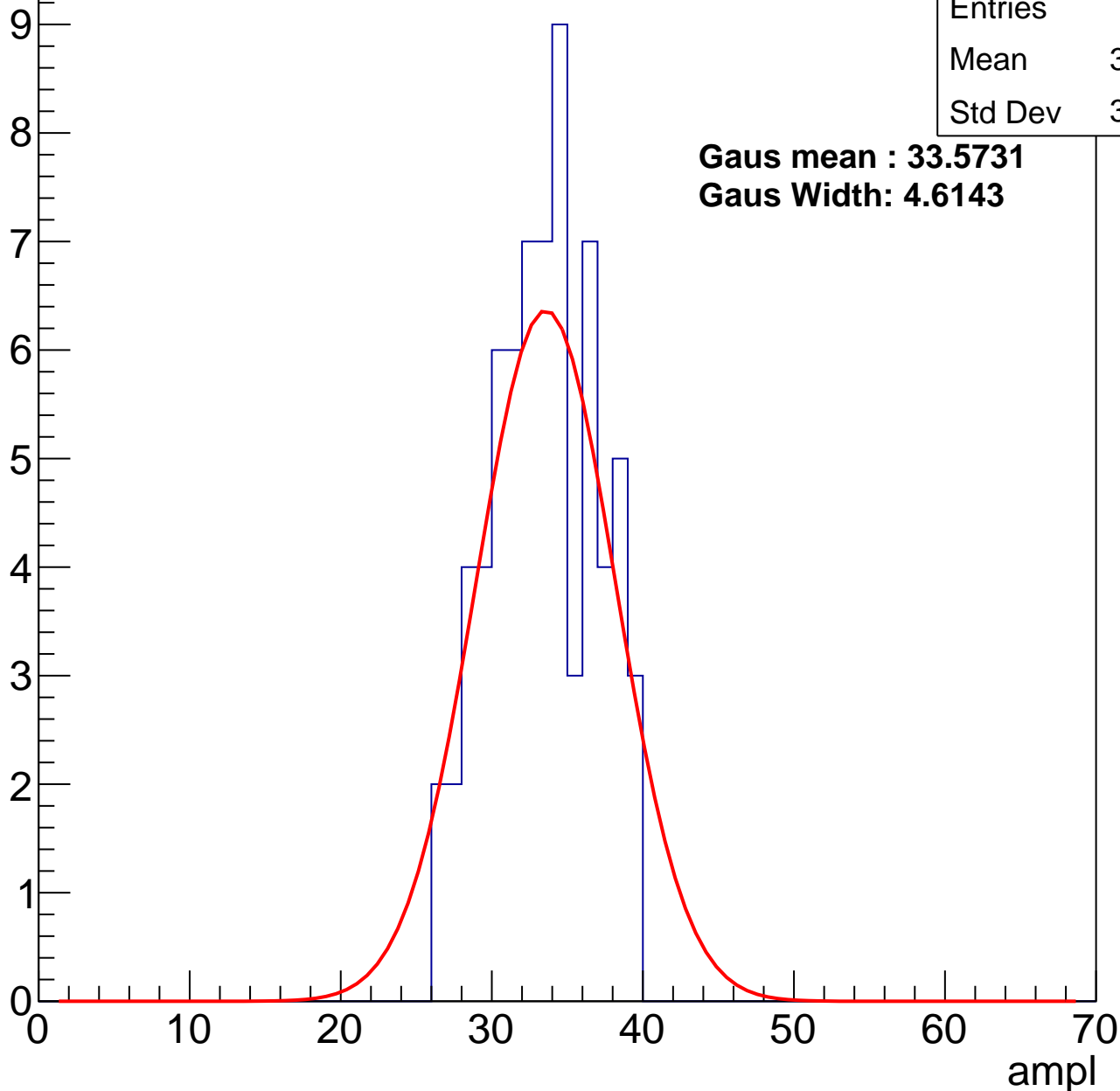
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	32.94
Std Dev	3.405

**Gaus mean : 33.5731**

**Gaus Width: 4.6143**



# B1L002S, U3-ch50, adc2

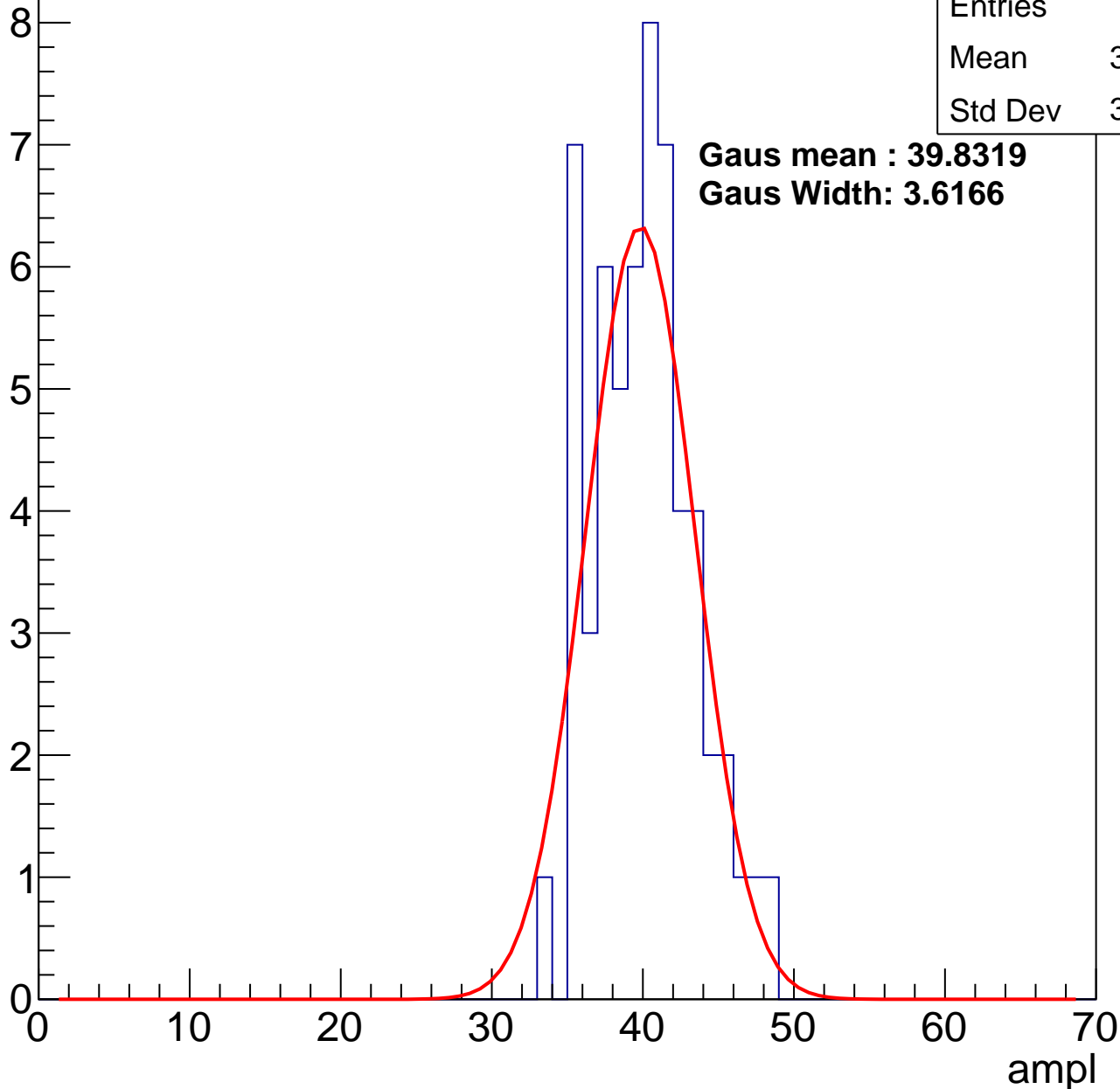
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.62
Std Dev	3.305

**Gaus mean : 39.8319**

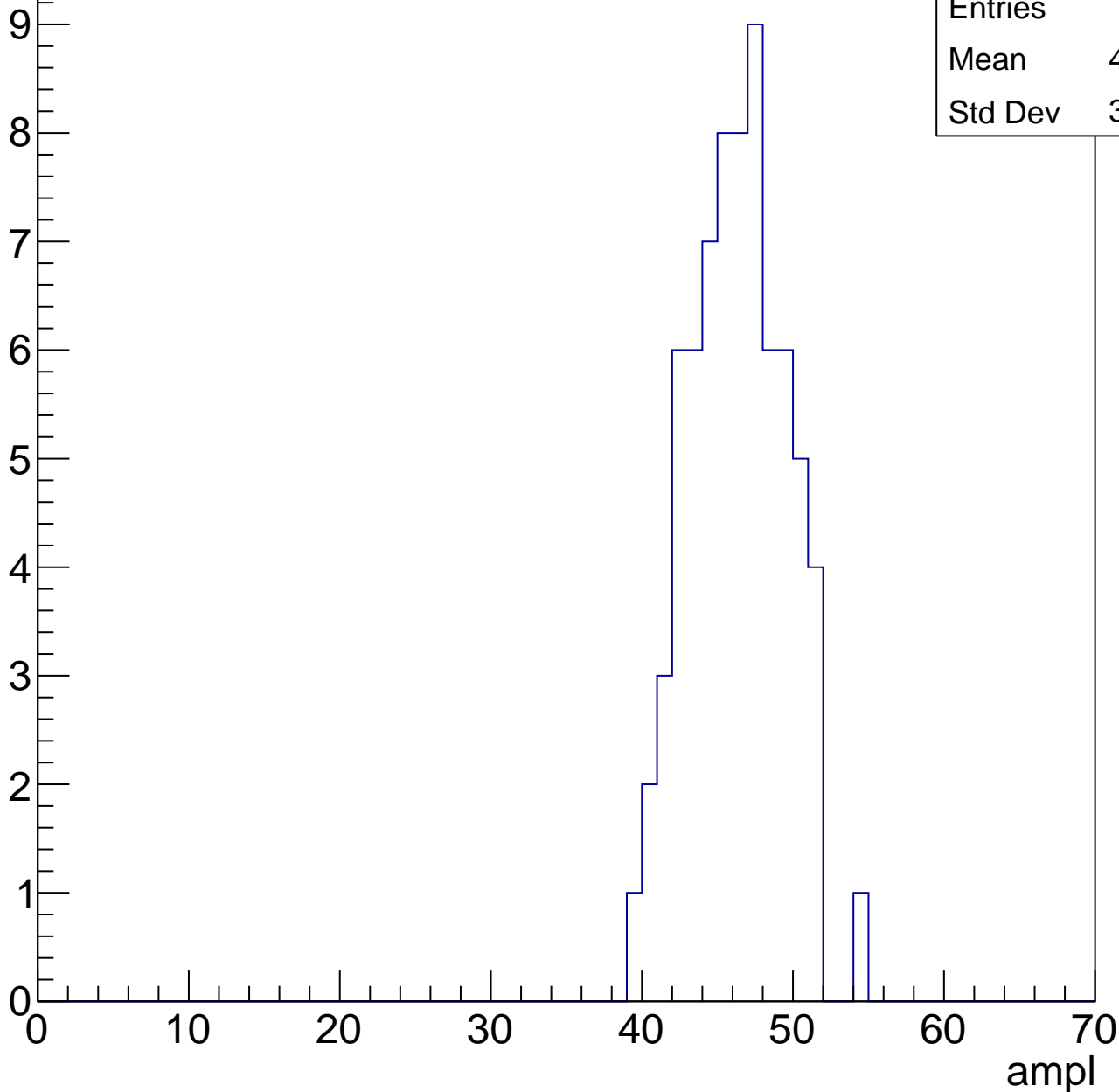
**Gaus Width: 3.6166**



# B1L002S, U3-ch50, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



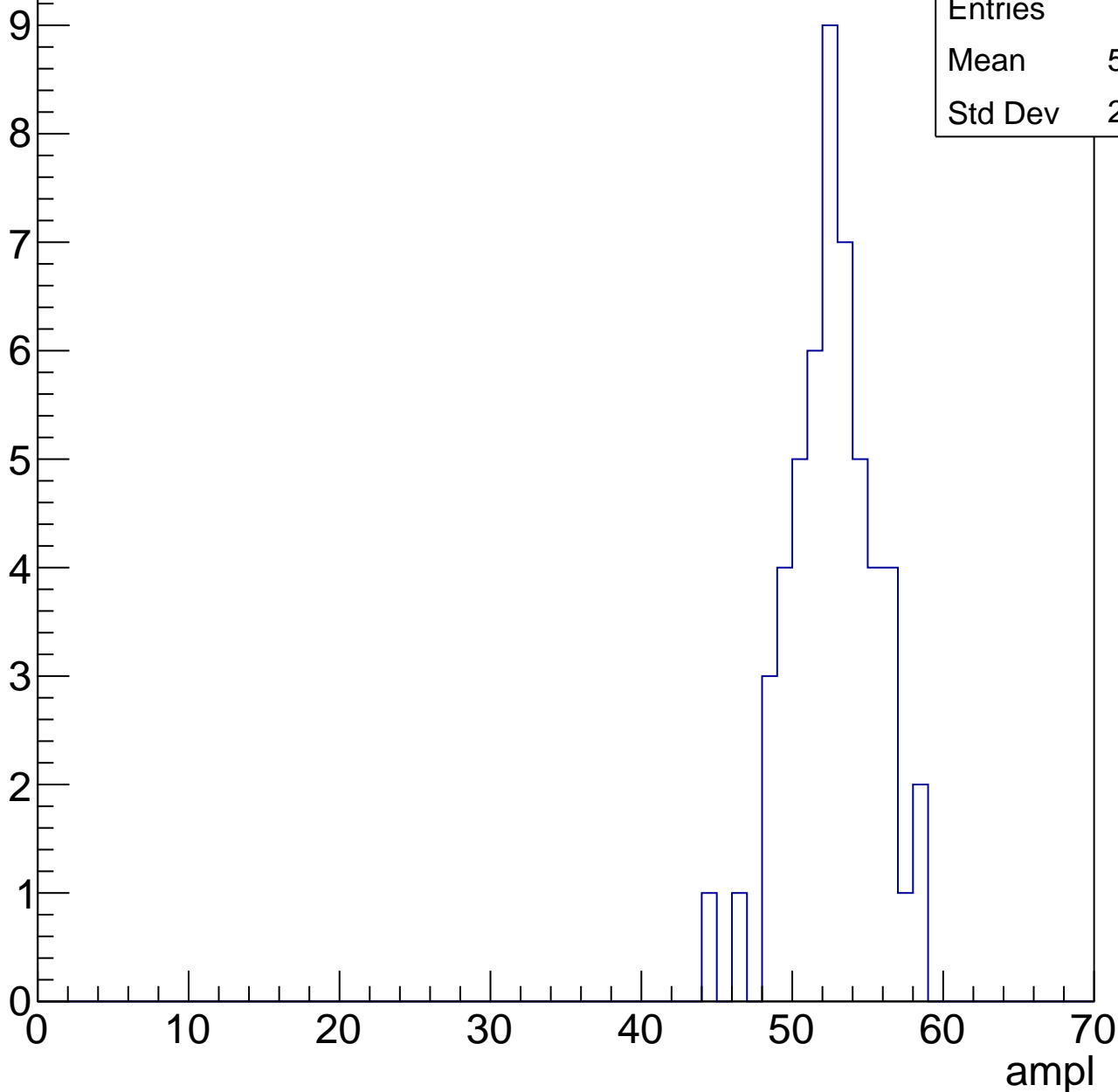
Entries	72
Mean	45.85
Std Dev	3.143

# B1L002S, U3-ch50, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

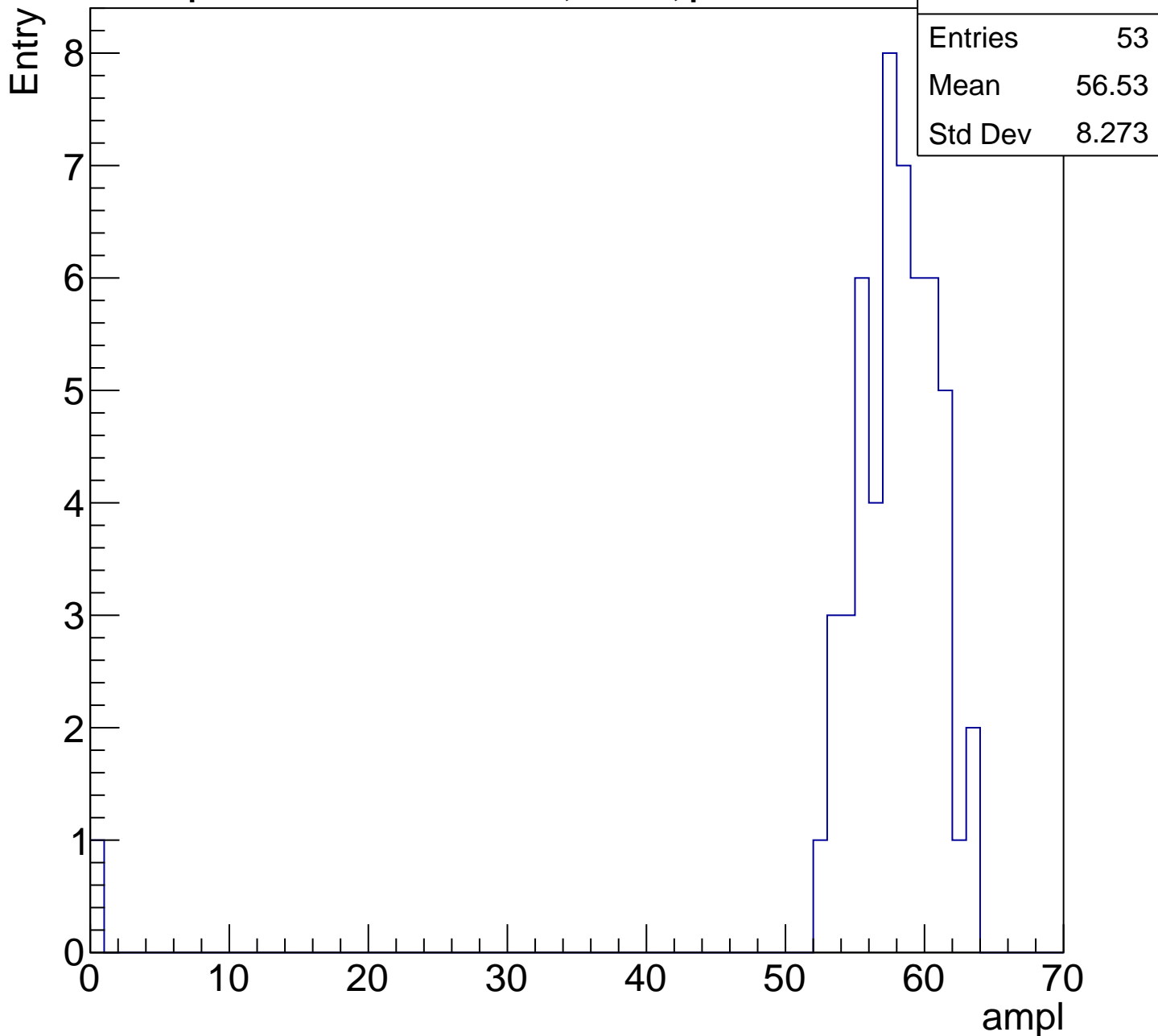
Entry

Entries	52
Mean	52.15
Std Dev	2.885



# B1L002S, U3-ch50, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

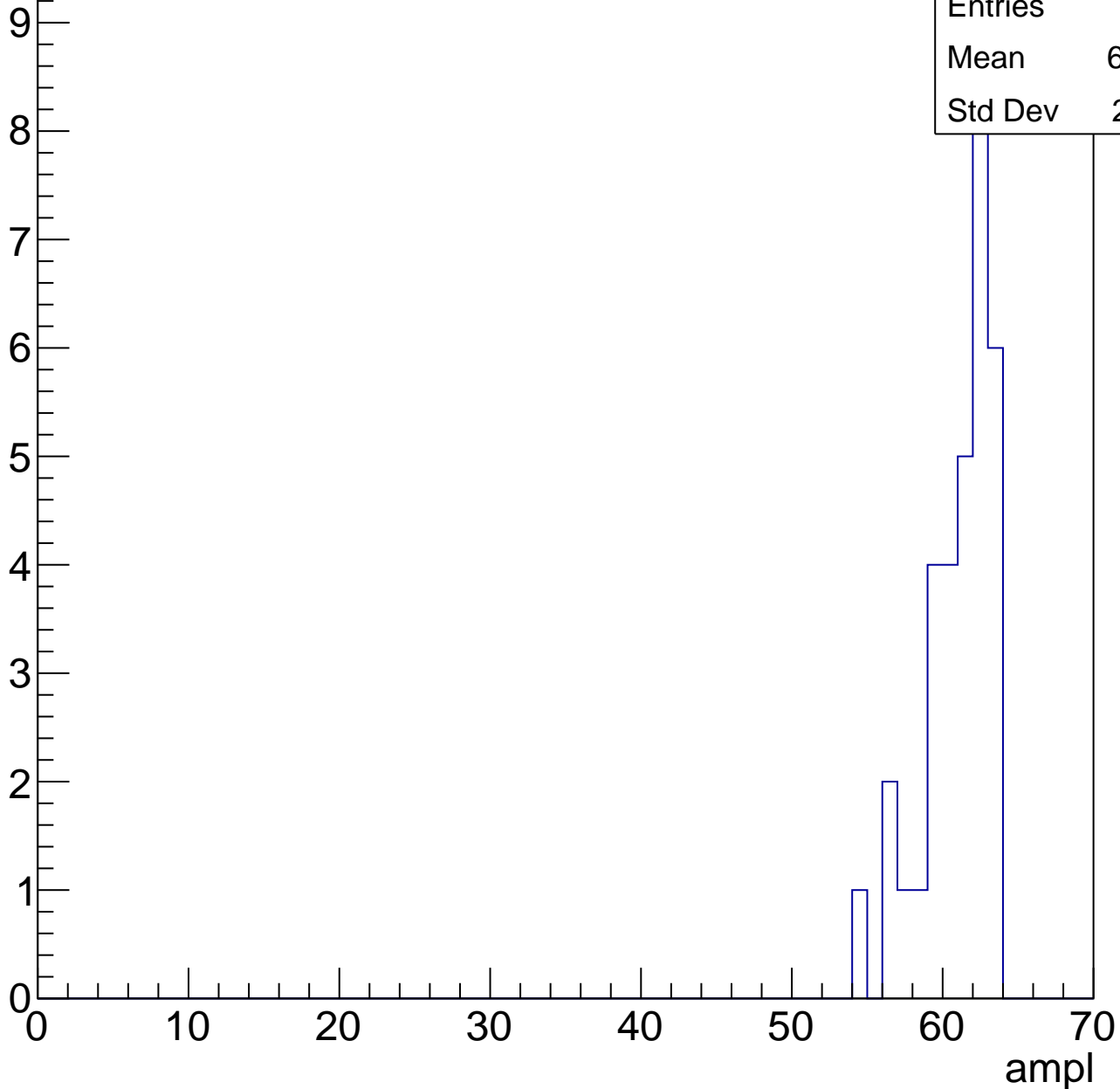


# B1L002S, U3-ch50, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	33
Mean	60.55
Std Dev	2.271

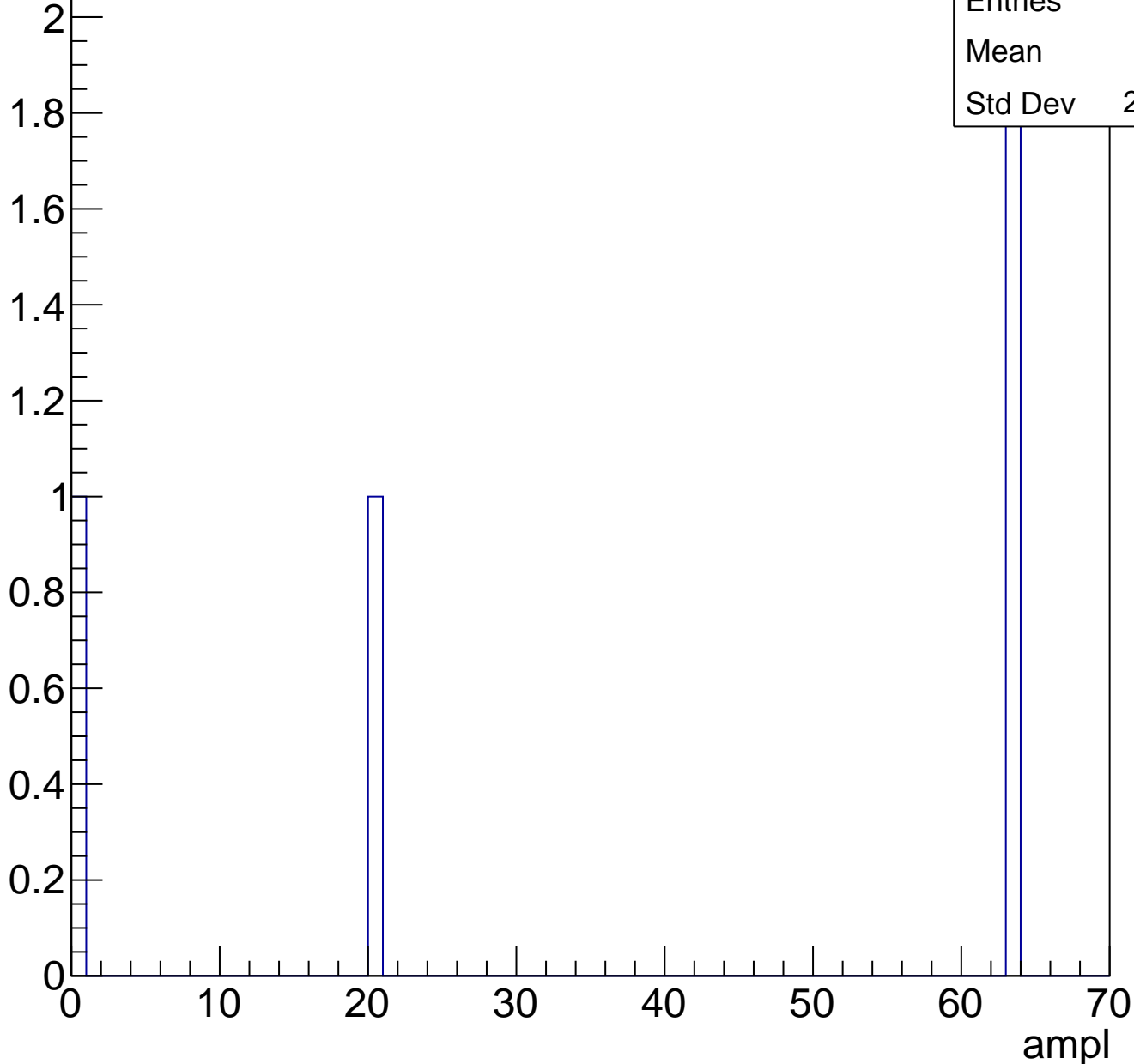




# B1L002S, U3-ch50, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch51, adc0

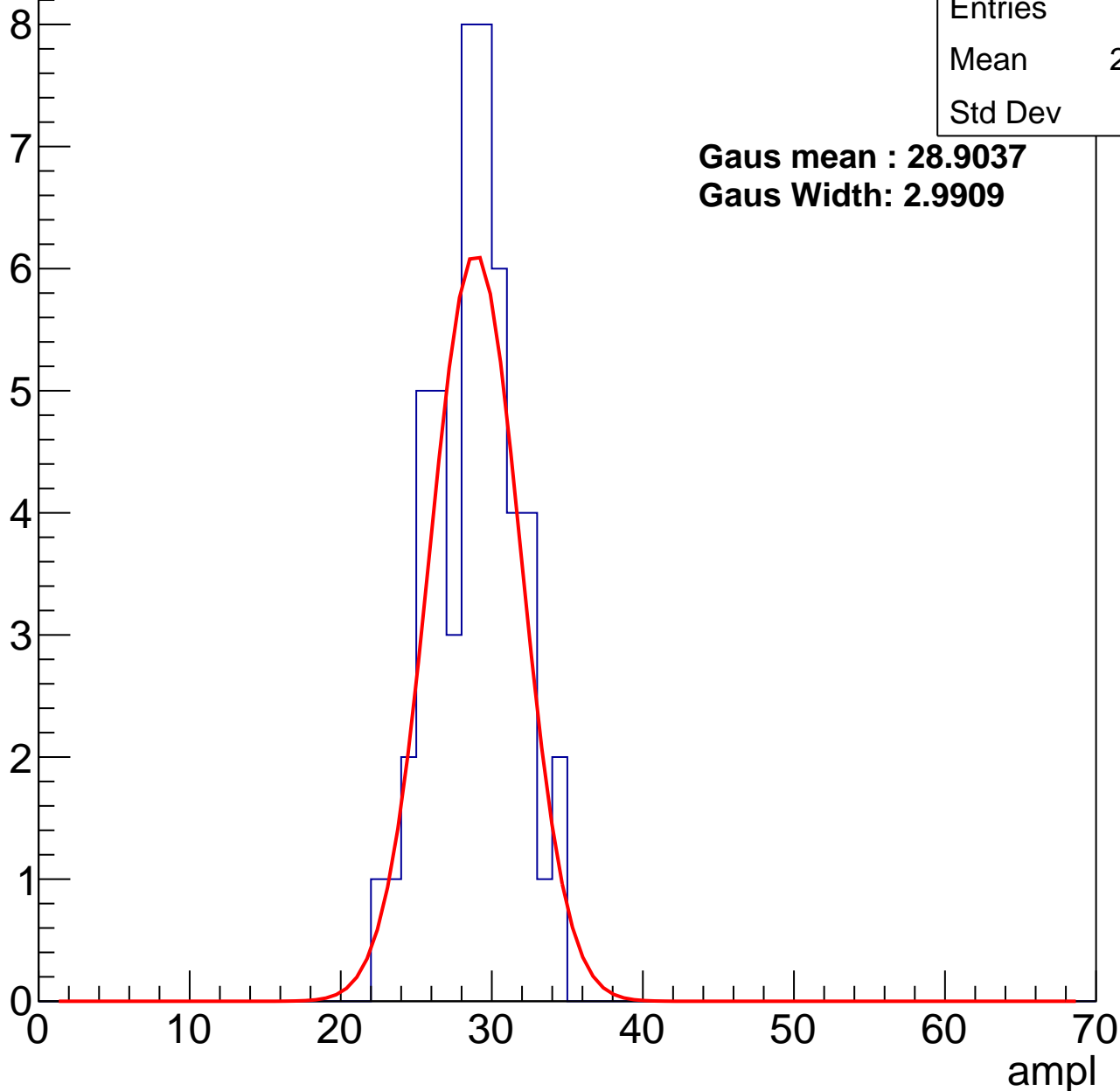
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	28.36
Std Dev	2.77

**Gaus mean : 28.9037**

**Gaus Width: 2.9909**



# B1L002S, U3-ch51, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	75
Mean	34.93
Std Dev	3.222

**Gaus mean : 35.5382**

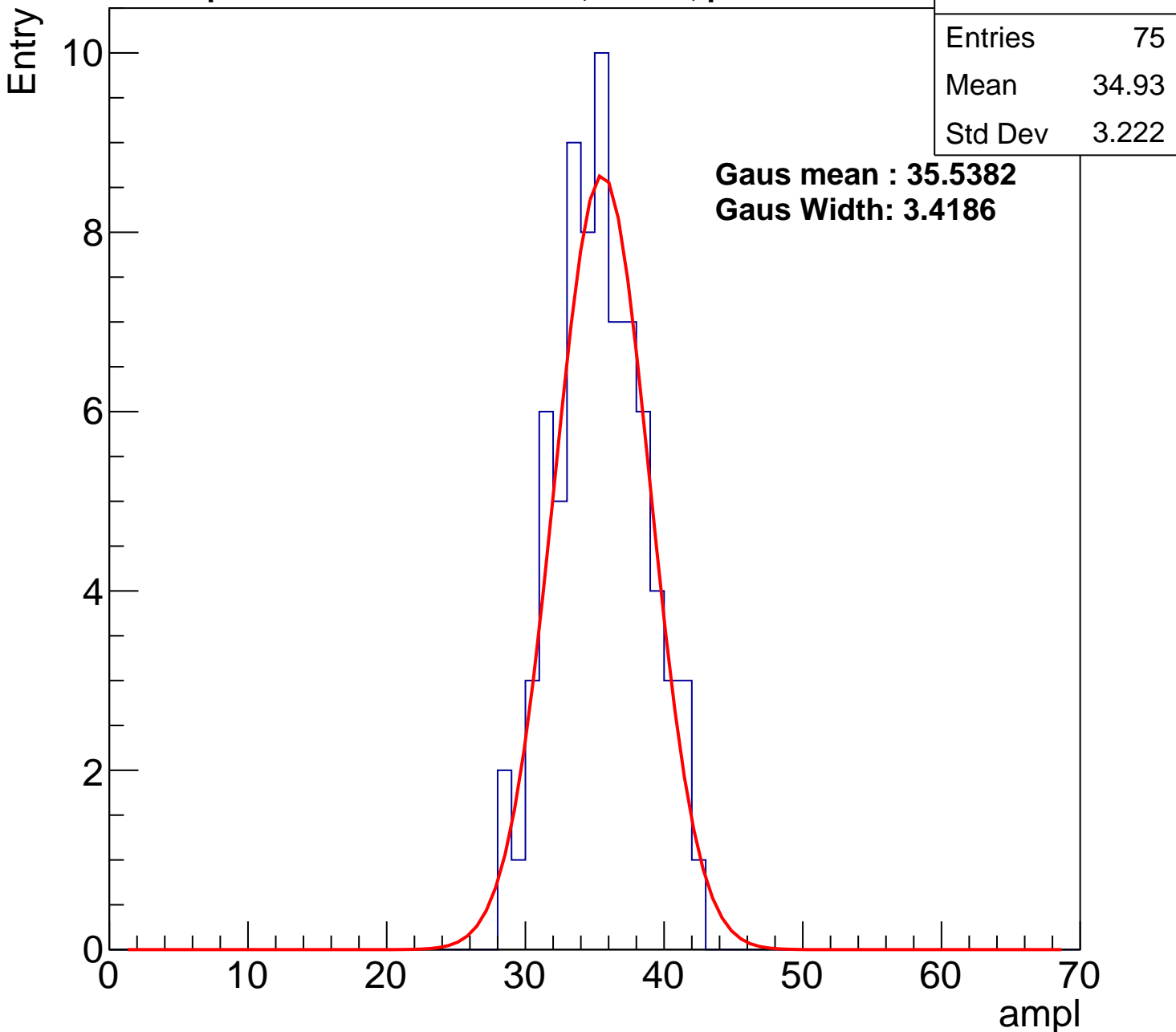
**Gaus Width: 3.4186**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch51, adc2

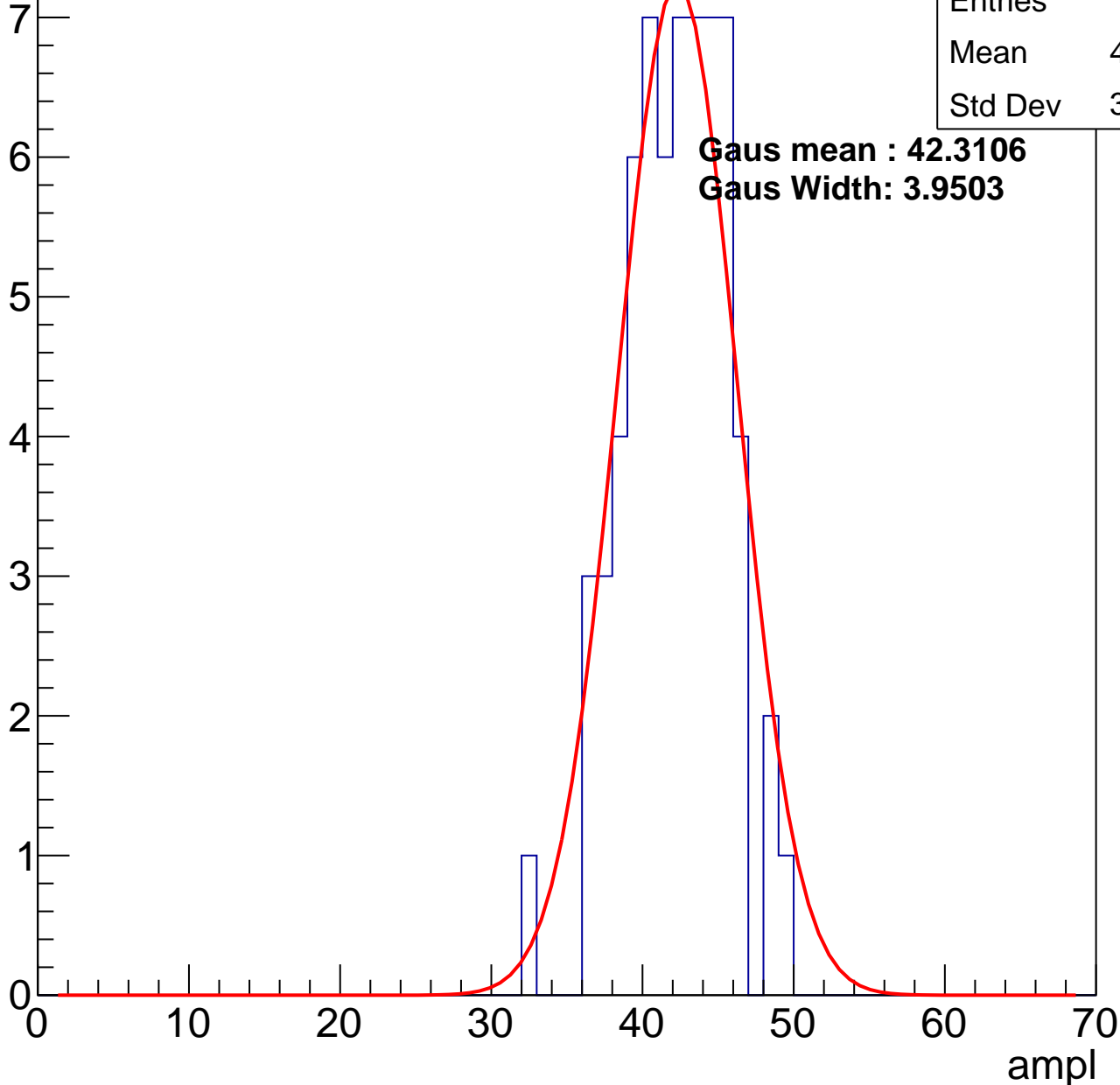
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	41.69
Std Dev	3.309

**Gaus mean : 42.3106**

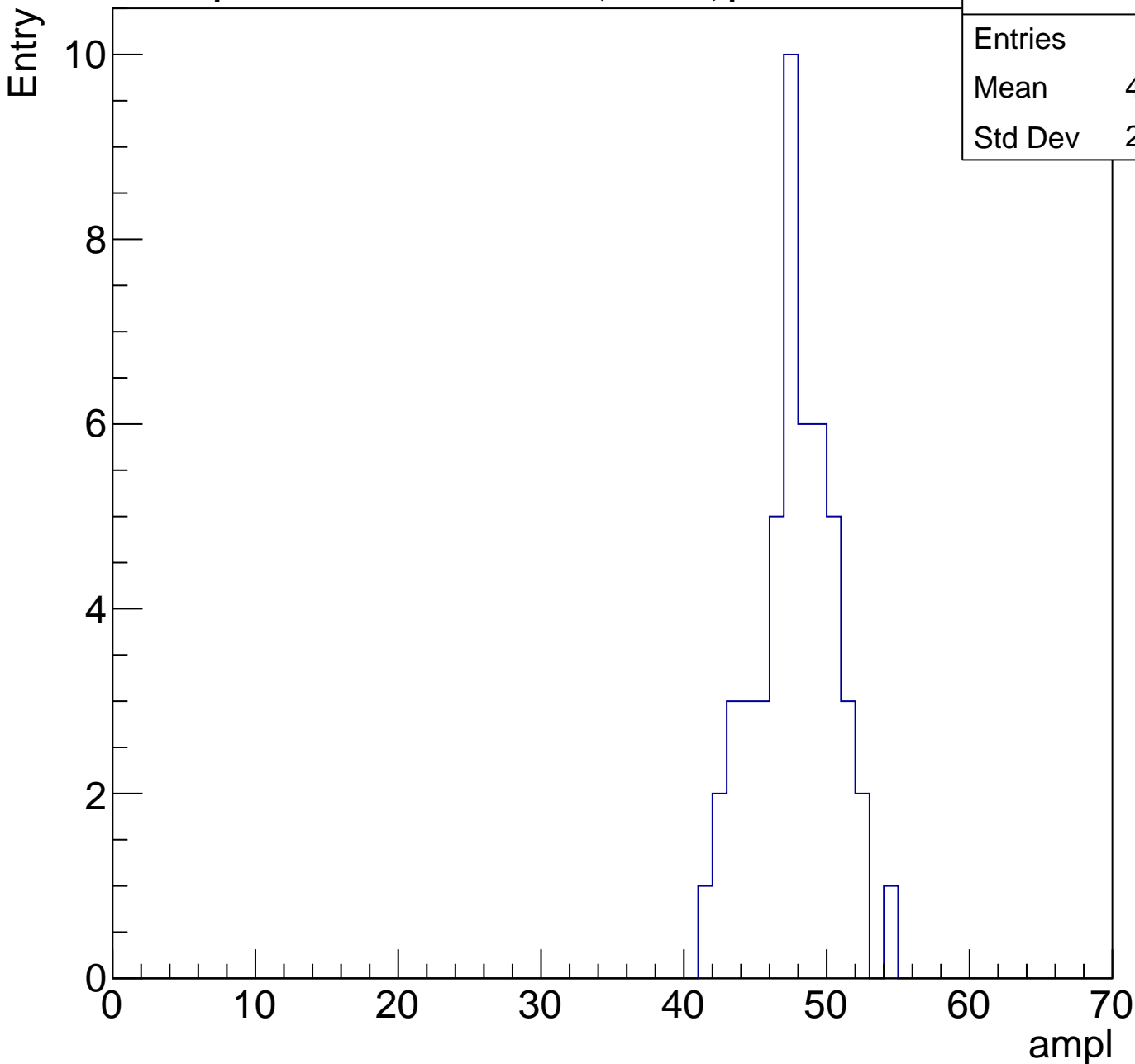
**Gaus Width: 3.9503**



# B1L002S, U3-ch51, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	50
Mean	47.28
Std Dev	2.815

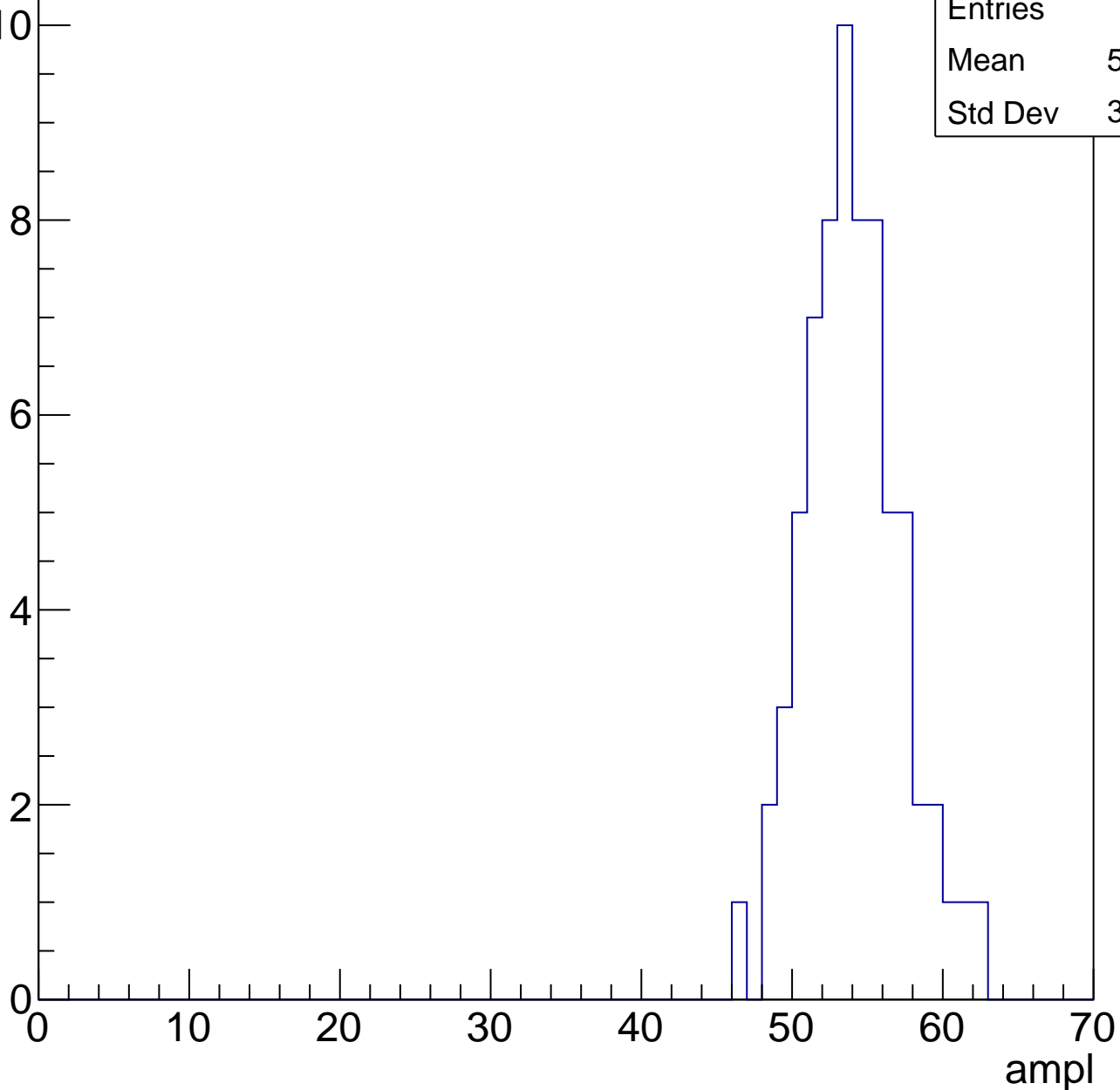


# B1L002S, U3-ch51, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	53.57
Std Dev	3.155

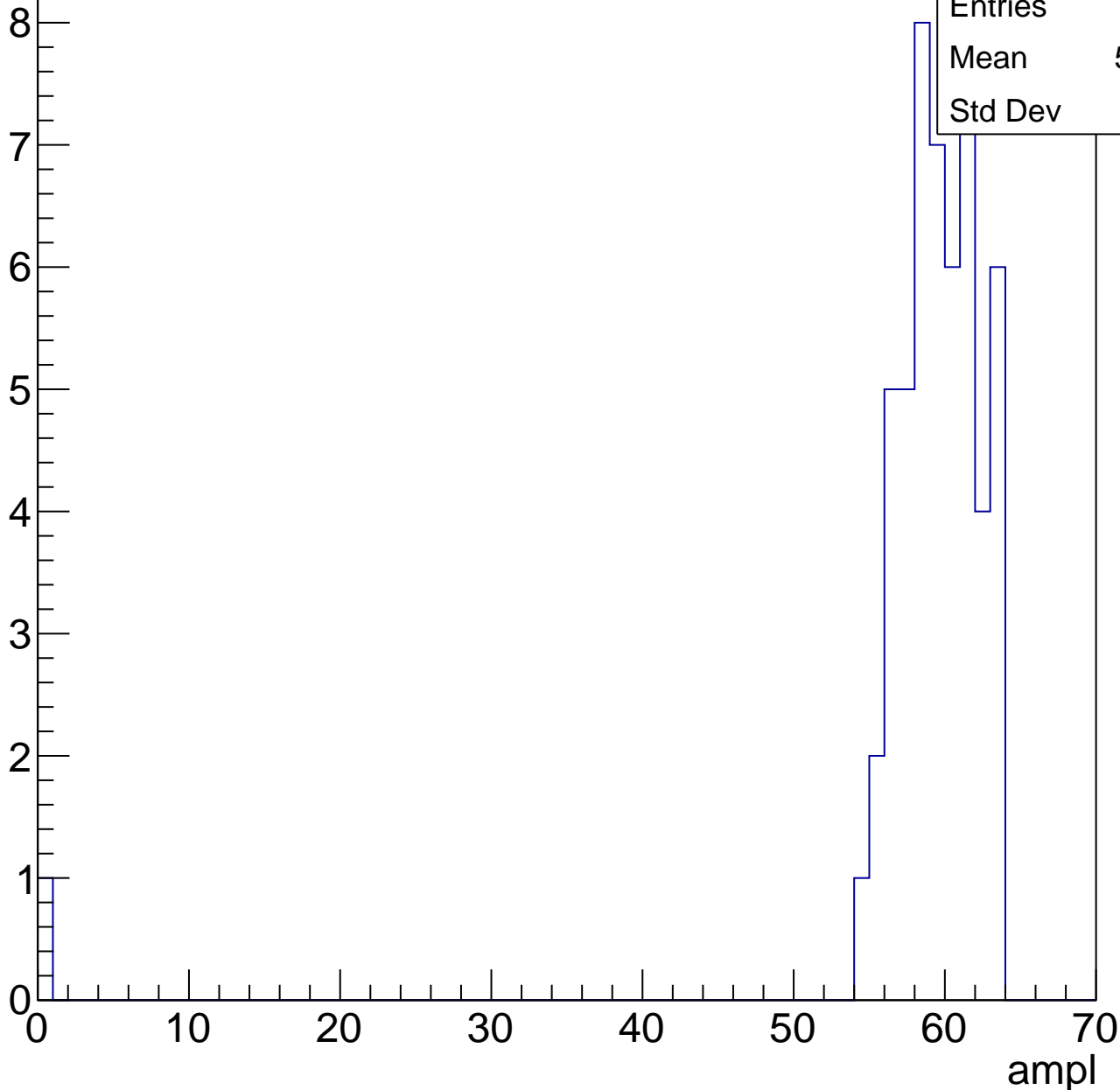


# B1L002S, U3-ch51, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	58.11
Std Dev	8.4

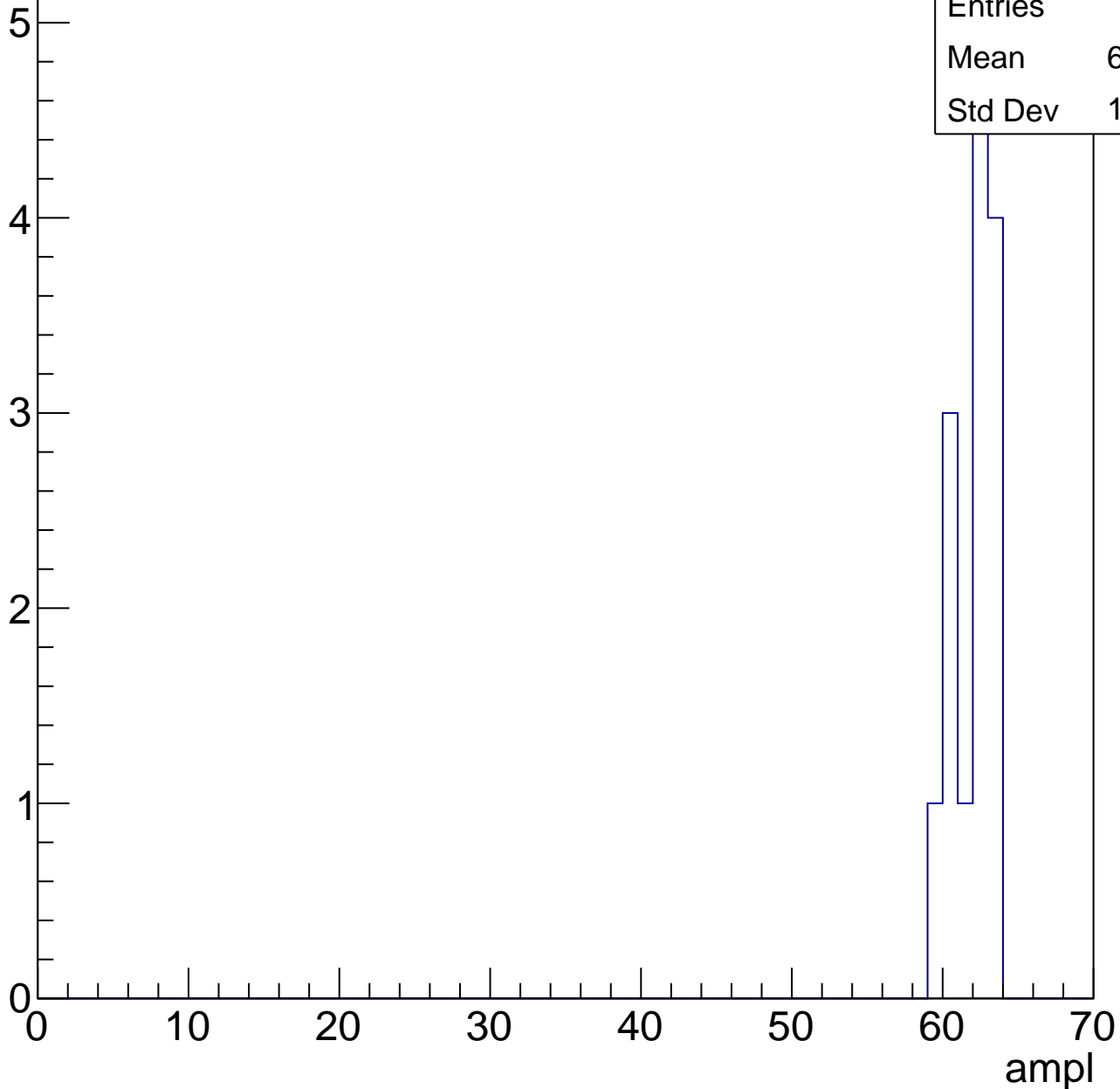


# B1L002S, U3-ch51, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	14
Mean	61.57
Std Dev	1.294





# B1L002S, U3-ch51, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch52, adc0

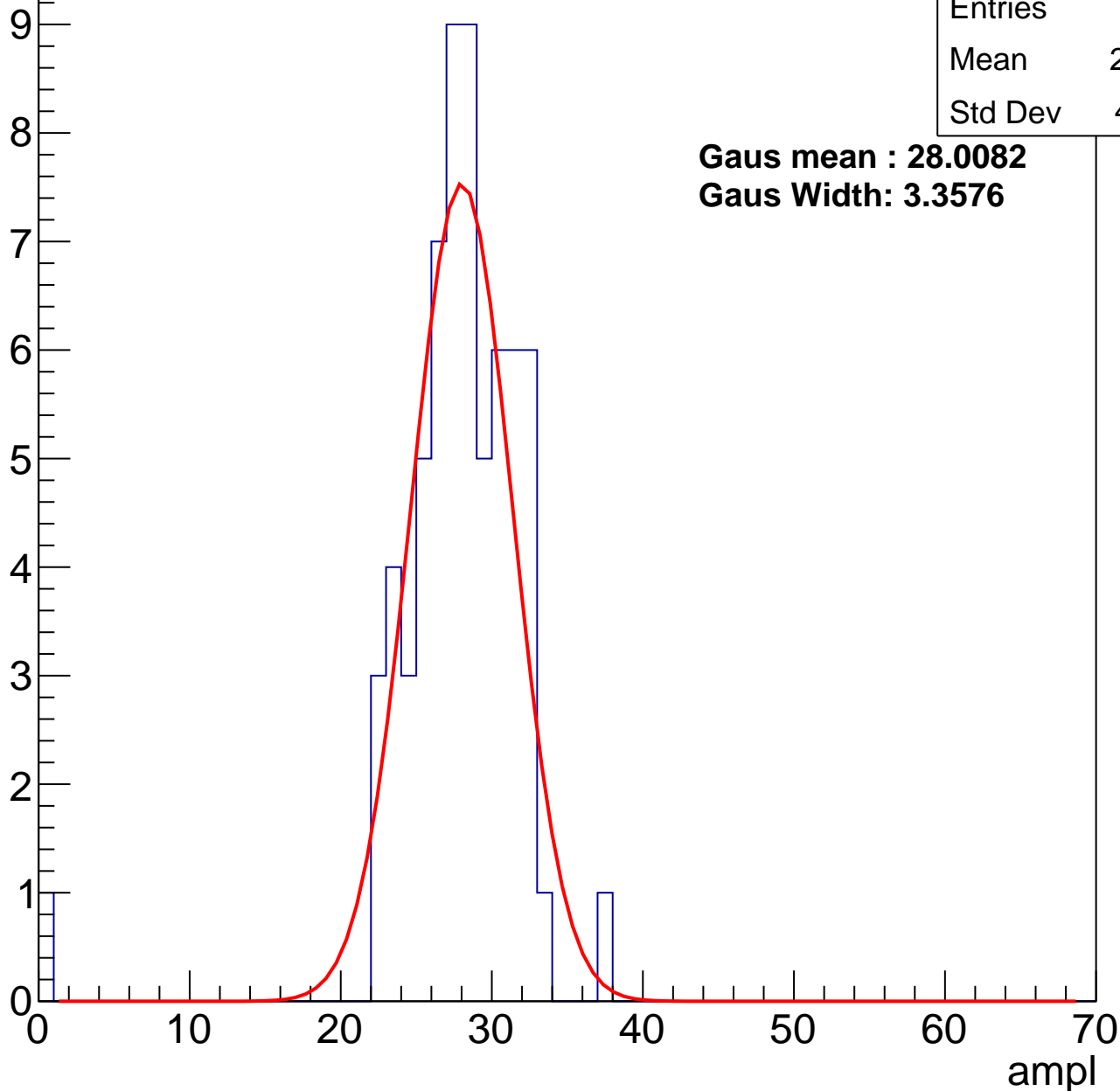
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.35
Std Dev	4.571

**Gaus mean : 28.0082**

**Gaus Width: 3.3576**



# B1L002S, U3-ch52, adc1

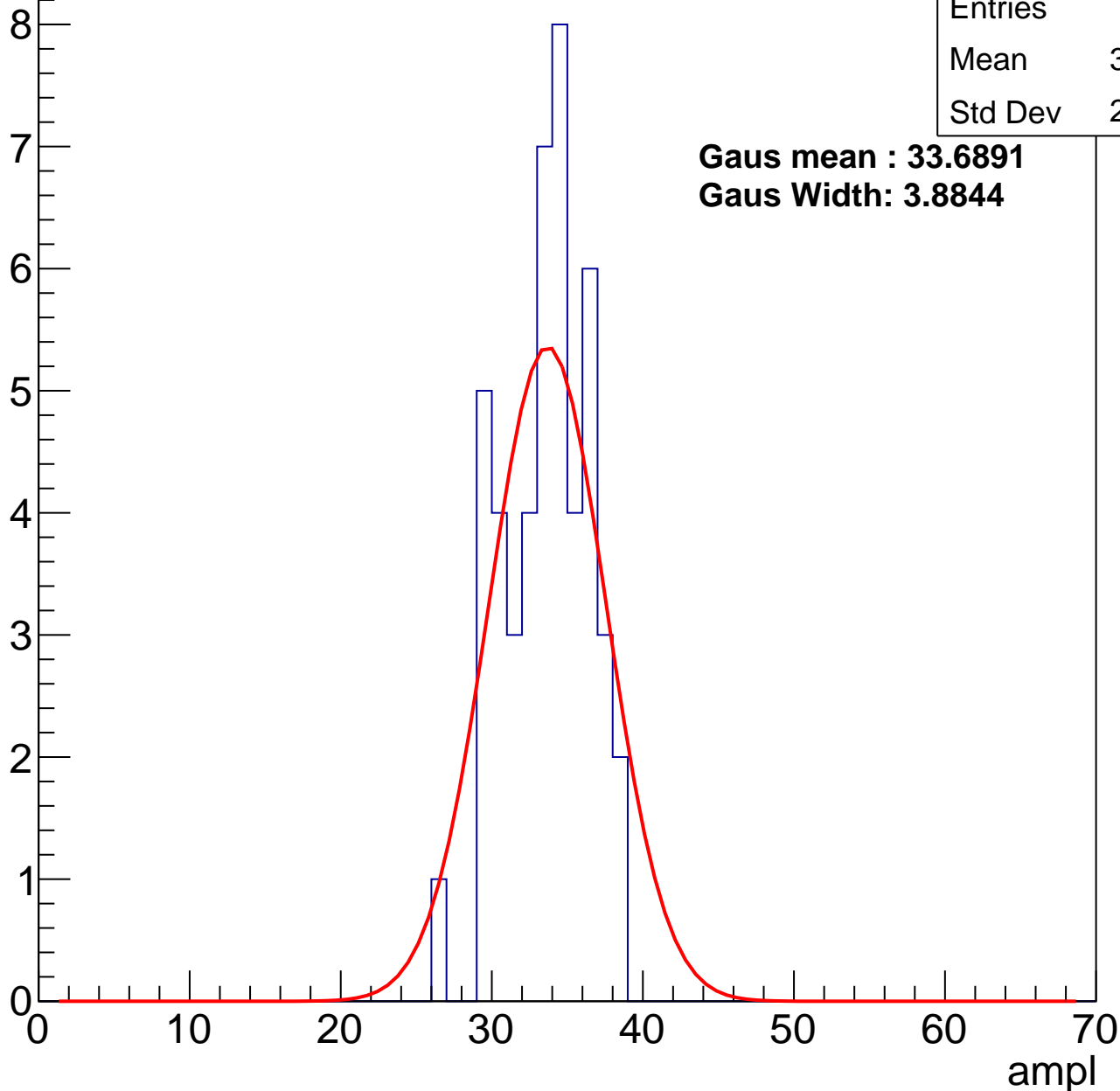
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	33.15
Std Dev	2.752

**Gaus mean : 33.6891**

**Gaus Width: 3.8844**



# B1L002S, U3-ch52, adc2

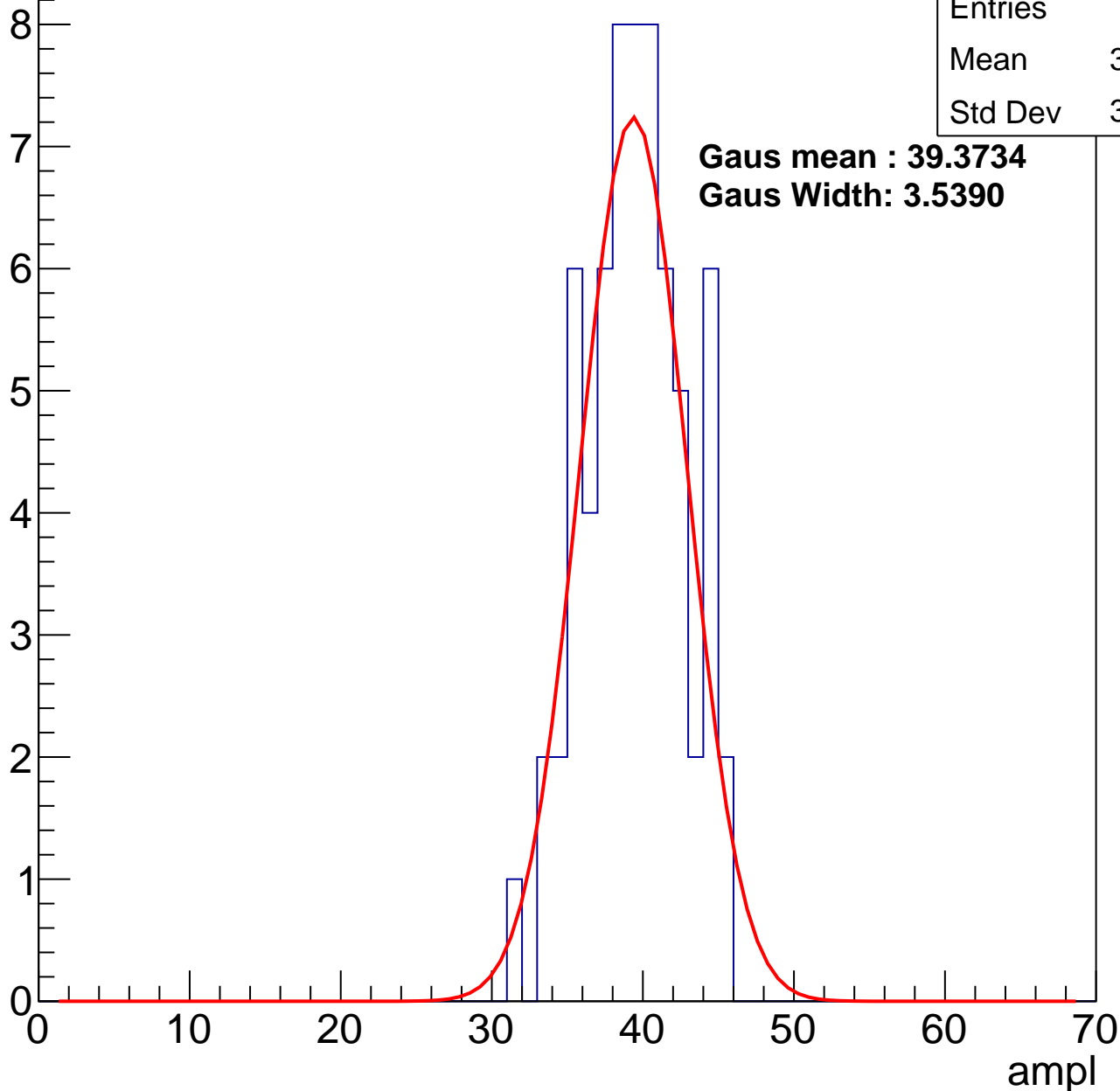
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	38.98
Std Dev	3.212

**Gaus mean : 39.3734**

**Gaus Width: 3.5390**

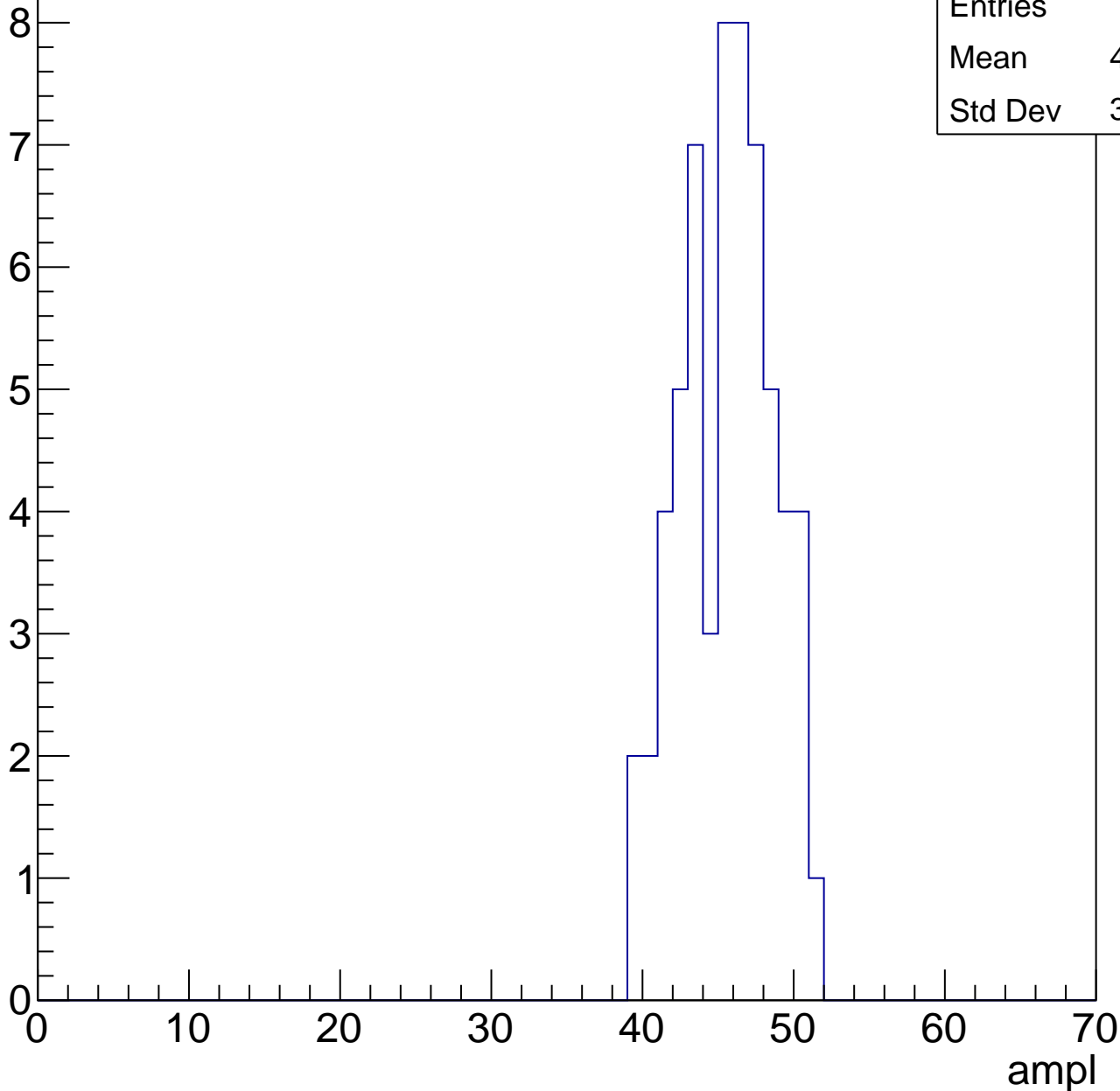


# B1L002S, U3-ch52, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.15
Std Dev	3.005

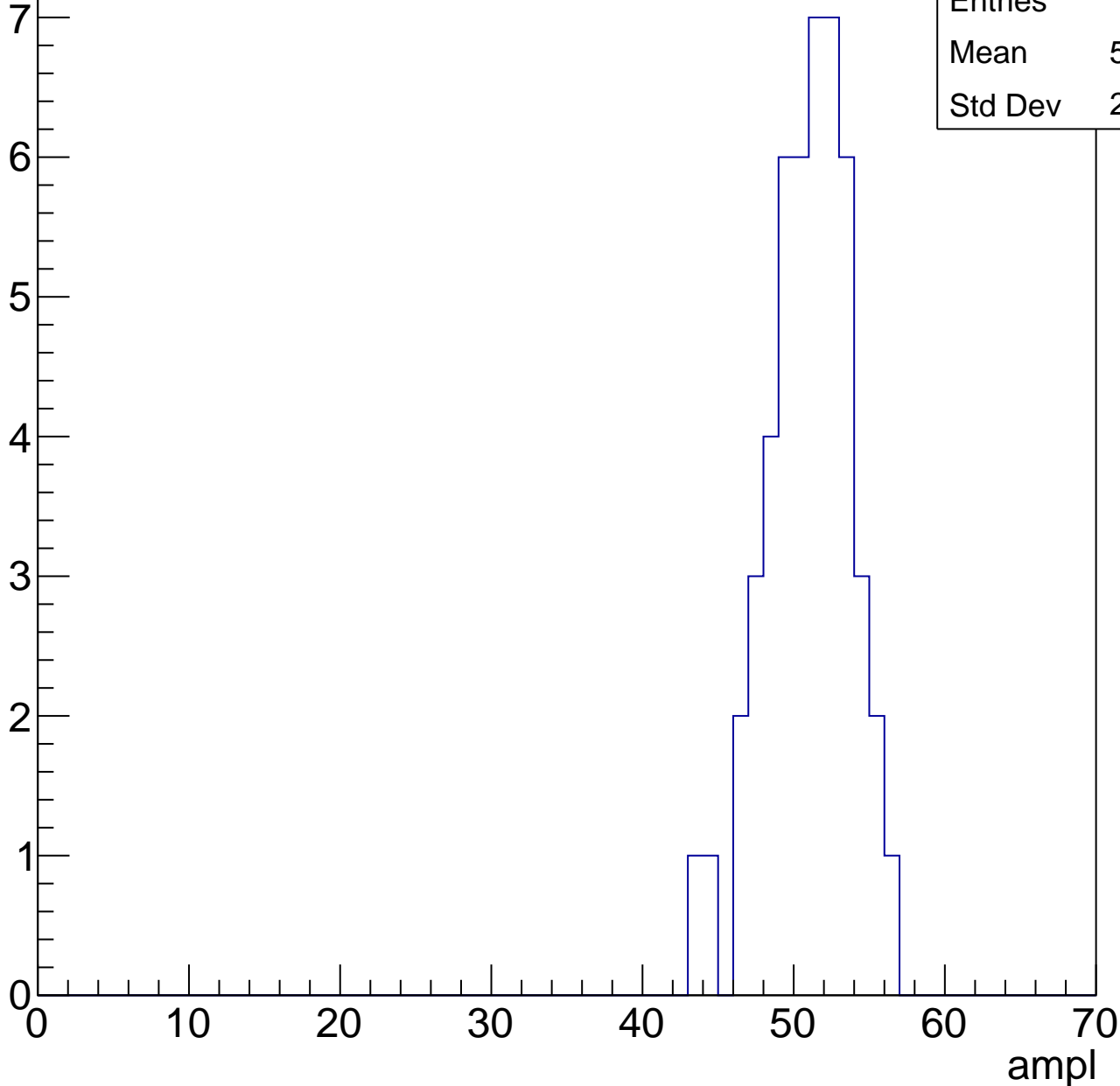


# B1L002S, U3-ch52, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

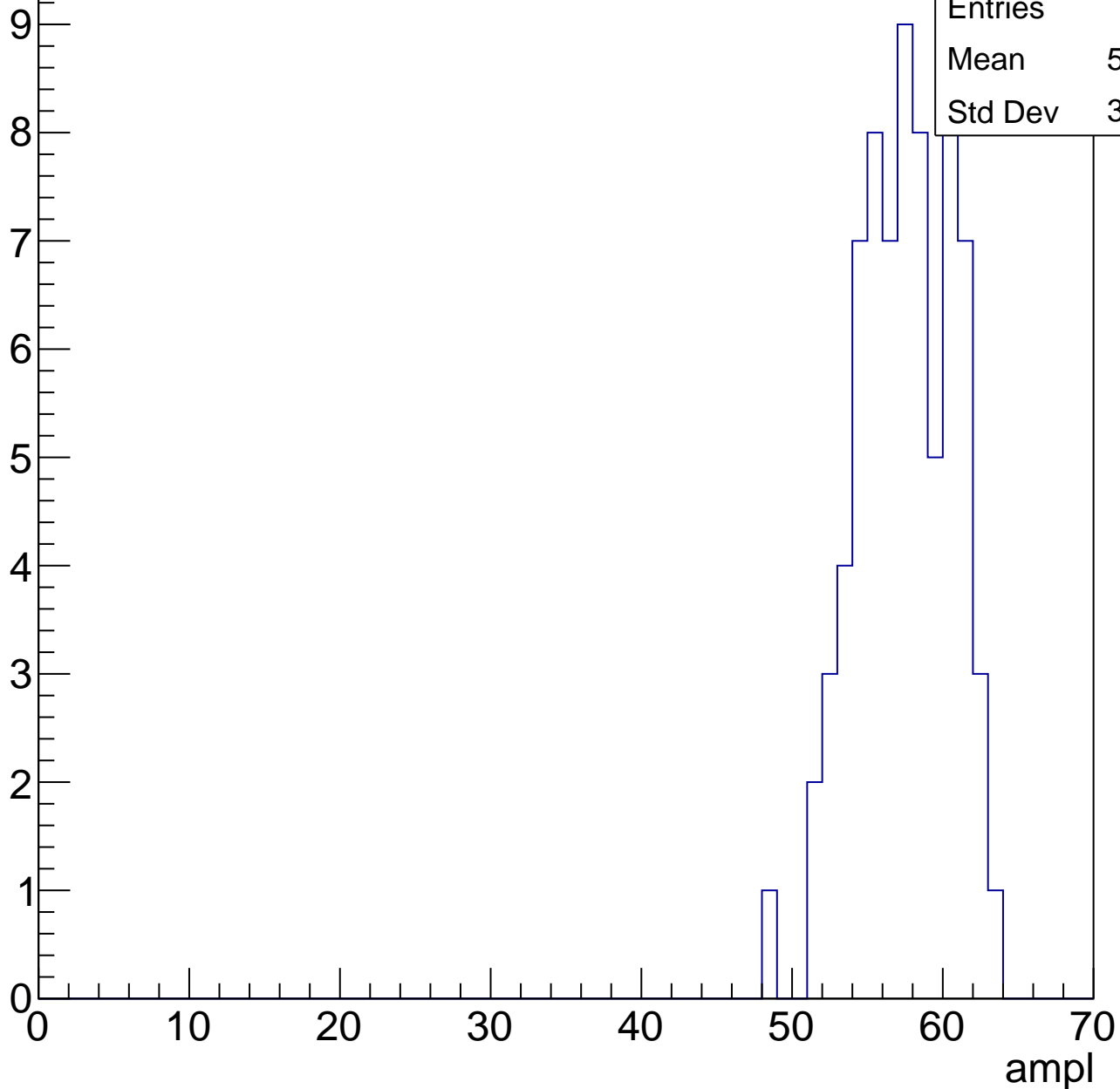
Entries	49
Mean	50.47
Std Dev	2.778



# B1L002S, U3-ch52, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

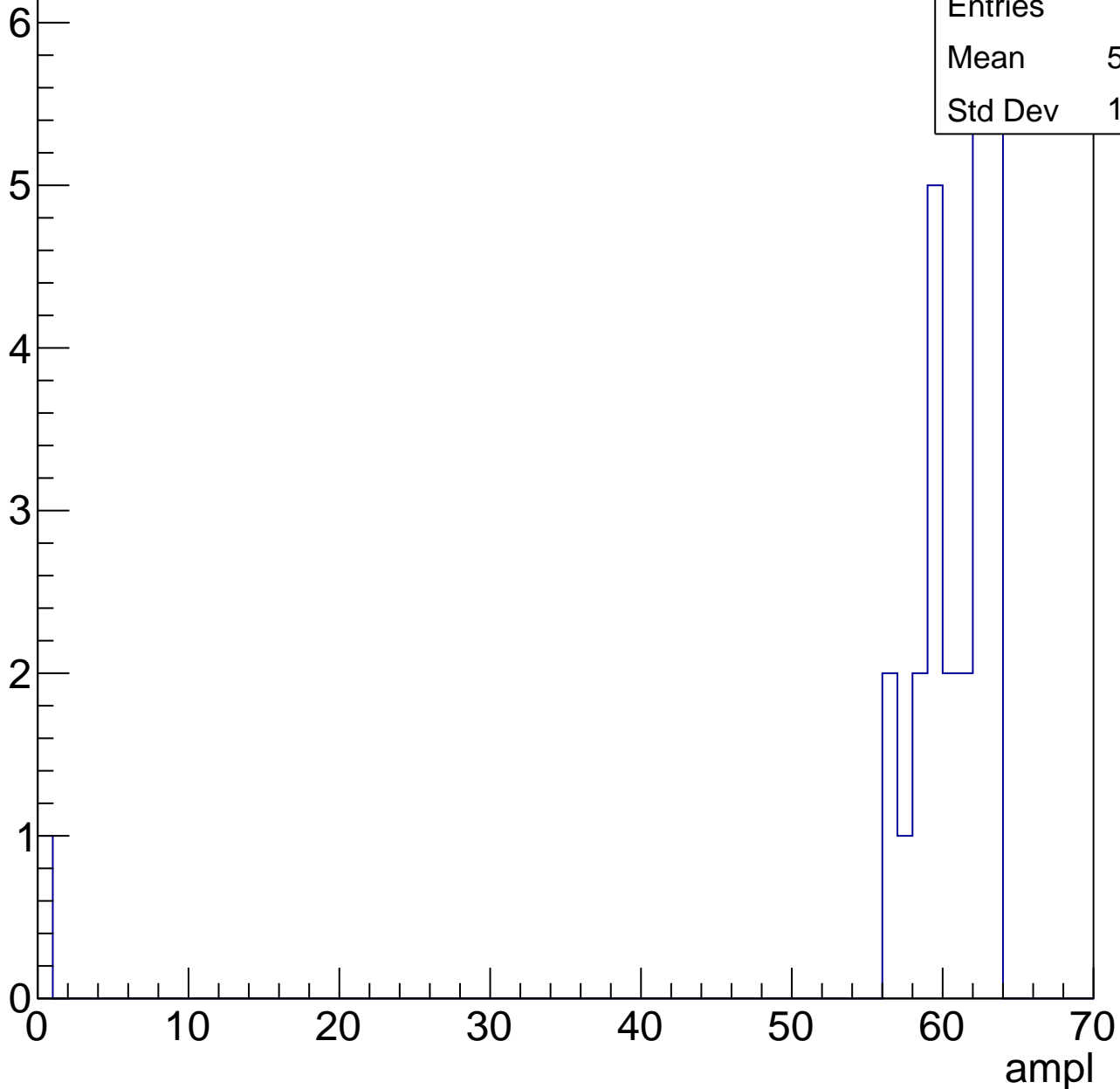


# B1L002S, U3-ch52, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.22
Std Dev	11.62

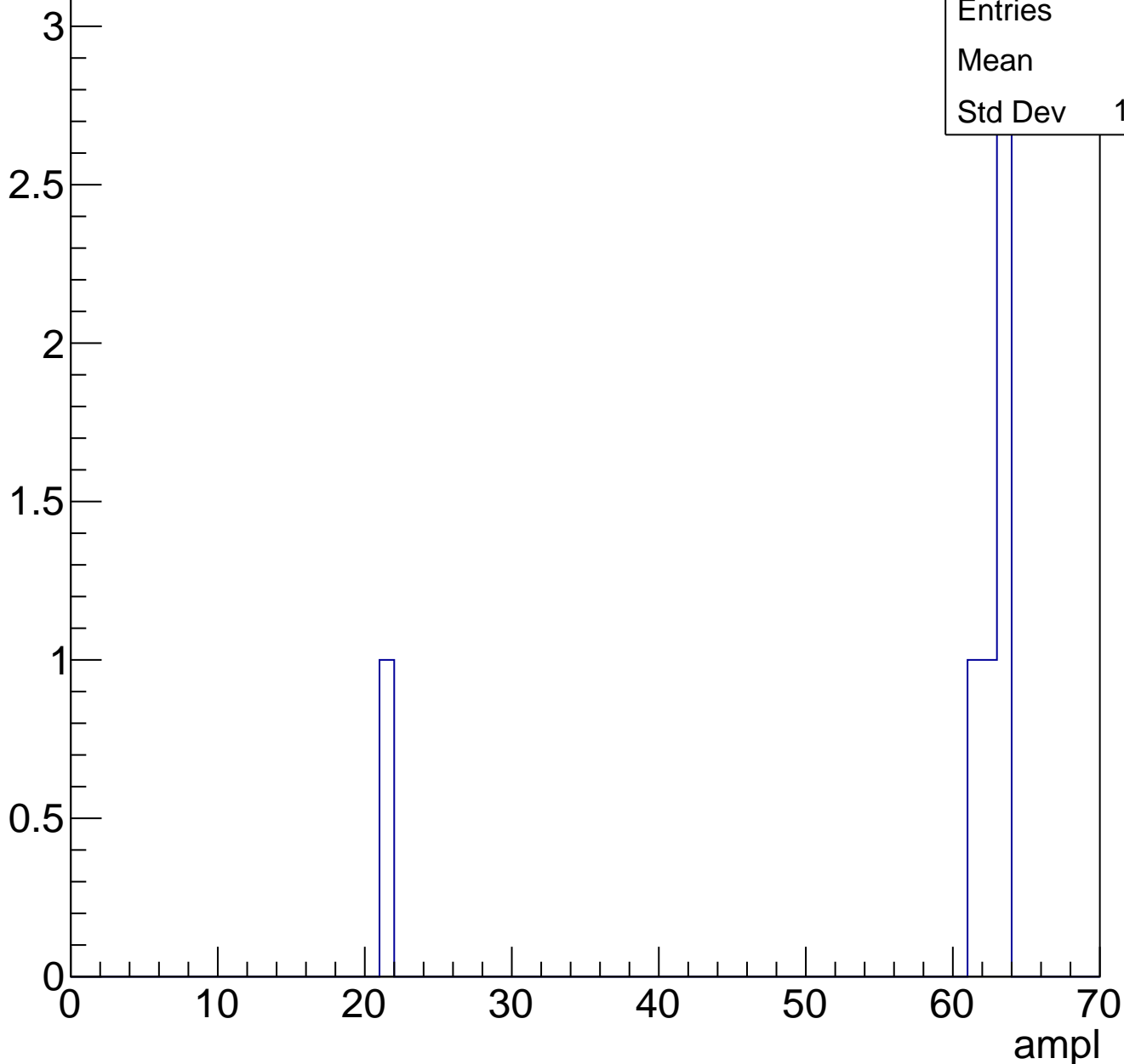




# B1L002S, U3-ch52, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch53, adc0

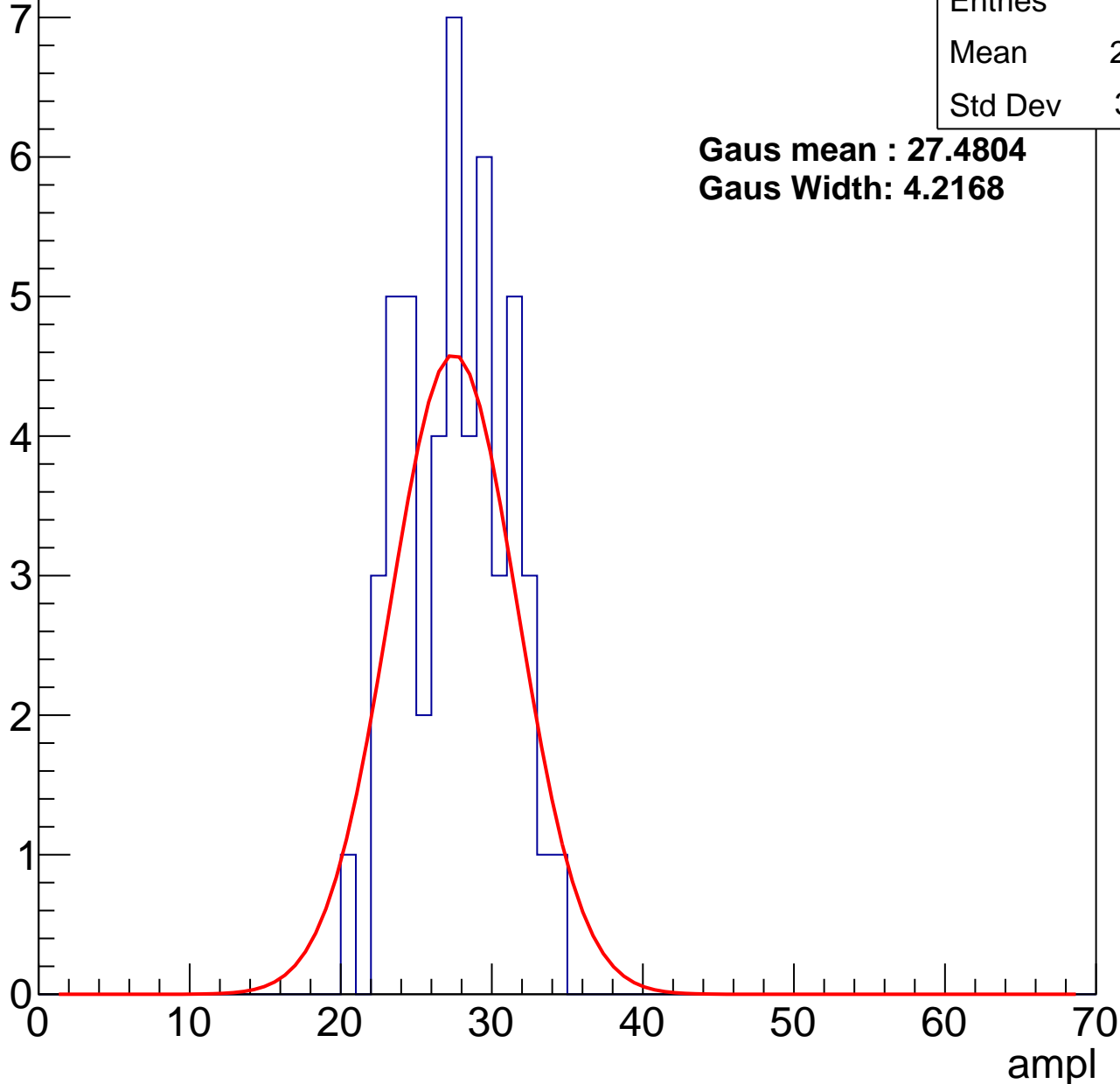
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	27.16
Std Dev	3.331

**Gaus mean : 27.4804**

**Gaus Width: 4.2168**



# B1L002S, U3-ch53, adc1

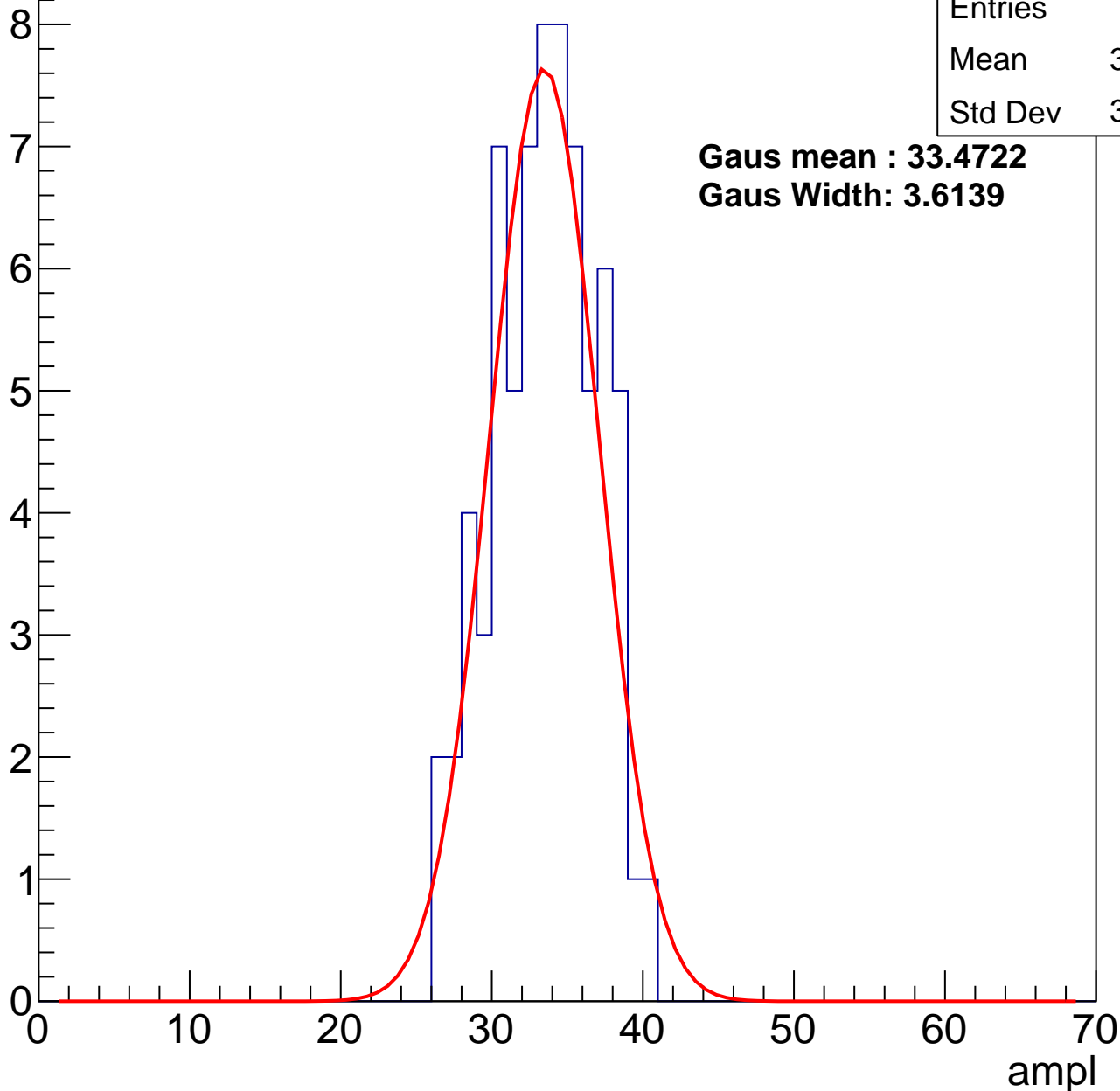
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	33.04
Std Dev	3.346

**Gaus mean : 33.4722**

**Gaus Width: 3.6139**



# B1L002S, U3-ch53, adc2

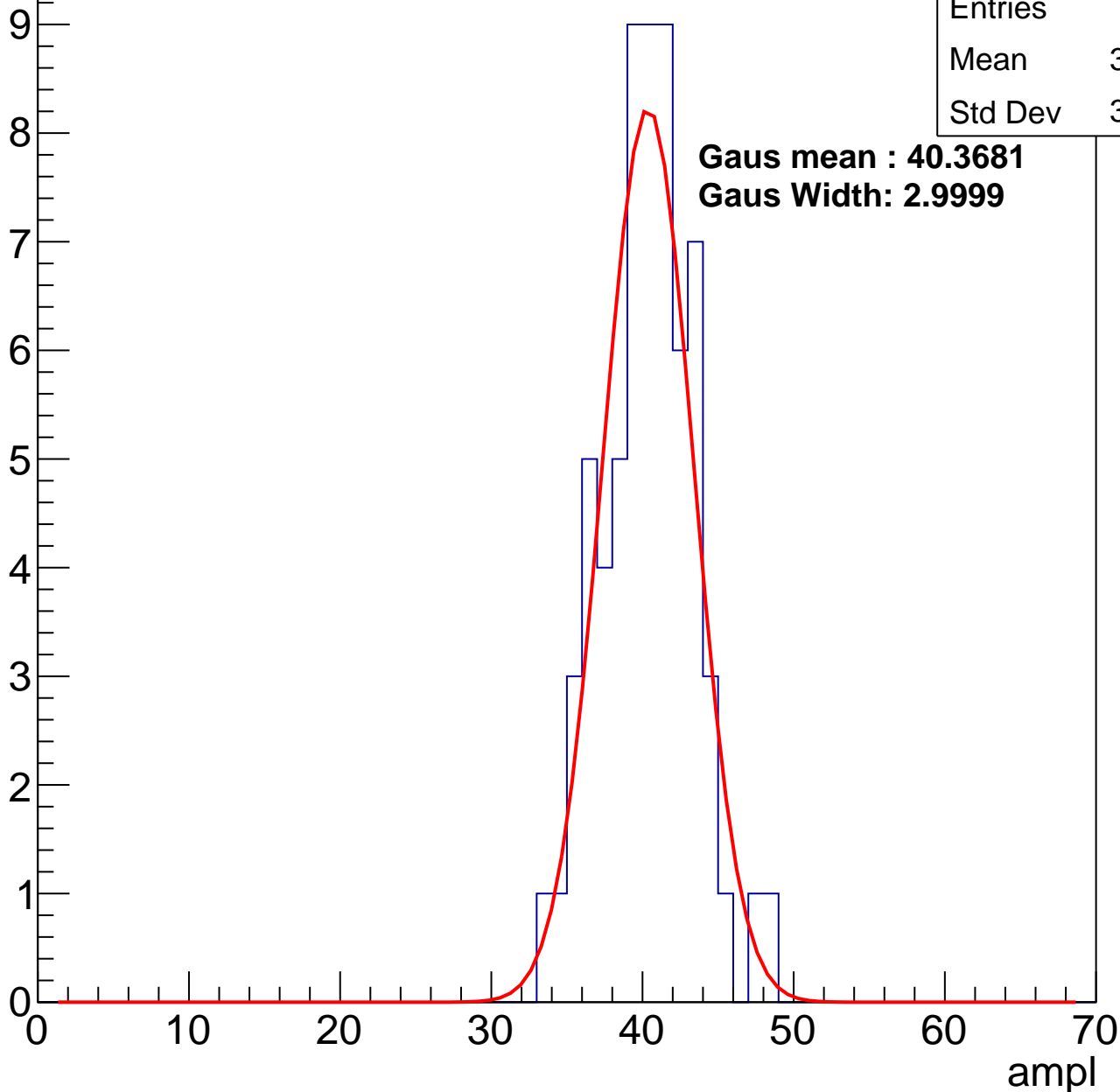
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.92
Std Dev	3.004

**Gaus mean : 40.3681**

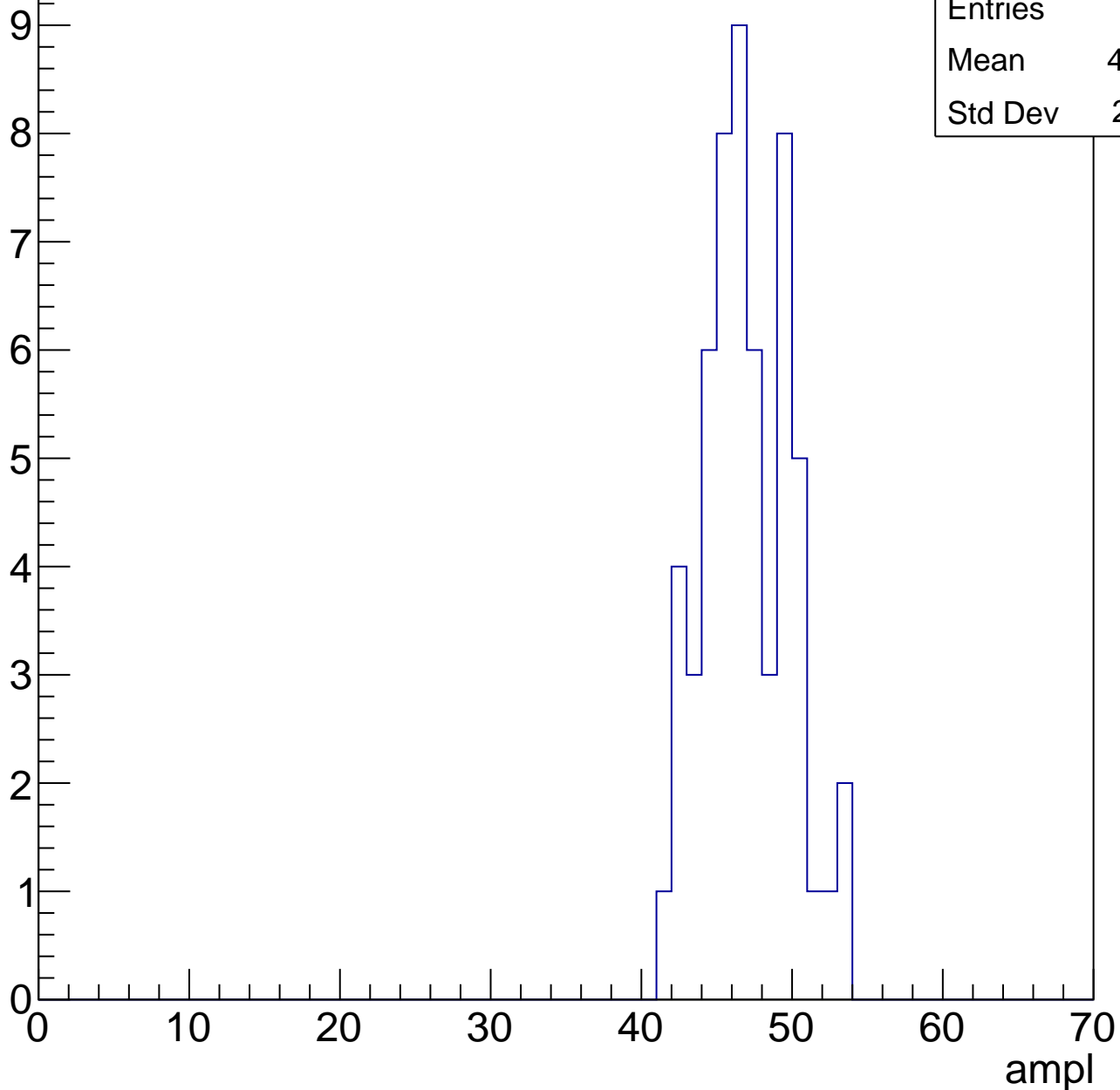
**Gaus Width: 2.9999**



# B1L002S, U3-ch53, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

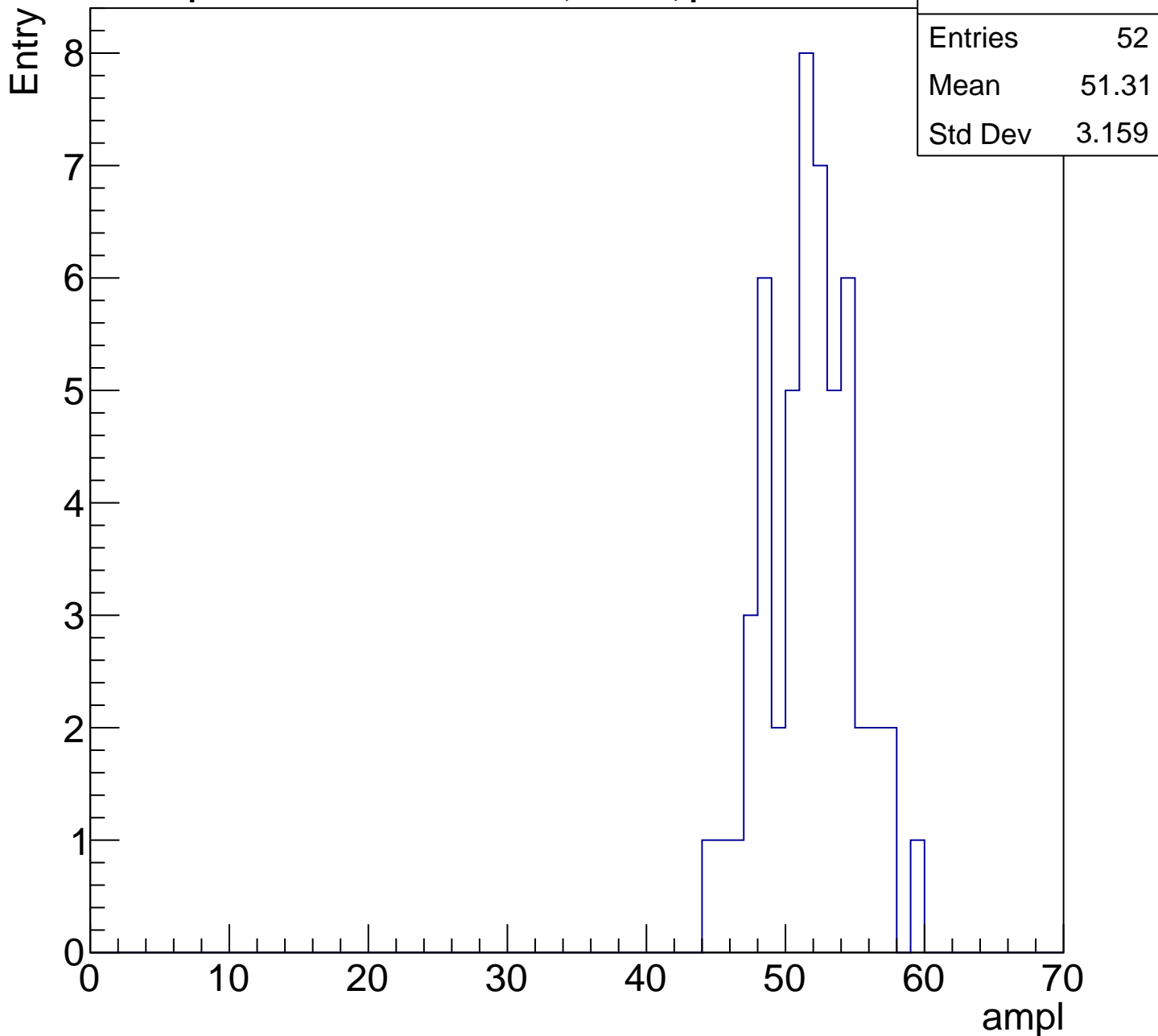
Entry



Entries	57
Mean	46.54
Std Dev	2.841

# B1L002S, U3-ch53, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

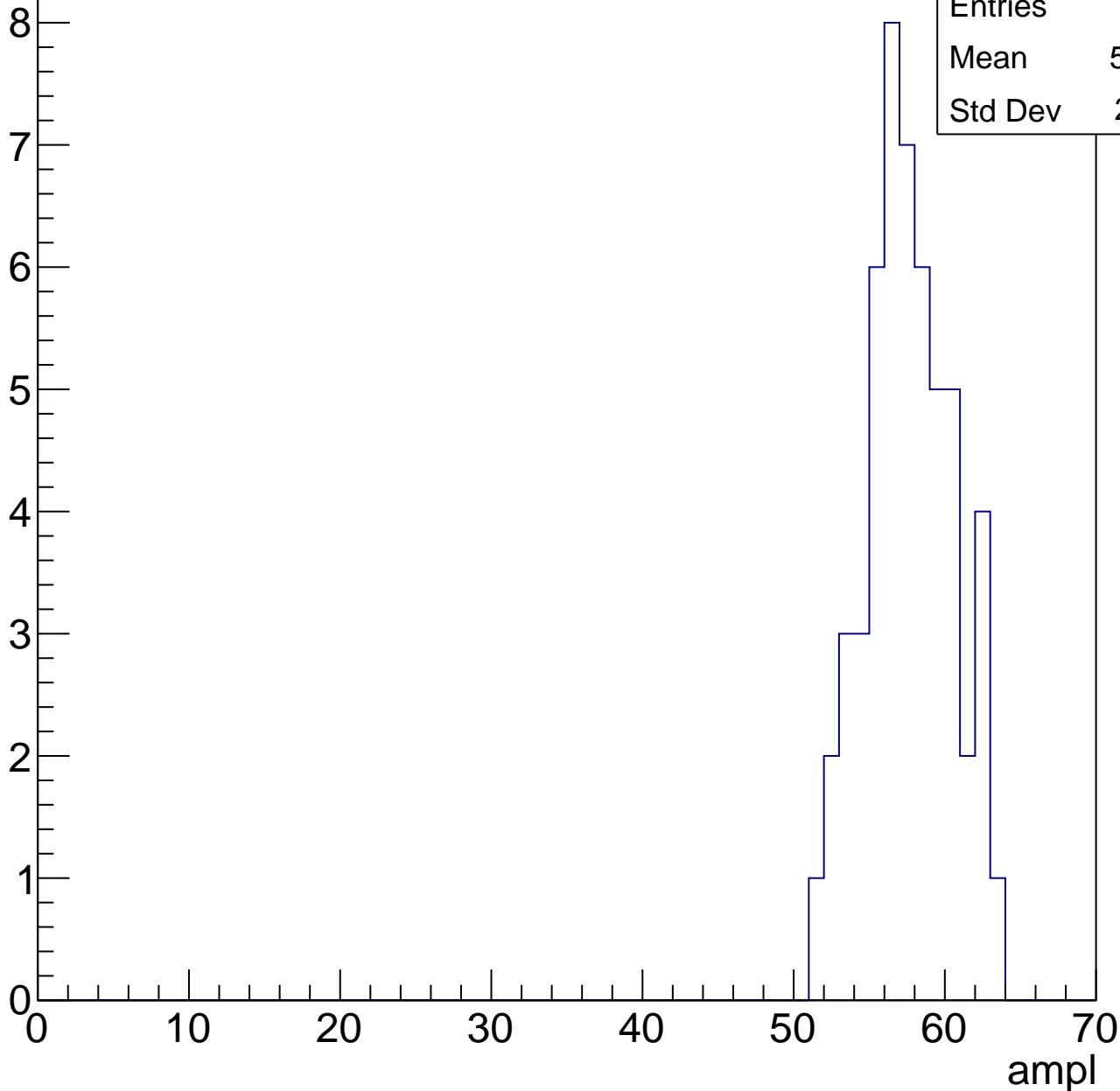


# B1L002S, U3-ch53, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	57.15
Std Dev	2.851

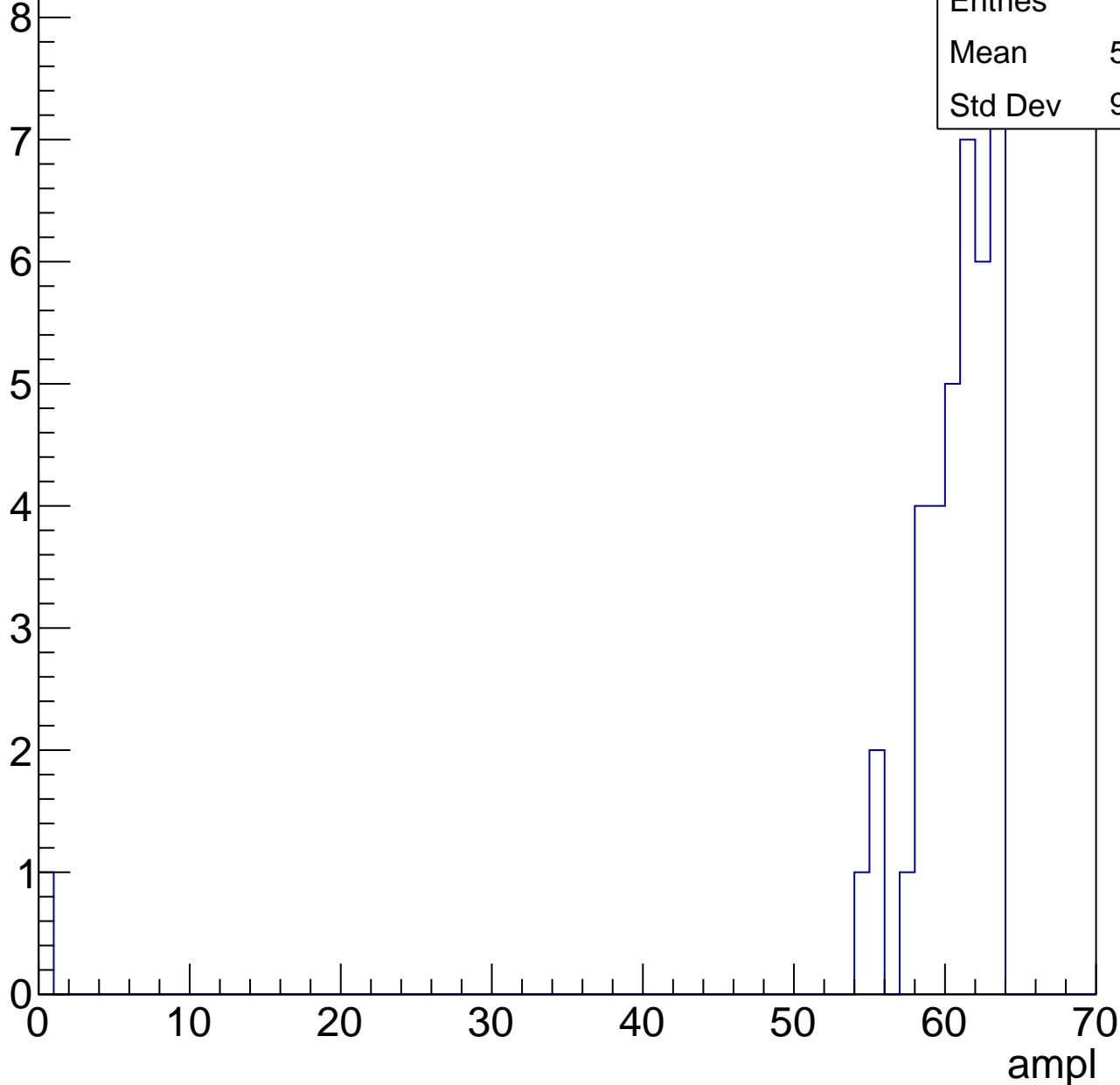


# B1L002S, U3-ch53, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	58.77
Std Dev	9.817





# B1L002S, U3-ch53, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch54, adc0

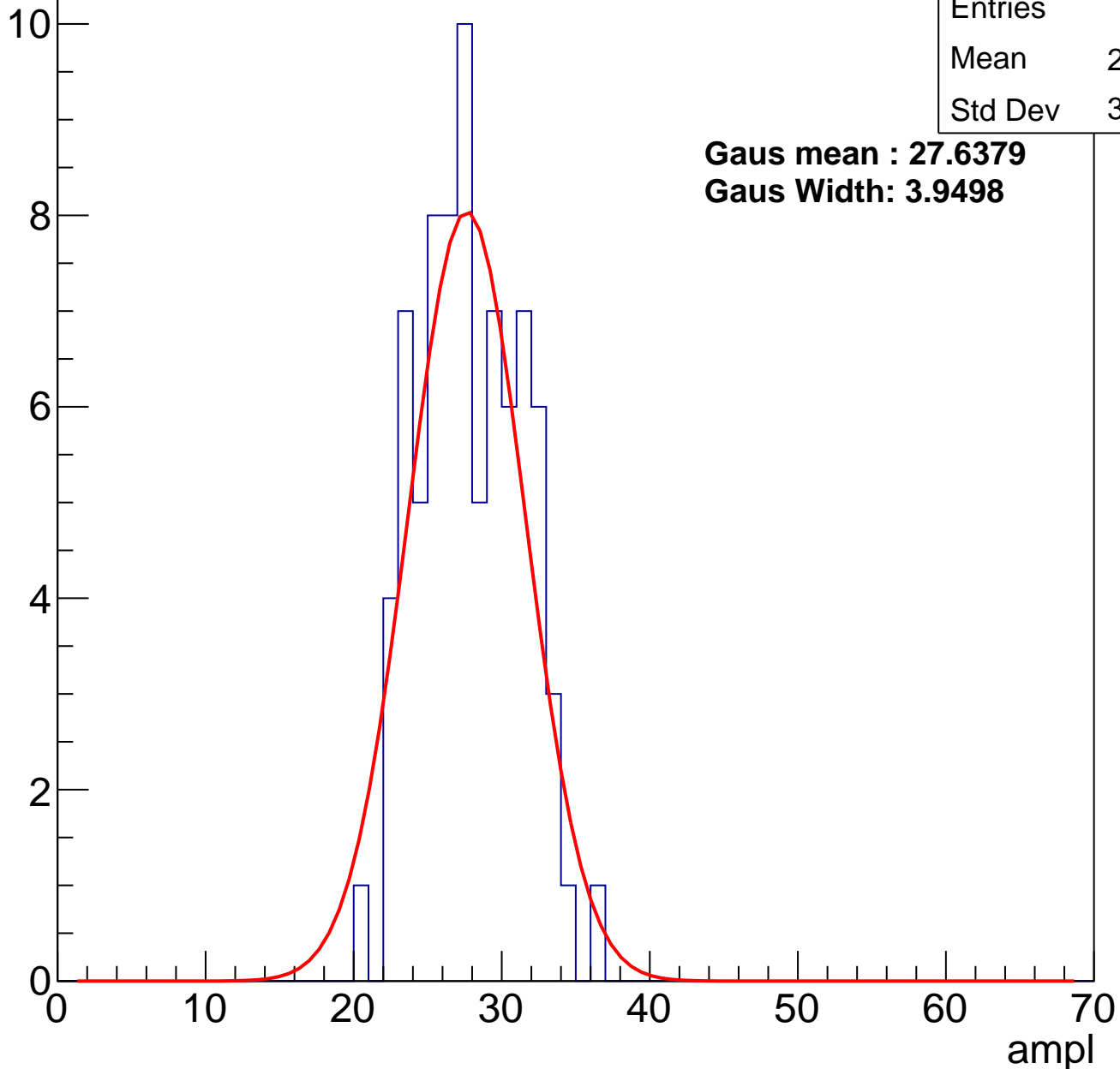
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	27.44
Std Dev	3.408

**Gaus mean : 27.6379**

**Gaus Width: 3.9498**

Entry



# B1L002S, U3-ch54, adc1

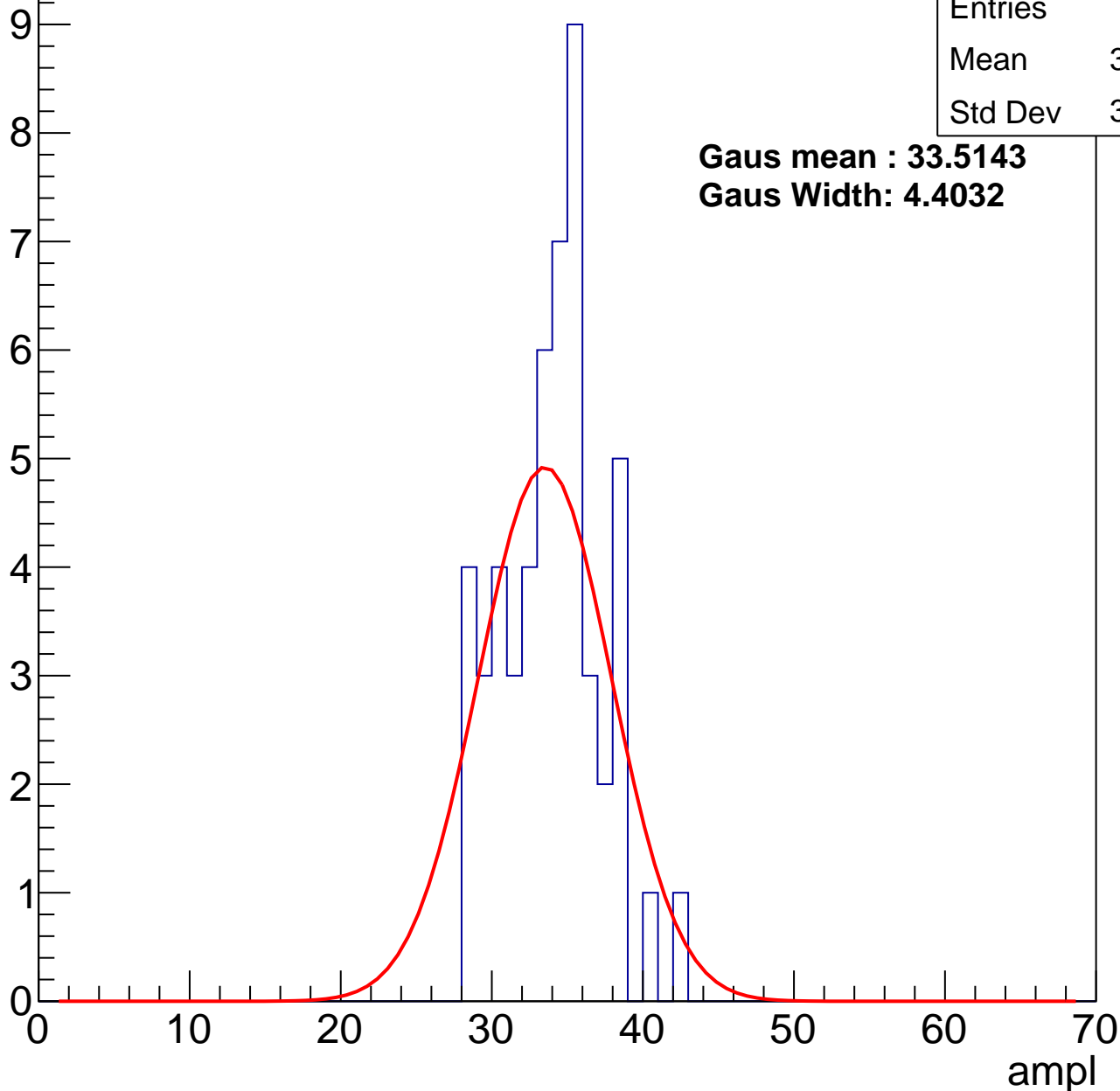
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	33.56
Std Dev	3.225

**Gaus mean : 33.5143**

**Gaus Width: 4.4032**



# B1L002S, U3-ch54, adc2

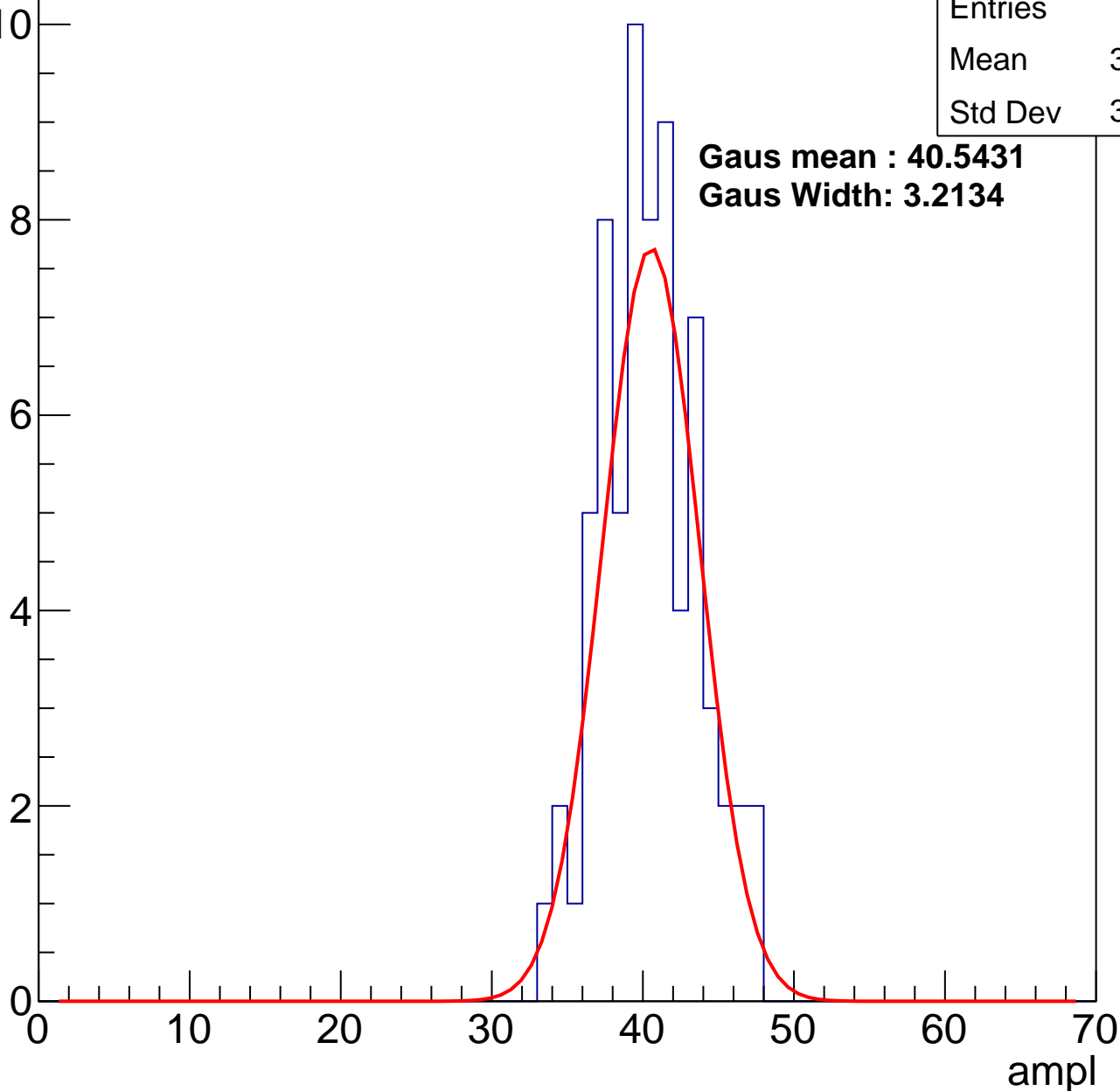
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	39.97
Std Dev	3.148

**Gaus mean : 40.5431**

**Gaus Width: 3.2134**

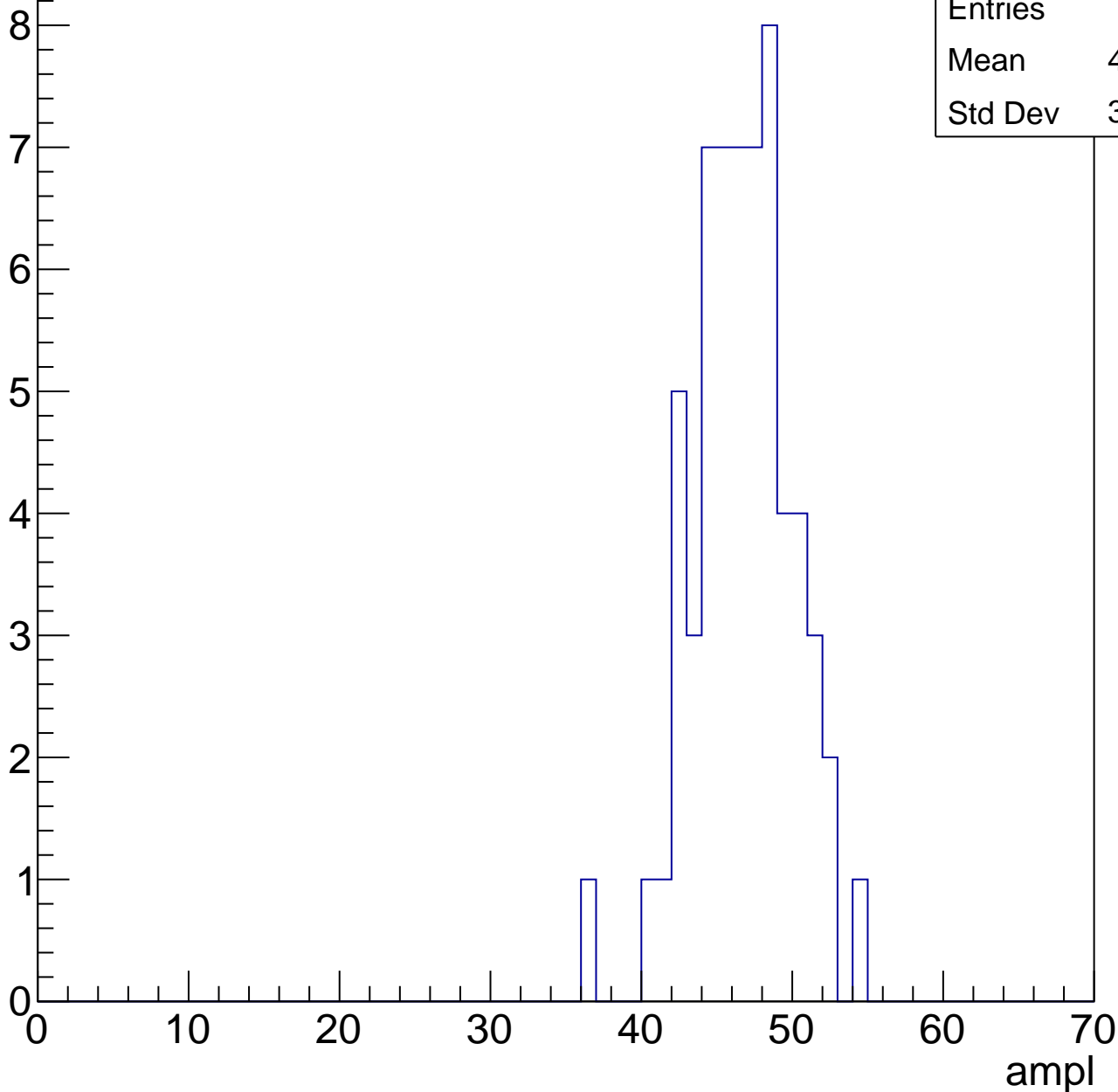


# B1L002S, U3-ch54, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

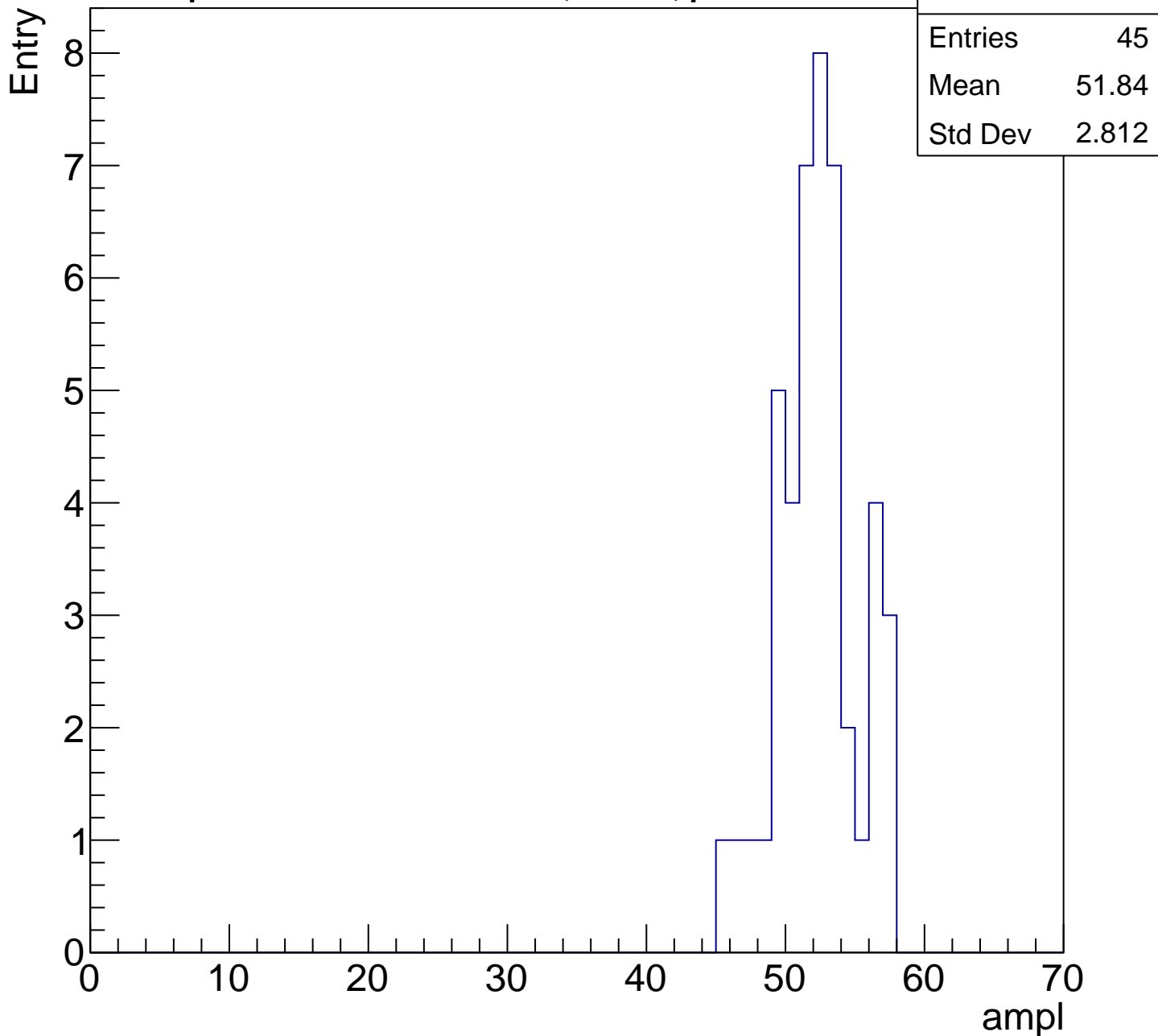
Entry

Entries	61
Mean	46.25
Std Dev	3.263



# B1L002S, U3-ch54, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

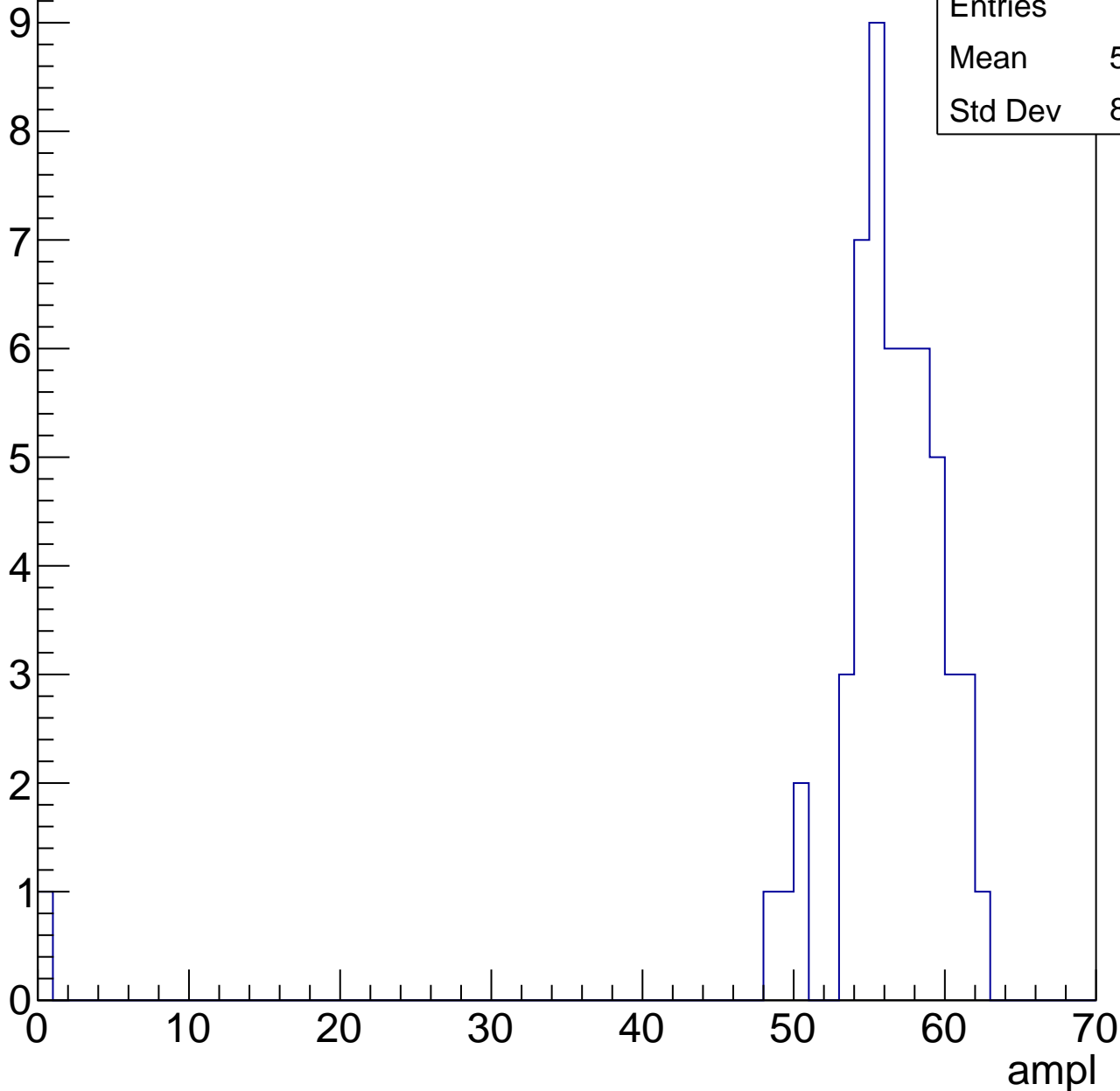


# B1L002S, U3-ch54, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	55.09
Std Dev	8.136

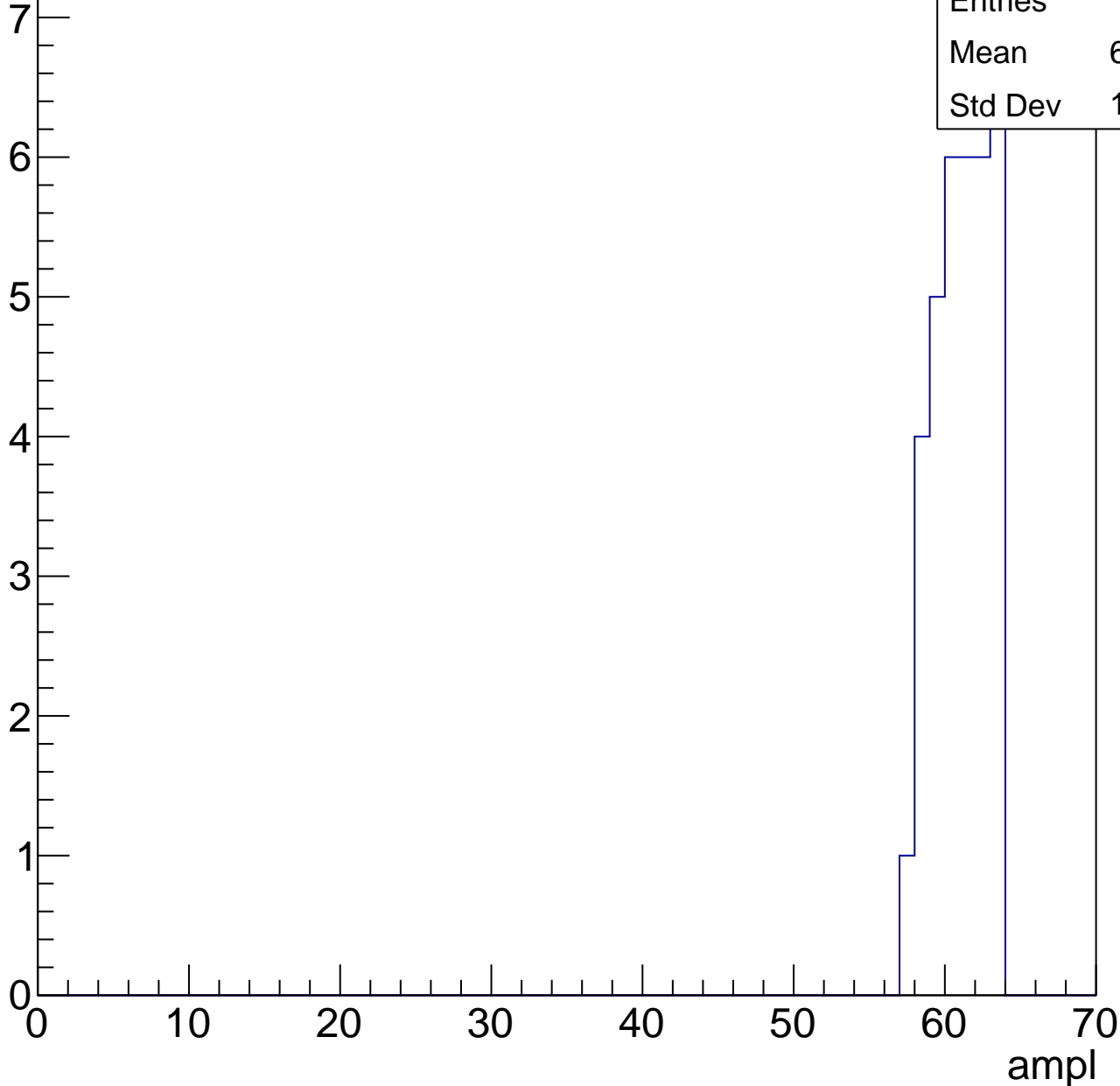


# B1L002S, U3-ch54, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	60.66
Std Dev	1.756

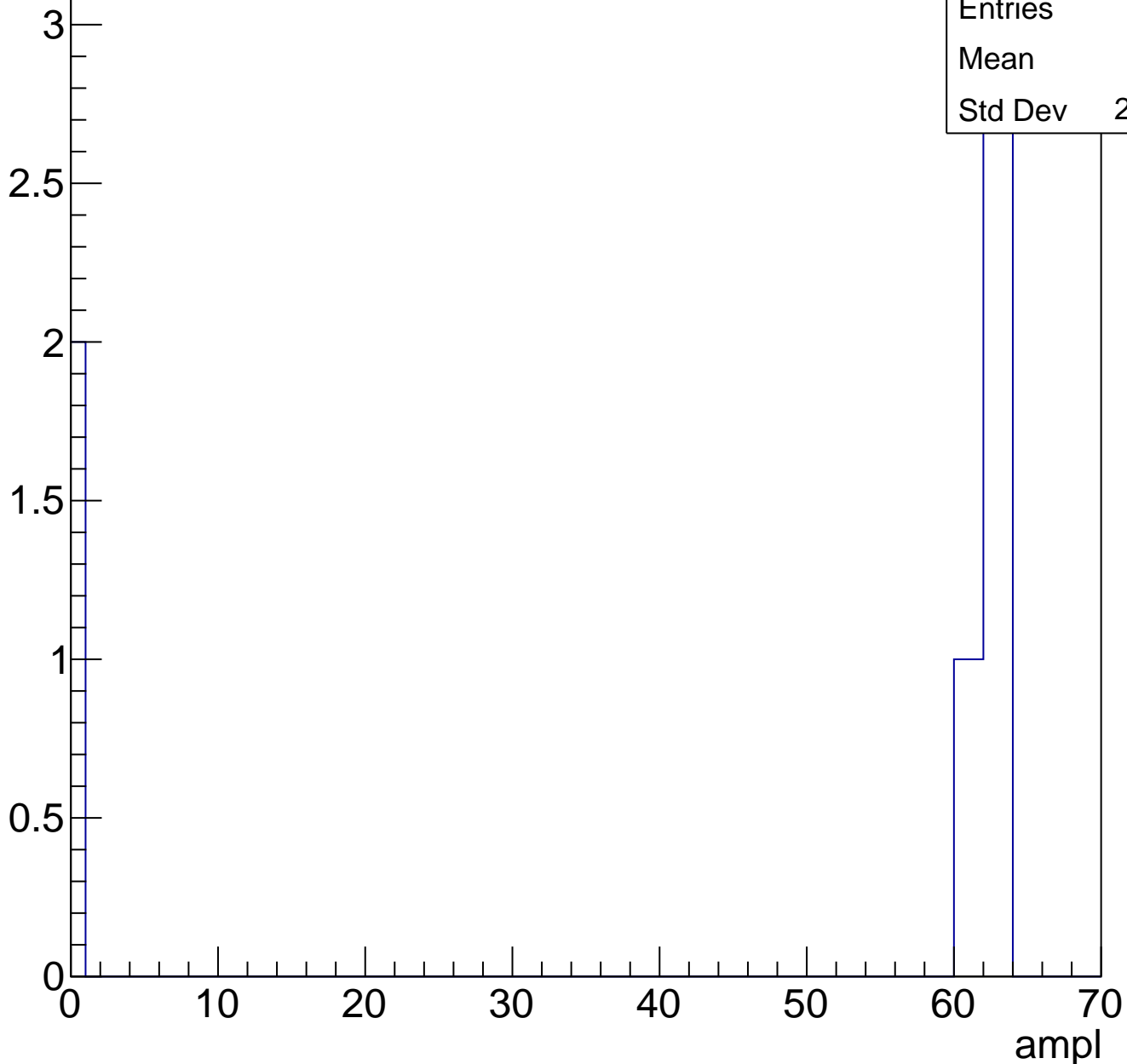




# B1L002S, U3-ch54, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch55, adc0

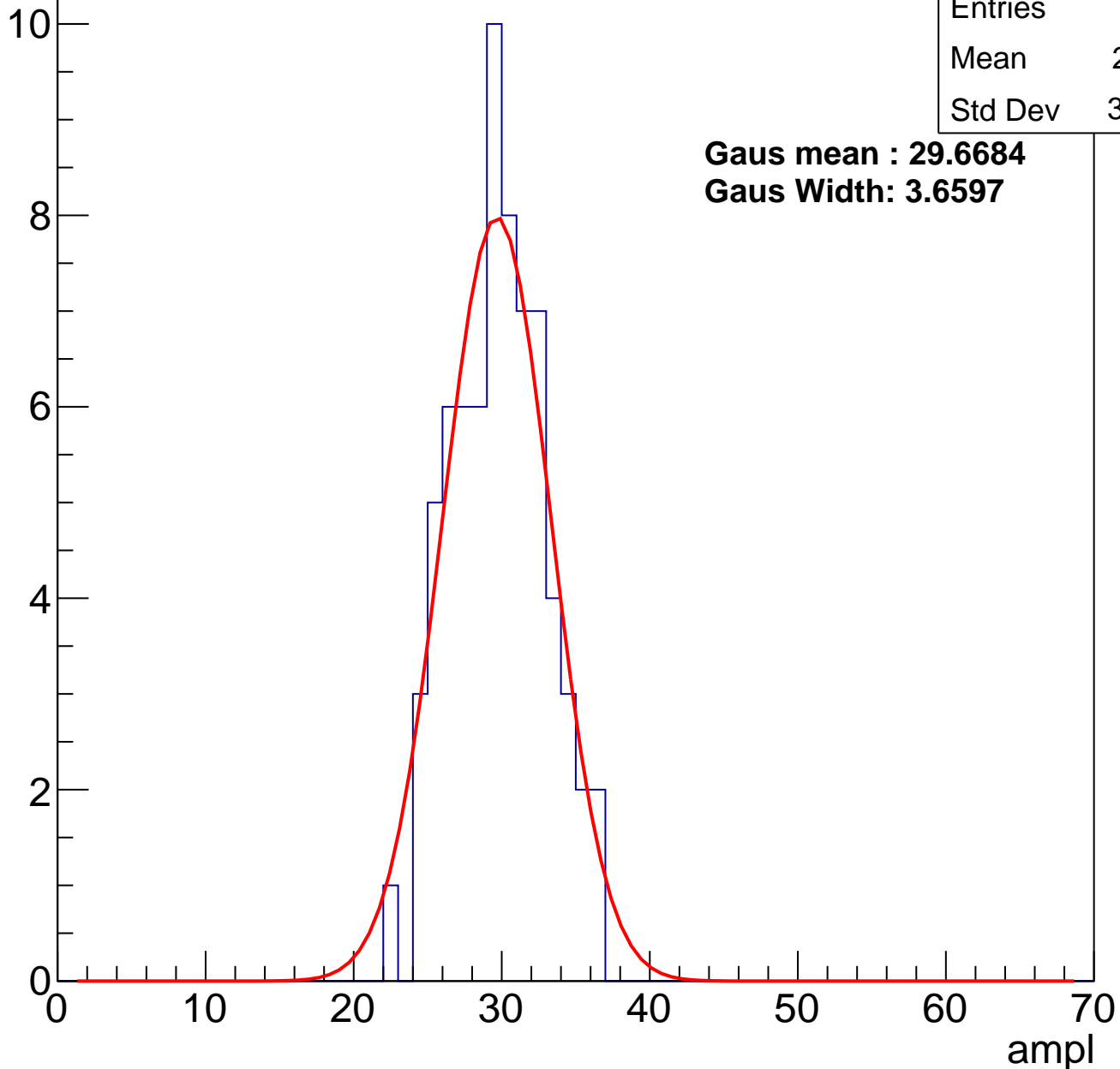
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	29.31
Std Dev	3.138

**Gaus mean : 29.6684**

**Gaus Width: 3.6597**

Entry



# B1L002S, U3-ch55, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	96
Mean	37.55
Std Dev	4.085

**Gaus mean : 37.8337**

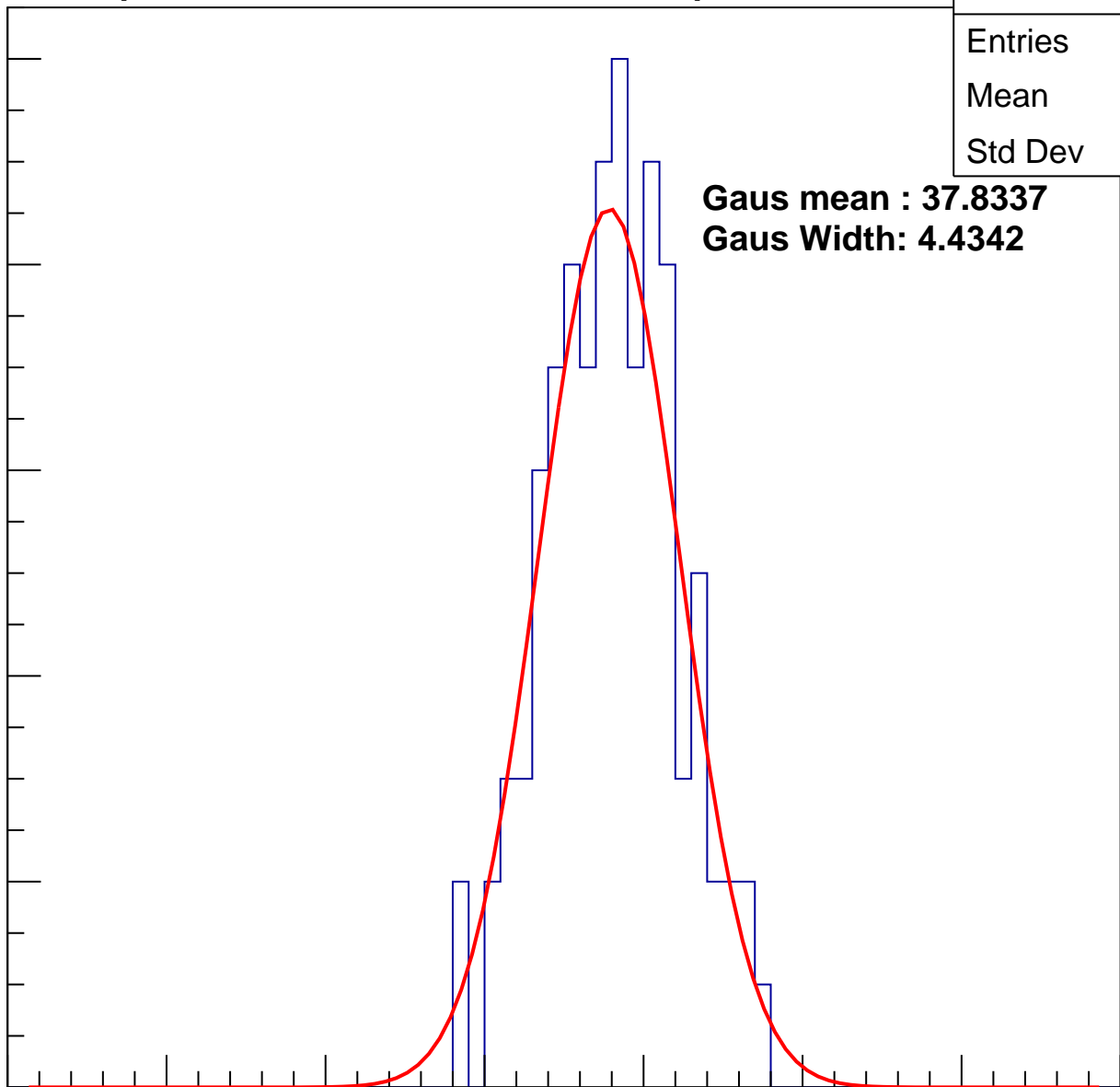
**Gaus Width: 4.4342**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch55, adc2

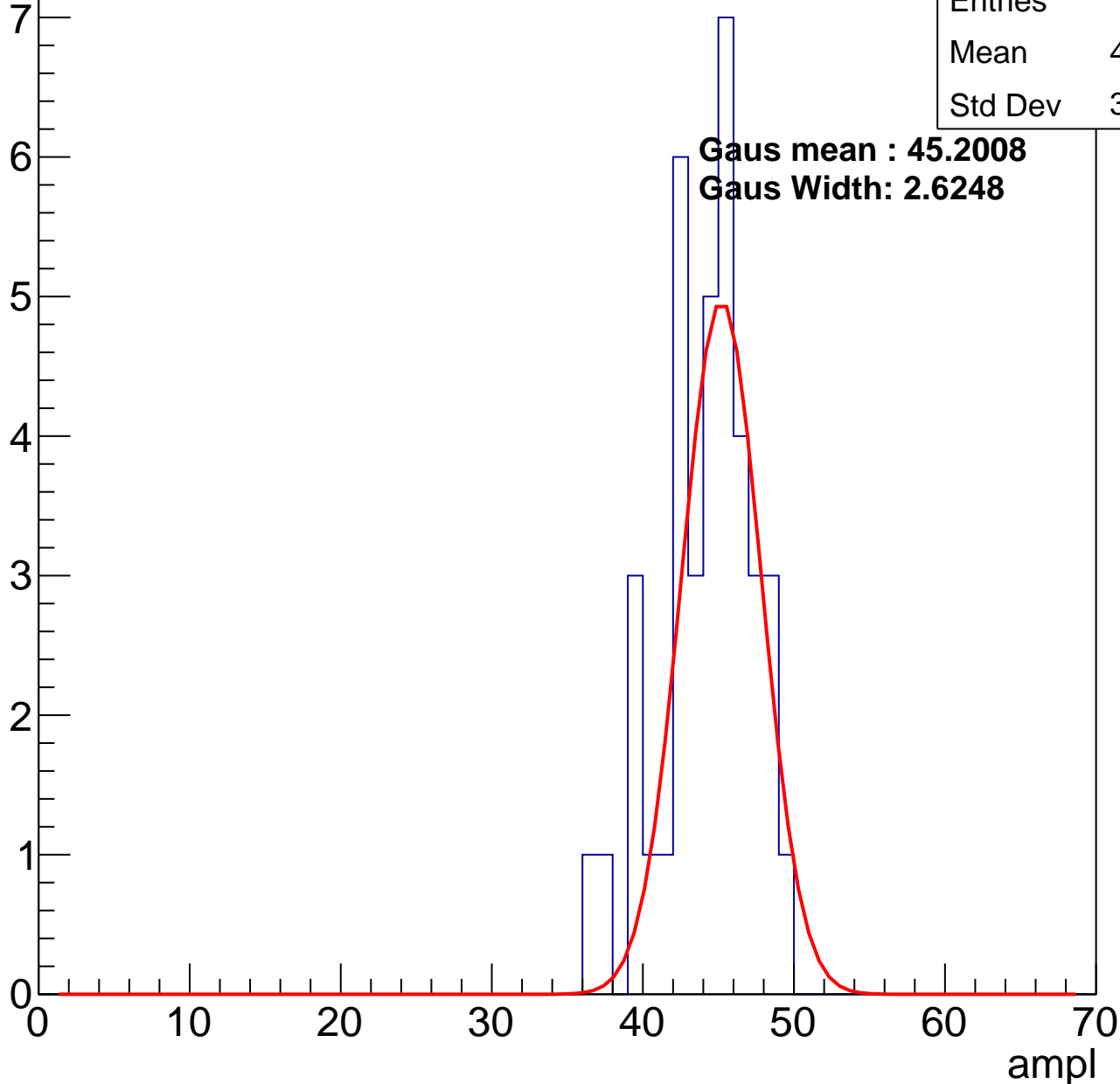
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	43.72
Std Dev	3.038

**Gaus mean : 45.2008**

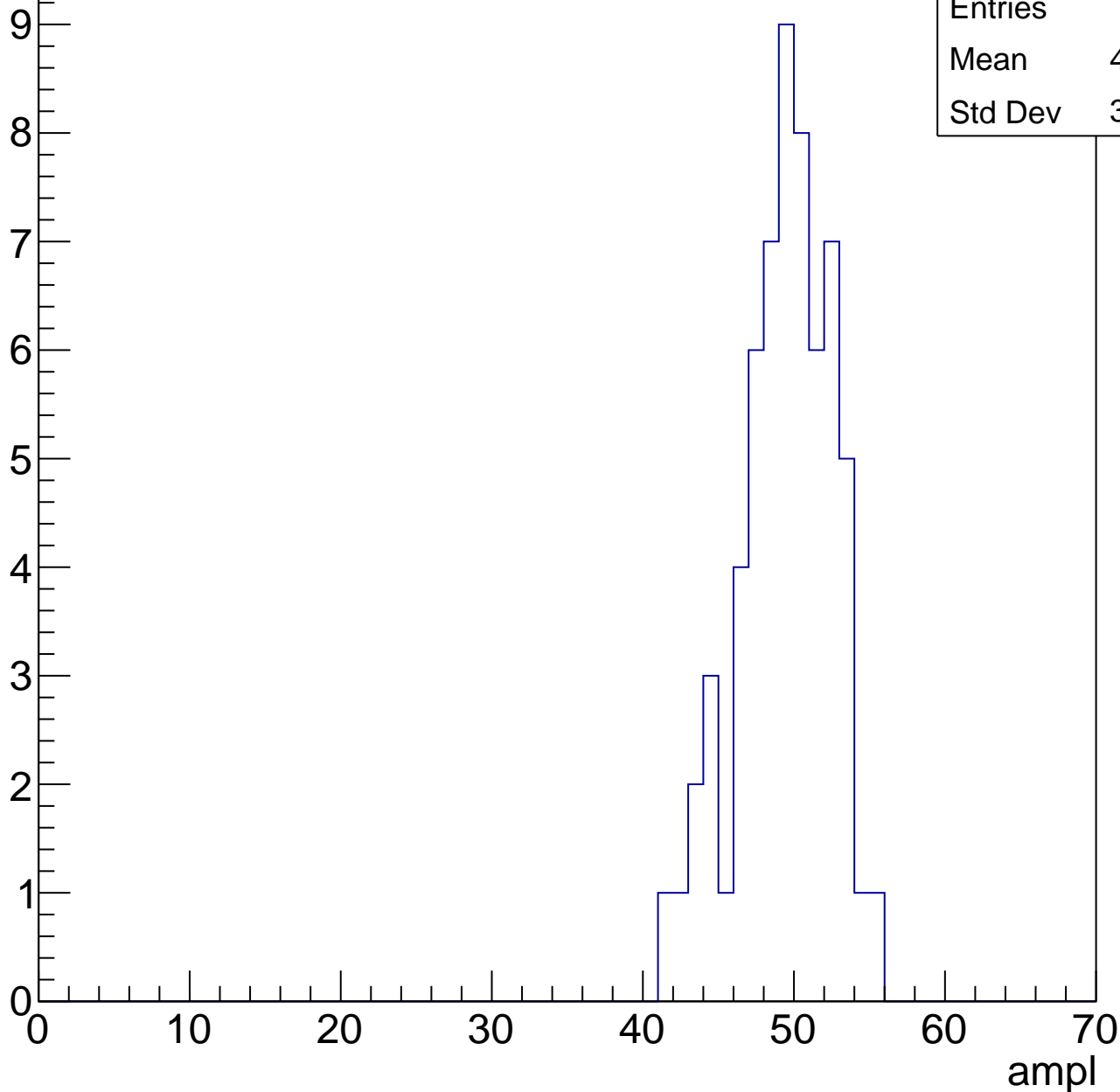
**Gaus Width: 2.6248**



# B1L002S, U3-ch55, adc3

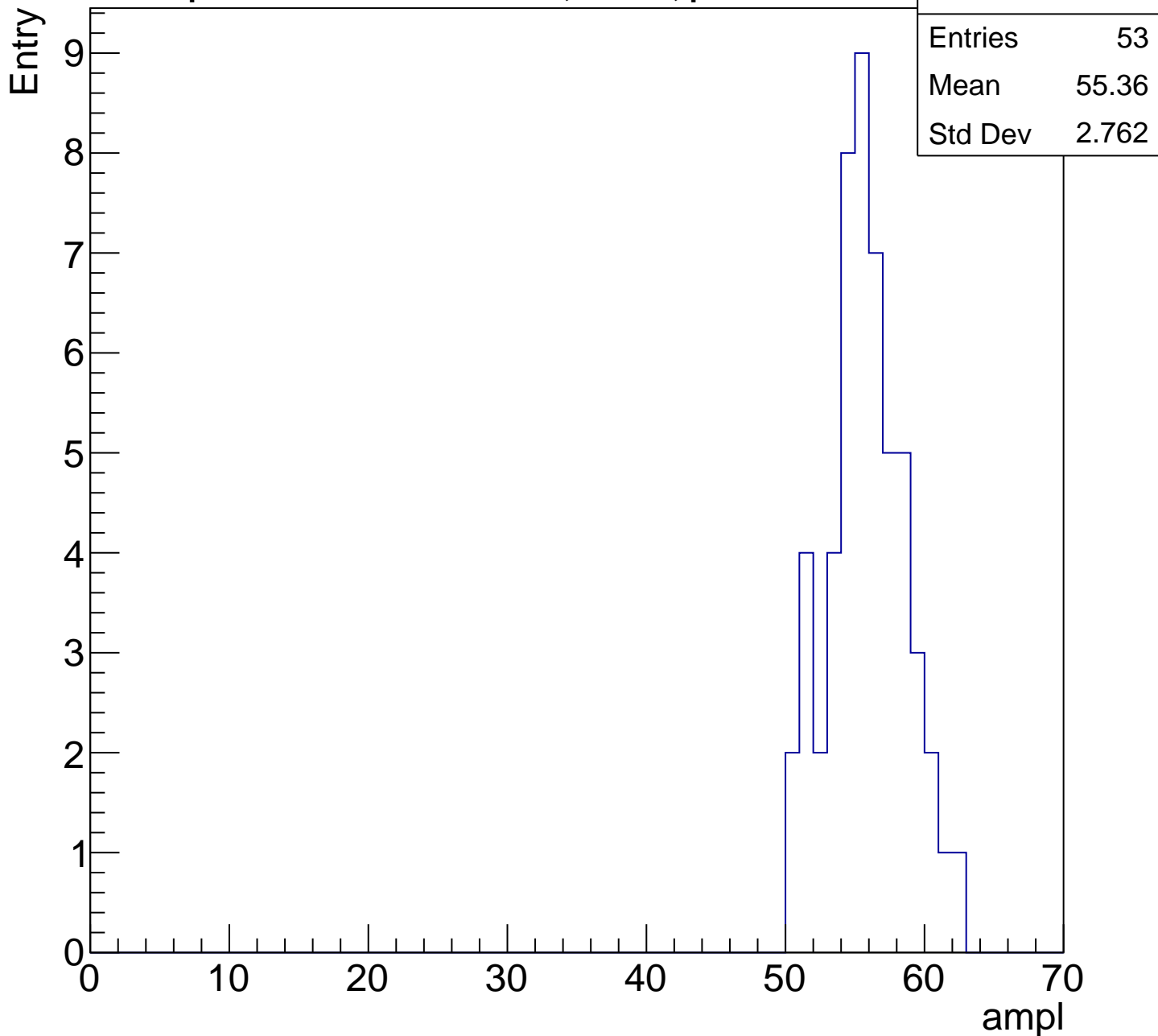
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch55, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch55, adc5

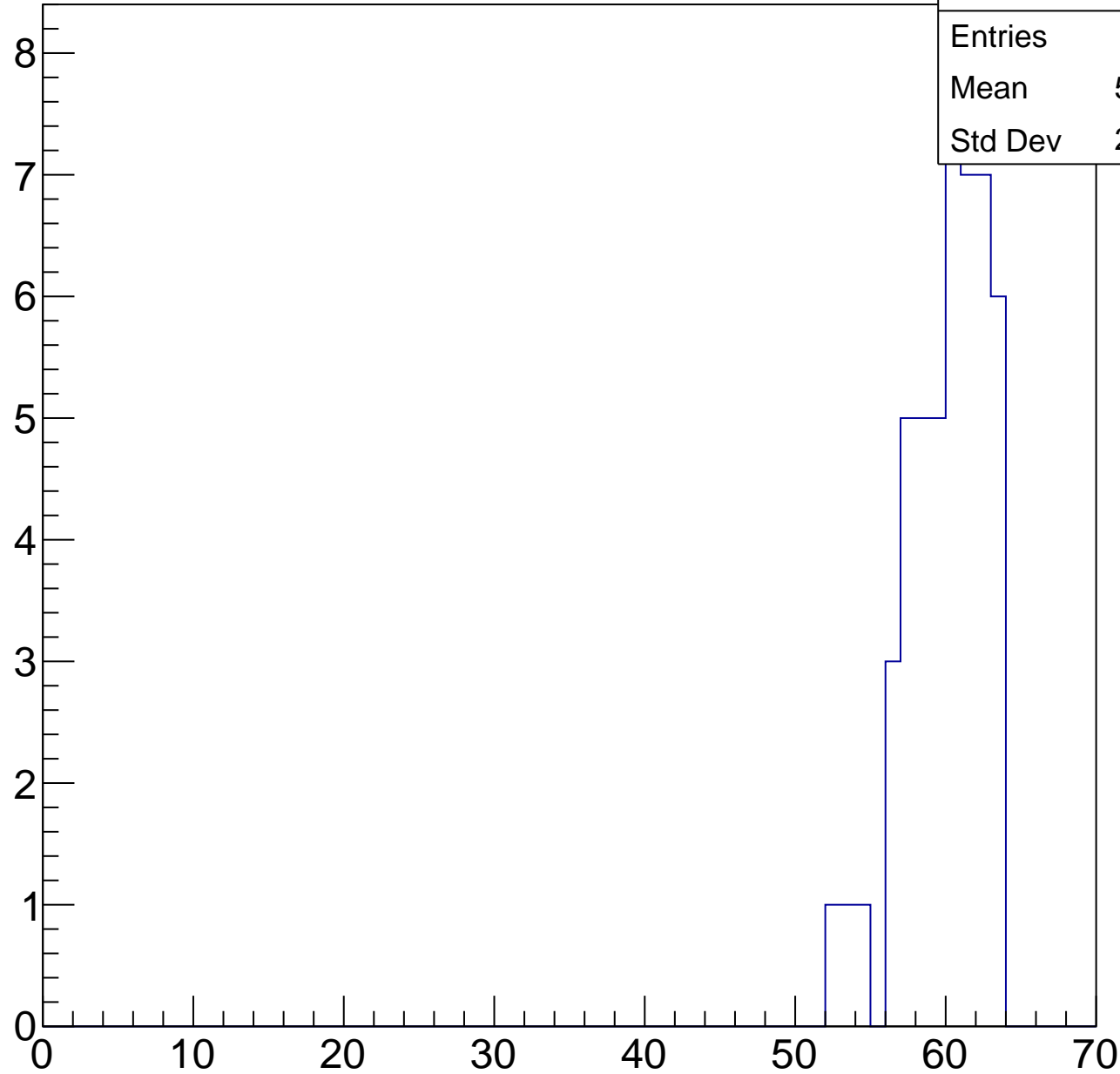
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	59.51
Std Dev	2.651

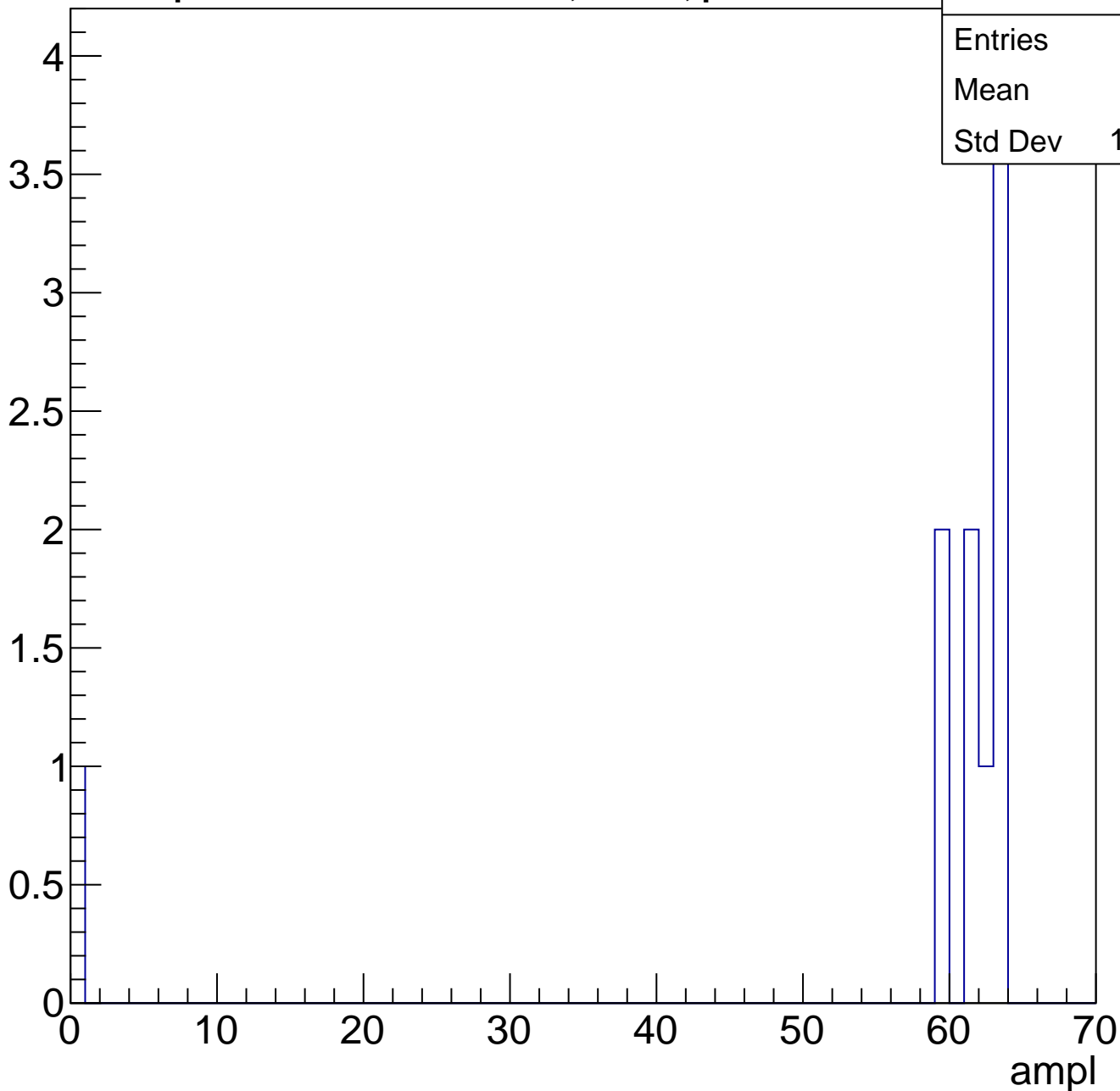
ampl



# B1L002S, U3-ch55, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

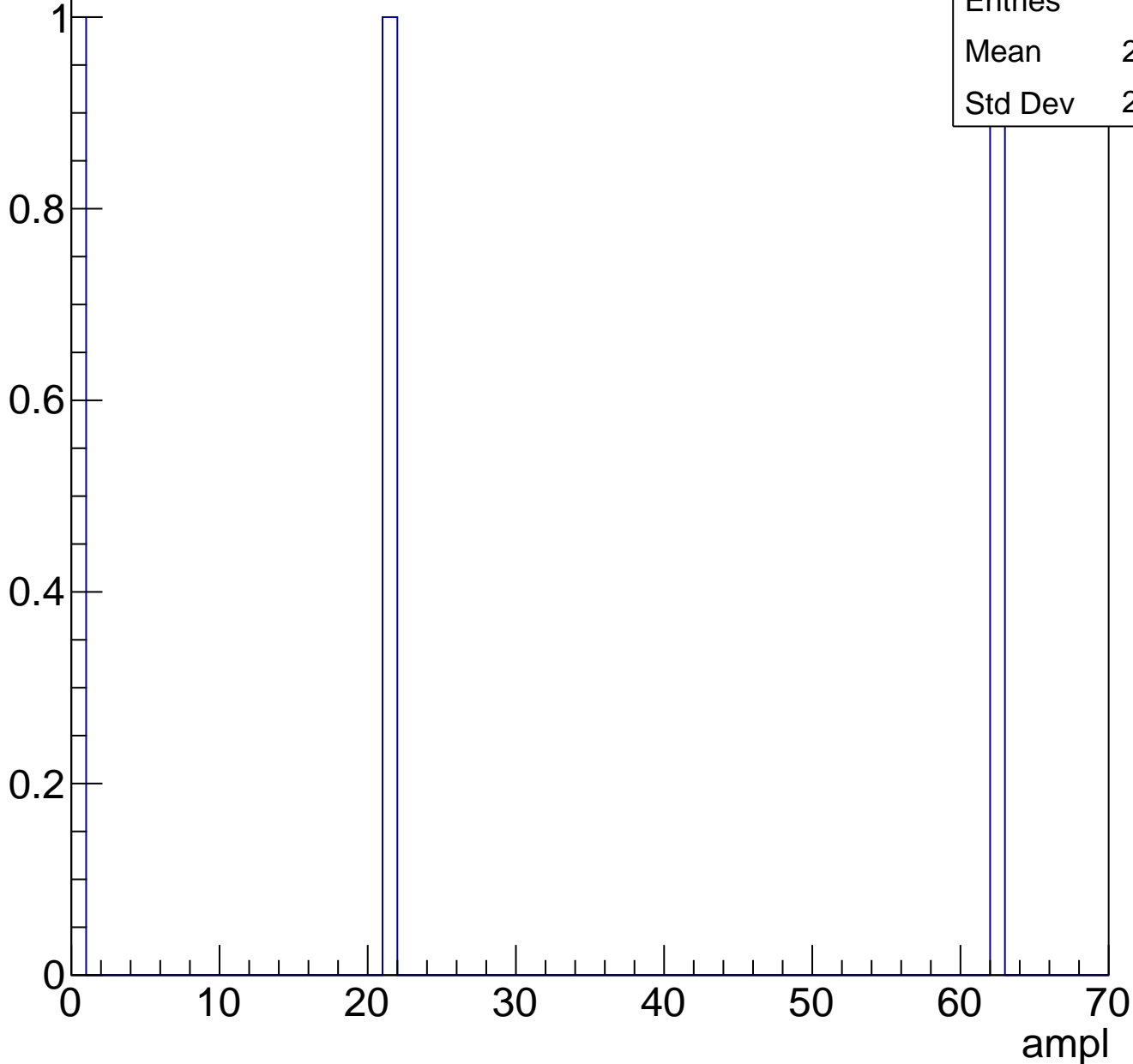




# B1L002S, U3-ch55, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.67
Std Dev	25.75

# B1L002S, U3-ch56, adc0

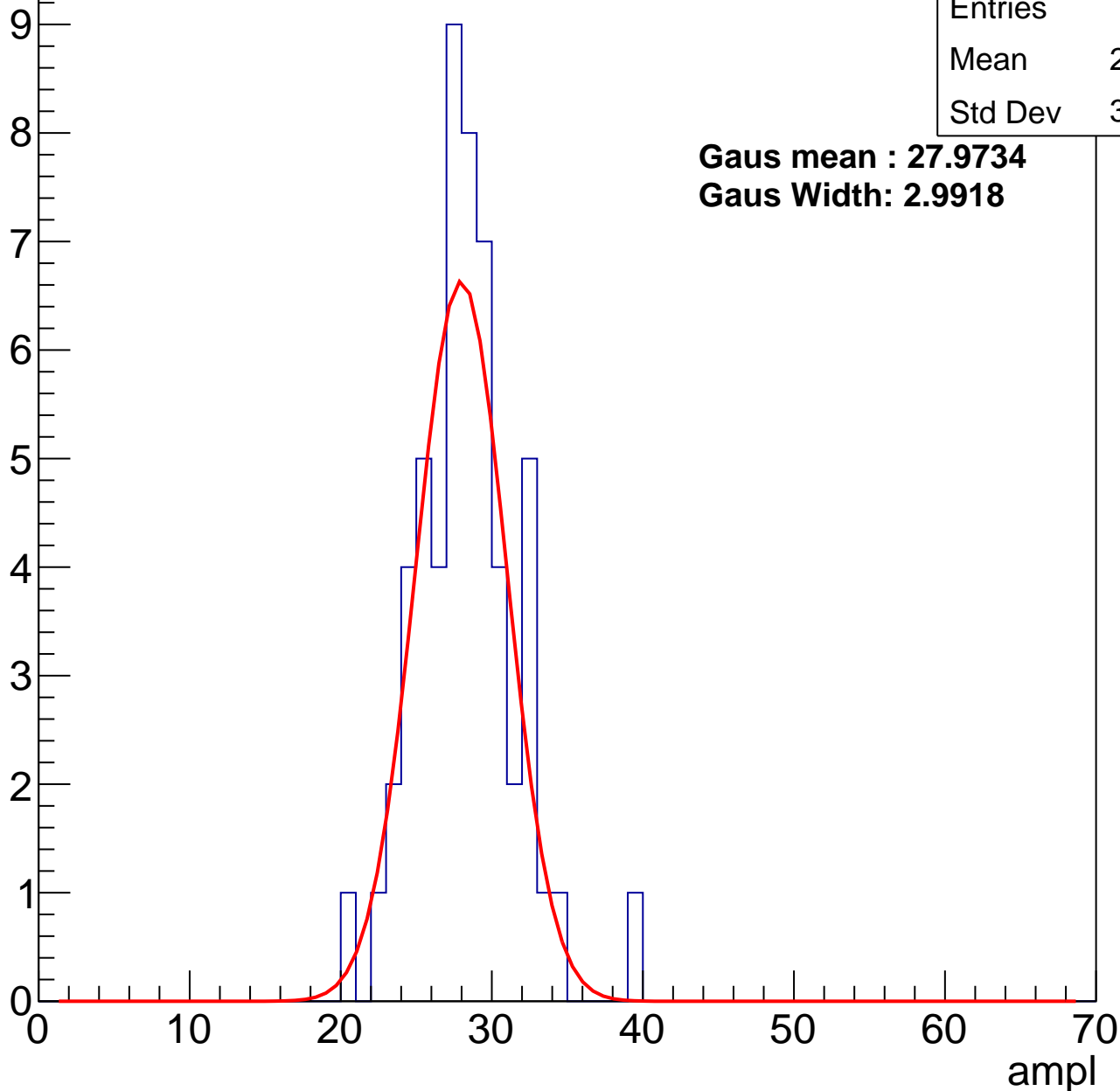
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	27.84
Std Dev	3.263

**Gaus mean : 27.9734**

**Gaus Width: 2.9918**



# B1L002S, U3-ch56, adc1

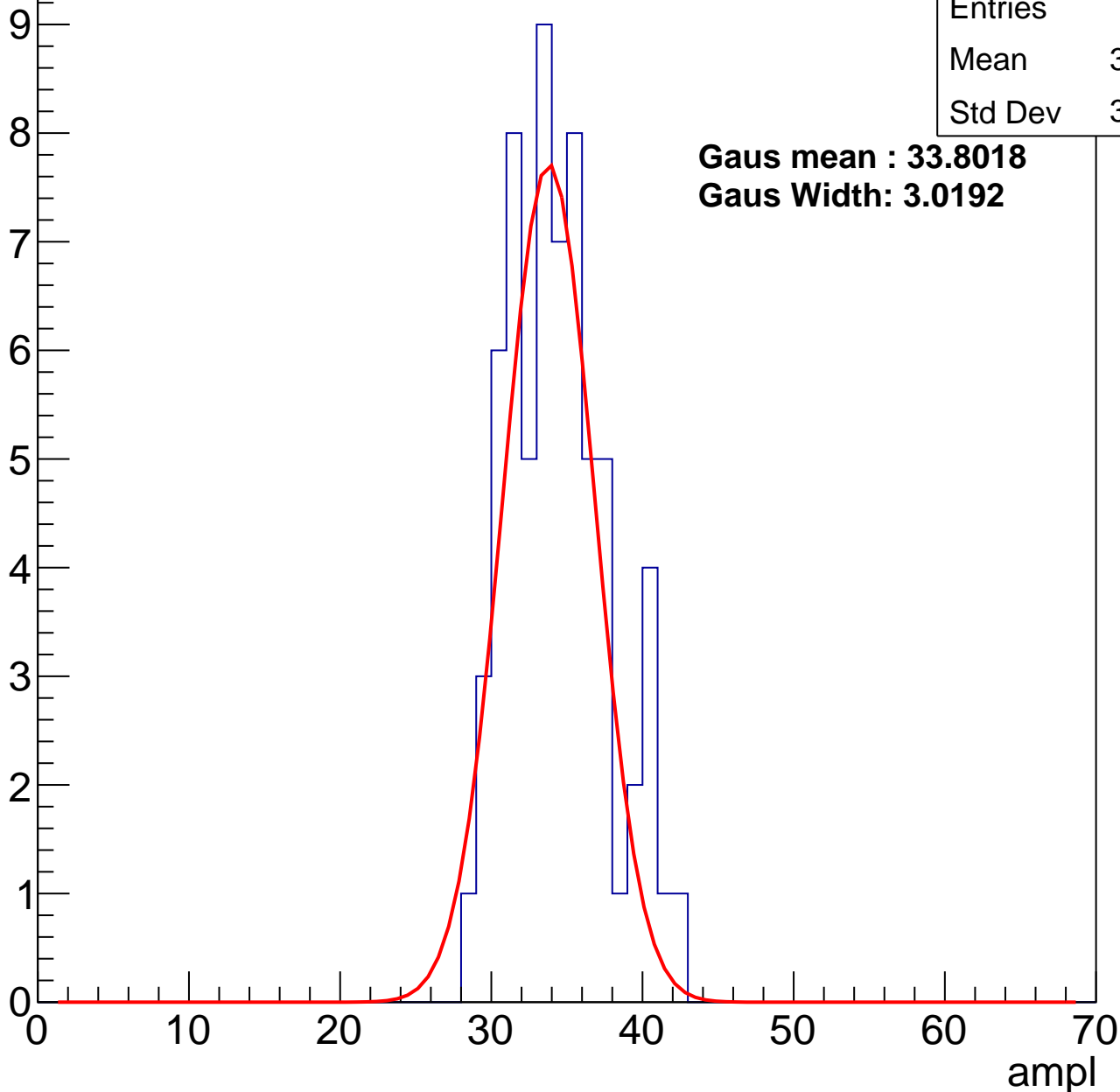
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.97
Std Dev	3.266

**Gaus mean : 33.8018**

**Gaus Width: 3.0192**



# B1L002S, U3-ch56, adc2

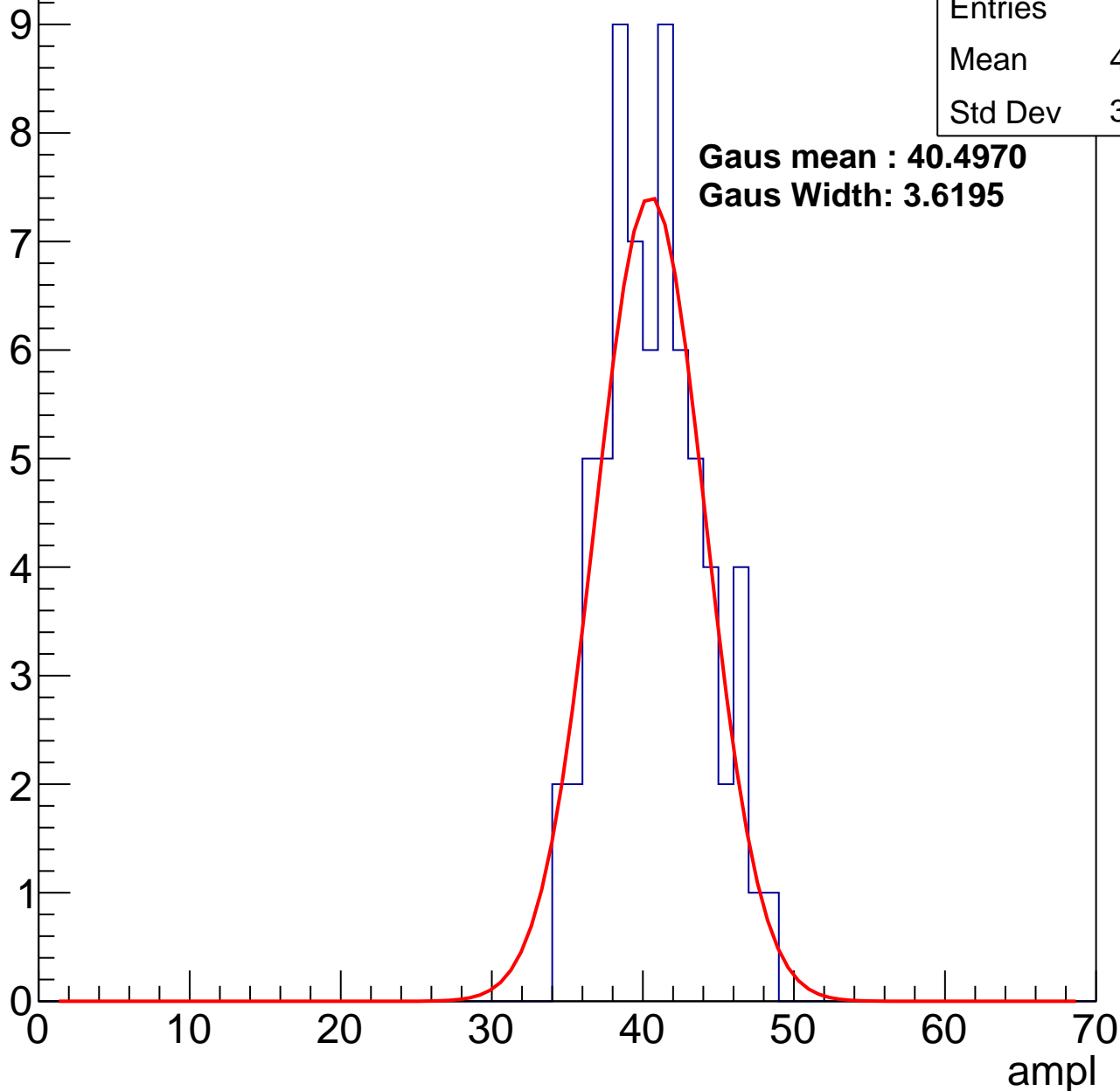
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.28
Std Dev	3.285

**Gaus mean : 40.4970**

**Gaus Width: 3.6195**



# B1L002S, U3-ch56, adc3

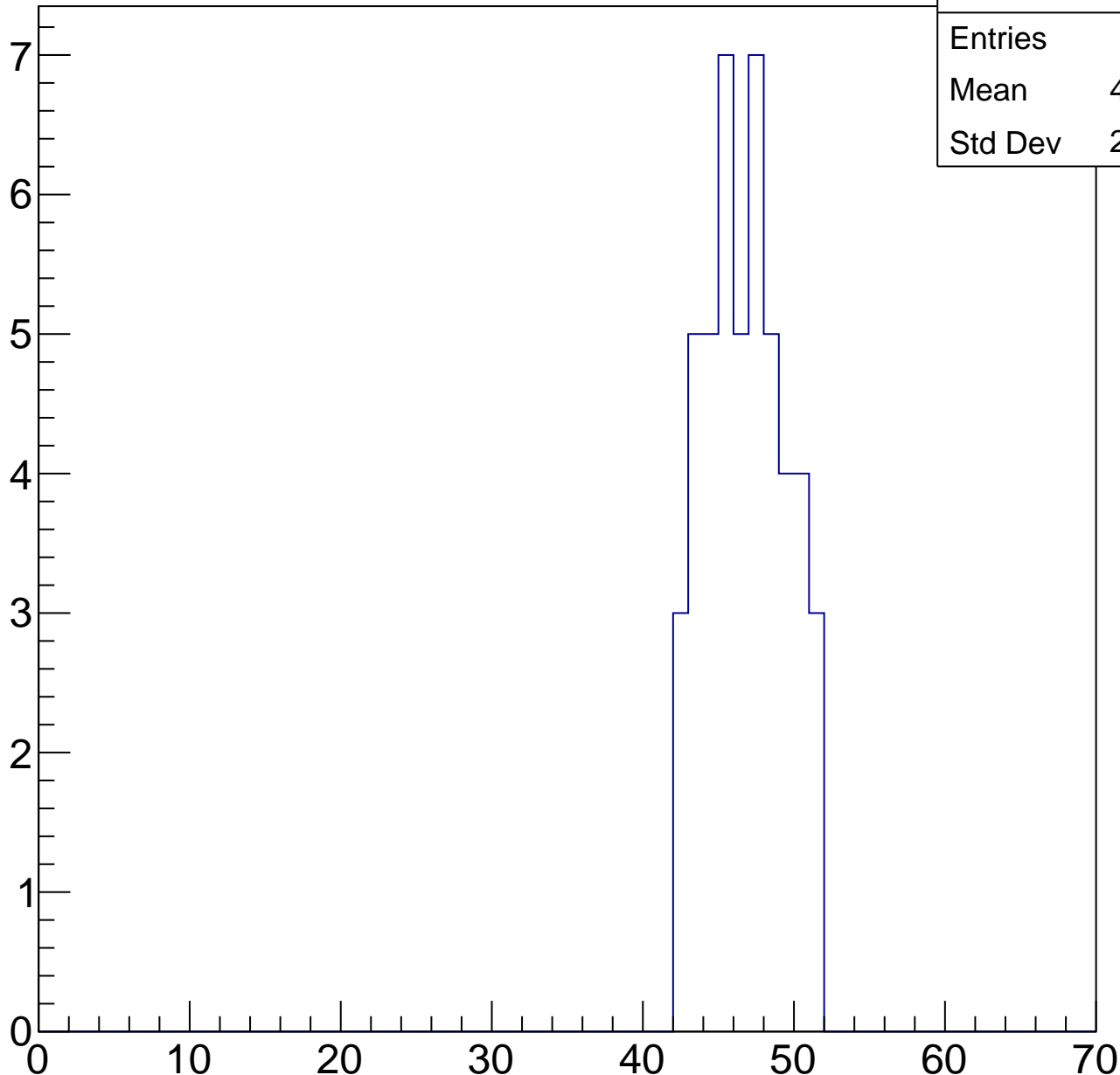
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	48
Mean	46.33
Std Dev	2.569

ampl

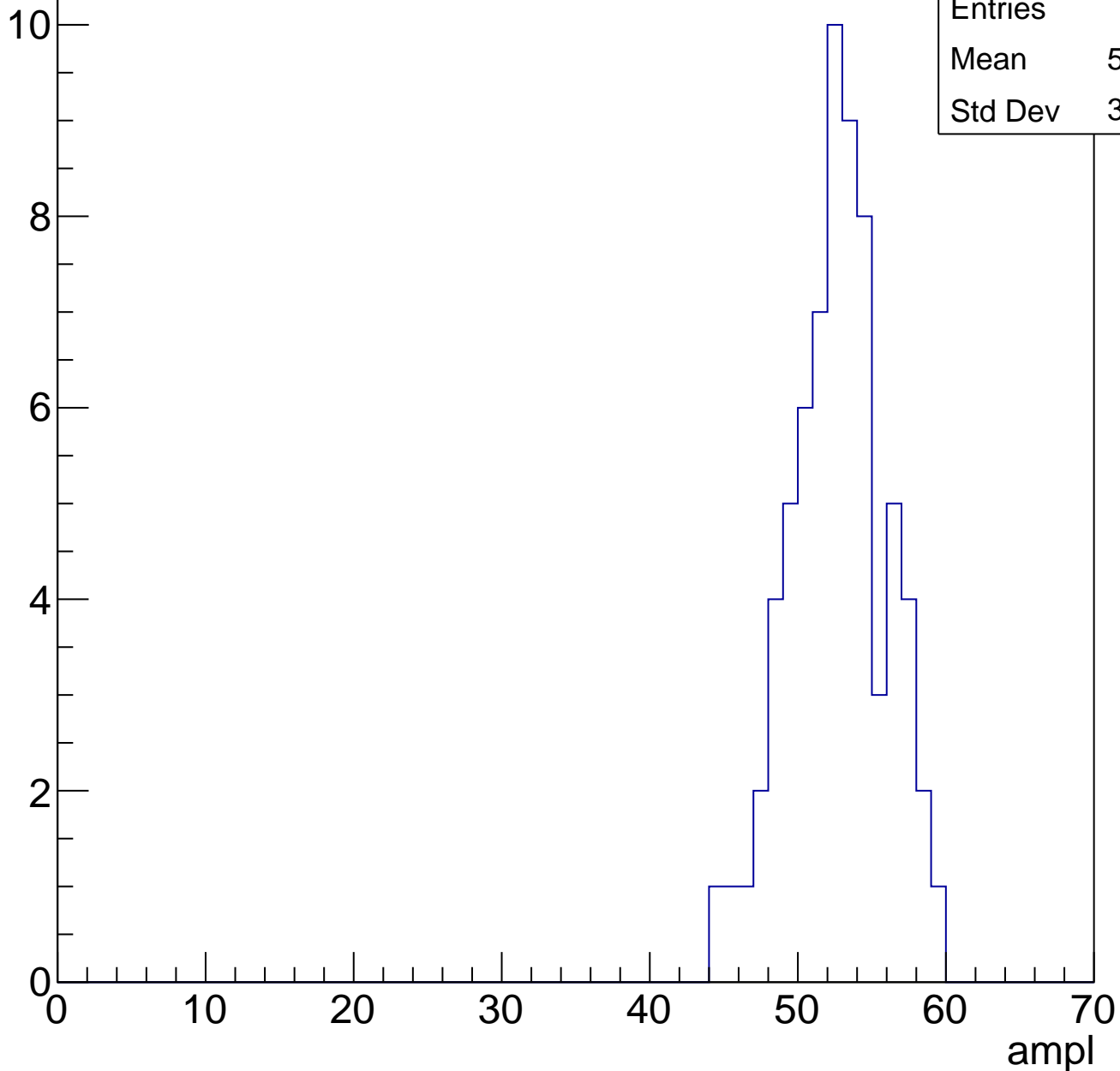


# B1L002S, U3-ch56, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

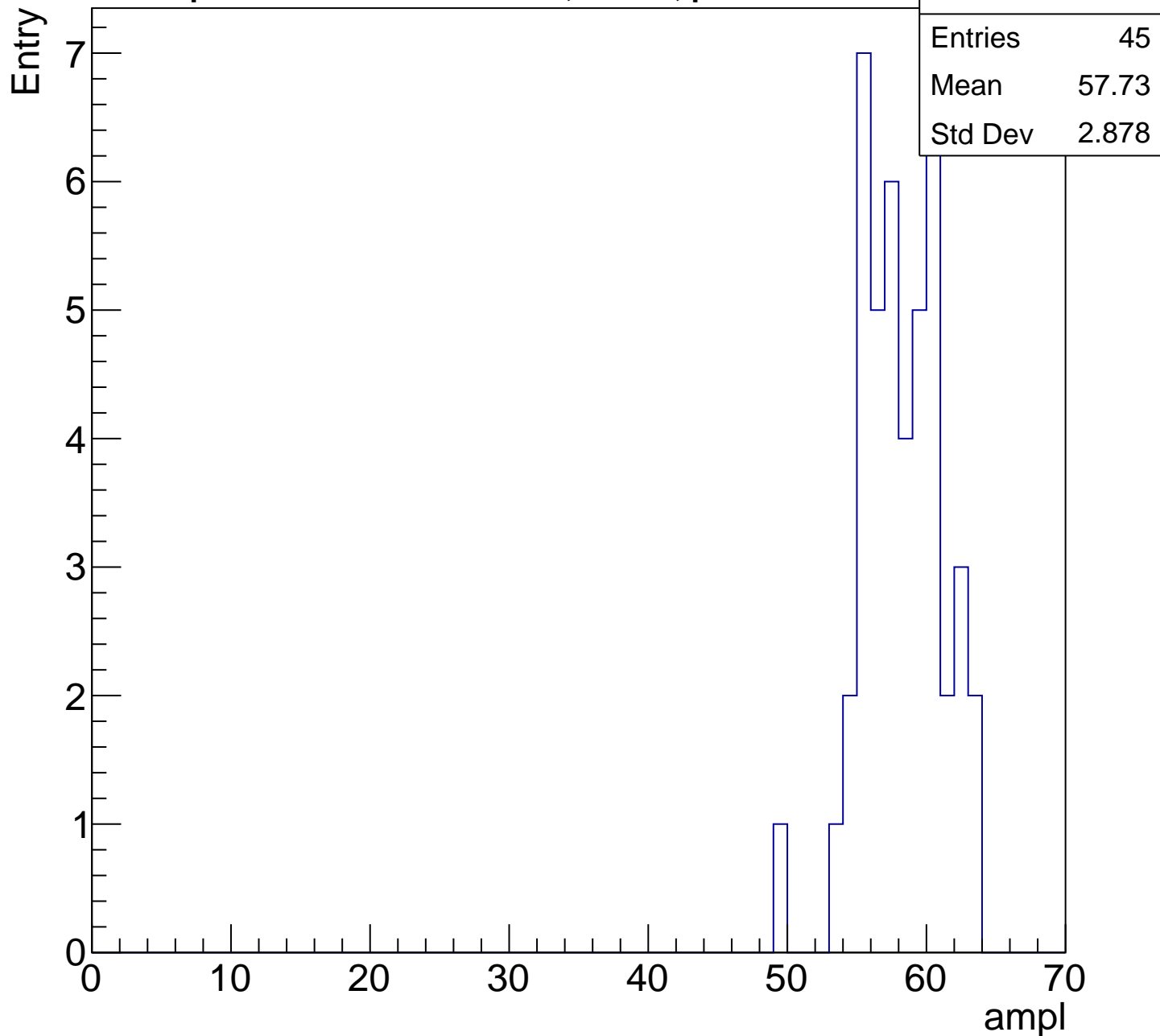
Entries	69
Mean	52.17
Std Dev	3.199

Entry



# B1L002S, U3-ch56, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

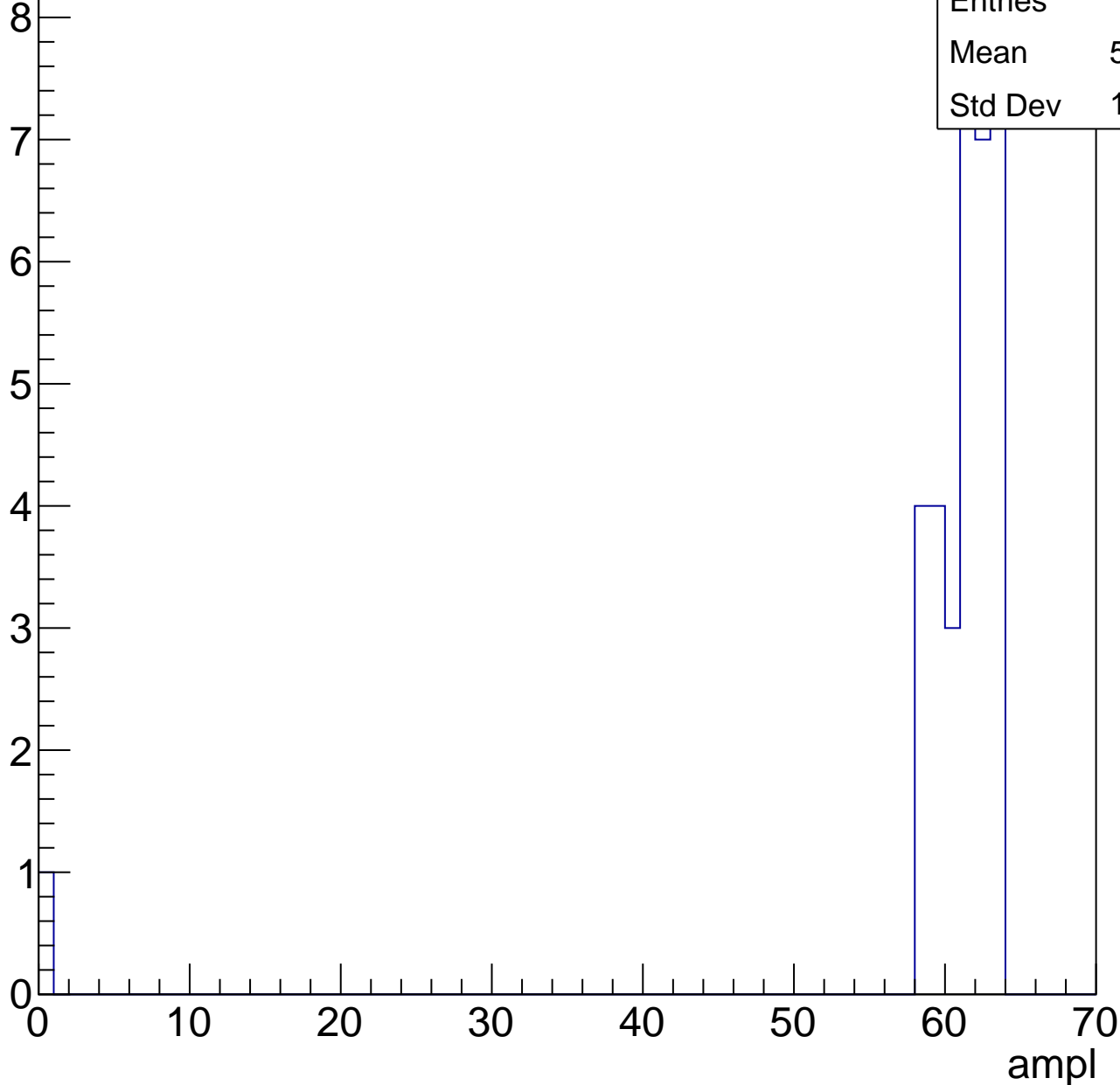


# B1L002S, U3-ch56, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	59.26
Std Dev	10.29





# B1L002S, U3-ch56, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch57, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	59
Mean	26.25
Std Dev	4.34

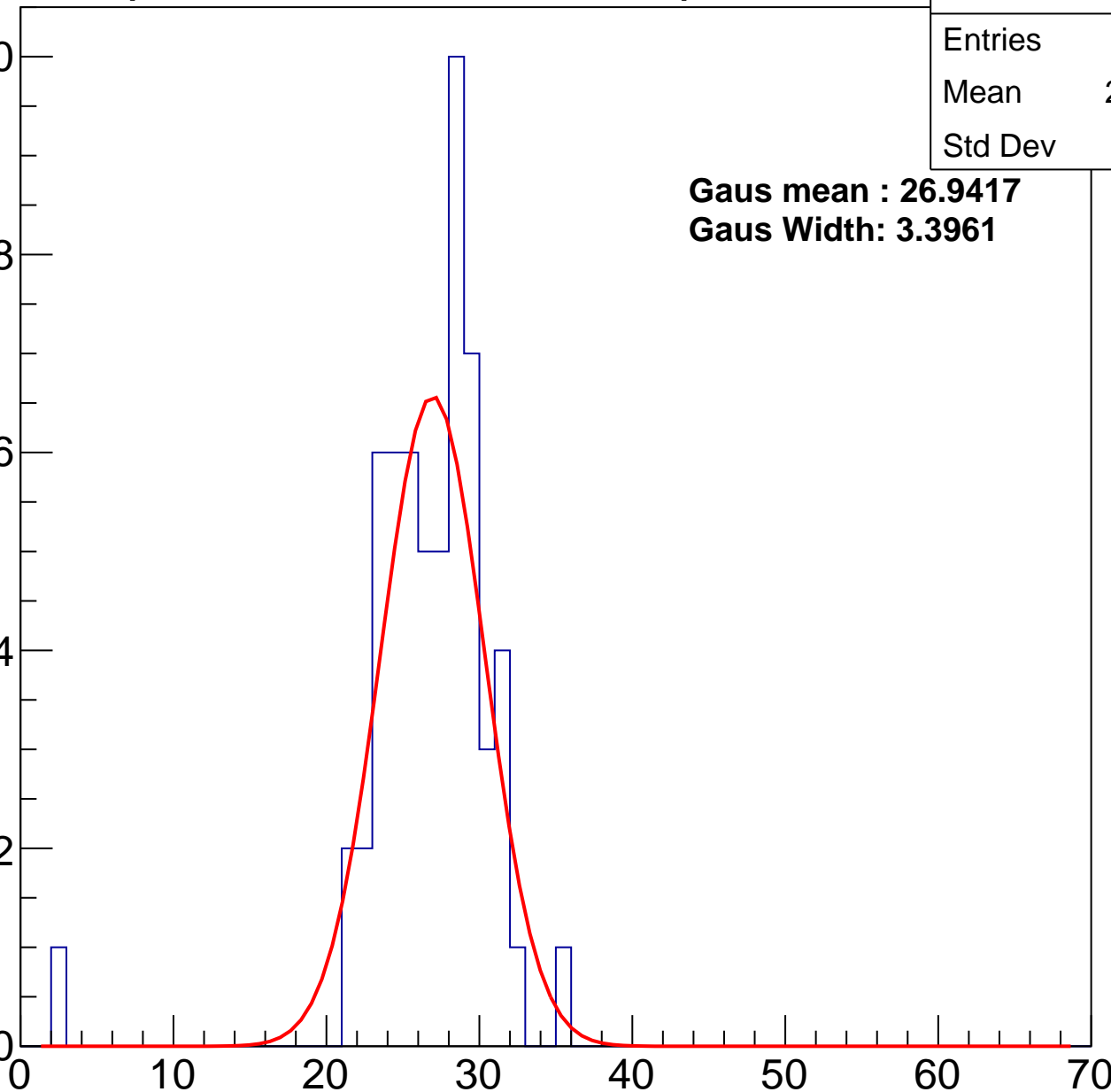
**Gaus mean : 26.9417**

**Gaus Width: 3.3961**

Entry

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U3-ch57, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	32.68
Std Dev	2.976

**Gaus mean : 33.4178**

**Gaus Width: 3.7166**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch57, adc2

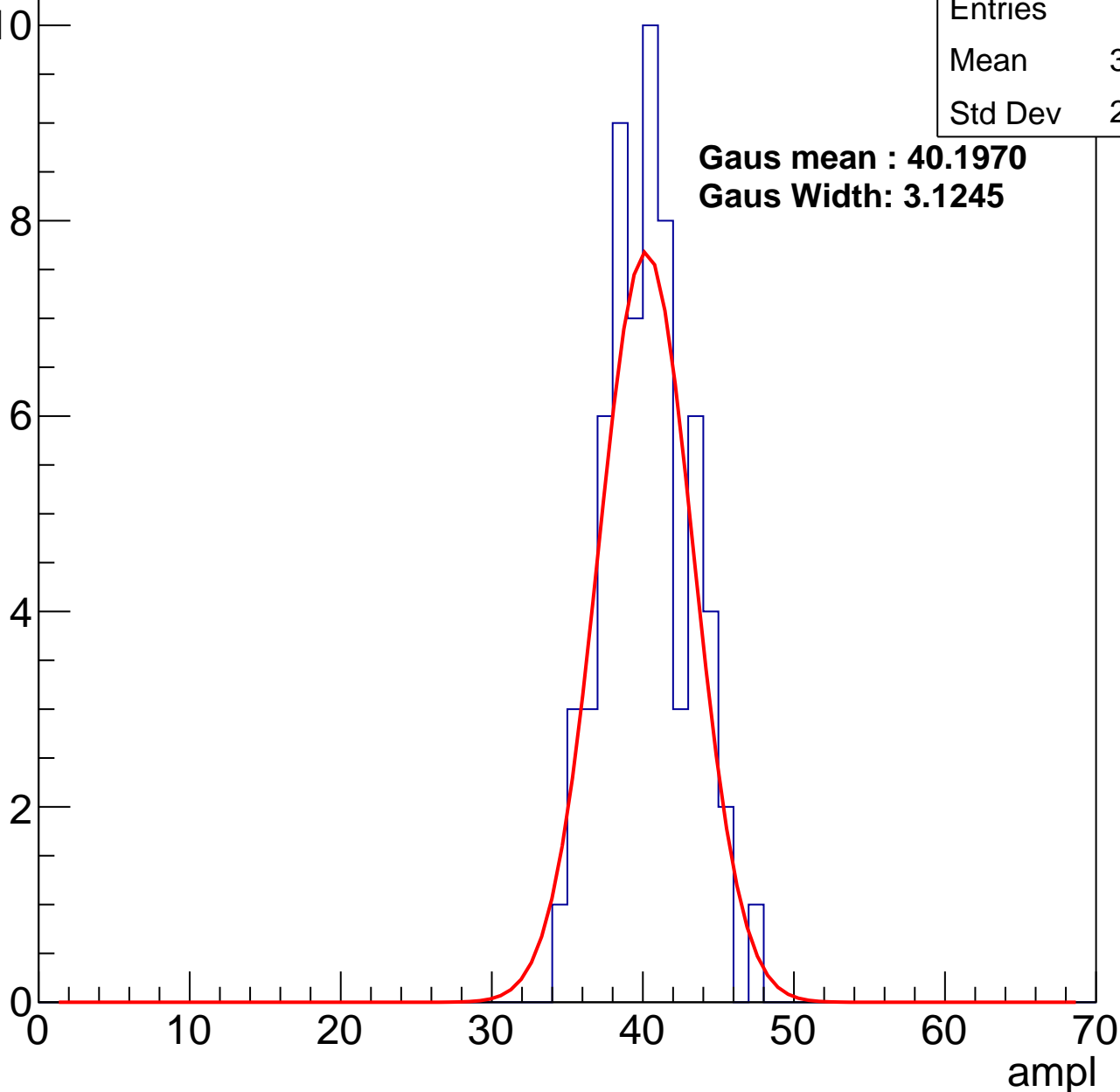
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	39.83
Std Dev	2.792

**Gaus mean : 40.1970**

**Gaus Width: 3.1245**

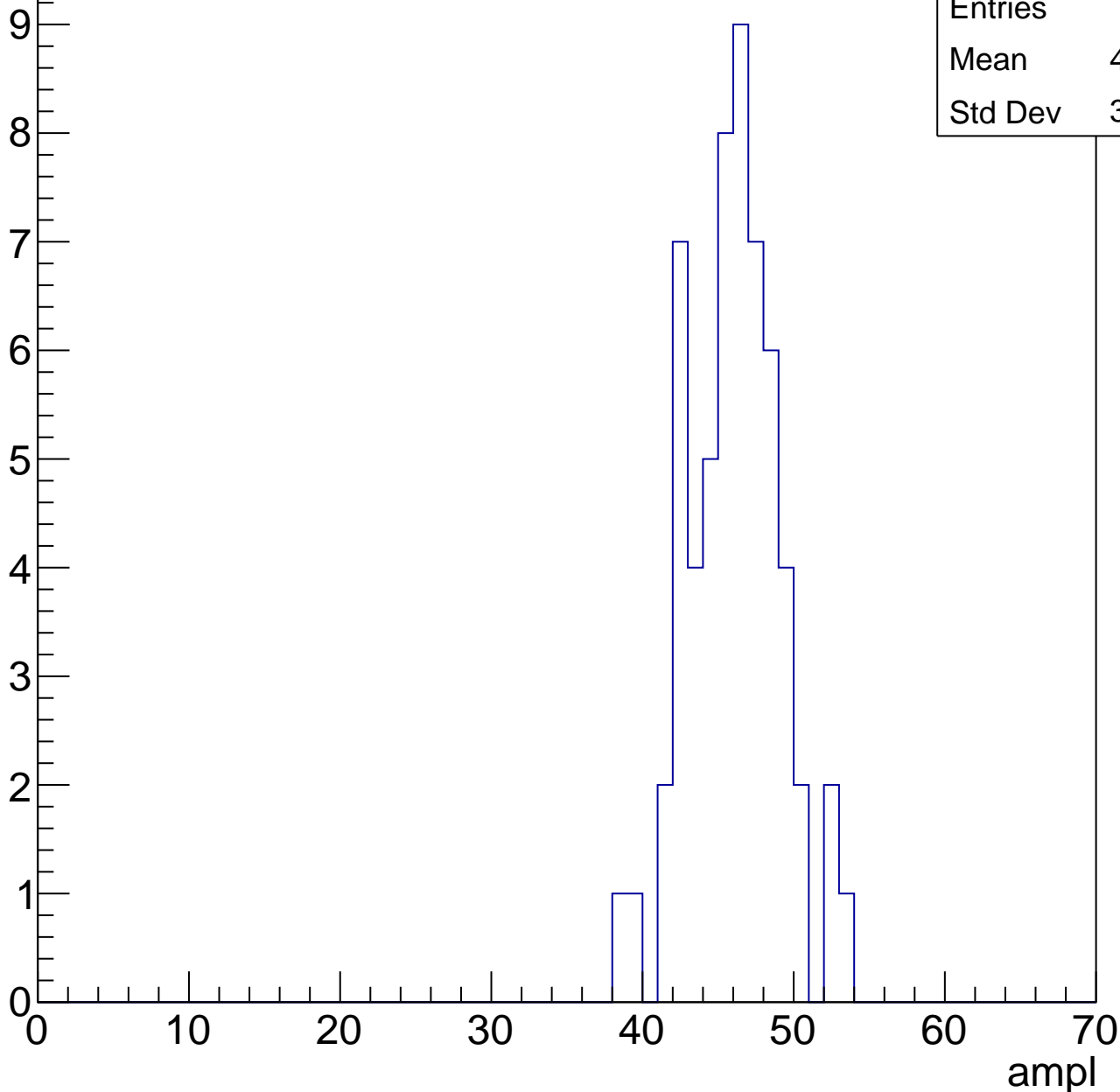


# B1L002S, U3-ch57, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	45.58
Std Dev	3.049

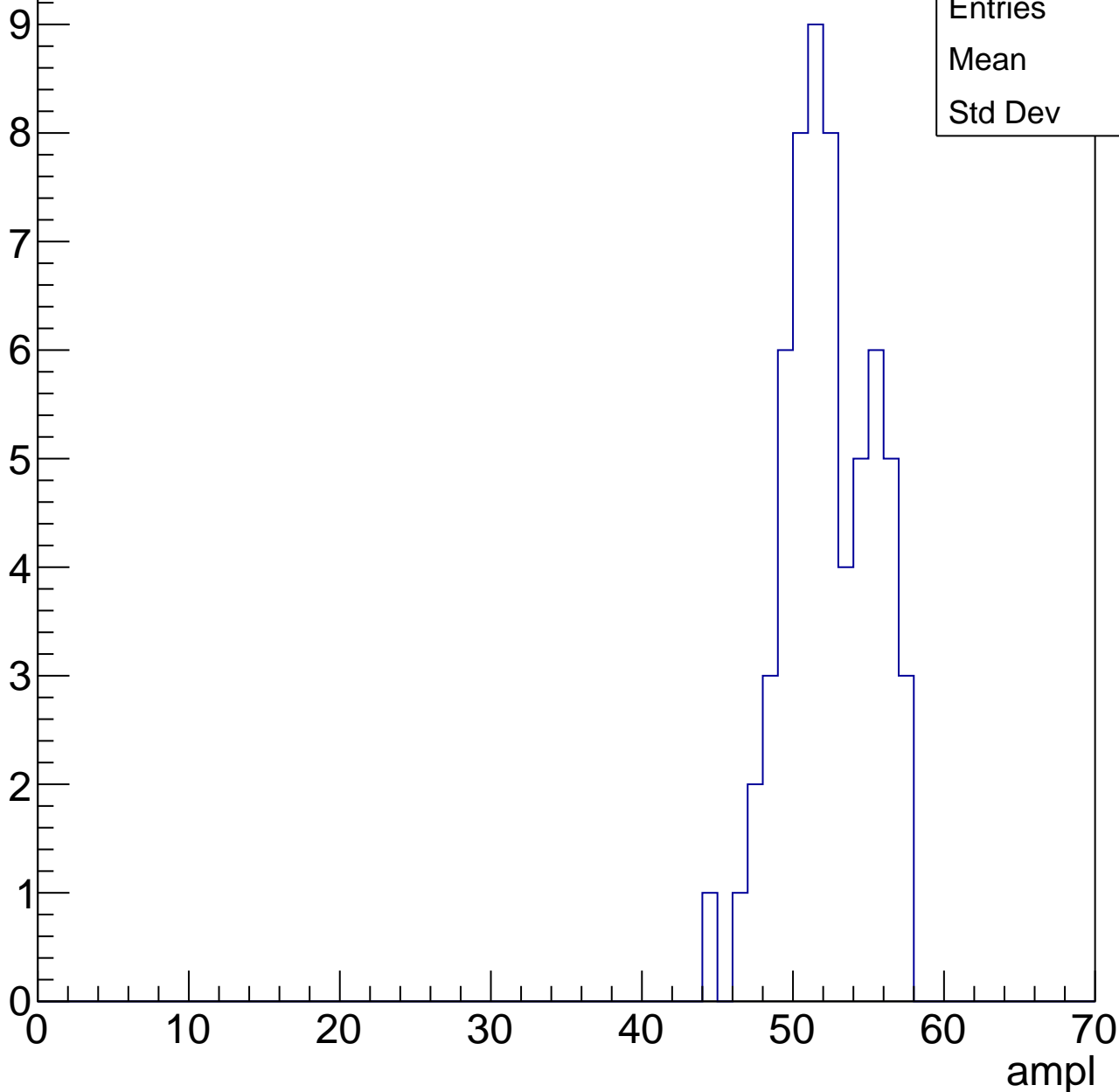


# B1L002S, U3-ch57, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

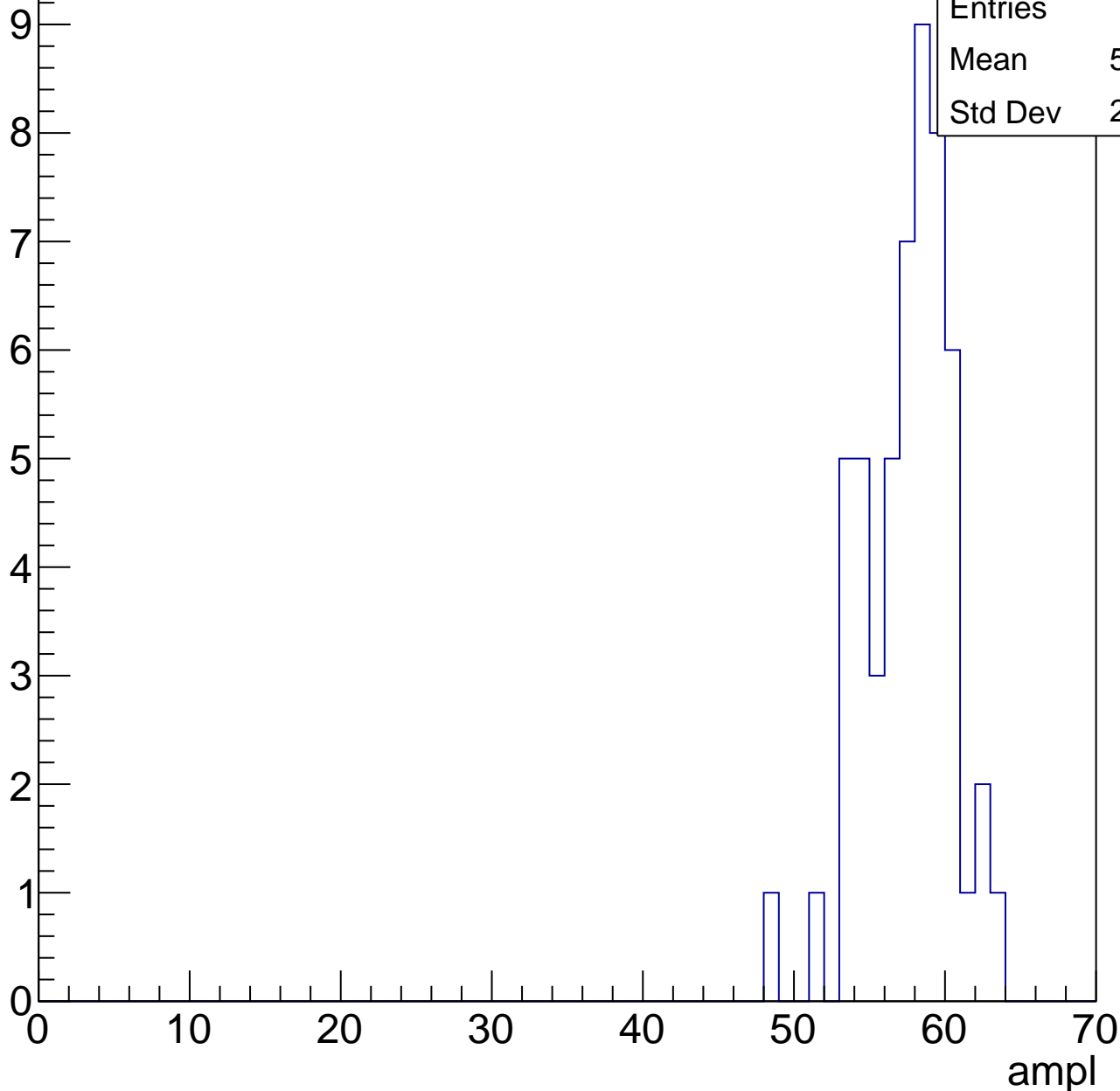
Entries	61
Mean	51.8
Std Dev	2.93



# B1L002S, U3-ch57, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

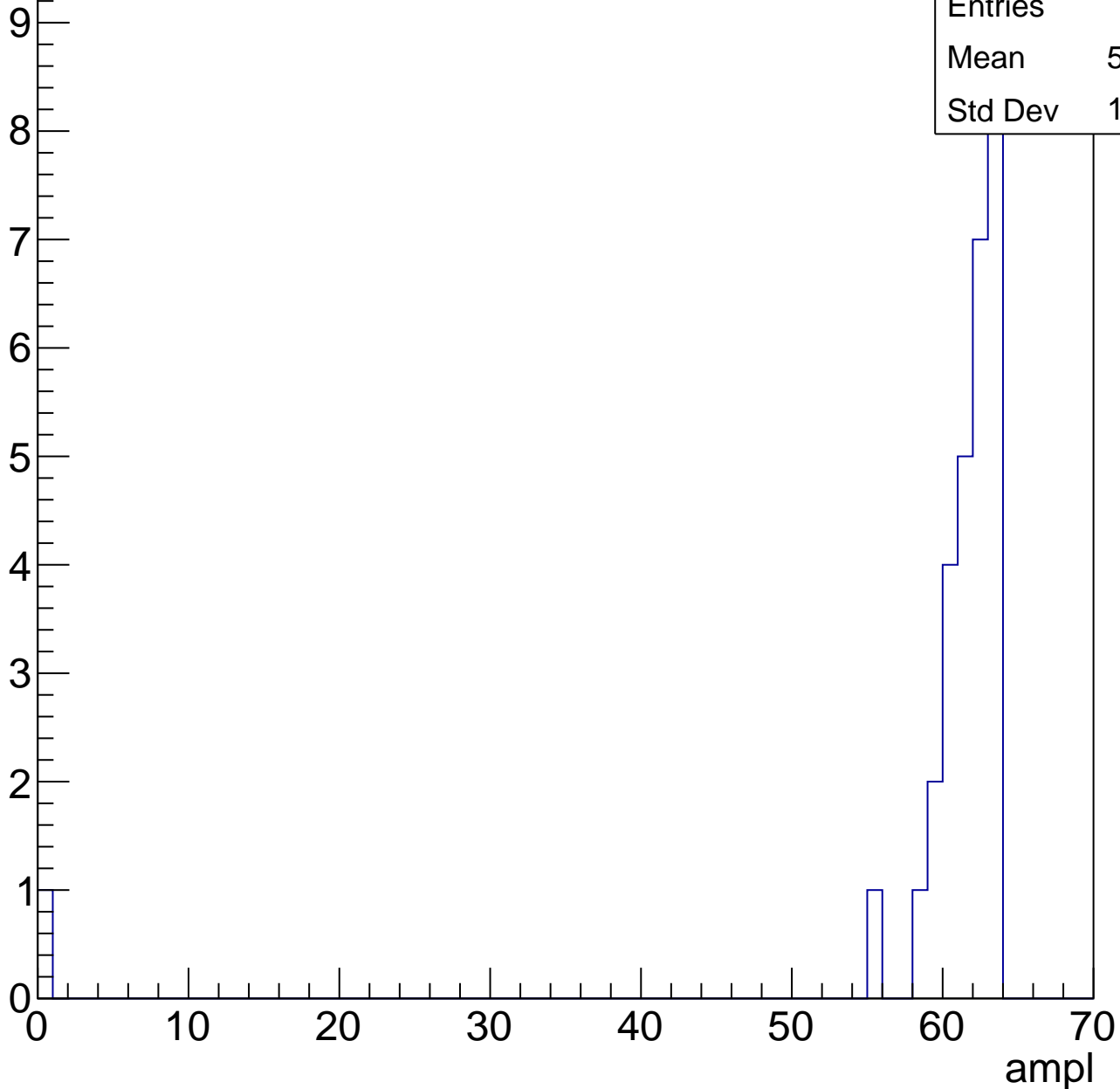


# B1L002S, U3-ch57, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	59.23
Std Dev	11.15

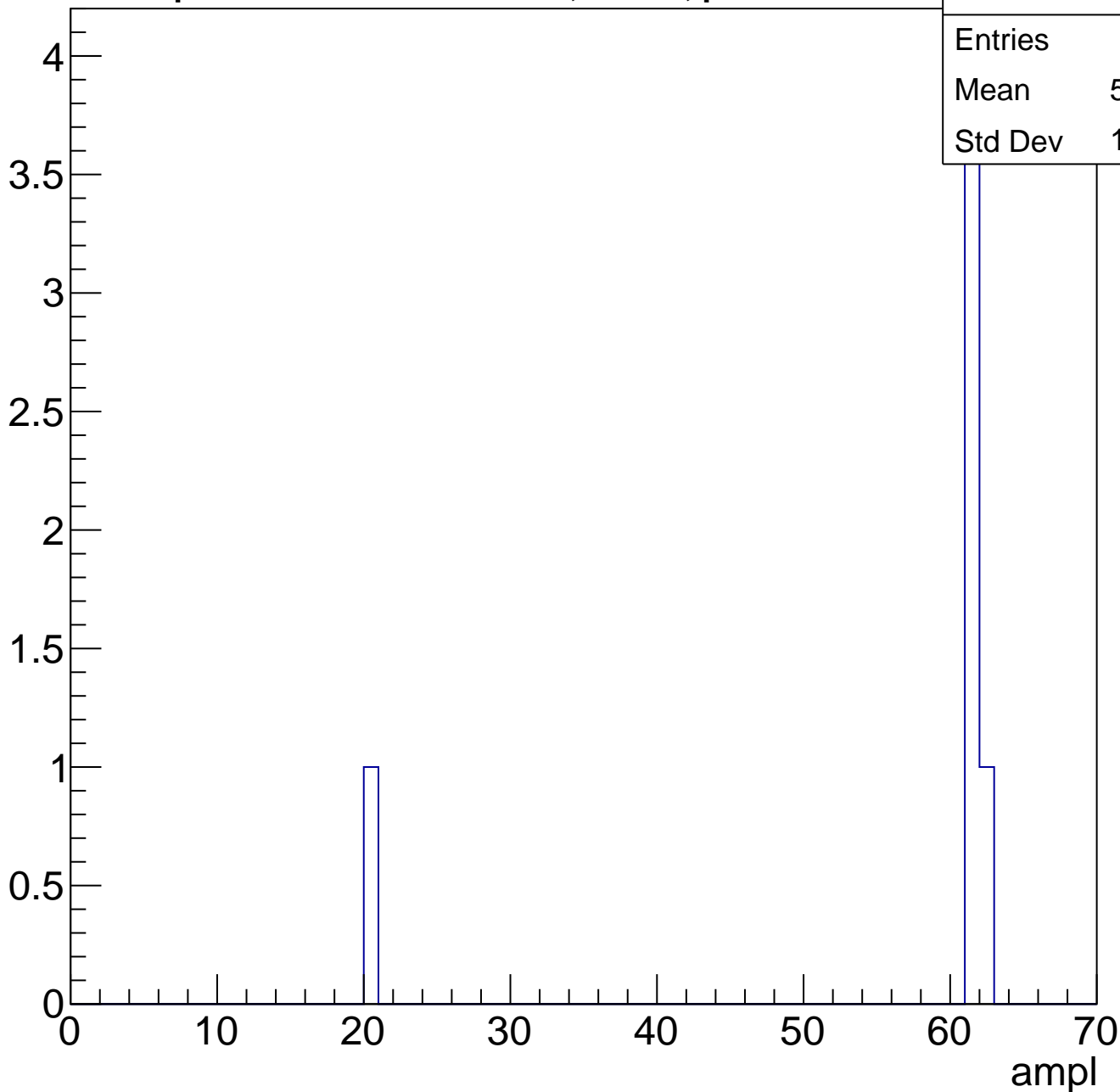




# B1L002S, U3-ch57, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch58, adc0

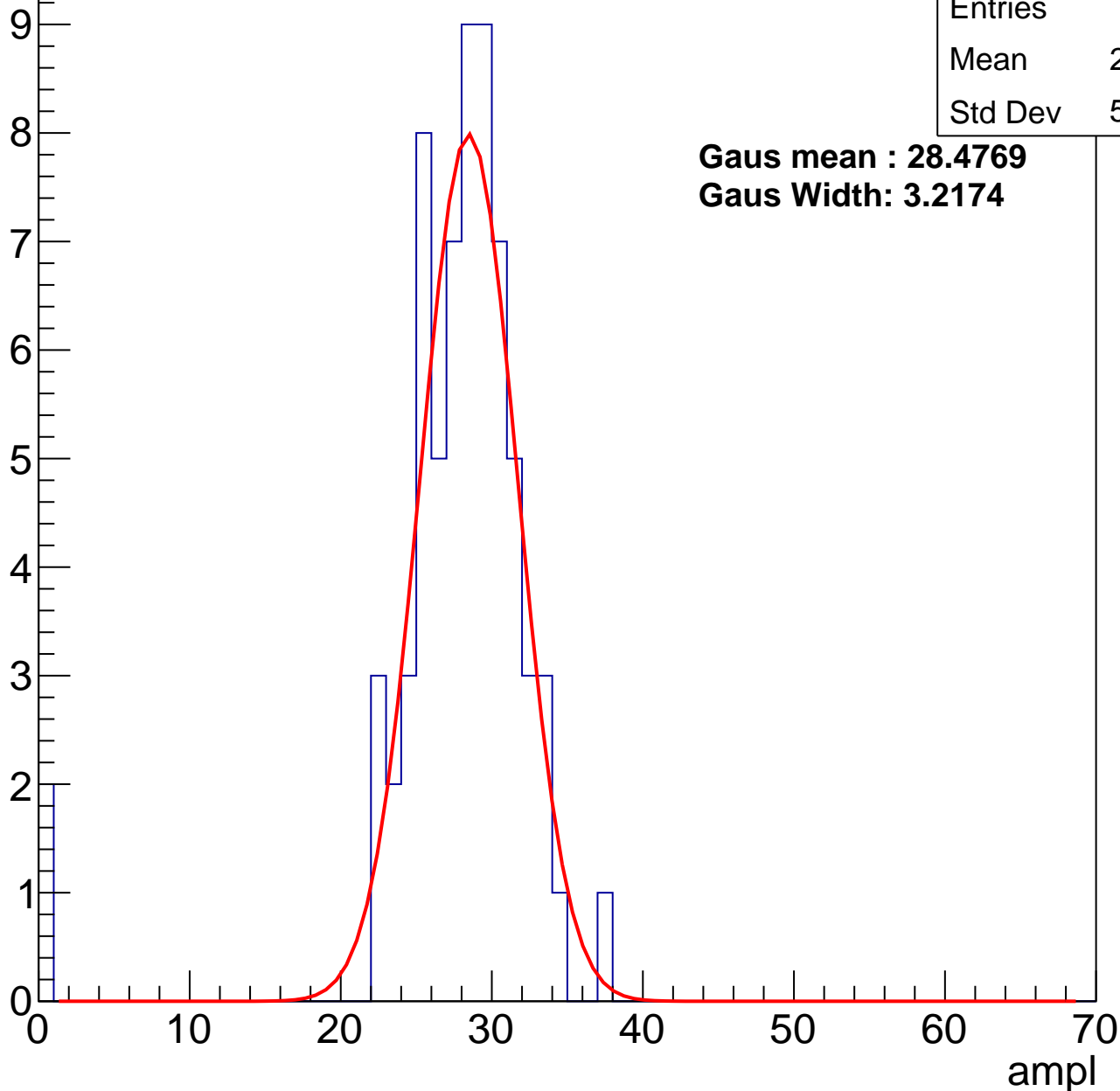
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.16
Std Dev	5.619

**Gaus mean : 28.4769**

**Gaus Width: 3.2174**



# B1L002S, U3-ch58, adc1

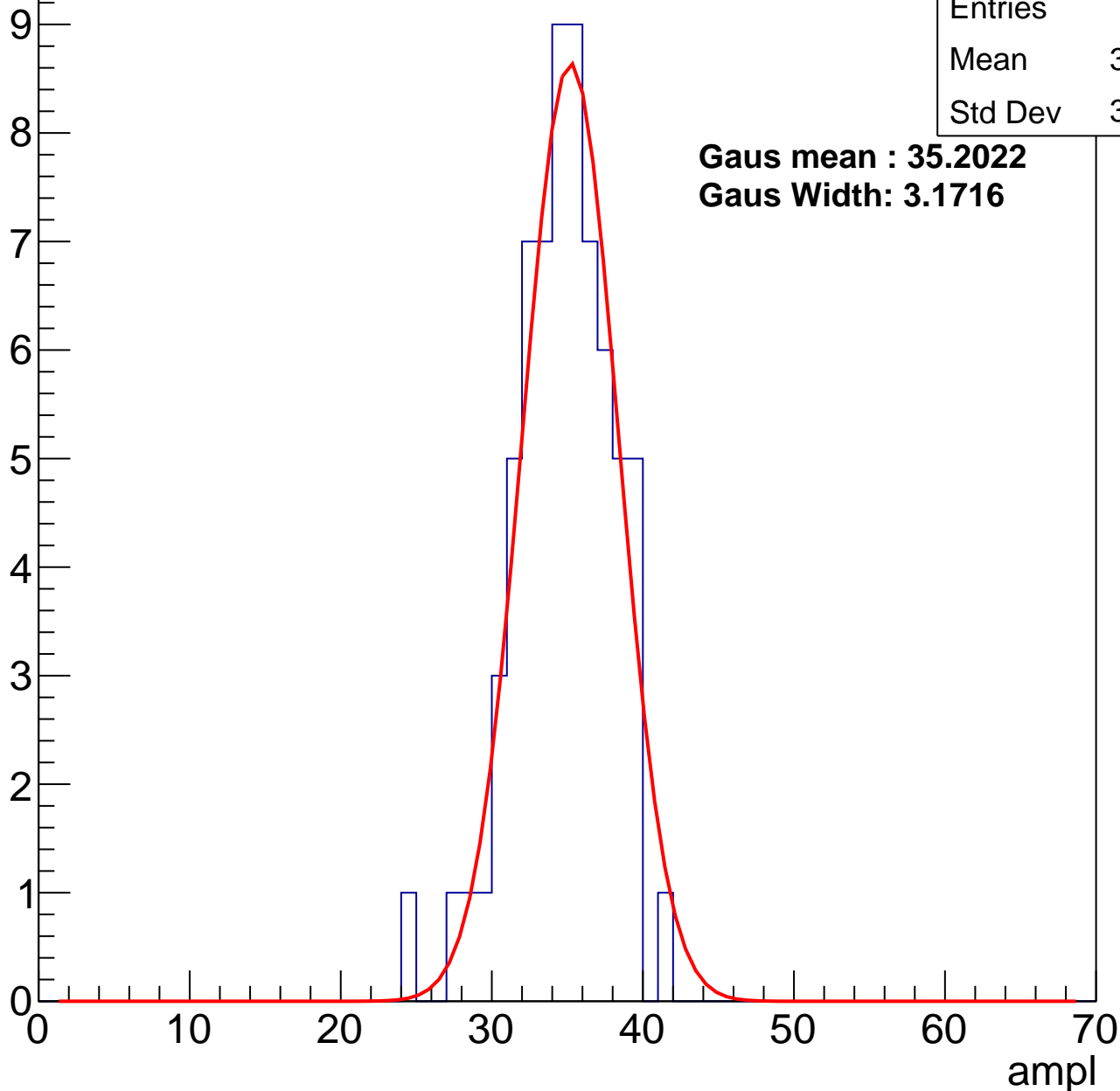
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.25
Std Dev	3.159

**Gaus mean : 35.2022**

**Gaus Width: 3.1716**



# B1L002S, U3-ch58, adc2

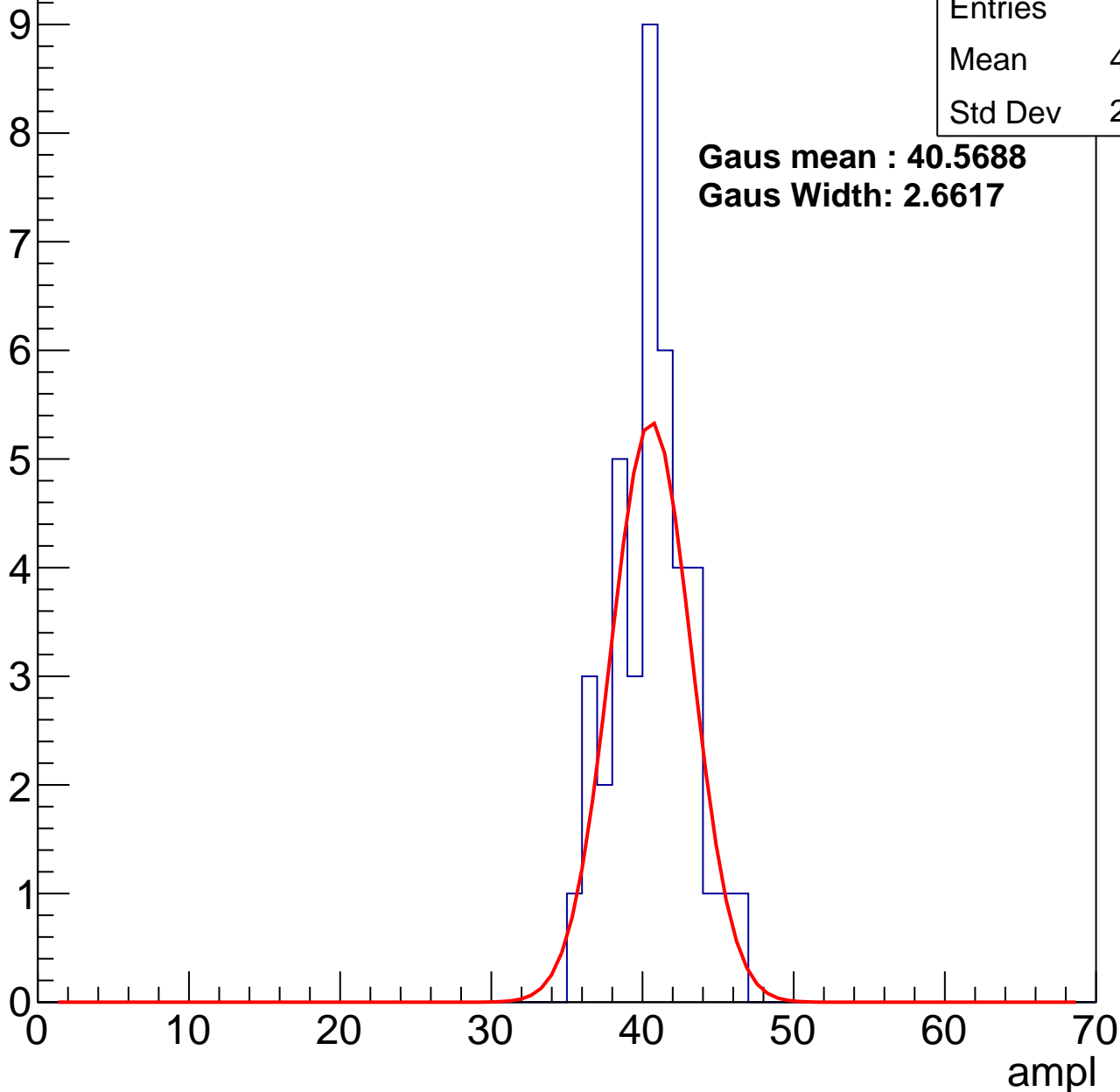
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	40.12
Std Dev	2.492

**Gaus mean : 40.5688**

**Gaus Width: 2.6617**

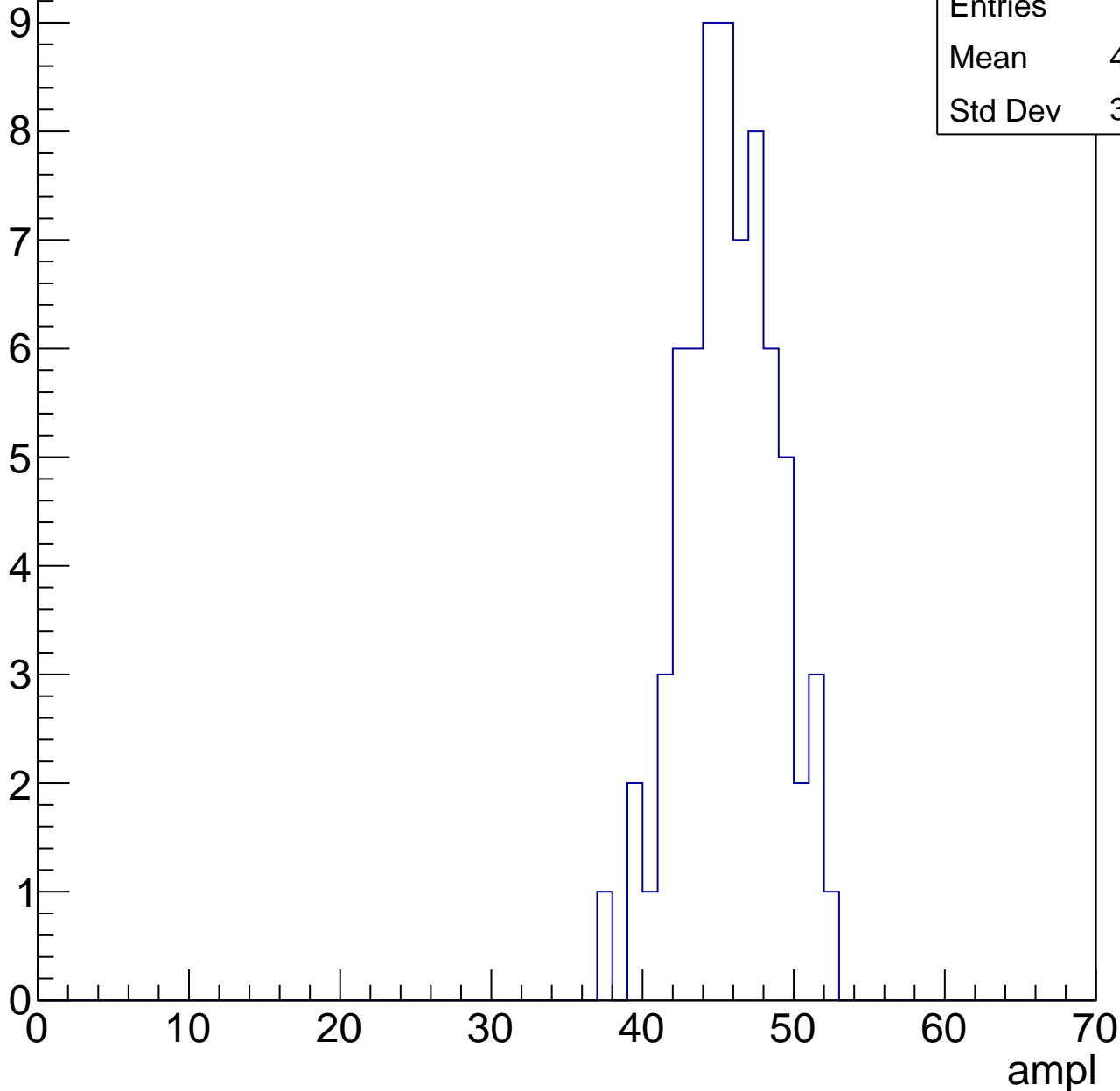


# B1L002S, U3-ch58, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.29
Std Dev	3.117

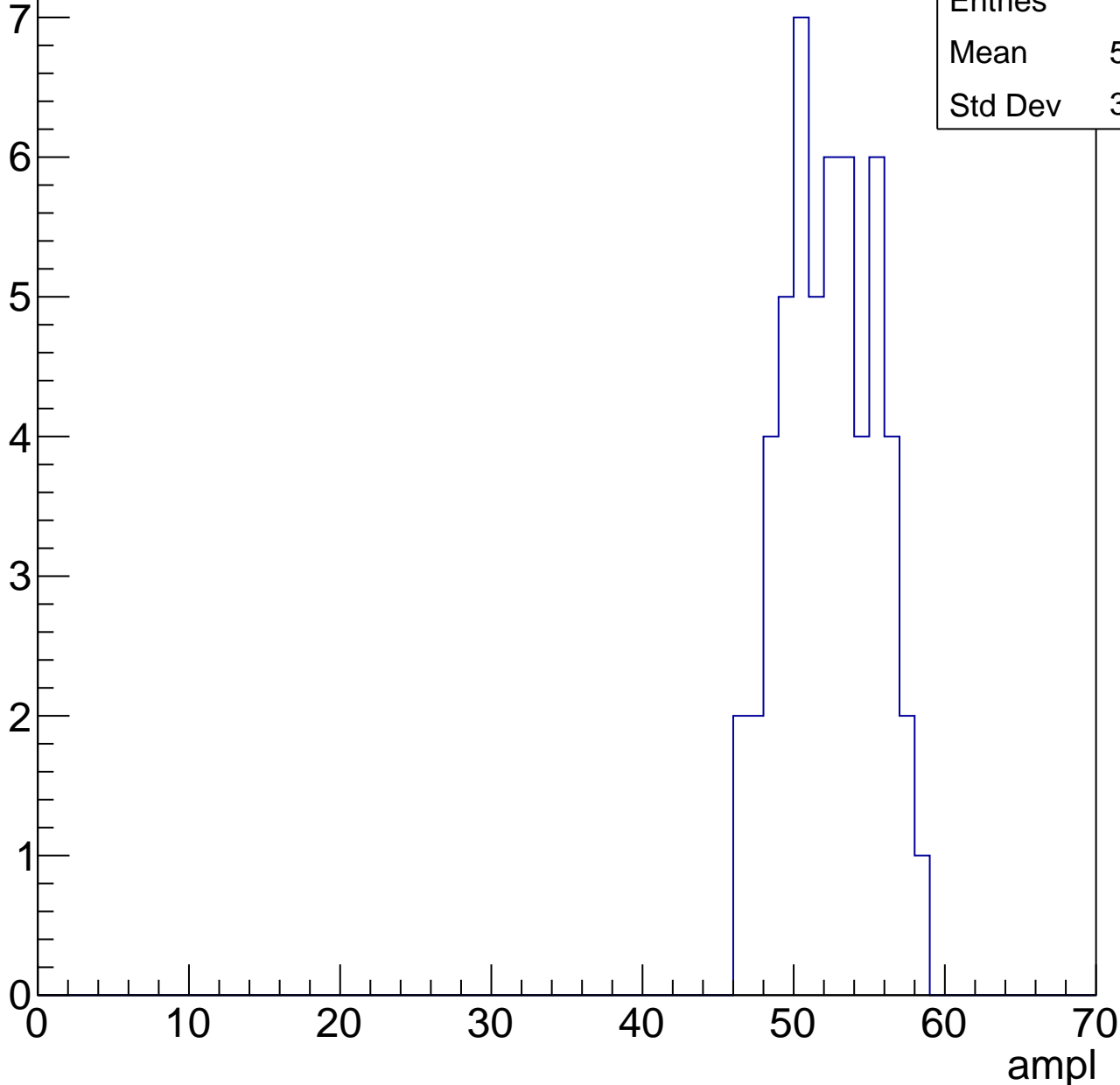


# B1L002S, U3-ch58, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	51.85
Std Dev	3.009



# B1L002S, U3-ch58, adc5

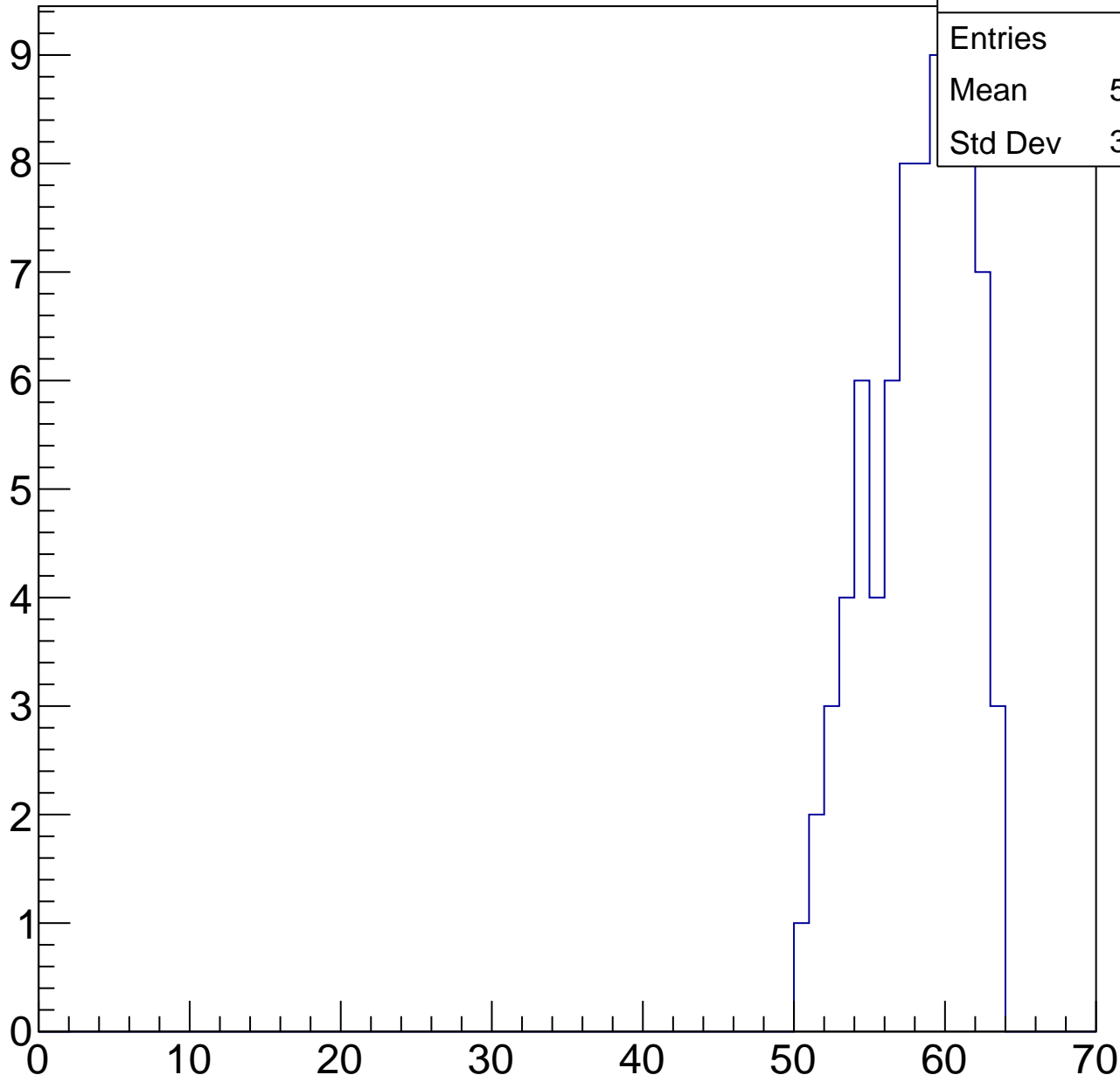
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	77
Mean	57.69
Std Dev	3.276

ampl

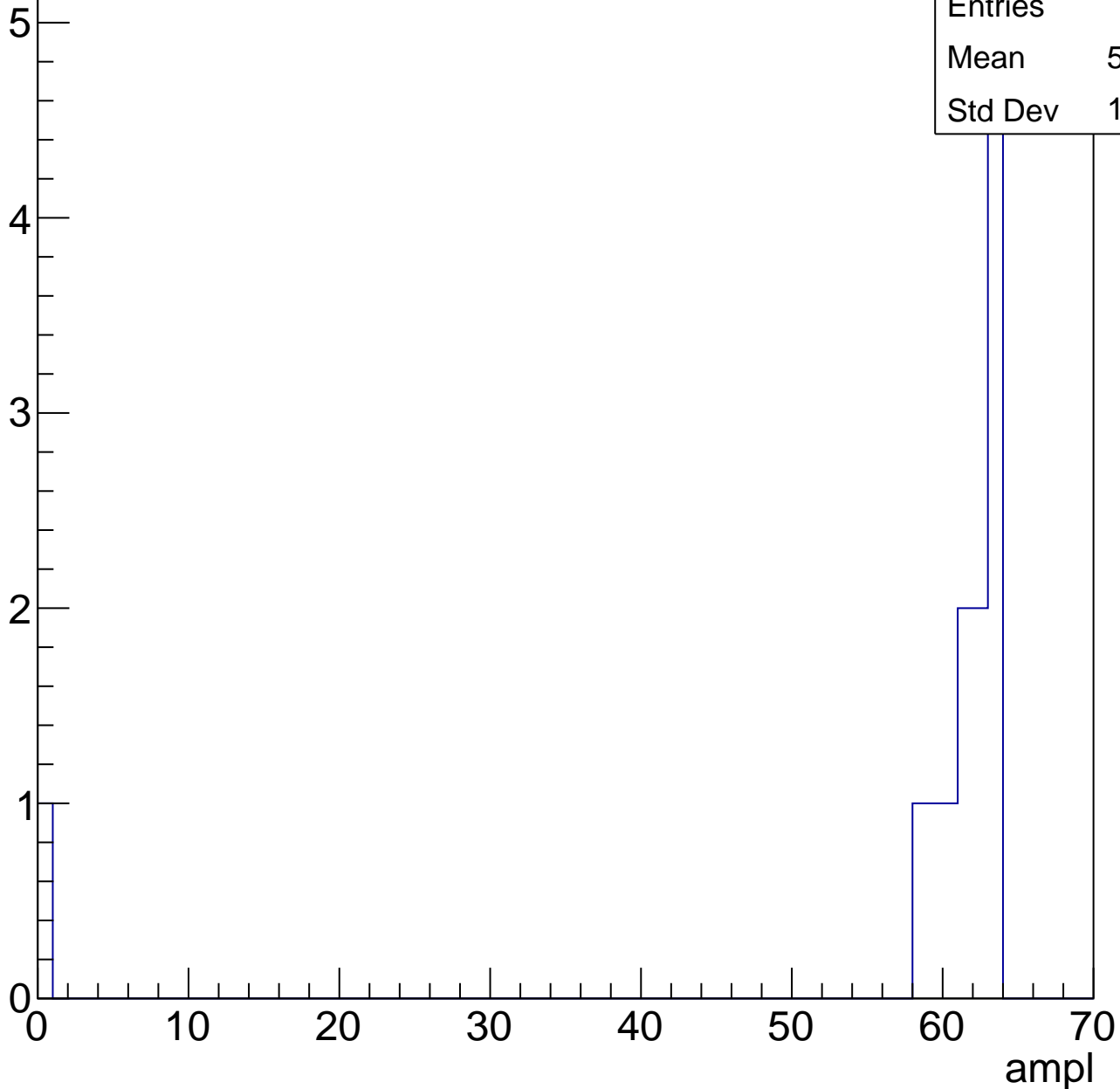


# B1L002S, U3-ch58, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	56.77
Std Dev	16.47

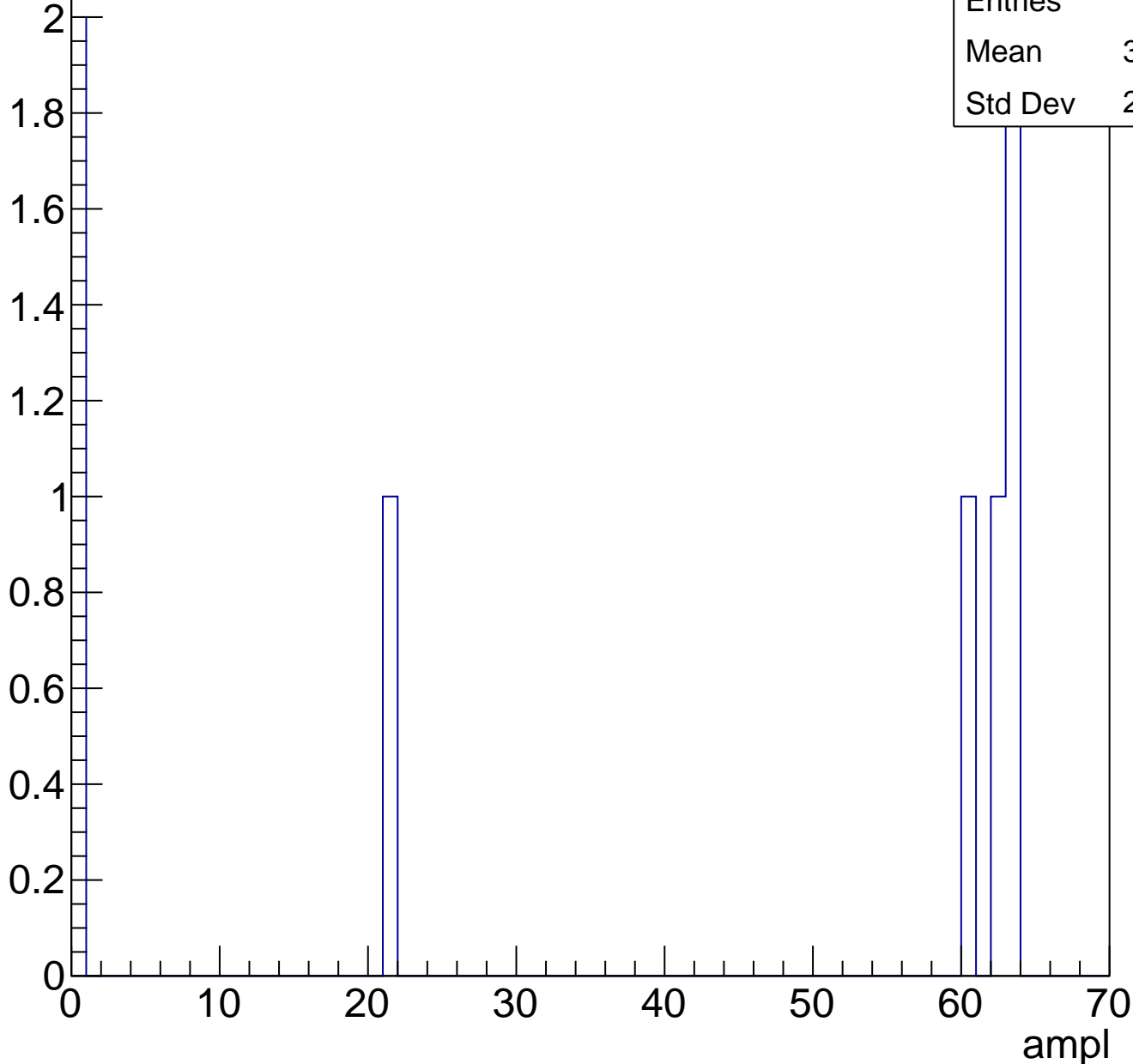




# B1L002S, U3-ch58, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	7
Mean	38.43
Std Dev	27.99

# B1L002S, U3-ch59, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

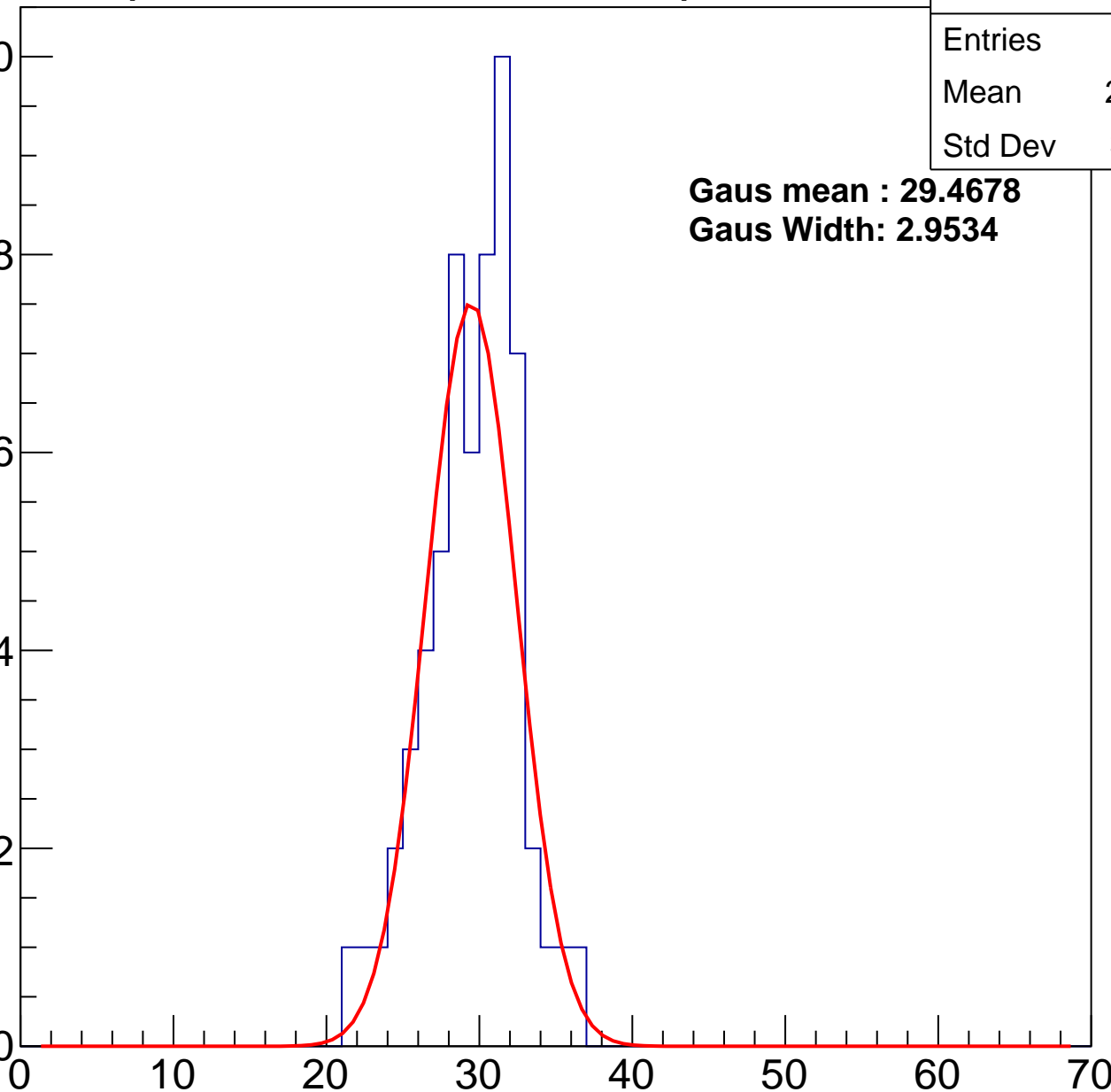
Entries	61
Mean	29.03
Std Dev	3.041

**Gaus mean : 29.4678**

**Gaus Width: 2.9534**

10  
8  
6  
4  
2  
0

ampl



# B1L002S, U3-ch59, adc1

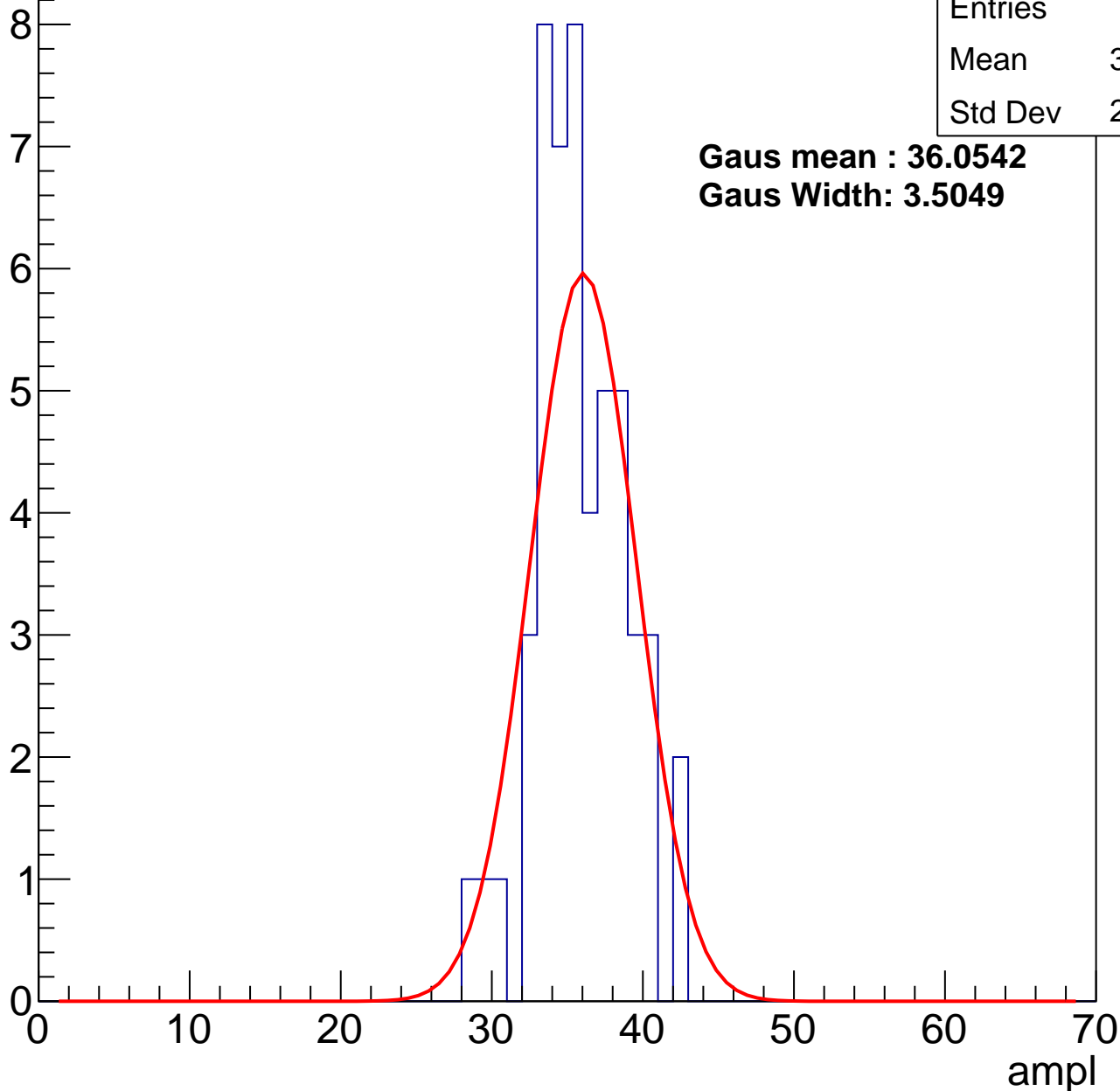
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	35.39
Std Dev	2.997

**Gaus mean : 36.0542**

**Gaus Width: 3.5049**



# B1L002S, U3-ch59, adc2

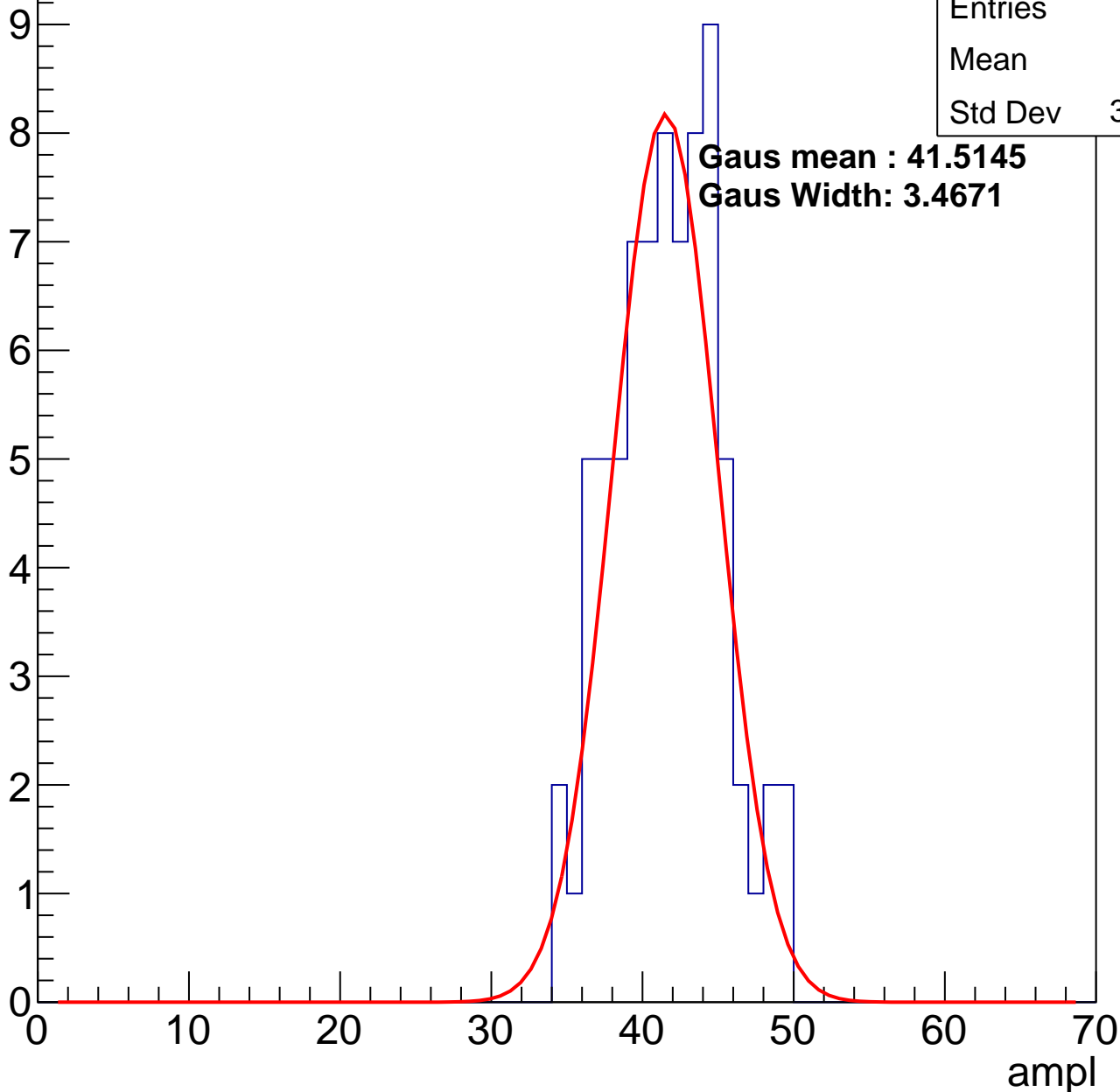
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	41.2
Std Dev	3.494

**Gaus mean : 41.5145**

**Gaus Width: 3.4671**

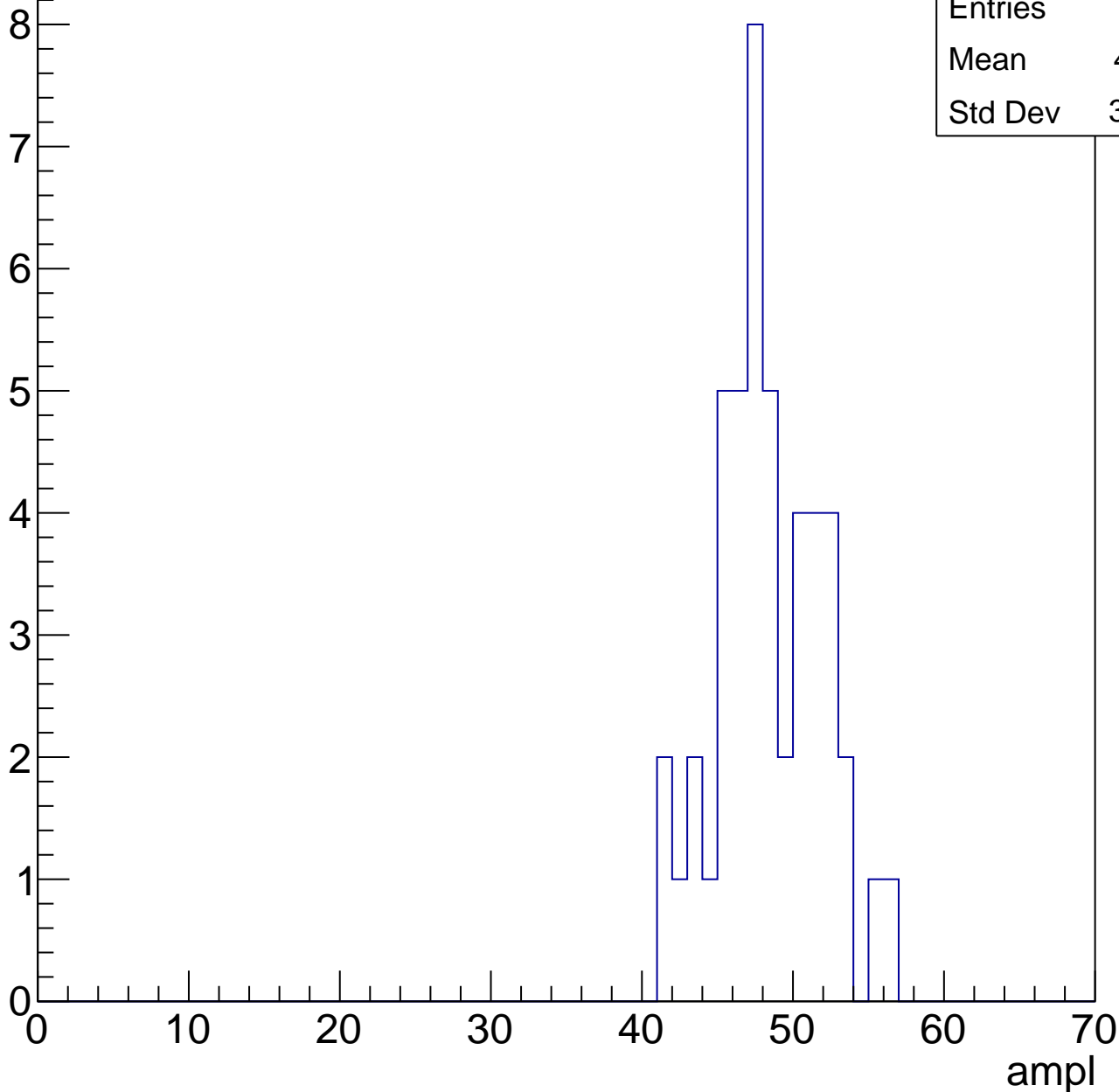


# B1L002S, U3-ch59, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

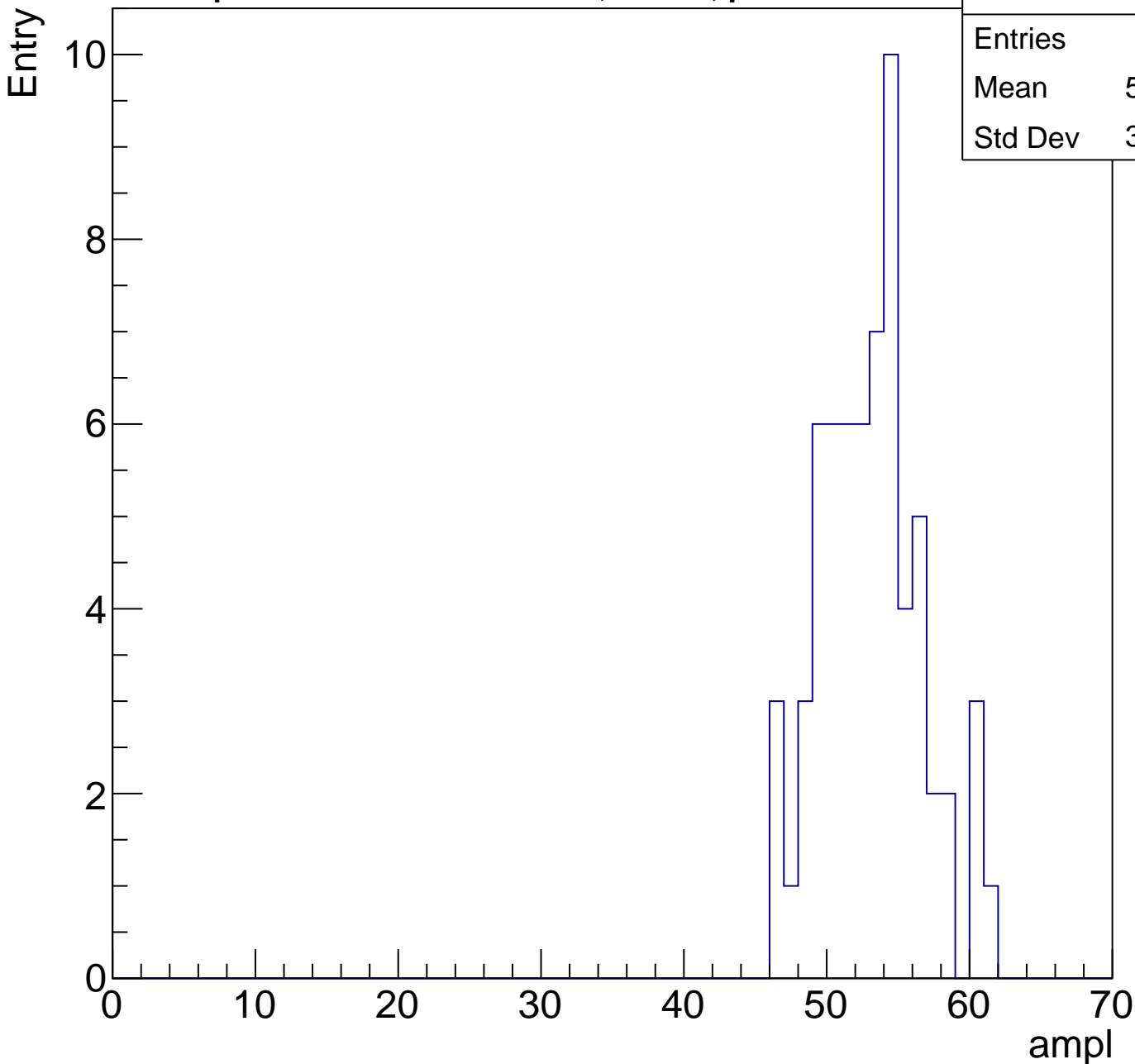
Entries	47
Mean	47.91
Std Dev	3.432



# B1L002S, U3-ch59, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	65
Mean	52.66
Std Dev	3.496

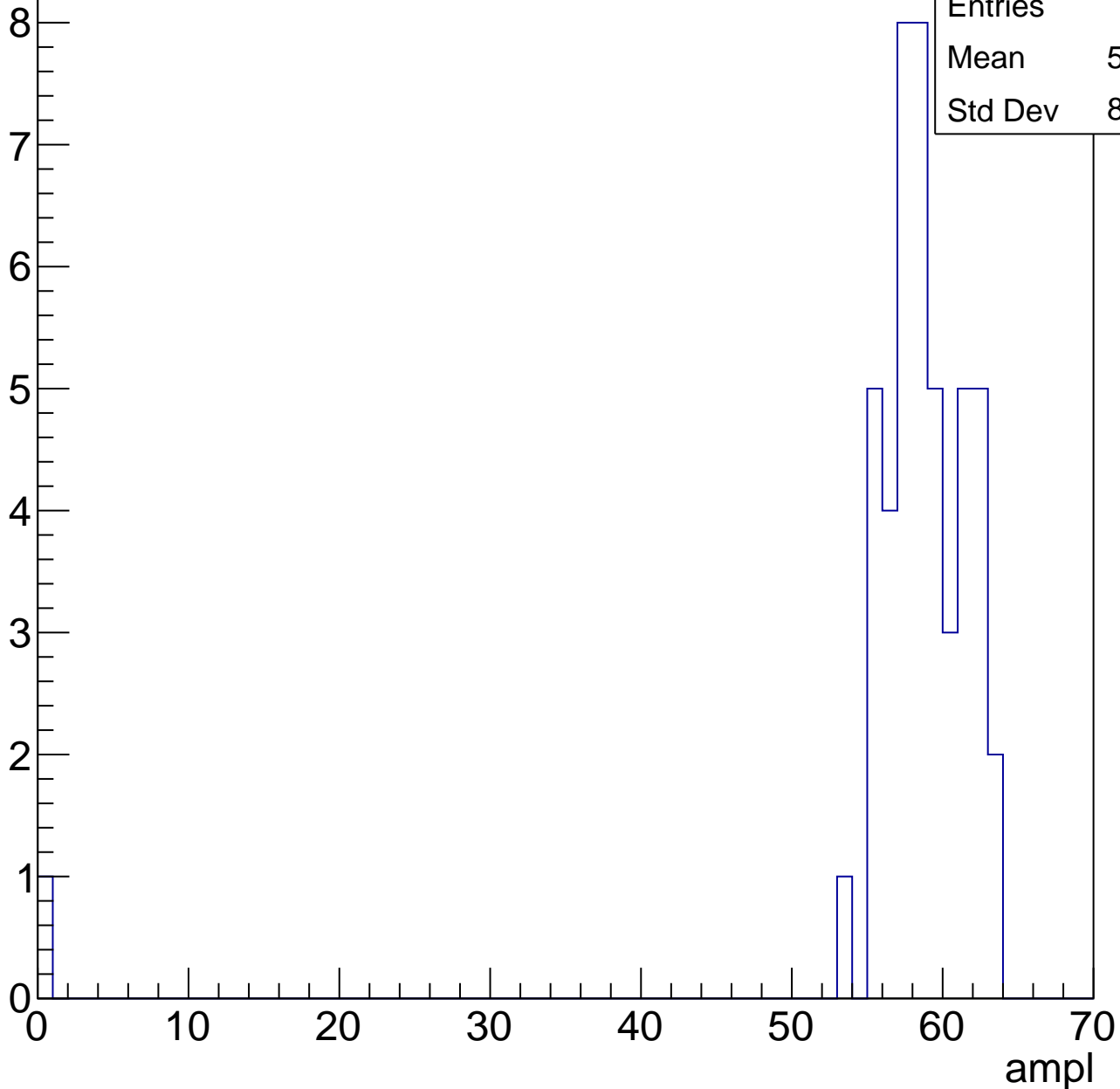


# B1L002S, U3-ch59, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.19
Std Dev	8.775

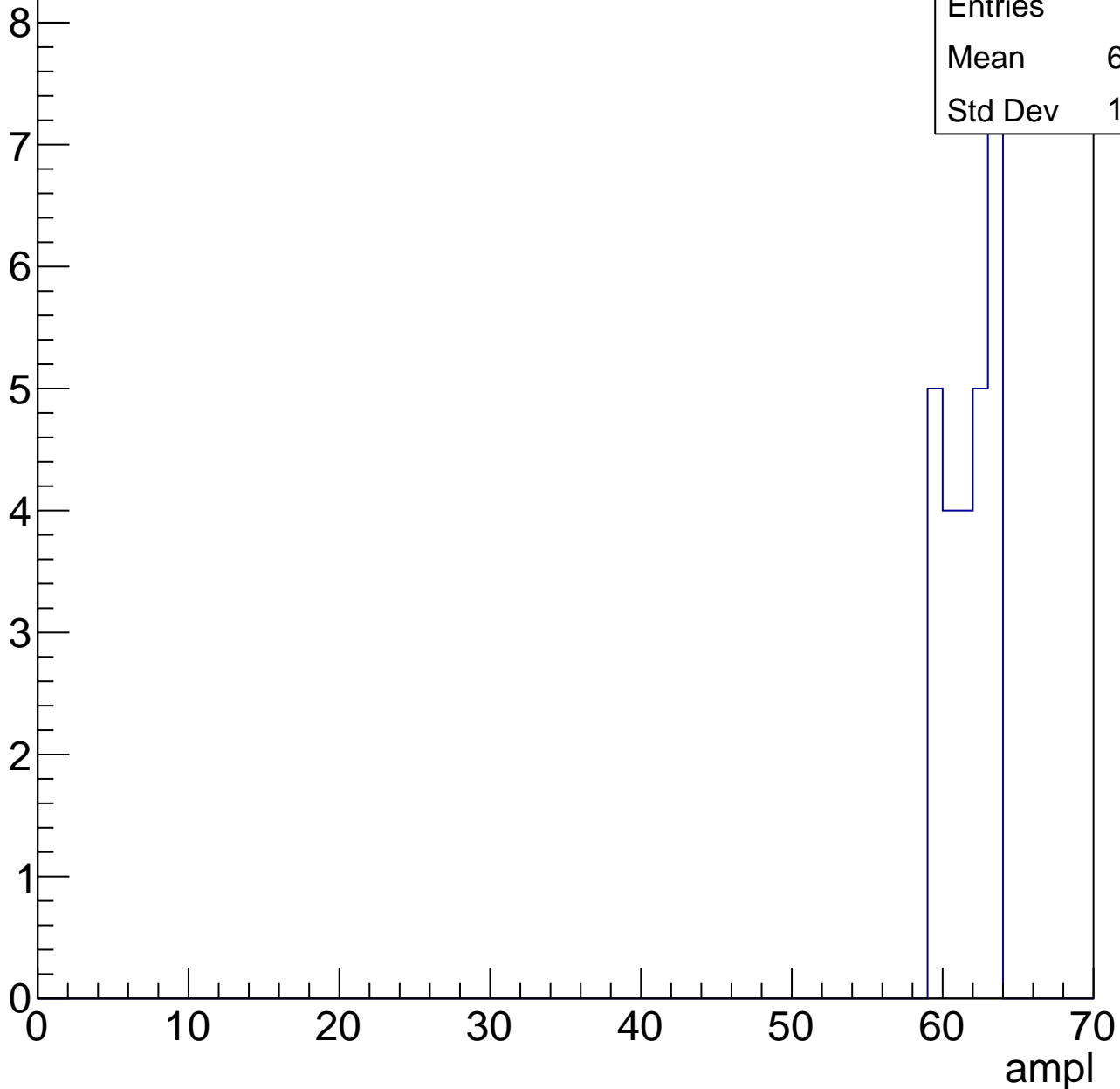


# B1L002S, U3-ch59, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	61.27
Std Dev	1.508





# B1L002S, U3-ch59, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch60, adc0

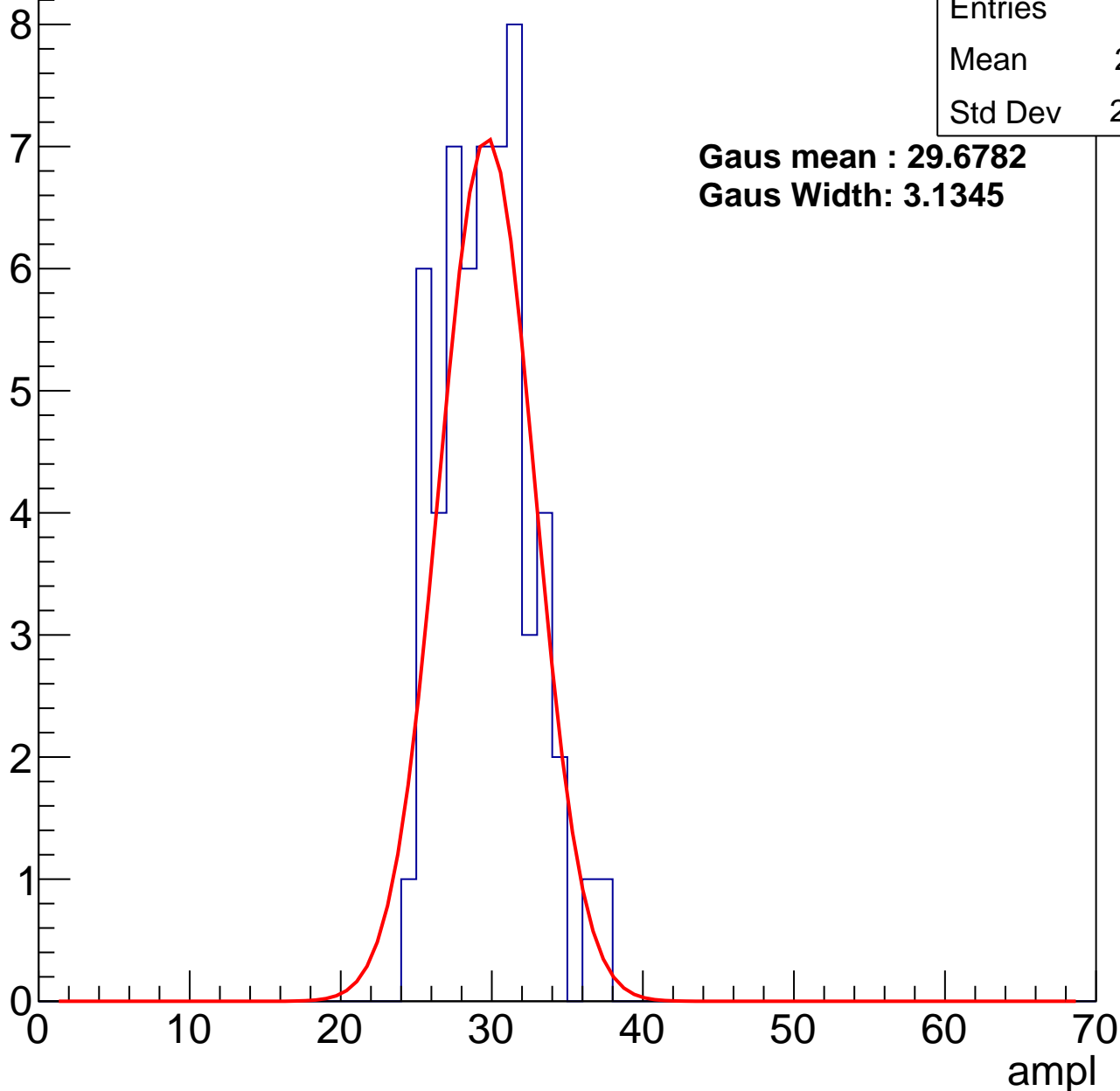
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	29.21
Std Dev	2.906

**Gaus mean : 29.6782**

**Gaus Width: 3.1345**



# B1L002S, U3-ch60, adc1

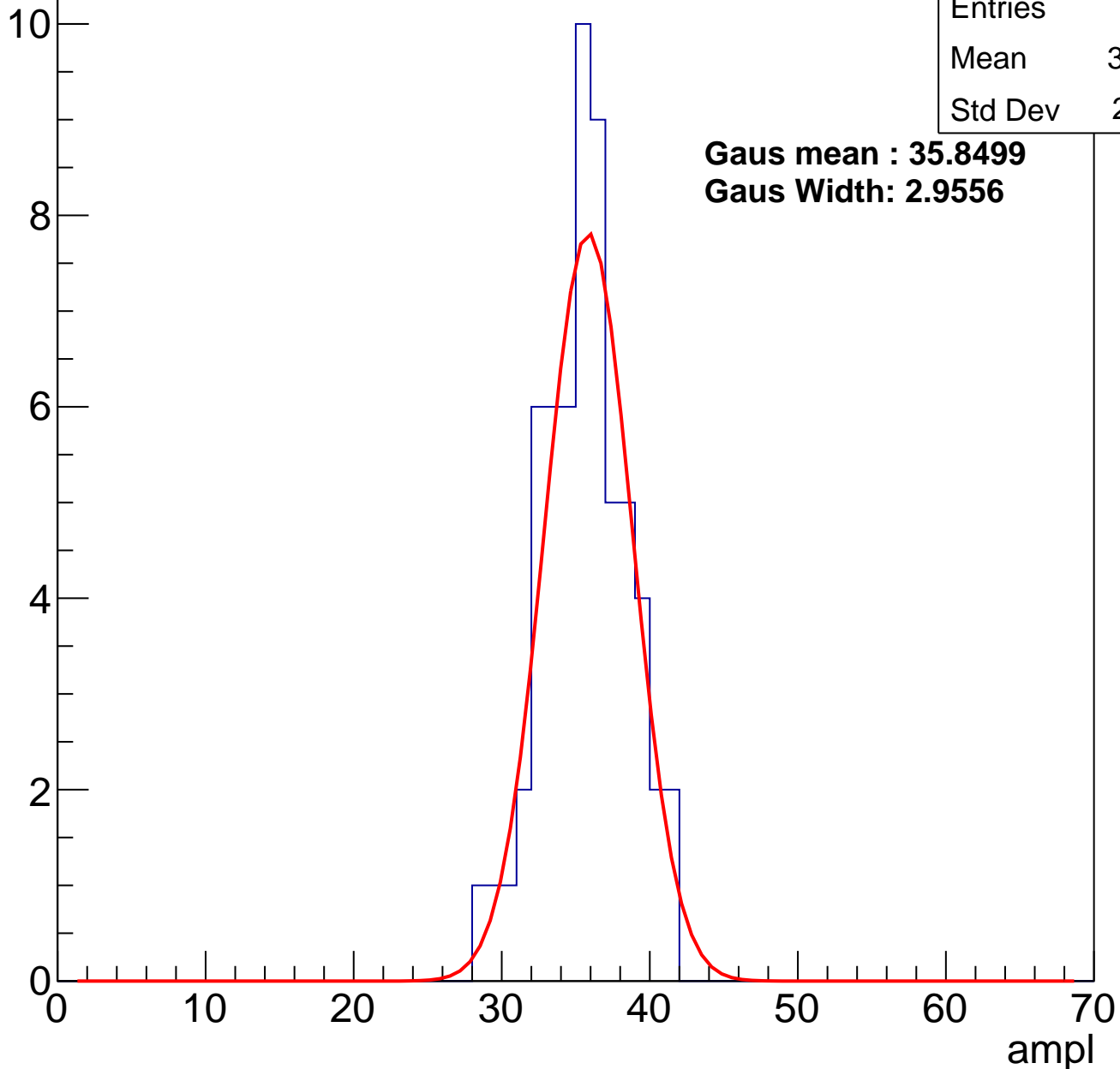
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	35.17
Std Dev	2.841

**Gaus mean : 35.8499**

**Gaus Width: 2.9556**

Entry



# B1L002S, U3-ch60, adc2

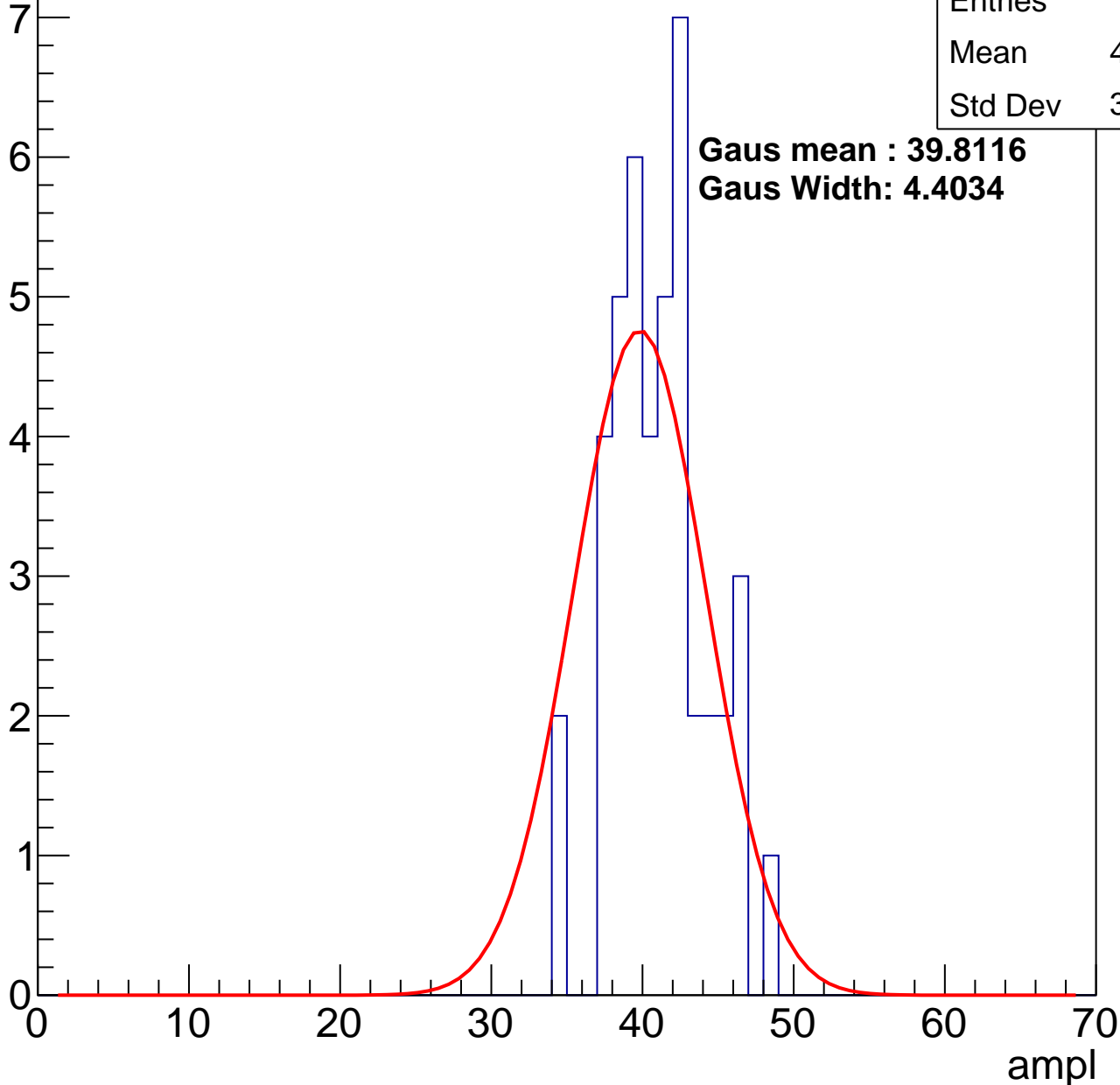
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	40.67
Std Dev	3.123

**Gaus mean : 39.8116**

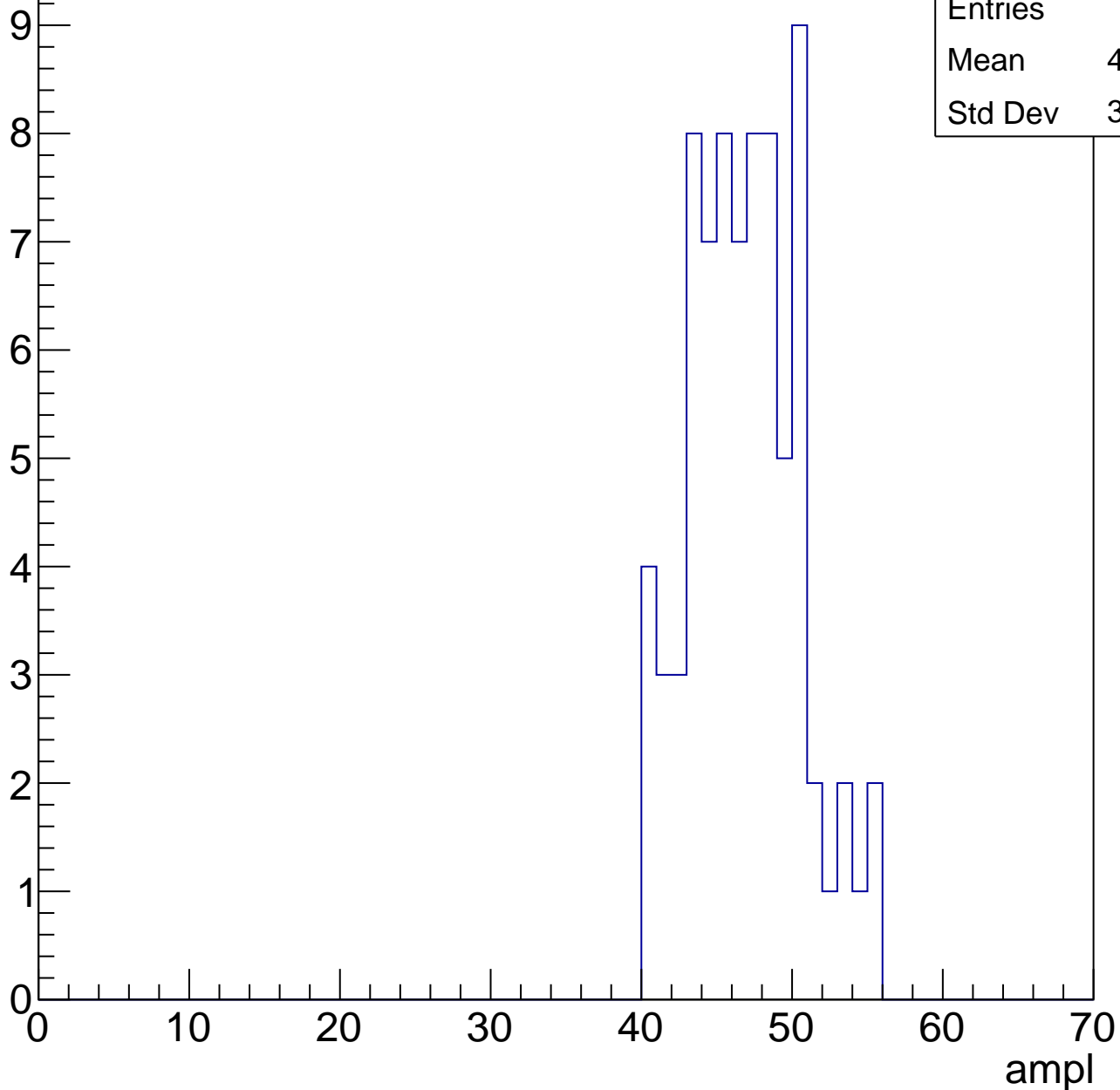
**Gaus Width: 4.4034**



# B1L002S, U3-ch60, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



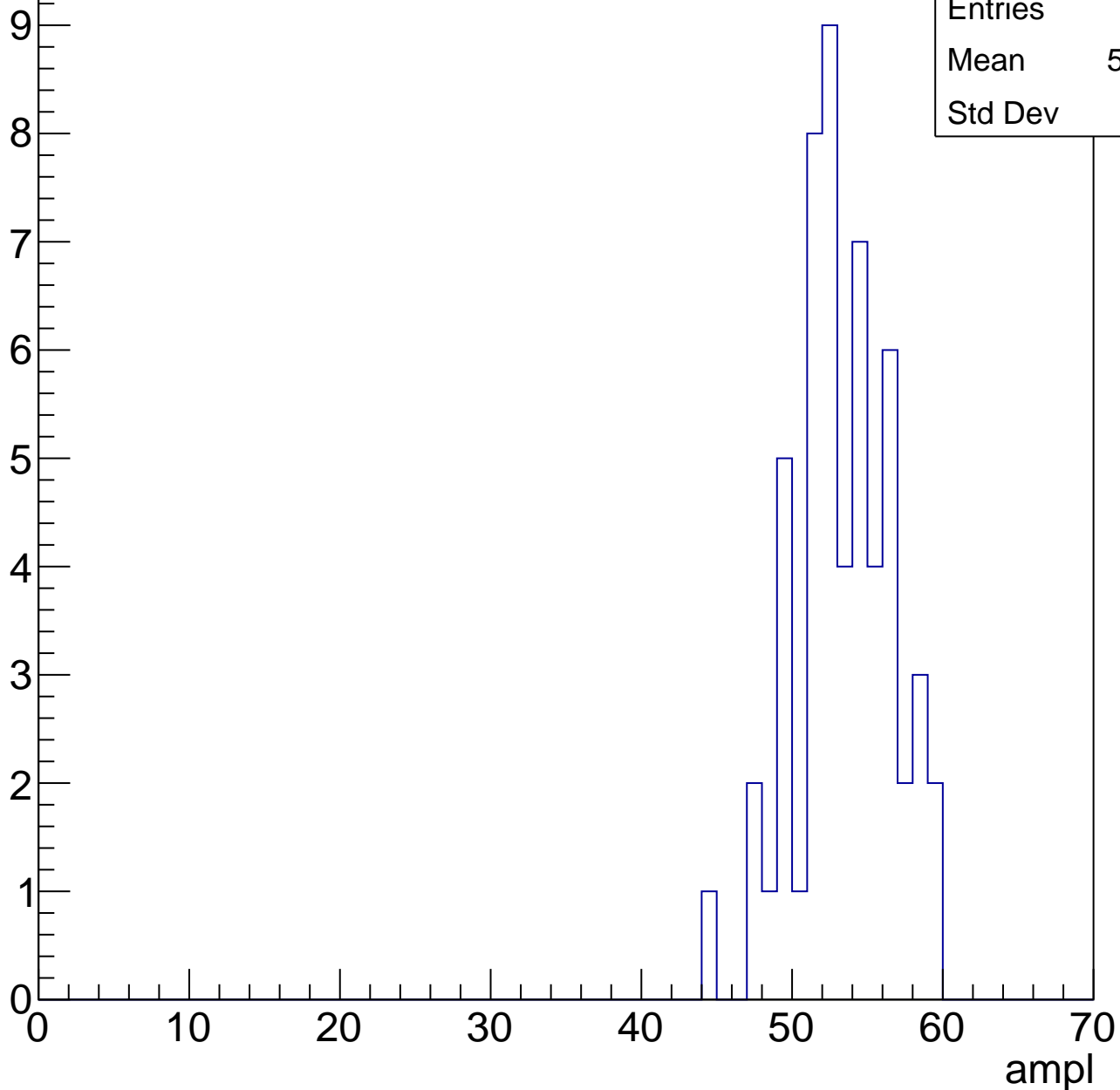
Entries	78
Mean	46.44
Std Dev	3.579

# B1L002S, U3-ch60, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	52.89
Std Dev	3.2



# B1L002S, U3-ch60, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

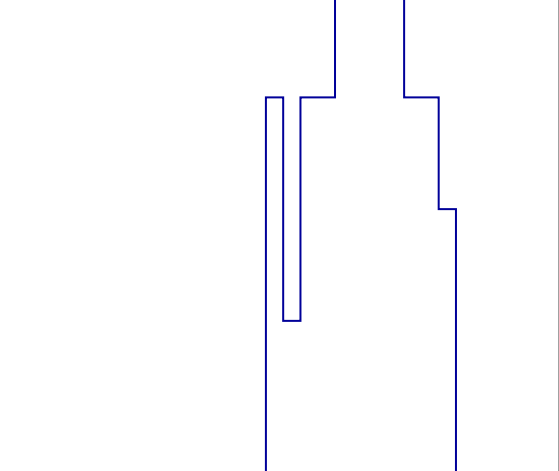
Entries	54
Mean	58.28
Std Dev	2.752

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

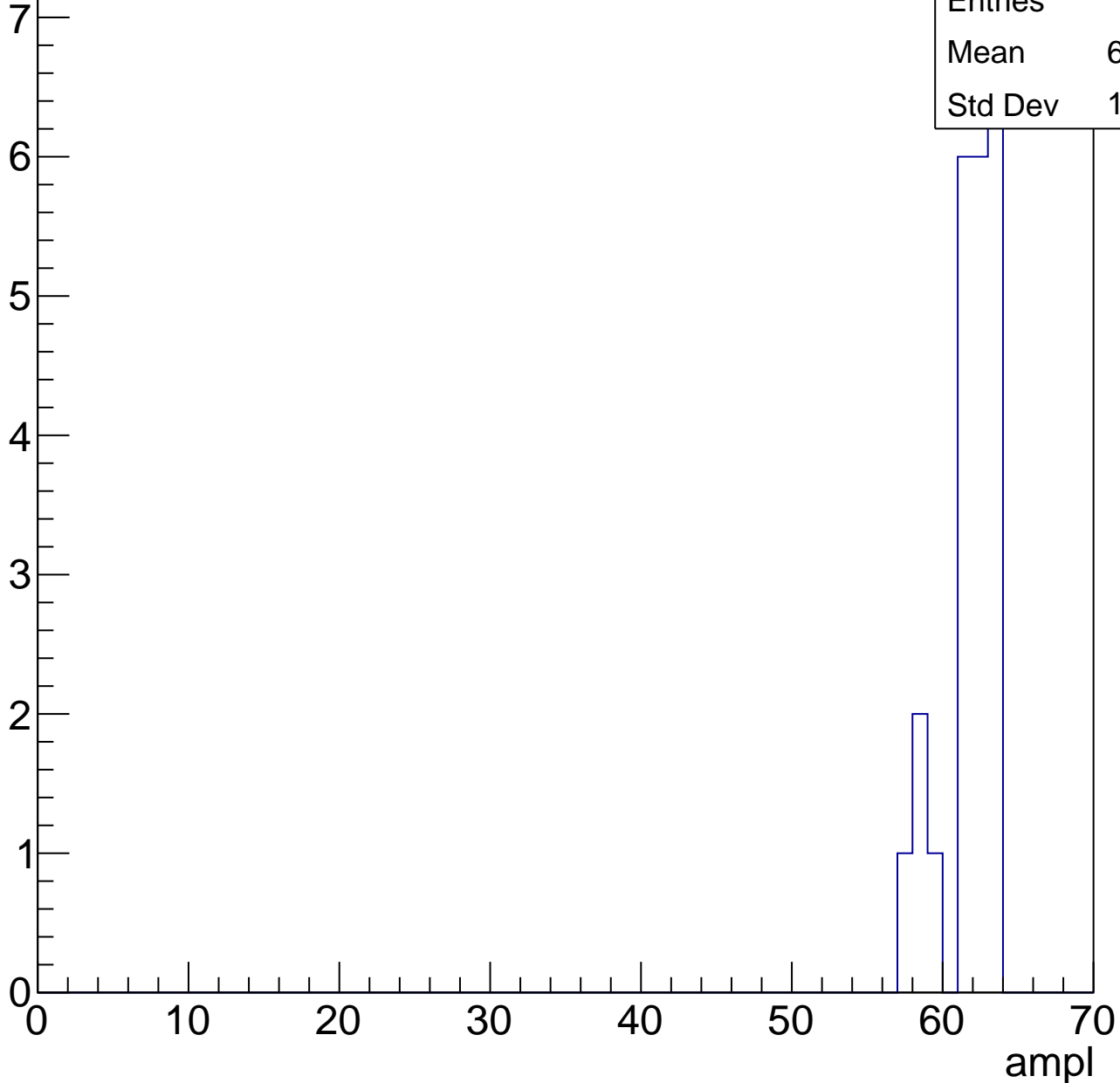


# B1L002S, U3-ch60, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.35
Std Dev	1.735





# B1L002S, U3-ch60, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	10
Std Dev	10

# B1L002S, U3-ch61, adc0

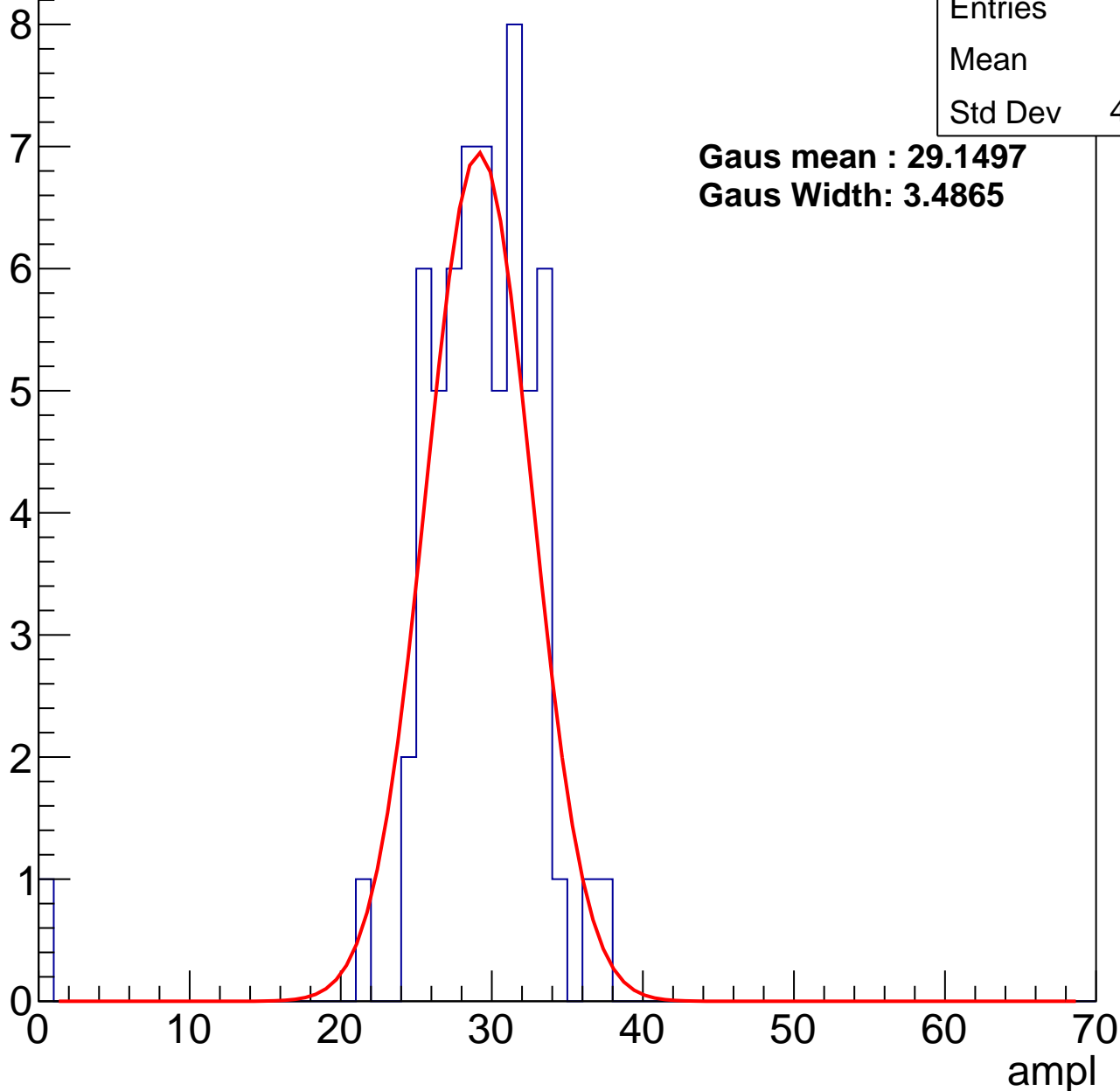
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	28.6
Std Dev	4.807

**Gaus mean : 29.1497**

**Gaus Width: 3.4865**



# B1L002S, U3-ch61, adc1

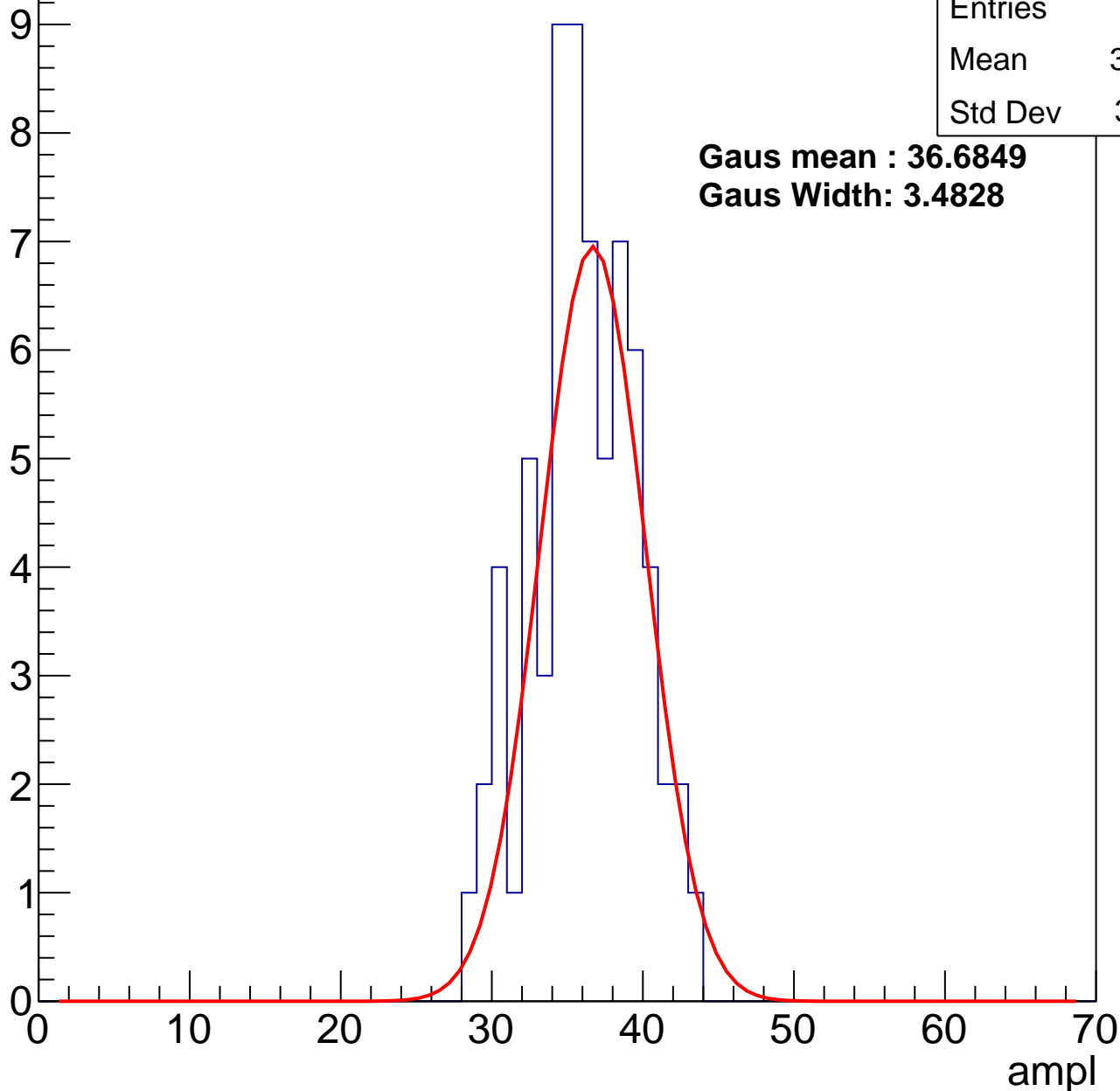
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	35.63
Std Dev	3.421

**Gaus mean : 36.6849**

**Gaus Width: 3.4828**



# B1L002S, U3-ch61, adc2

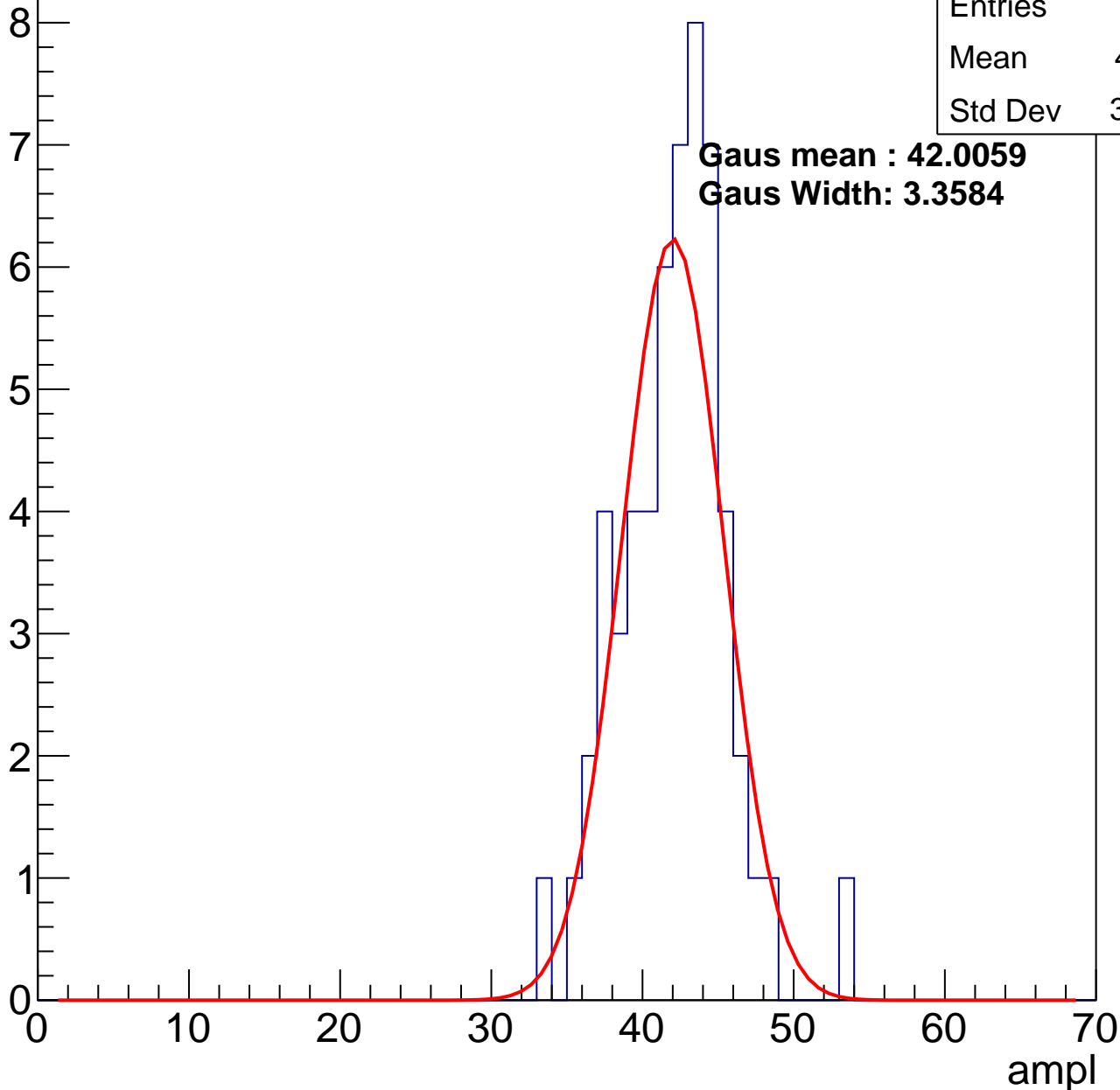
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.61
Std Dev	3.498

**Gaus mean : 42.0059**

**Gaus Width: 3.3584**

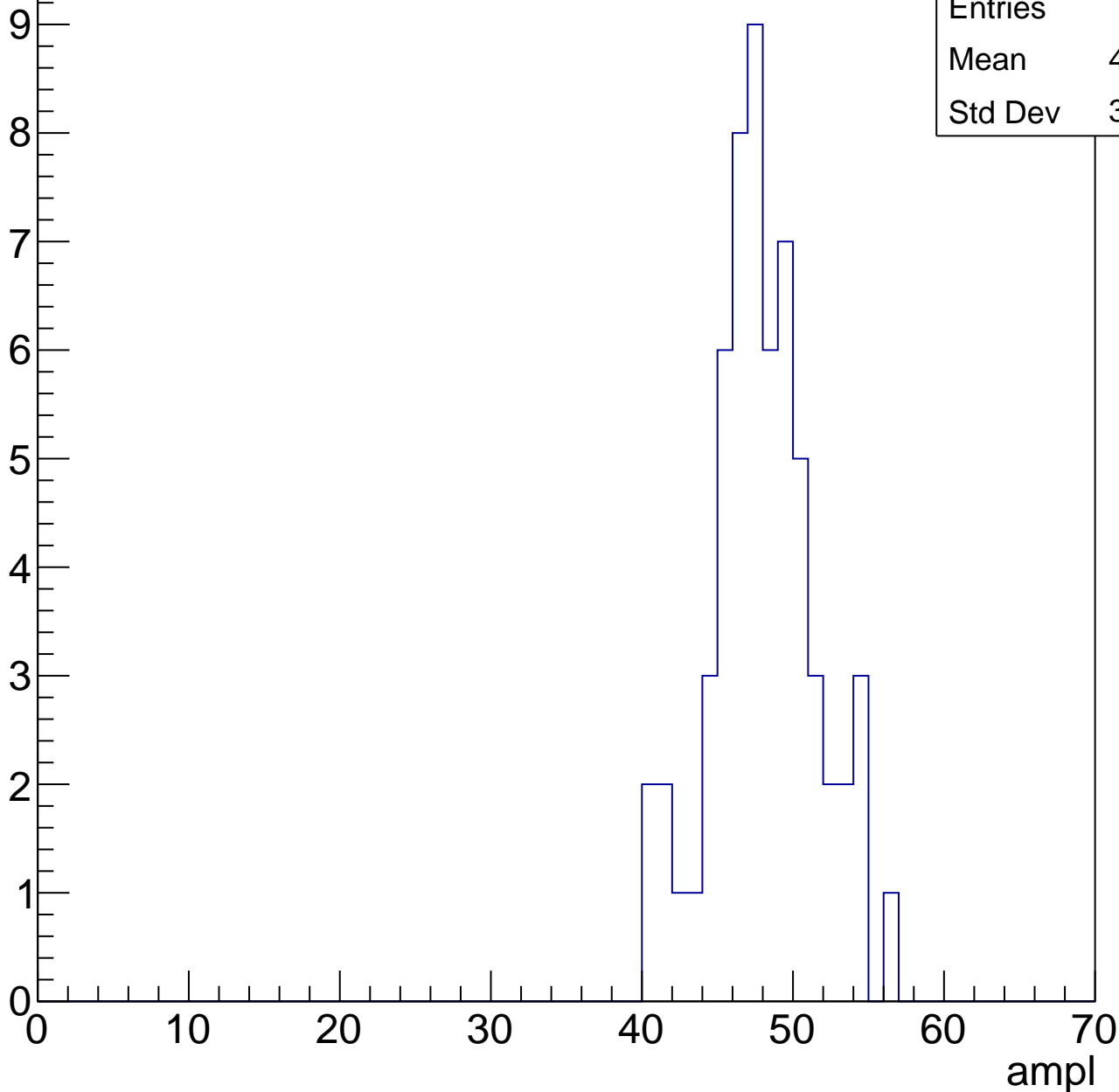


# B1L002S, U3-ch61, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

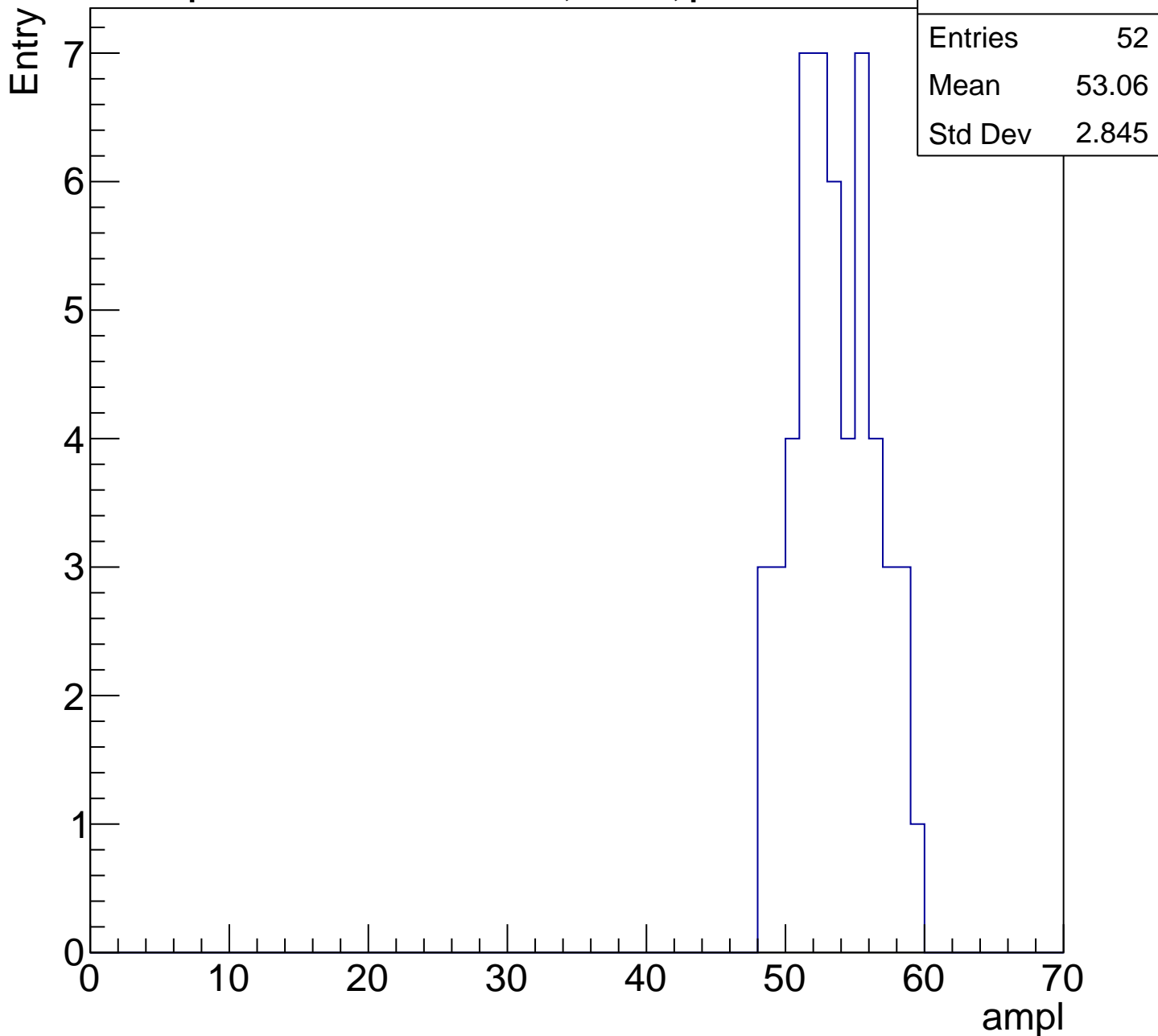
Entry

Entries	61
Mean	47.57
Std Dev	3.452



# B1L002S, U3-ch61, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

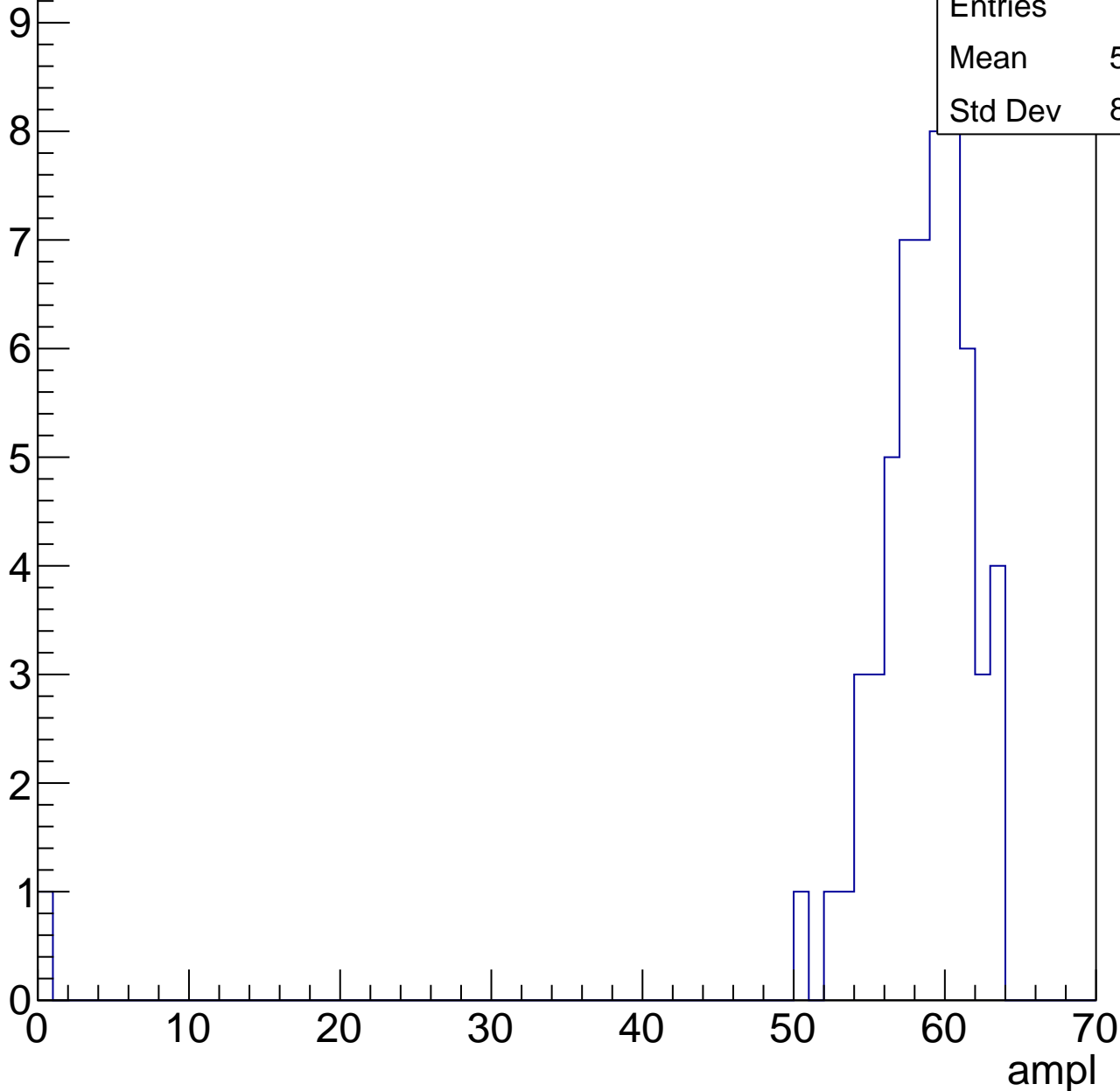


# B1L002S, U3-ch61, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	57.34
Std Dev	8.037

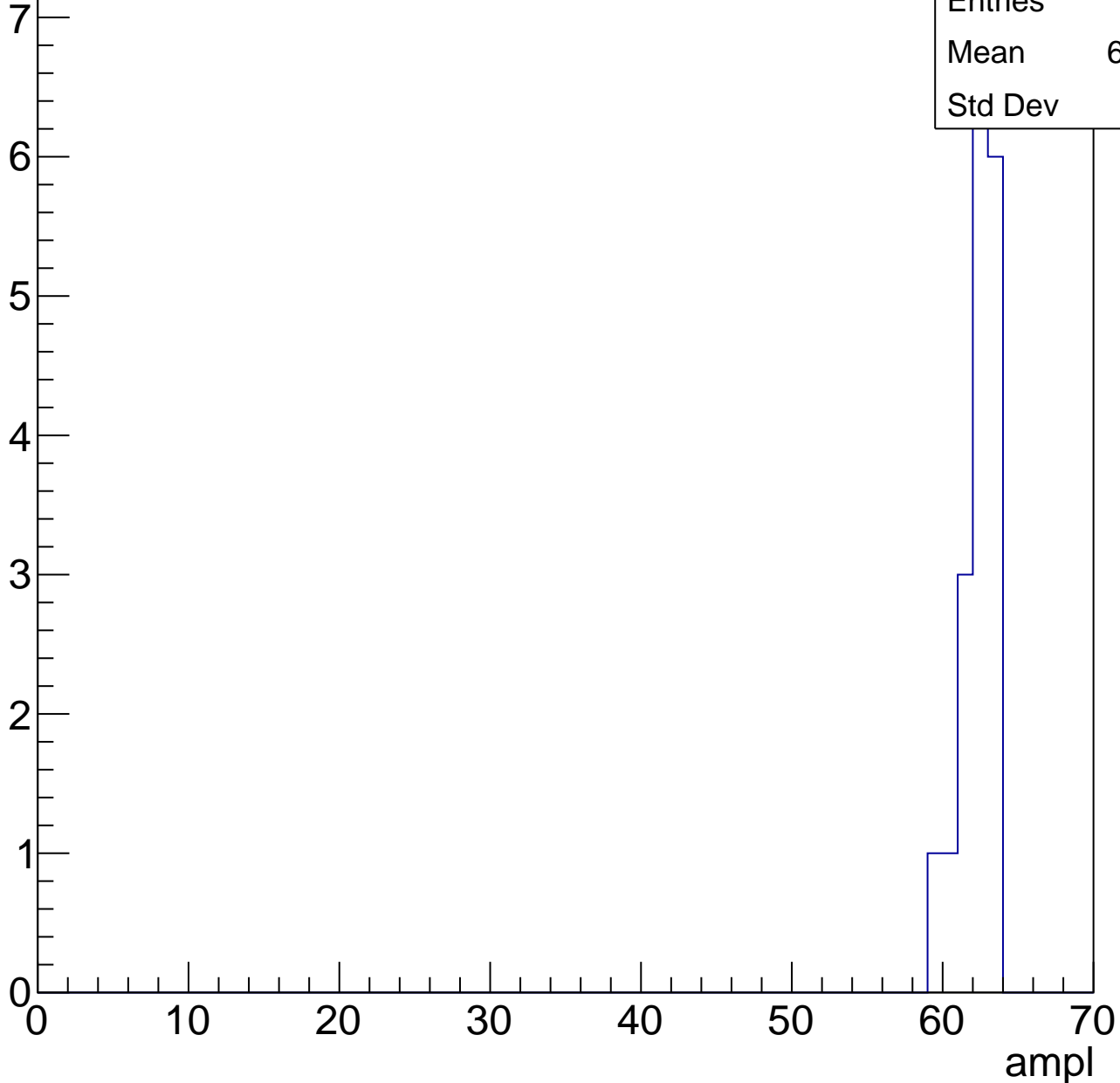


# B1L002S, U3-ch61, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	18
Mean	61.89
Std Dev	1.1





# B1L002S, U3-ch61, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

61

Std Dev

0

# B1L002S, U3-ch62, adc0

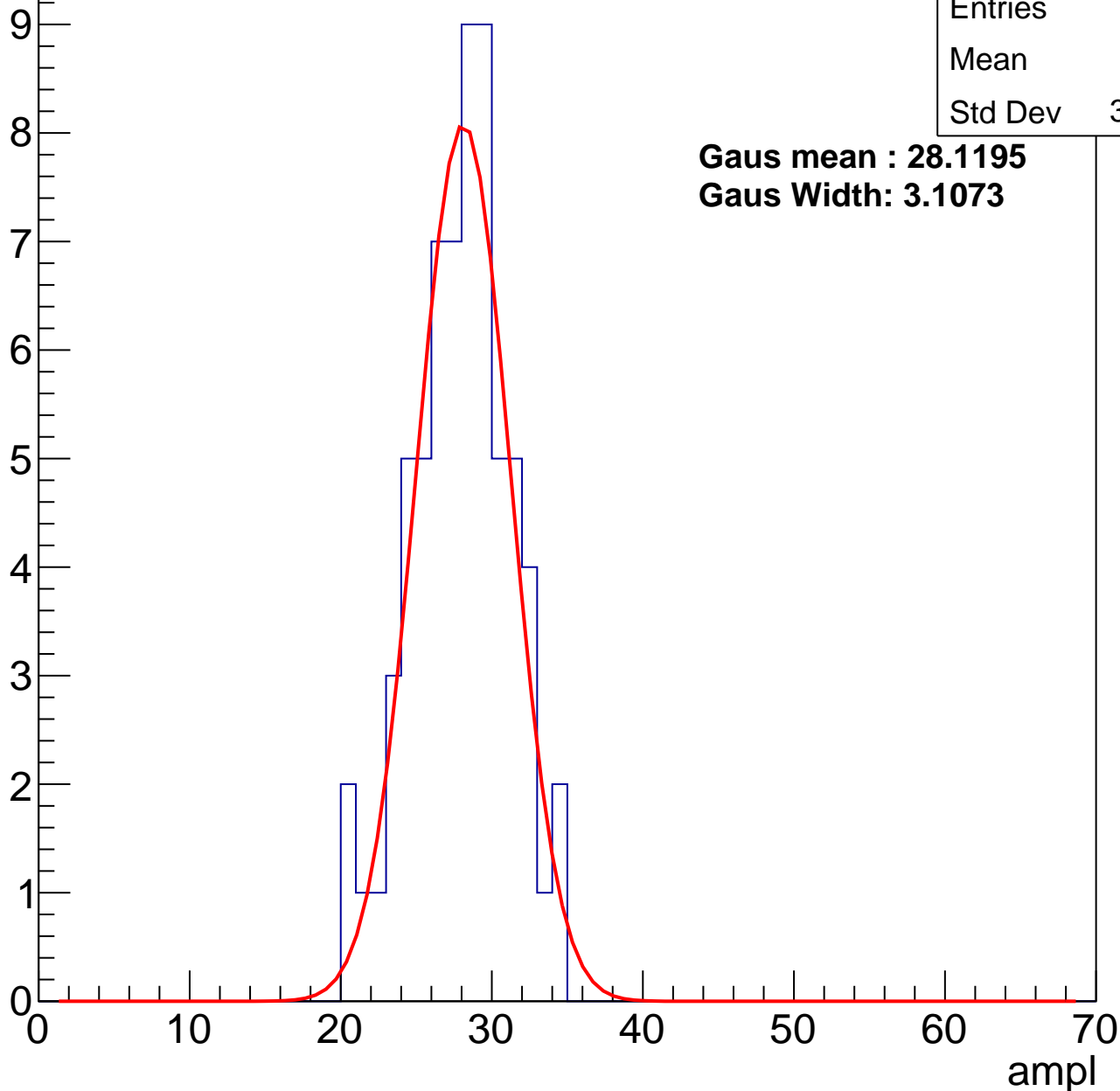
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	27.5
Std Dev	3.178

**Gaus mean : 28.1195**

**Gaus Width: 3.1073**



# B1L002S, U3-ch62, adc1

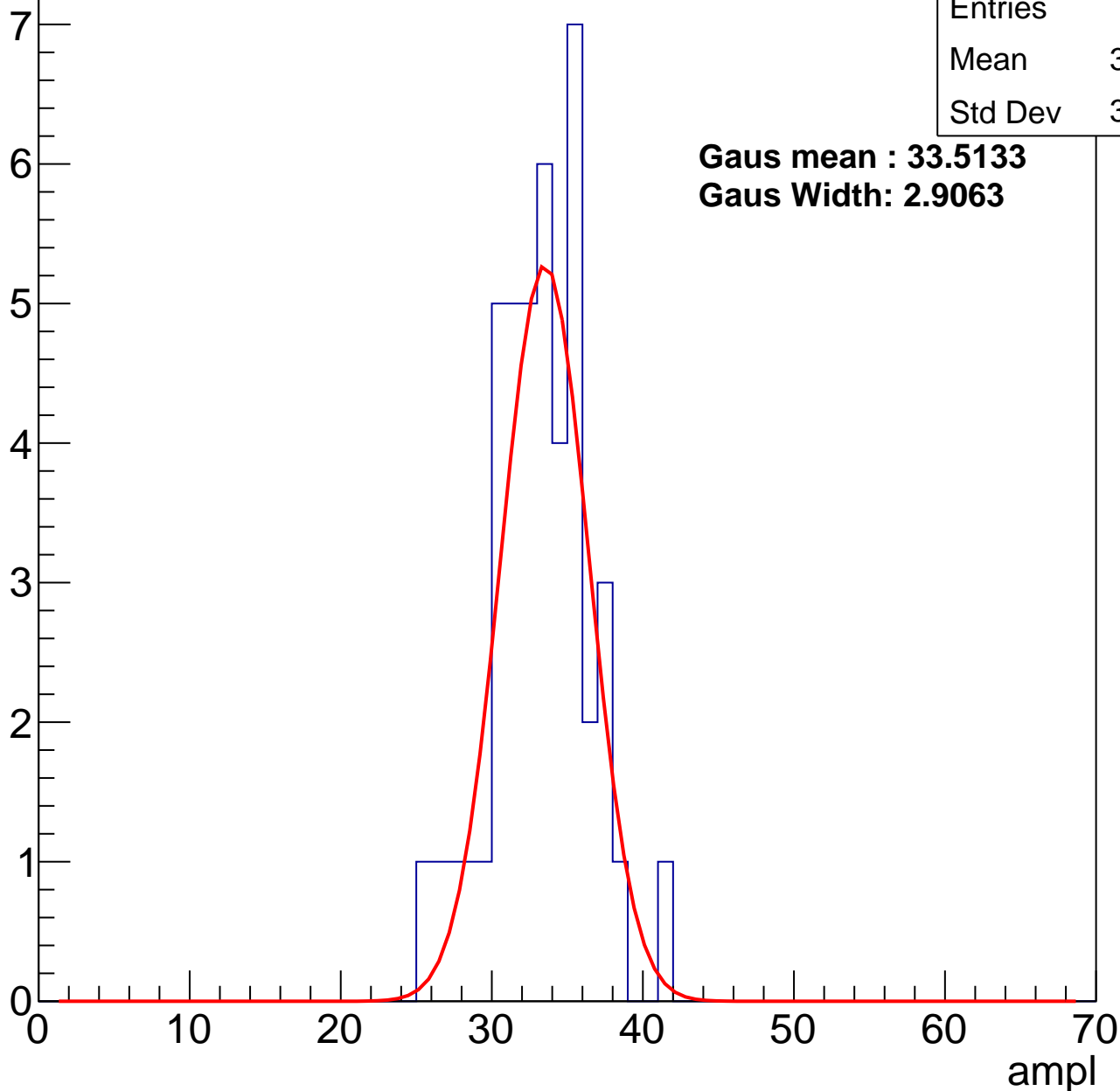
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	32.75
Std Dev	3.178

**Gaus mean : 33.5133**

**Gaus Width: 2.9063**



# B1L002S, U3-ch62, adc2

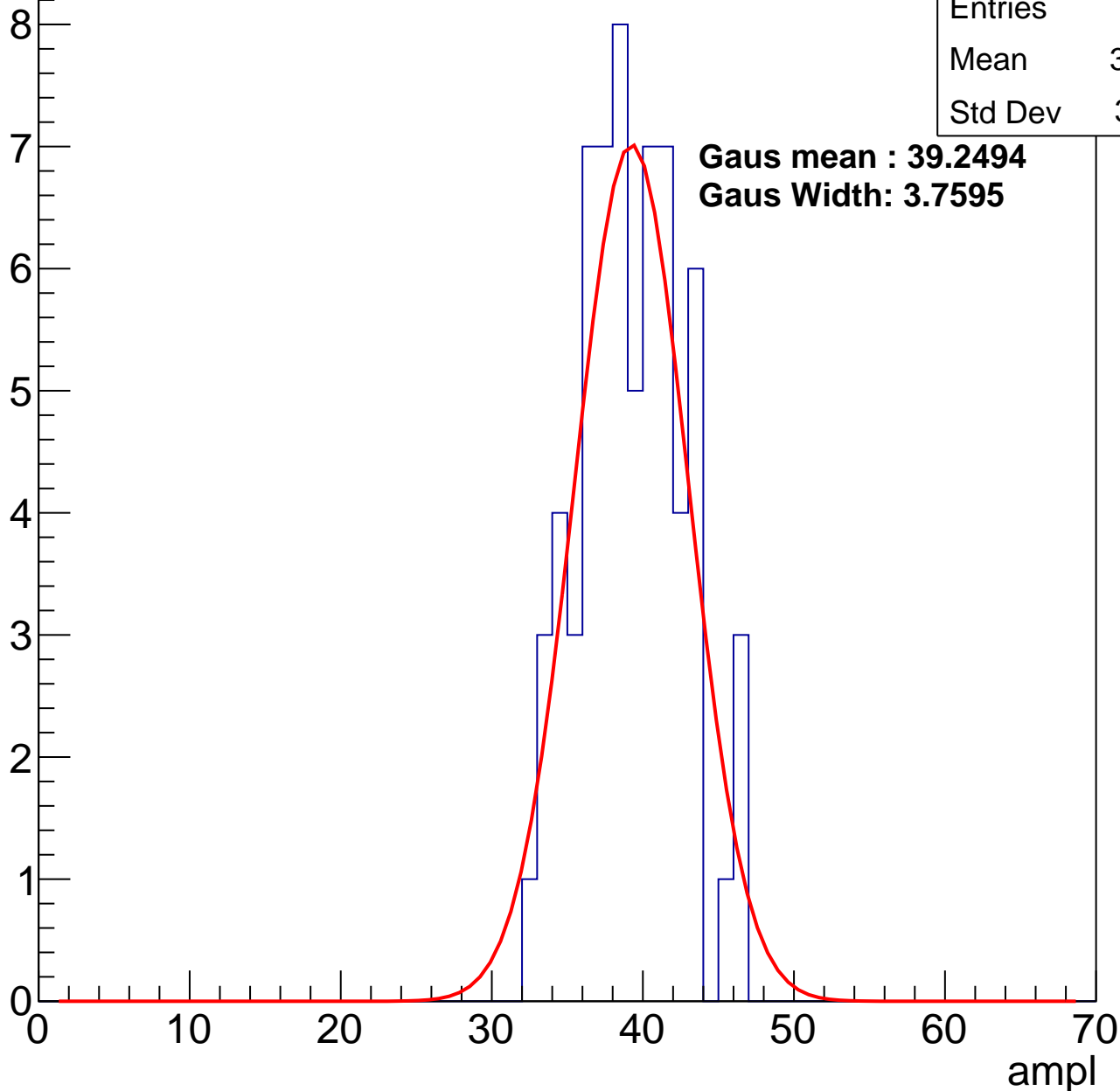
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	38.76
Std Dev	3.371

**Gaus mean : 39.2494**

**Gaus Width: 3.7595**

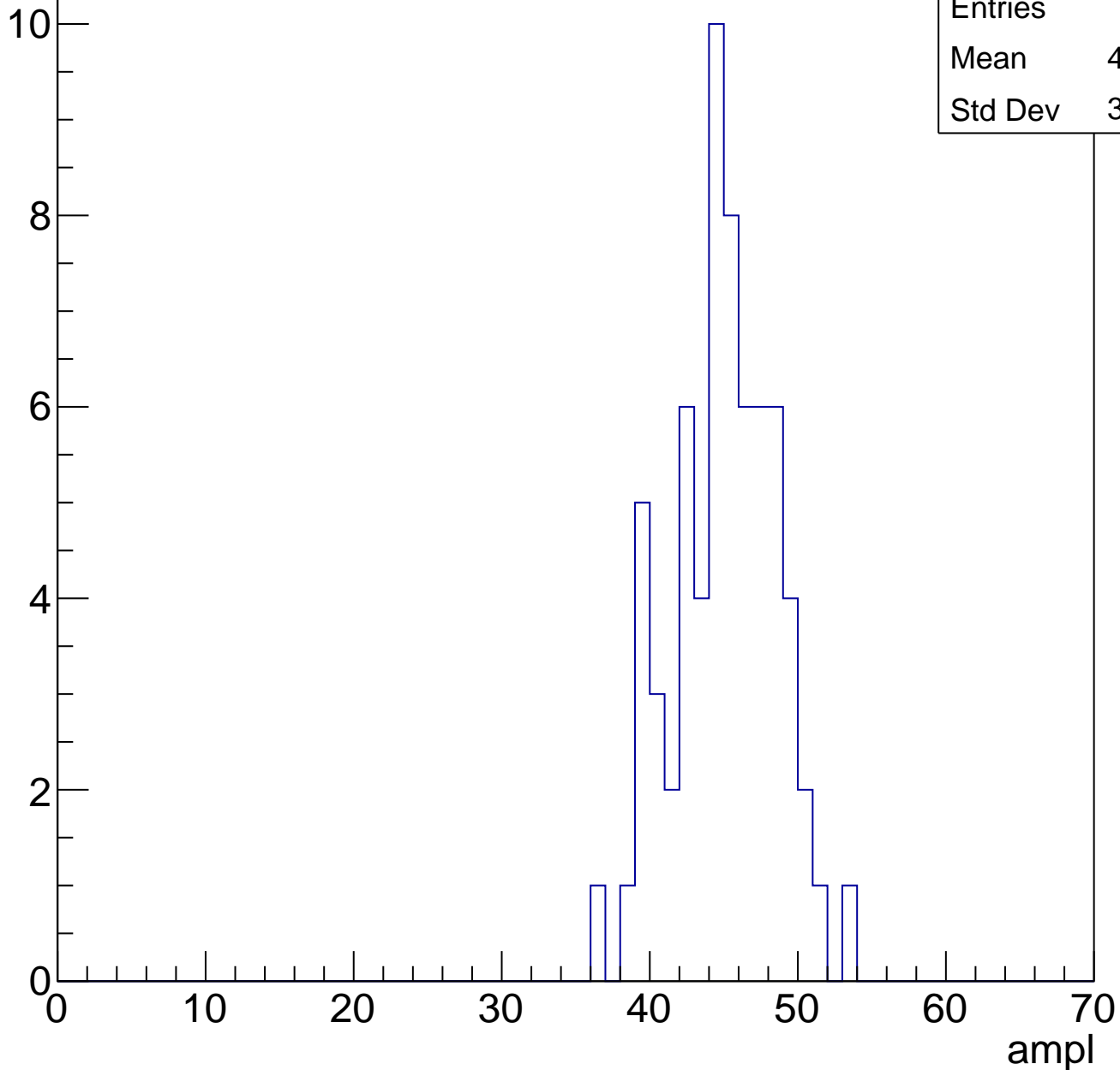


# B1L002S, U3-ch62, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	44.56
Std Dev	3.443

Entry

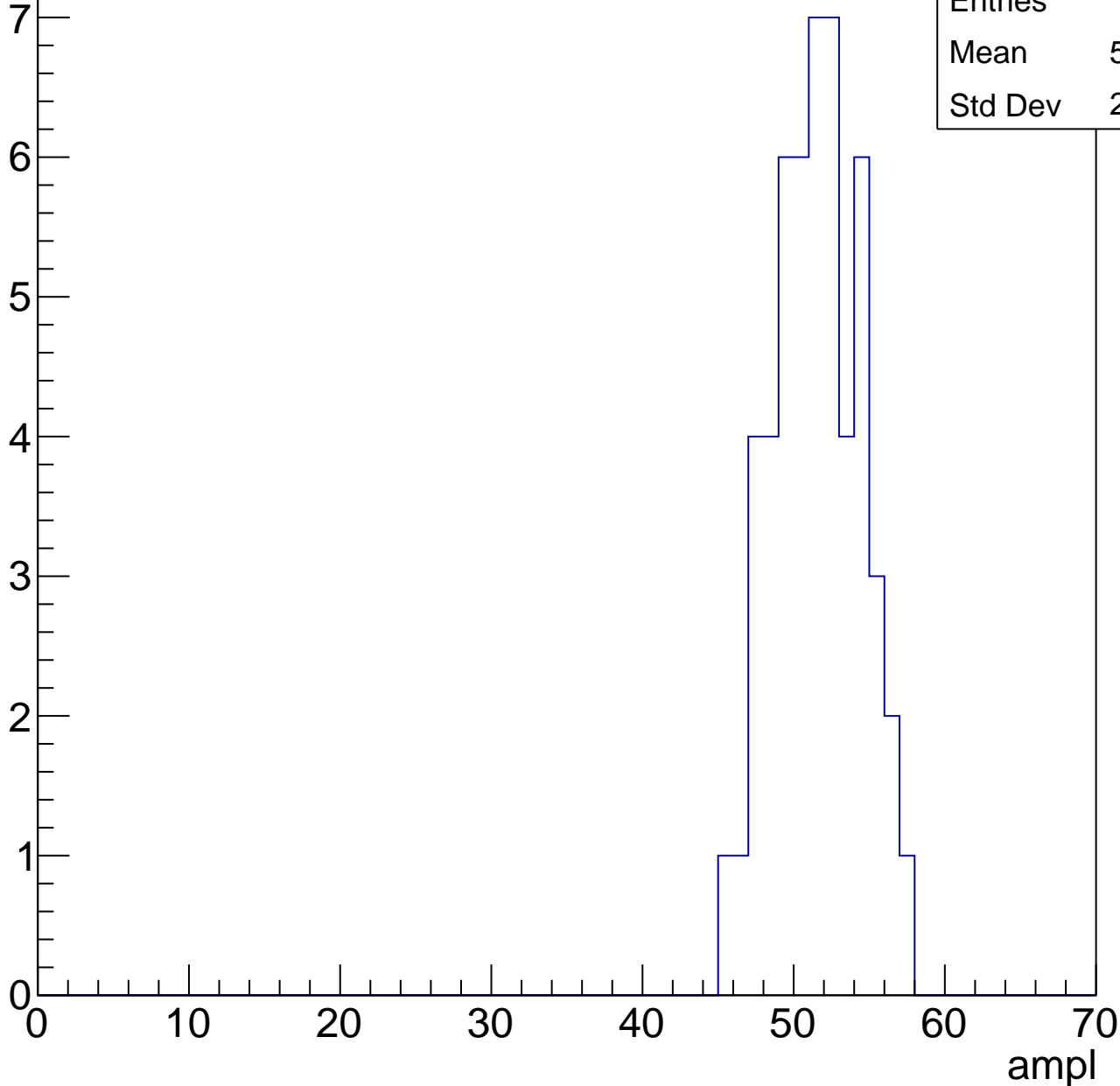


# B1L002S, U3-ch62, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	51.08
Std Dev	2.779

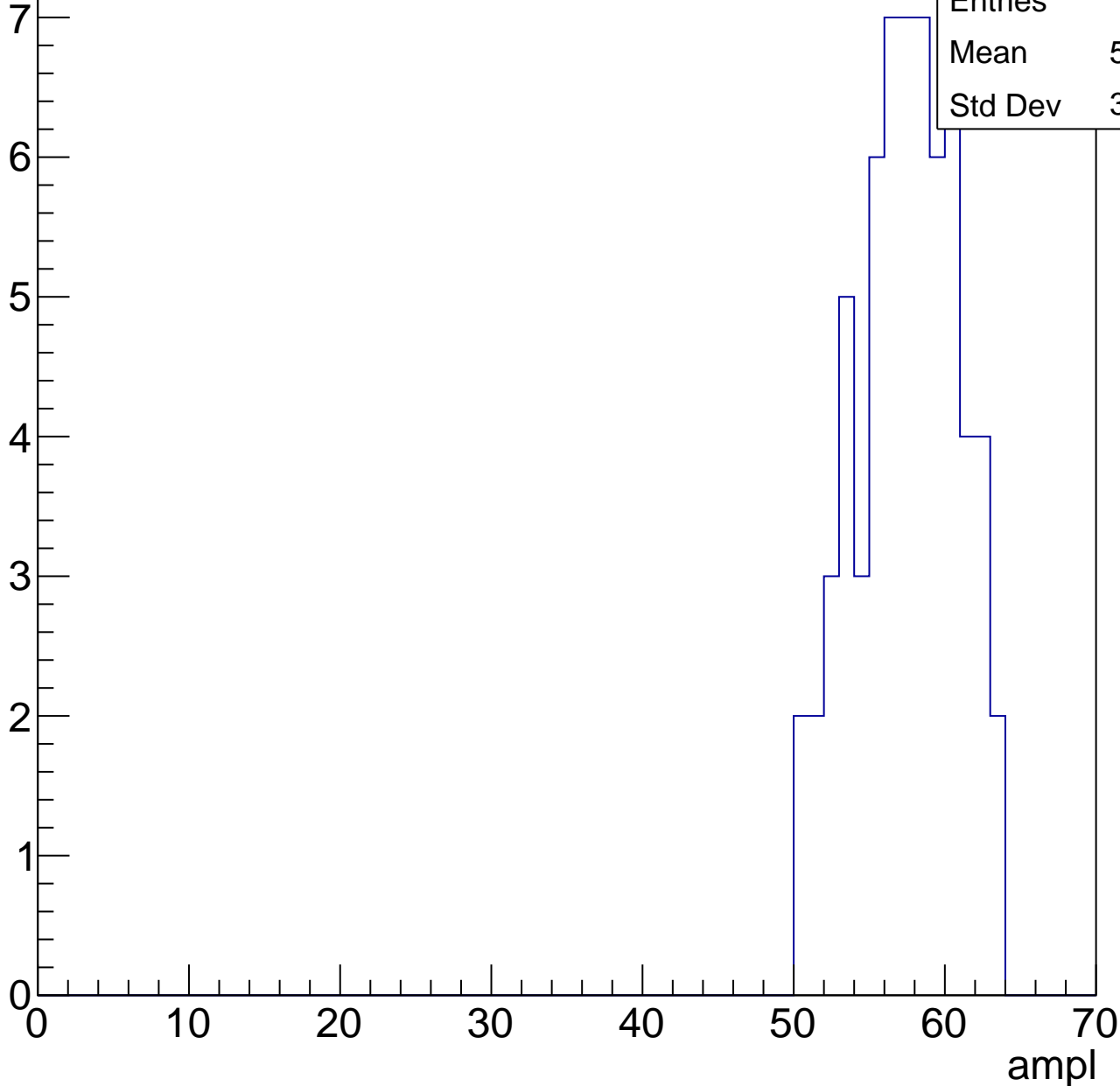


# B1L002S, U3-ch62, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

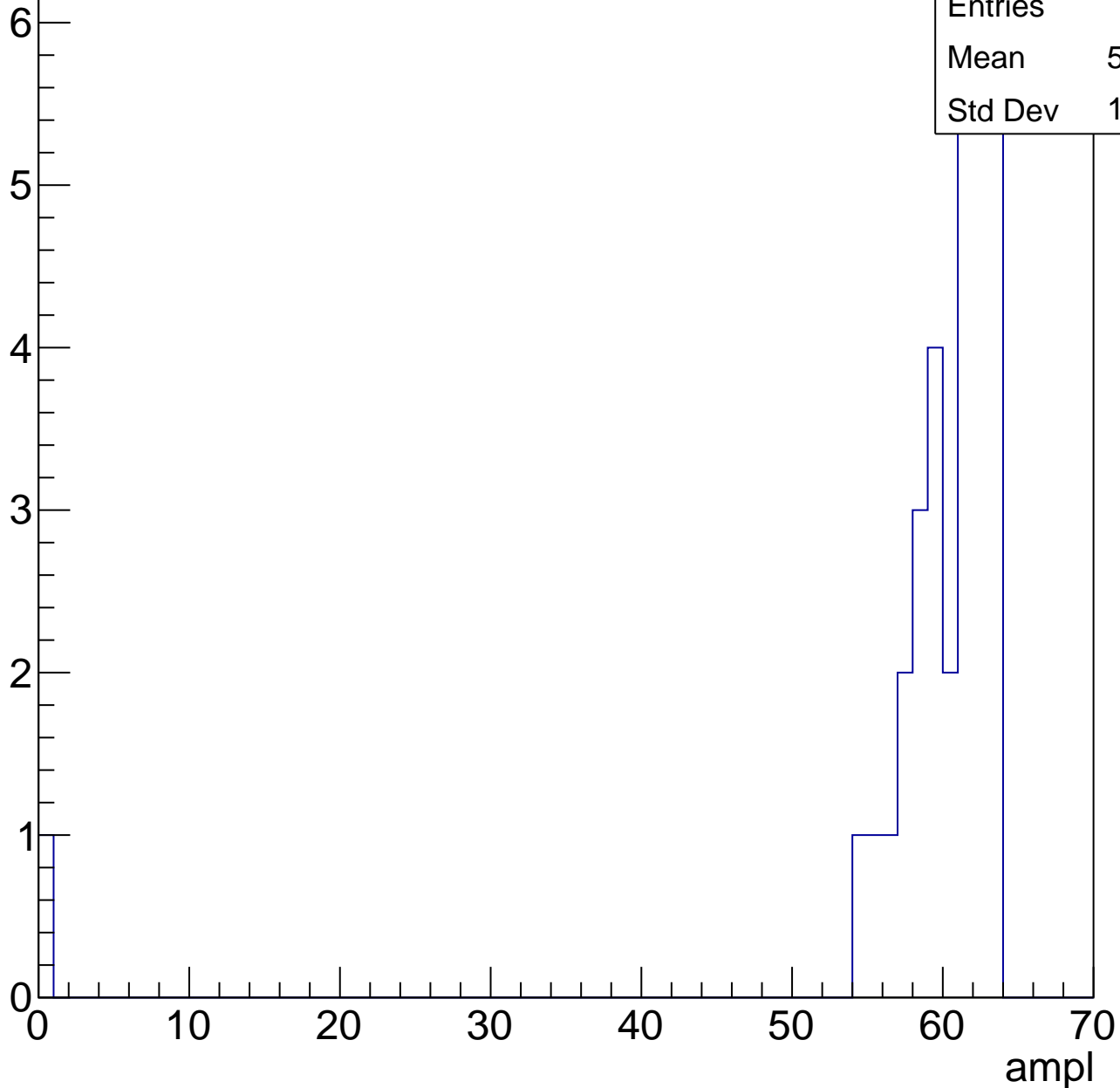
Entries	65
Mean	56.98
Std Dev	3.312



# B1L002S, U3-ch62, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

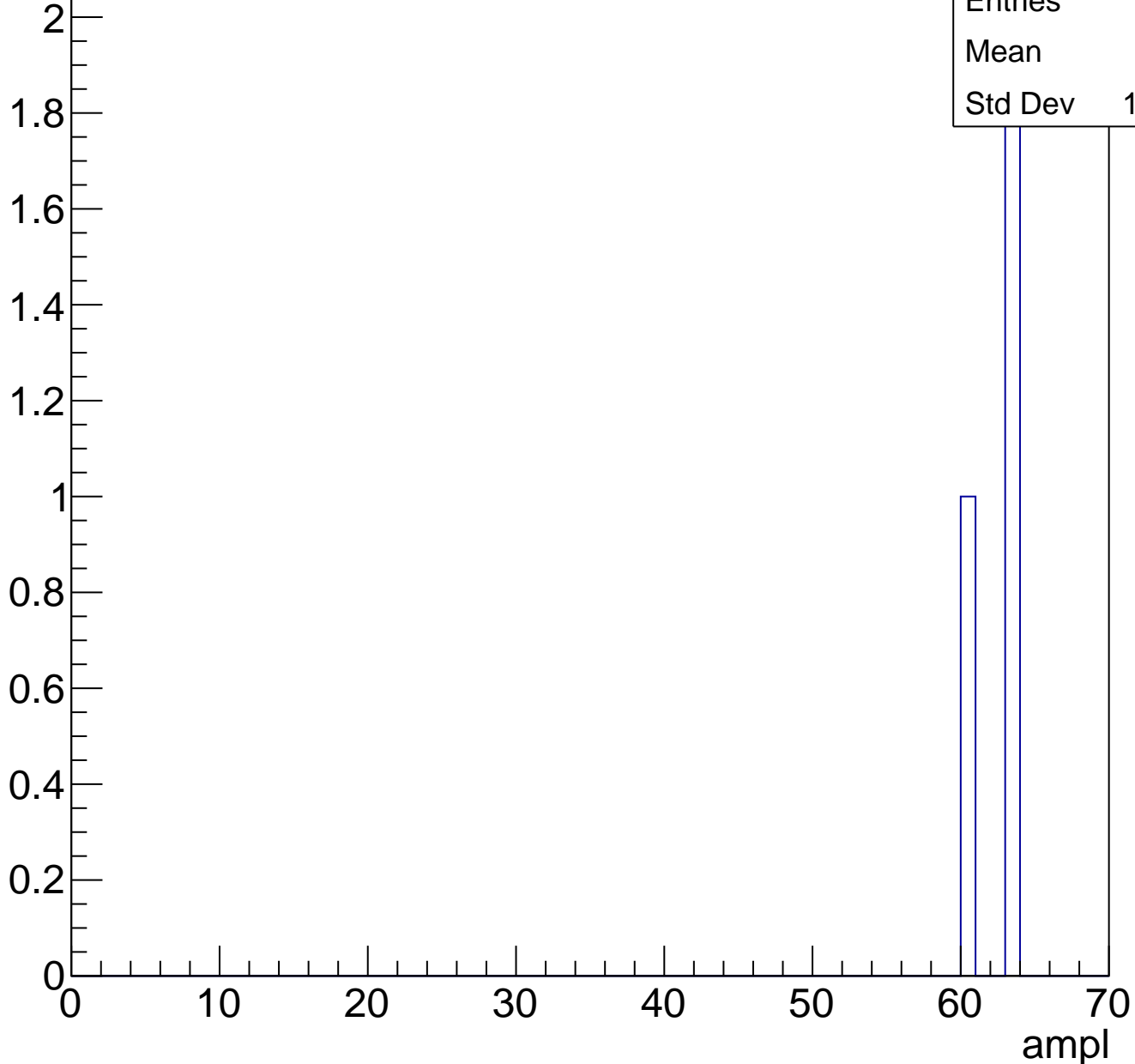




# B1L002S, U3-ch62, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch63, adc0

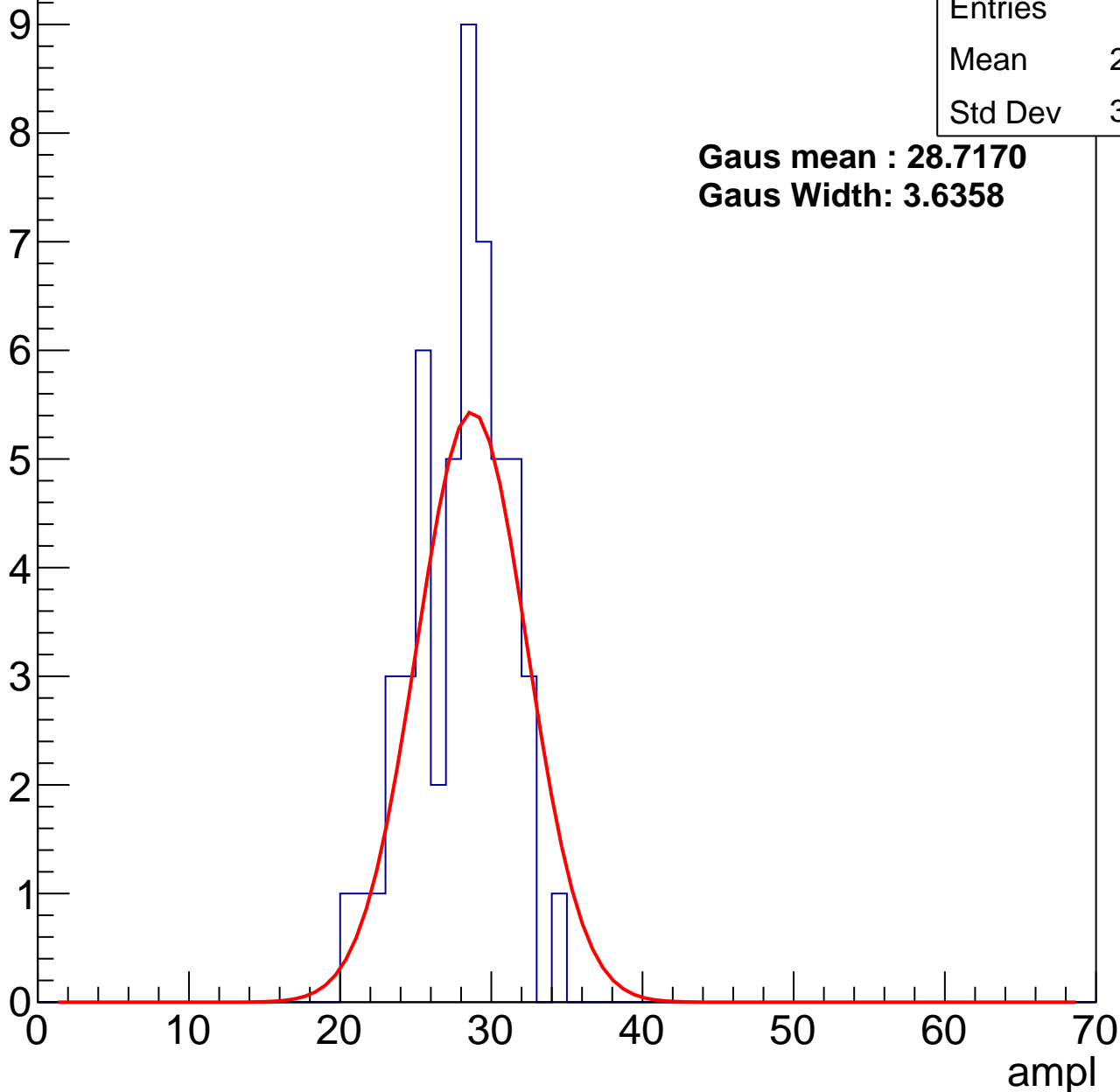
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.52
Std Dev	3.048

**Gaus mean : 28.7170**

**Gaus Width: 3.6358**



# B1L002S, U3-ch63, adc1

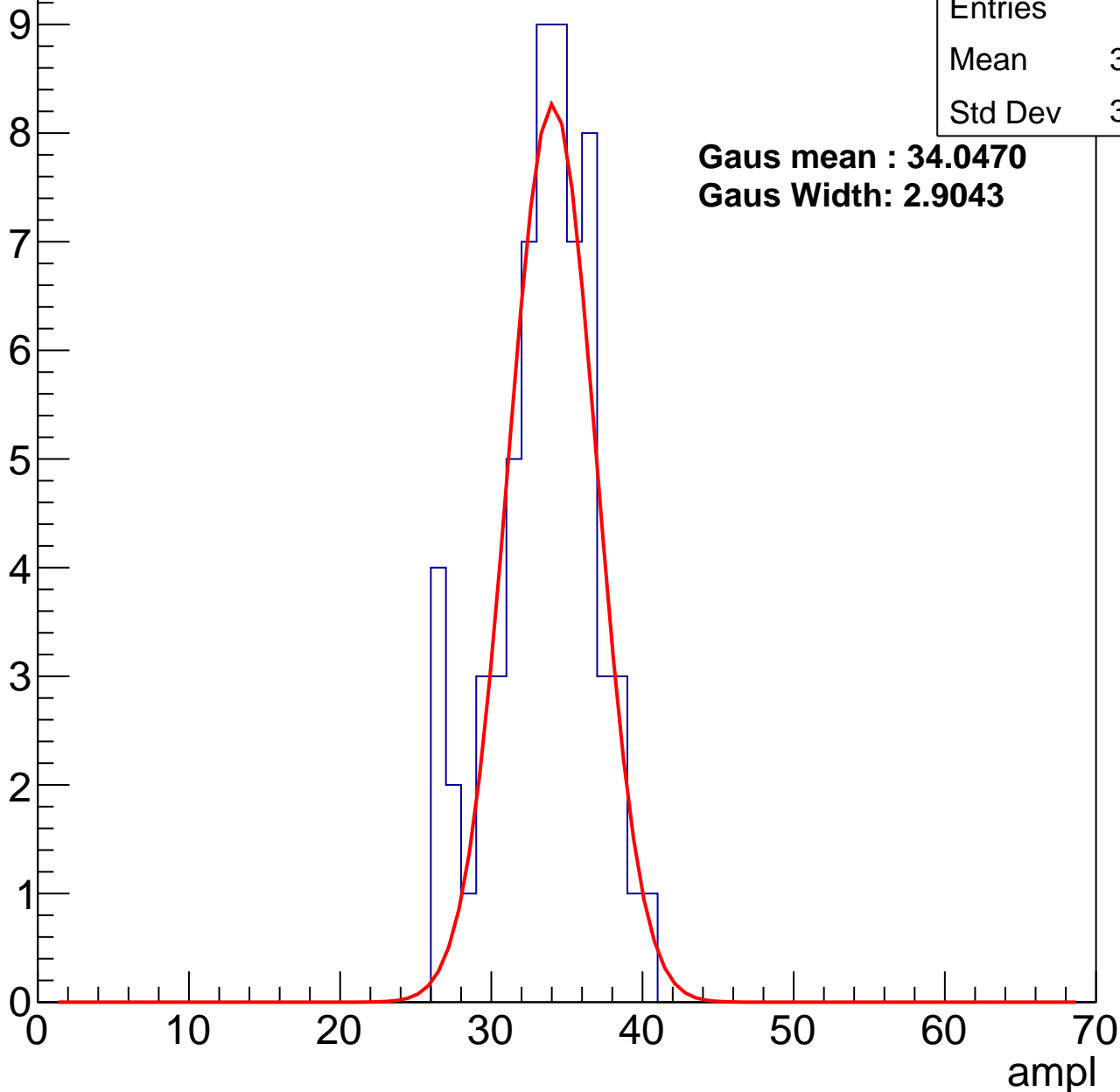
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.06
Std Dev	3.284

**Gaus mean : 34.0470**

**Gaus Width: 2.9043**



# B1L002S, U3-ch63, adc2

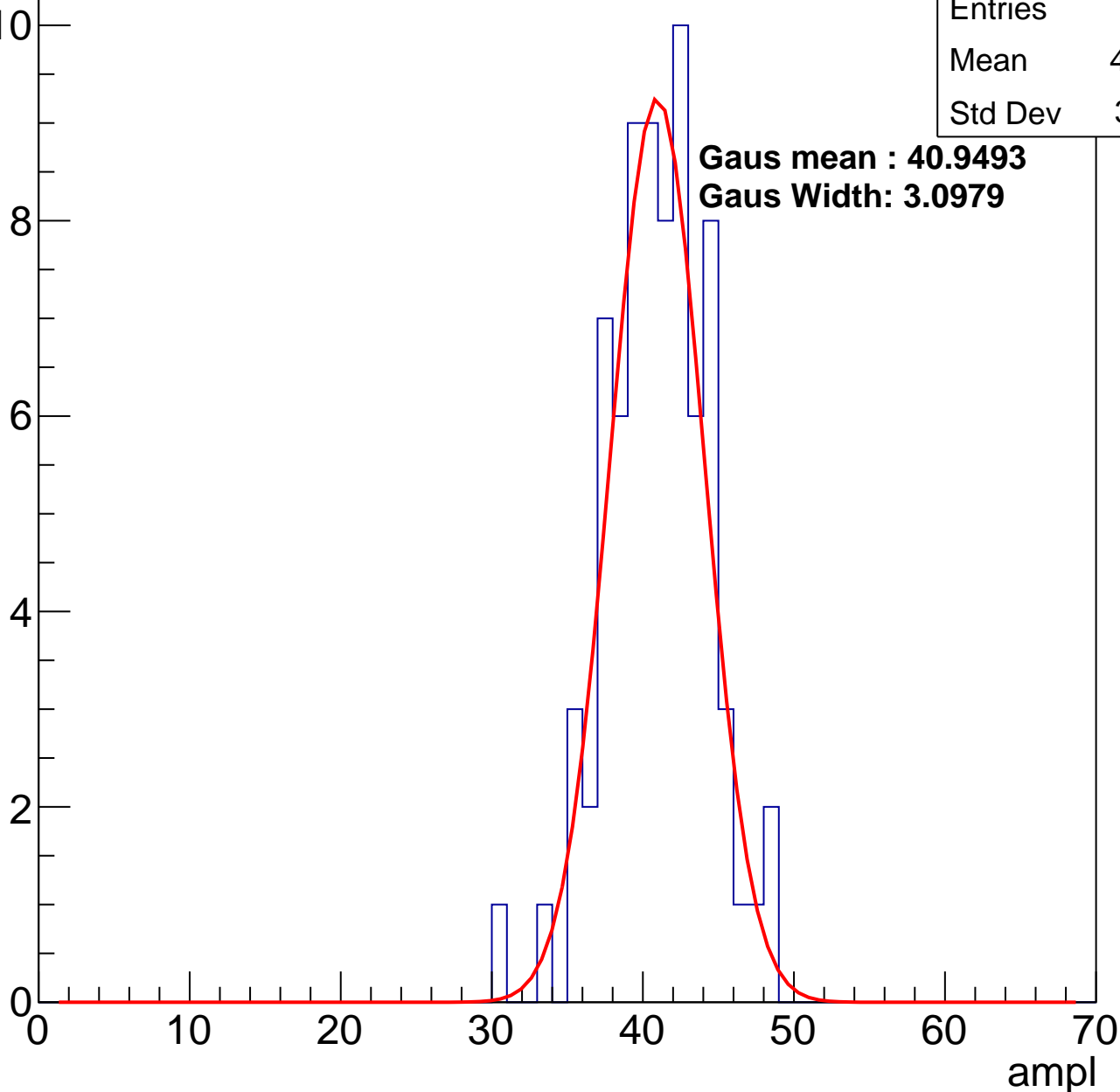
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	40.52
Std Dev	3.321

**Gaus mean : 40.9493**

**Gaus Width: 3.0979**

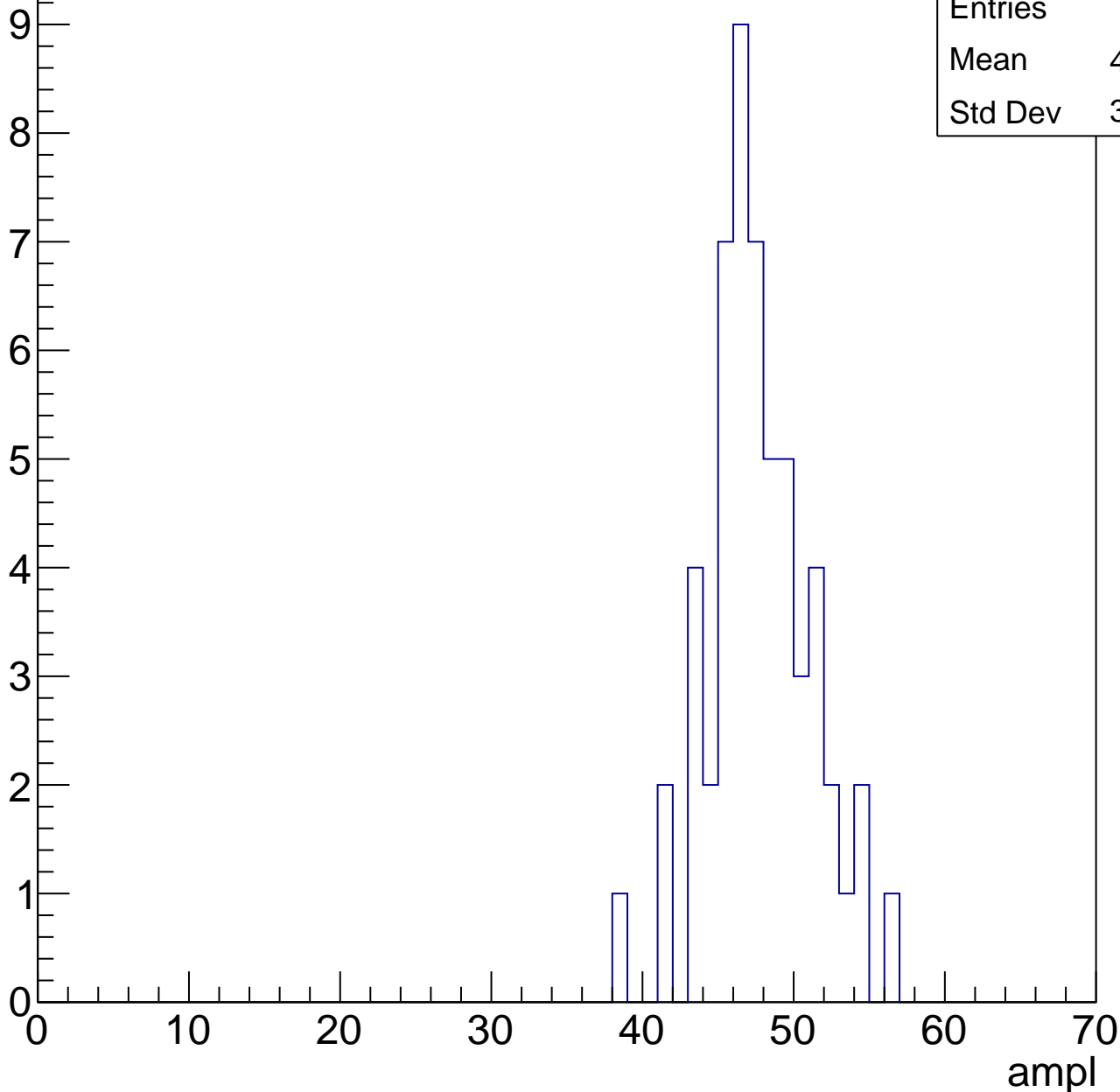


# B1L002S, U3-ch63, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	47.24
Std Dev	3.438

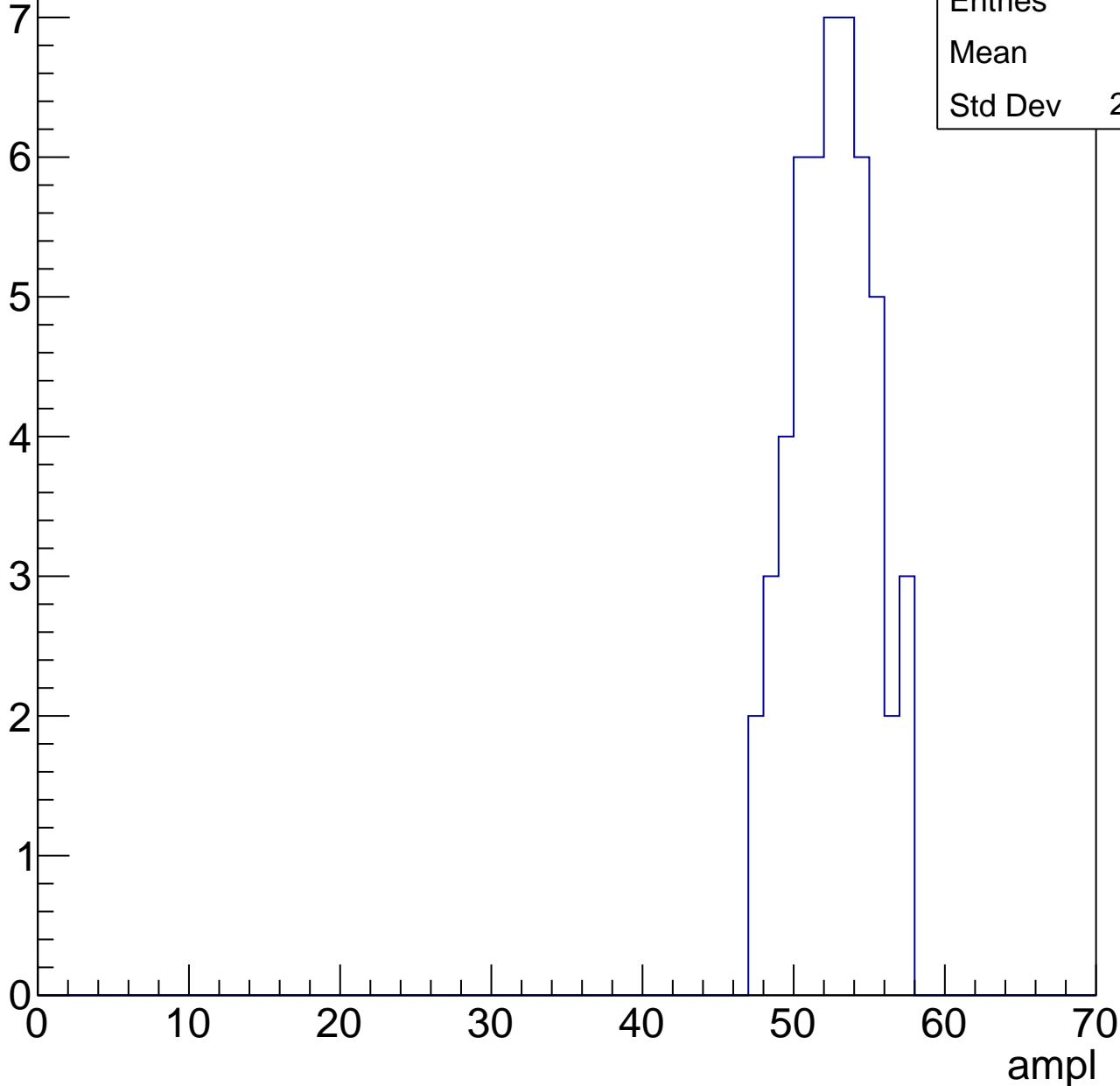


# B1L002S, U3-ch63, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

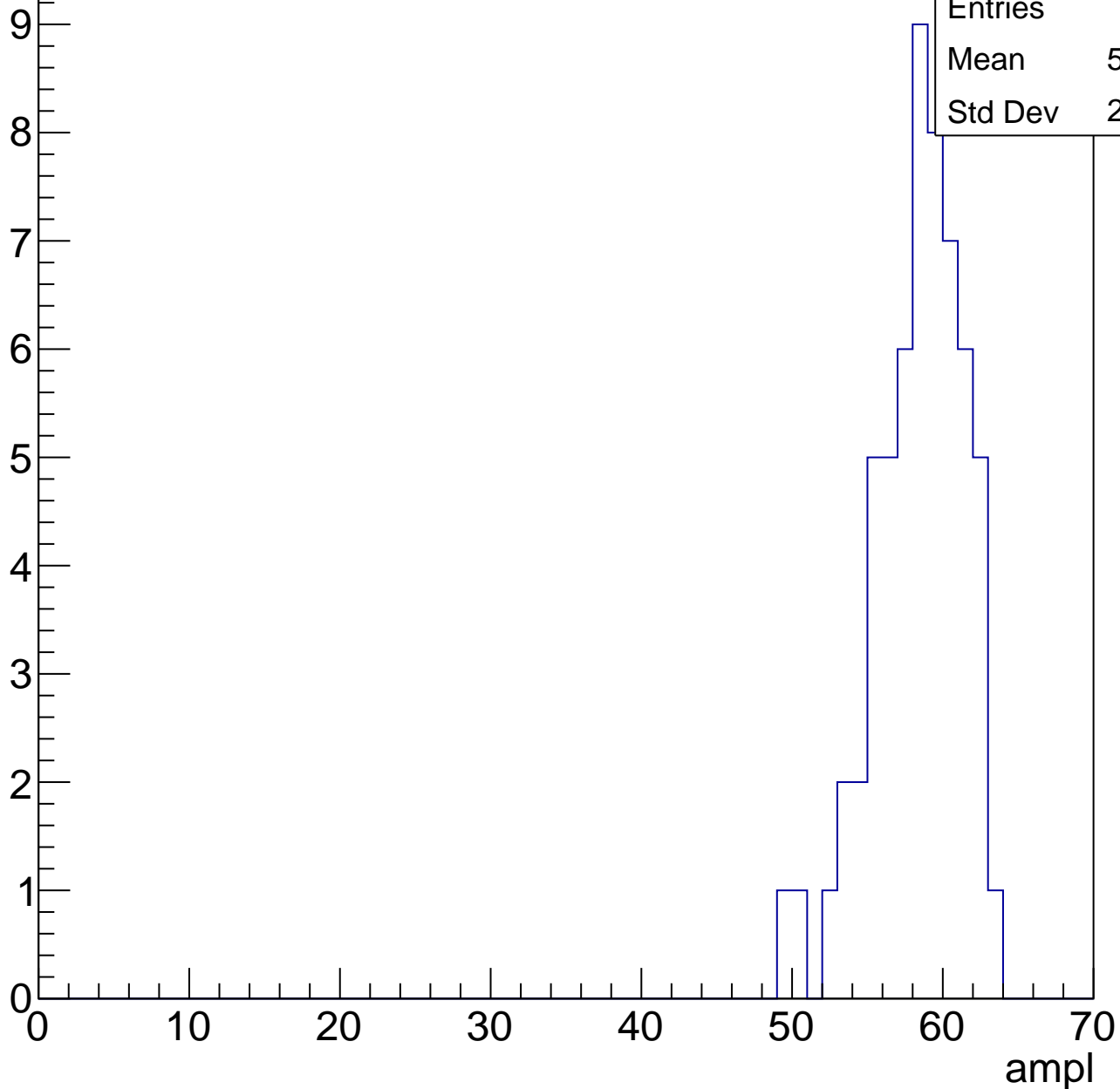
Entries	51
Mean	52.1
Std Dev	2.607



# B1L002S, U3-ch63, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



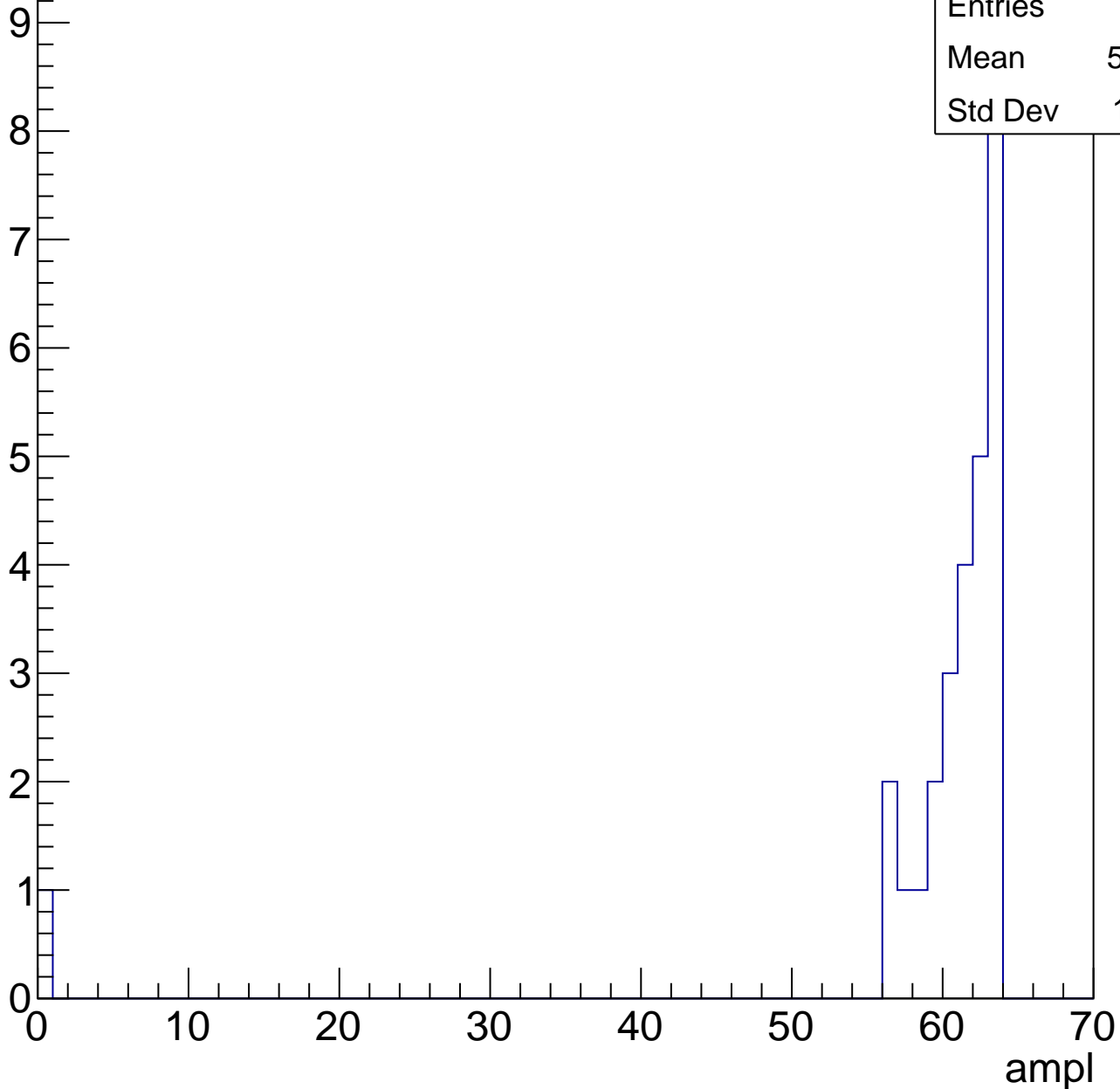
Entries	59
Mean	57.88
Std Dev	2.992

# B1L002S, U3-ch63, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.79
Std Dev	11.51





# B1L002S, U3-ch63, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch64, adc0

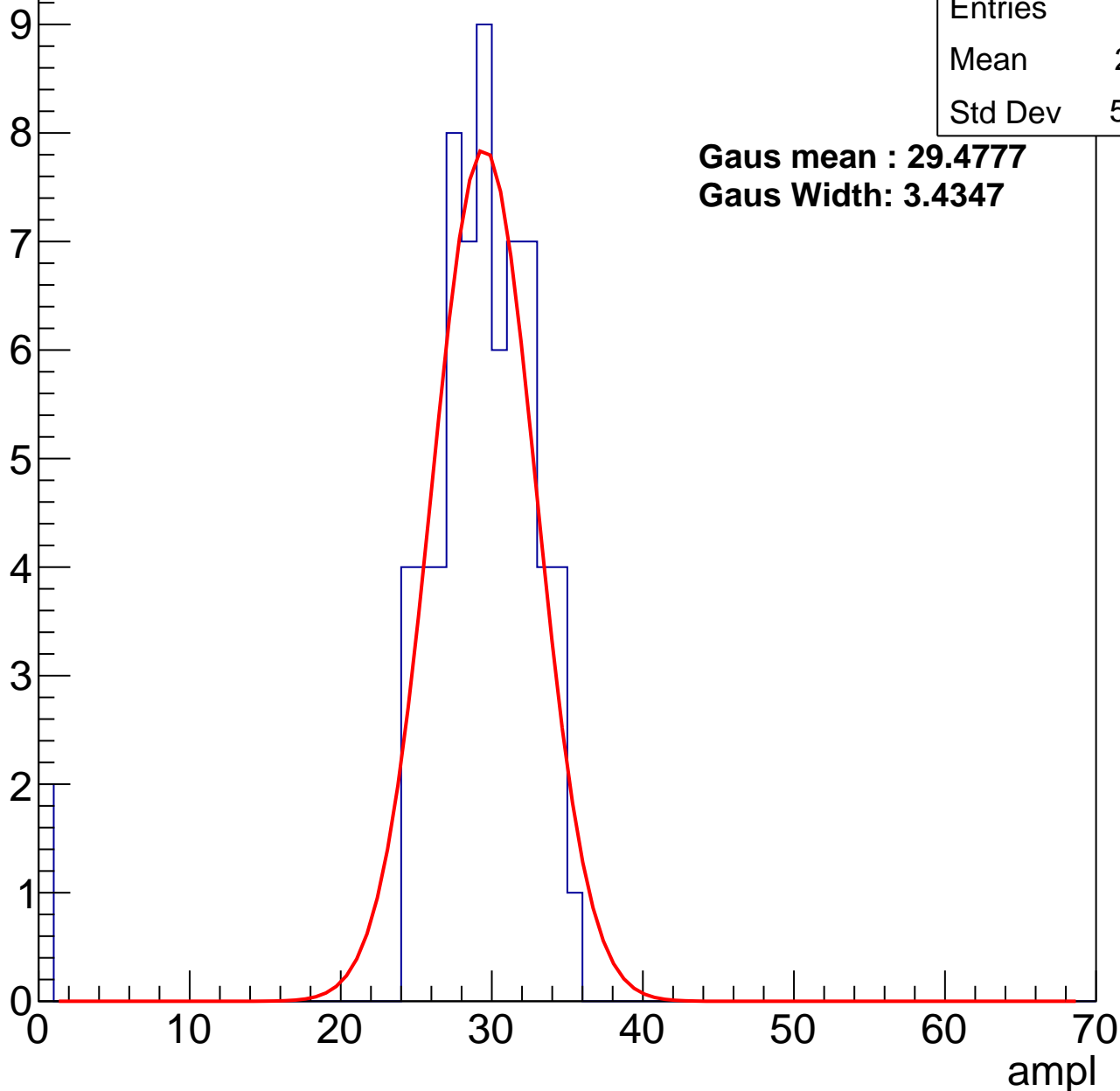
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.31
Std Dev	5.713

**Gaus mean : 29.4777**

**Gaus Width: 3.4347**



# B1L002S, U3-ch64, adc1

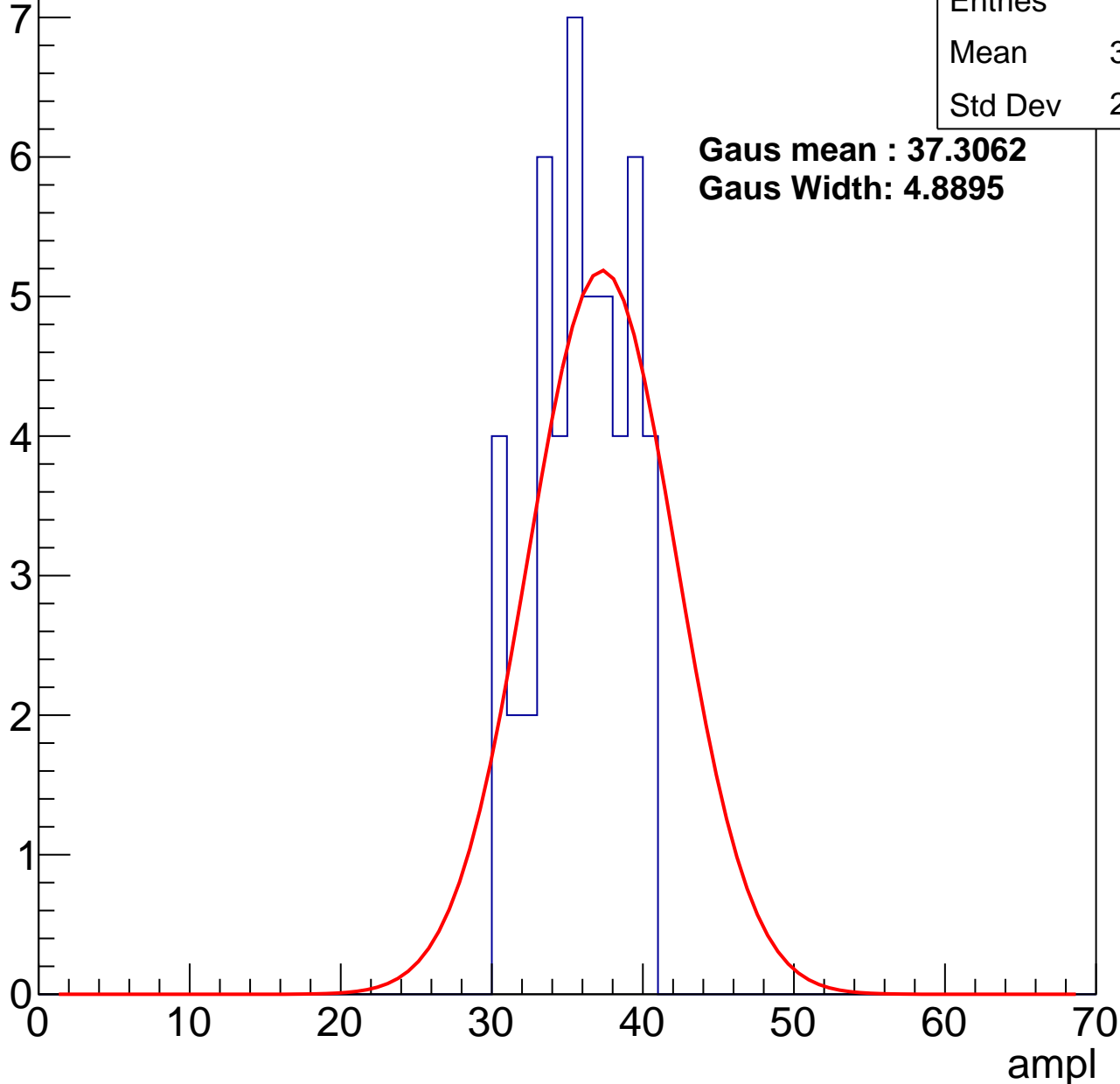
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	35.43
Std Dev	2.949

**Gaus mean : 37.3062**

**Gaus Width: 4.8895**



# B1L002S, U3-ch64, adc2

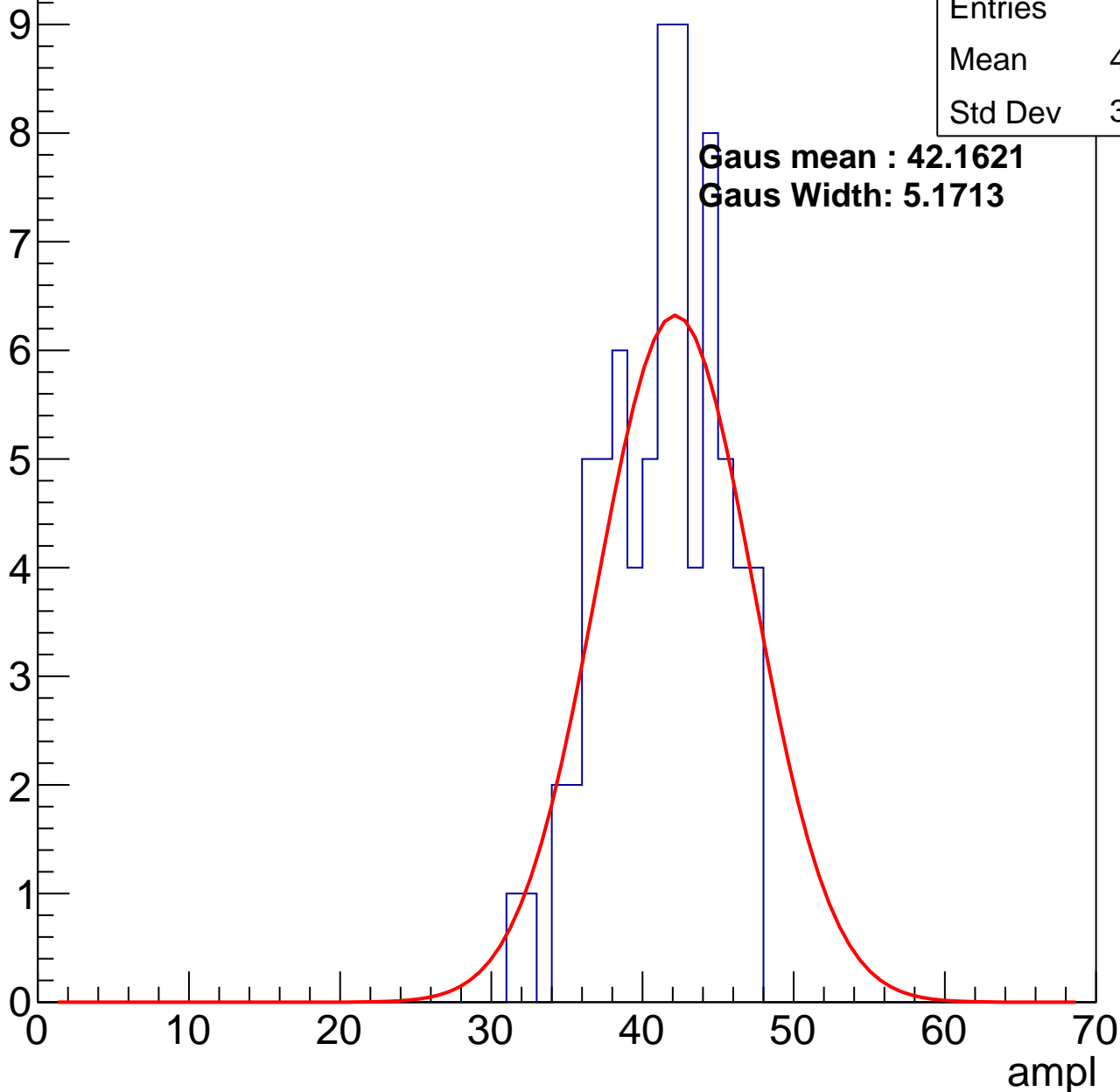
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	40.78
Std Dev	3.764

**Gaus mean : 42.1621**

**Gaus Width: 5.1713**

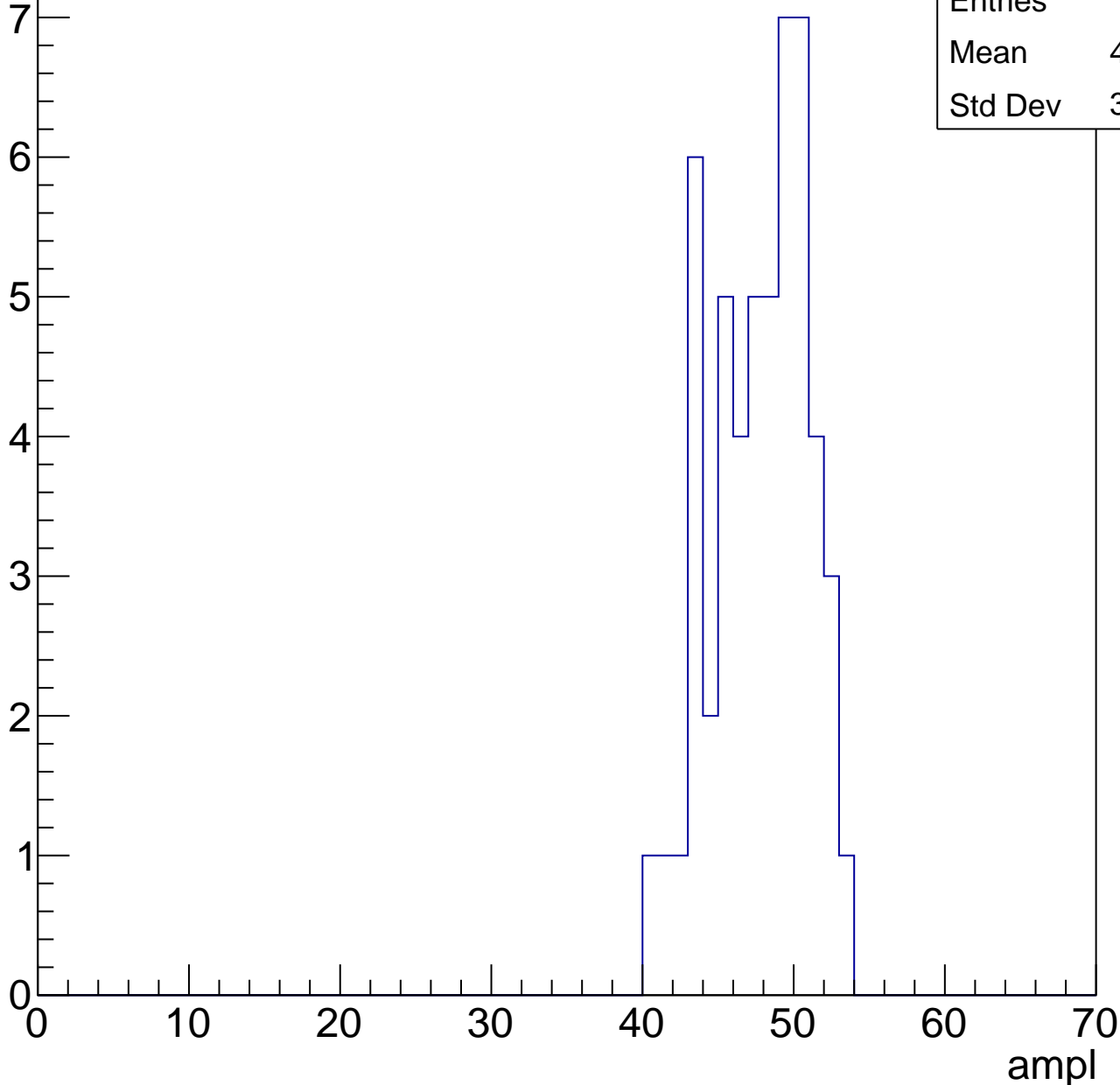


# B1L002S, U3-ch64, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	47.29
Std Dev	3.146

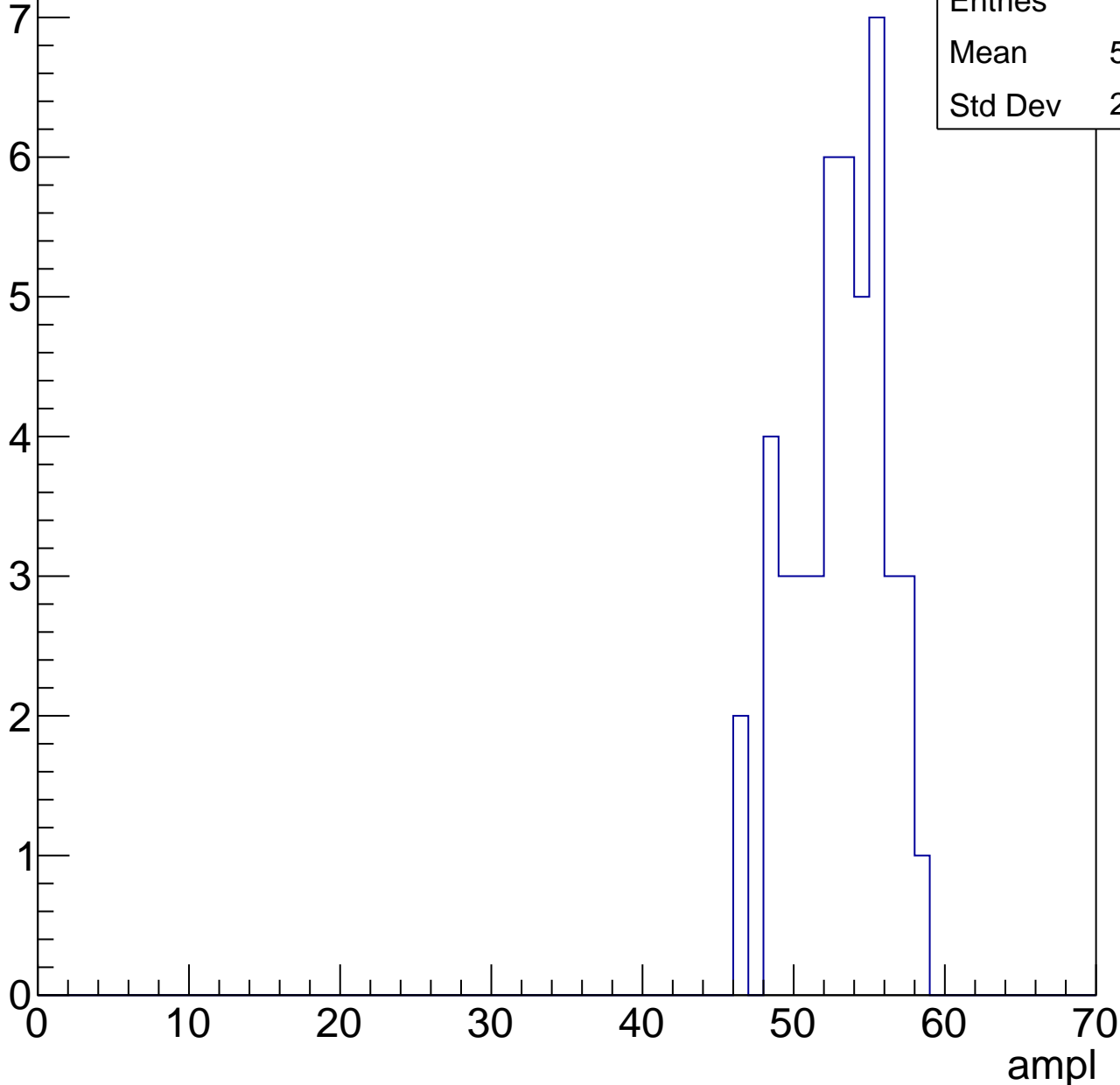


# B1L002S, U3-ch64, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	52.52
Std Dev	2.998



# B1L002S, U3-ch64, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	57.36
Std Dev	3.435

Entry

10

8

6

4

2

0

0

10

20

30

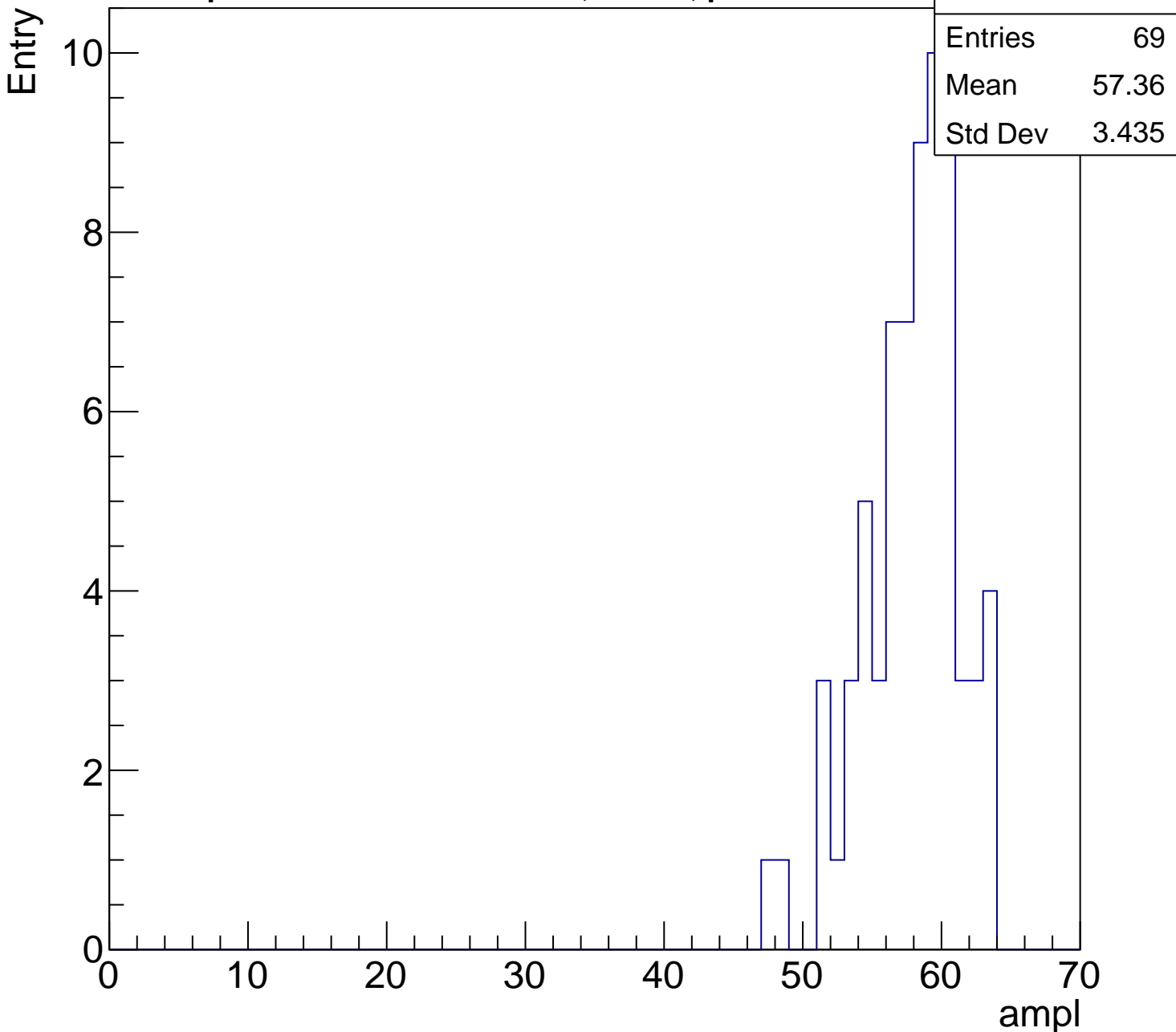
40

50

60

ampl

70

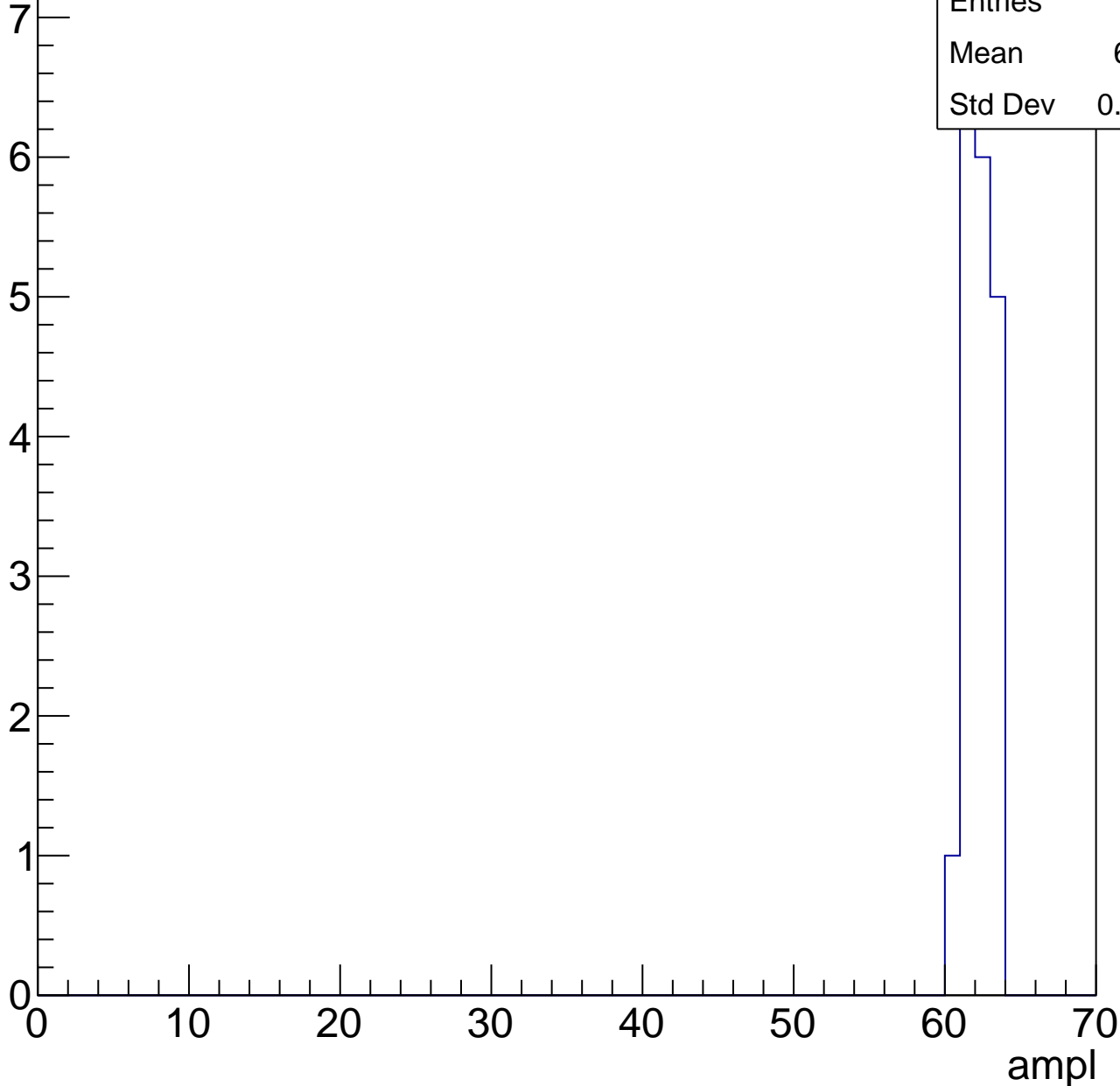


# B1L002S, U3-ch64, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.79
Std Dev	0.8932

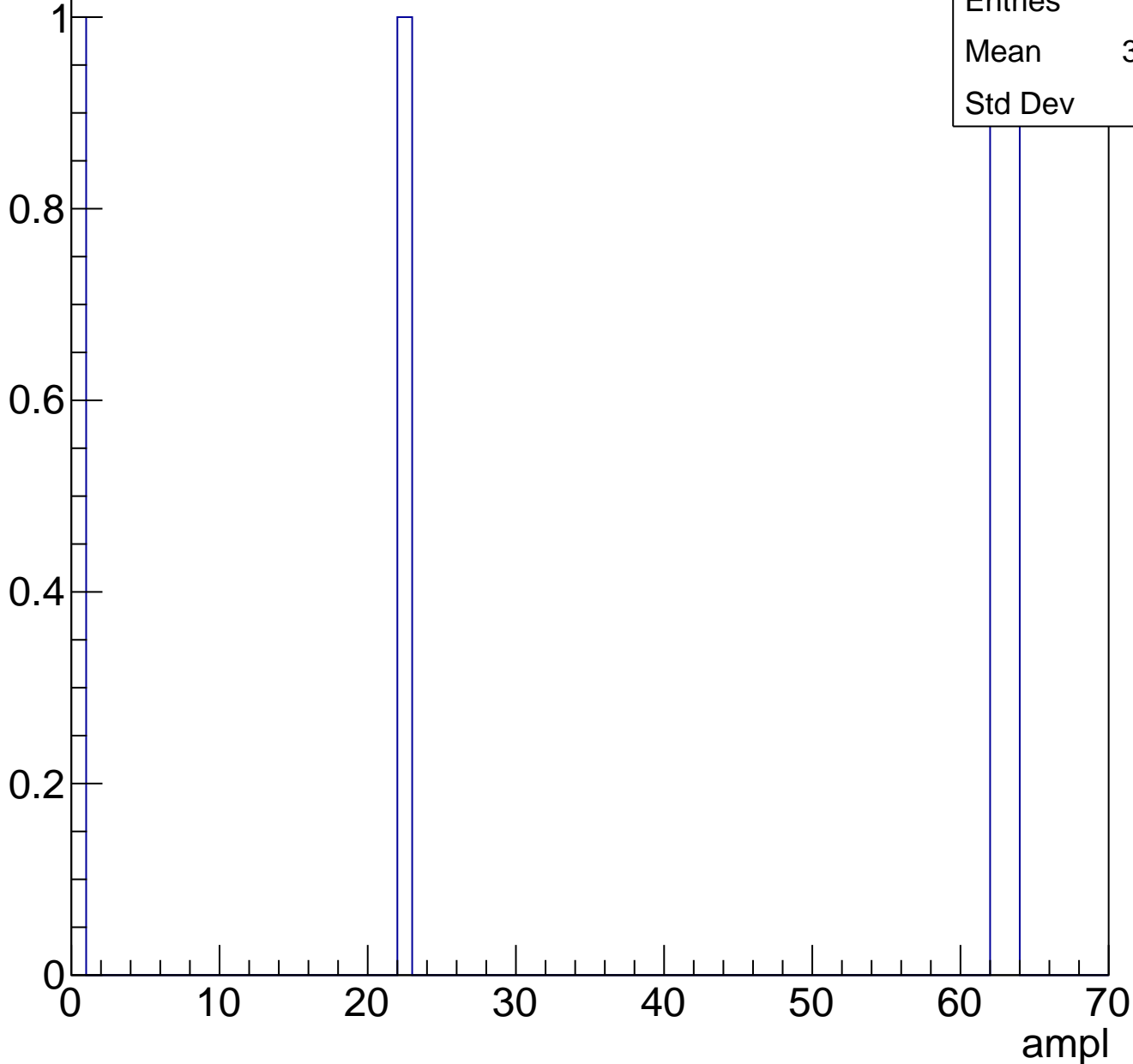




# B1L002S, U3-ch64, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch65, adc0

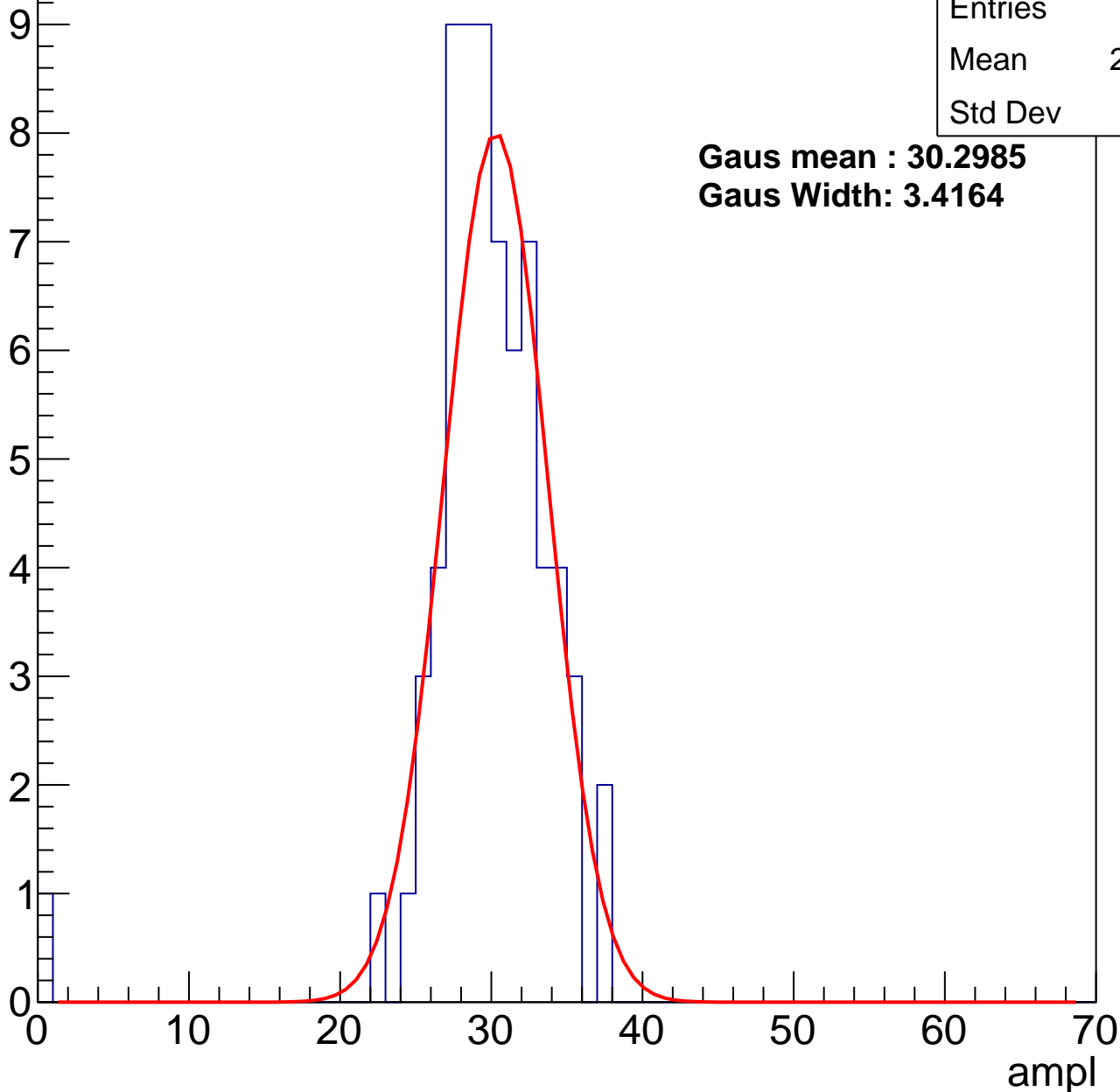
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	29.26
Std Dev	4.68

**Gaus mean : 30.2985**

**Gaus Width: 3.4164**



# B1L002S, U3-ch65, adc1

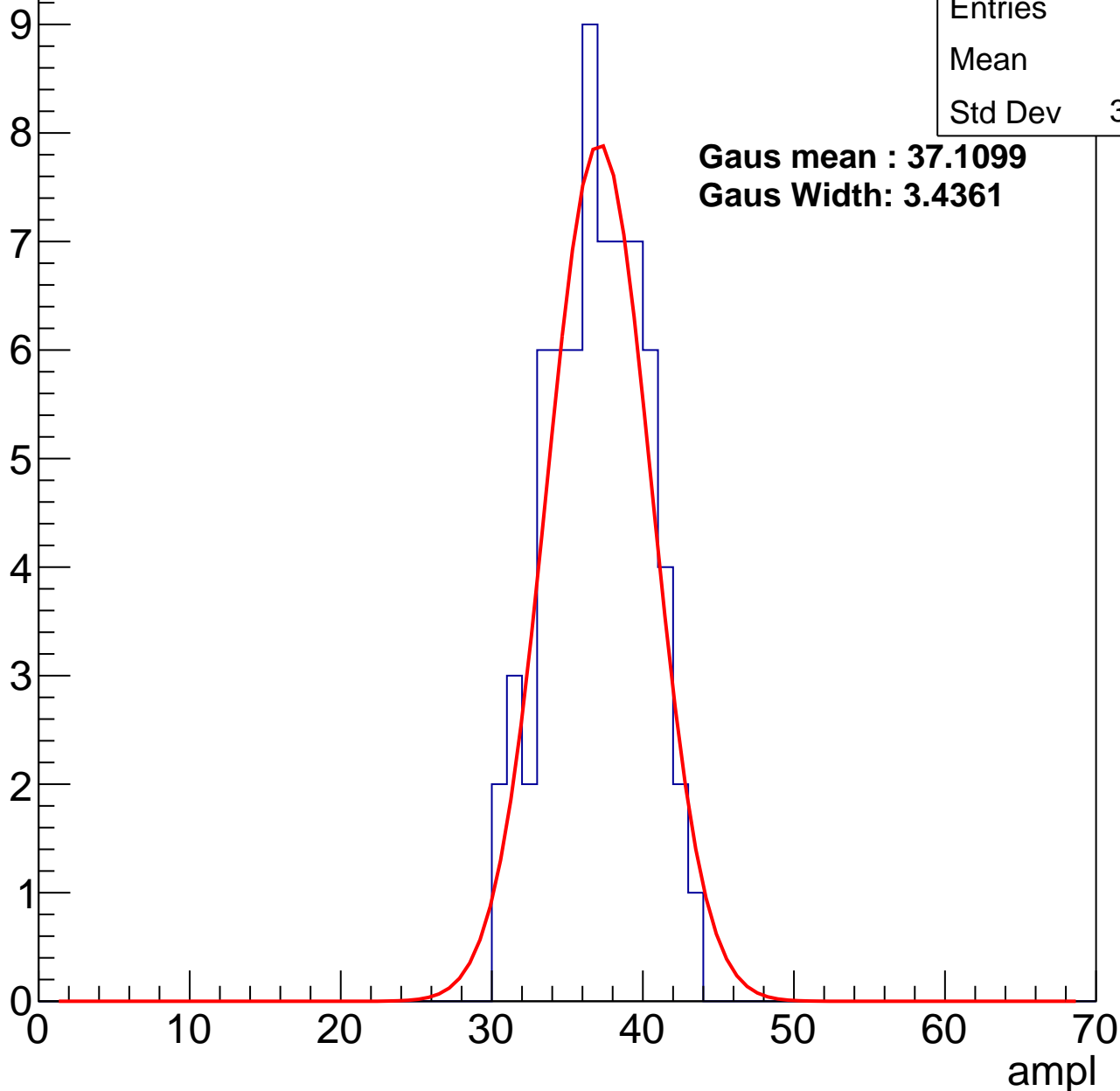
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	36.5
Std Dev	3.118

**Gaus mean : 37.1099**

**Gaus Width: 3.4361**



# B1L002S, U3-ch65, adc2

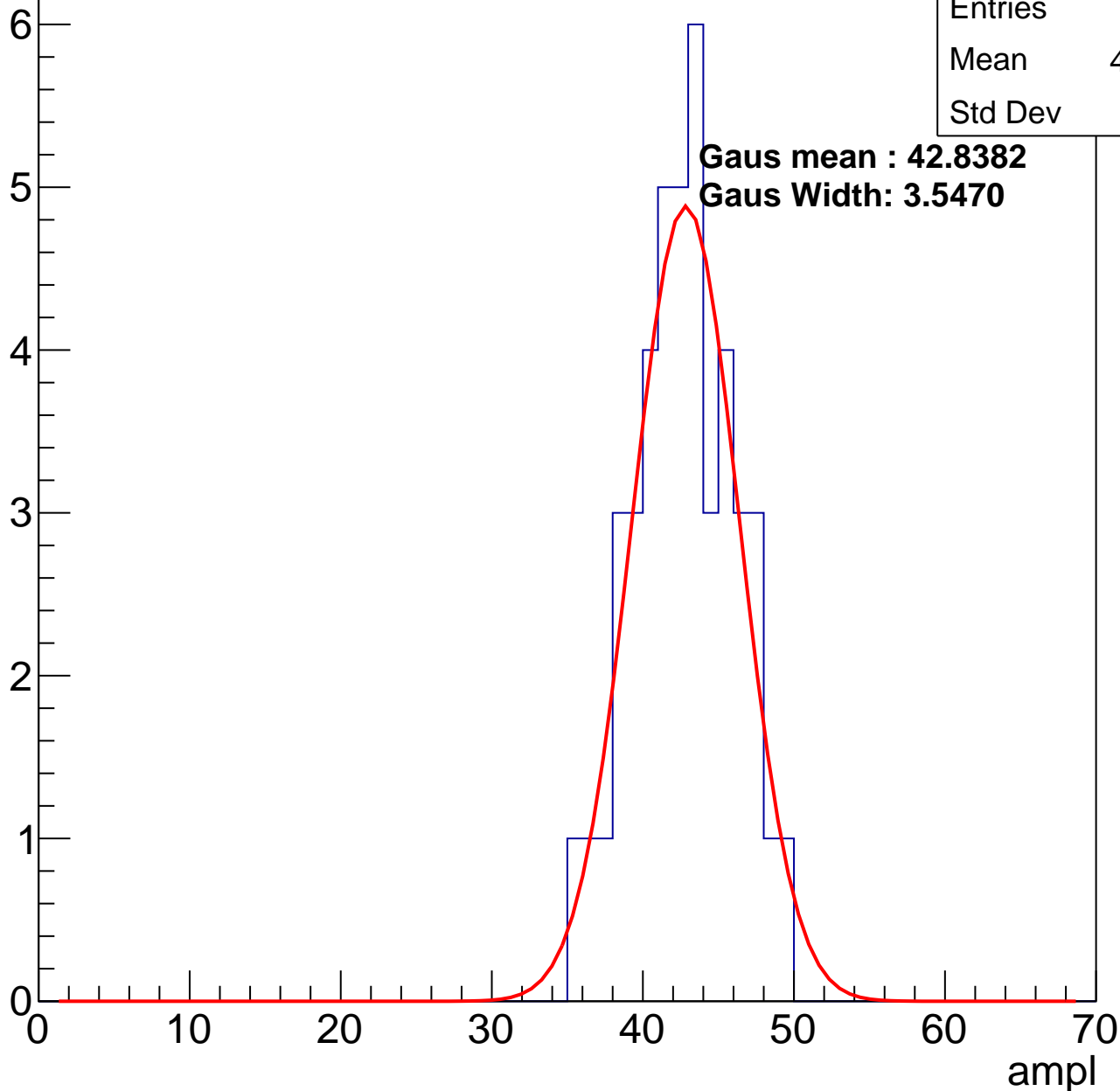
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	42.27
Std Dev	3.25

**Gaus mean : 42.8382**

**Gaus Width: 3.5470**

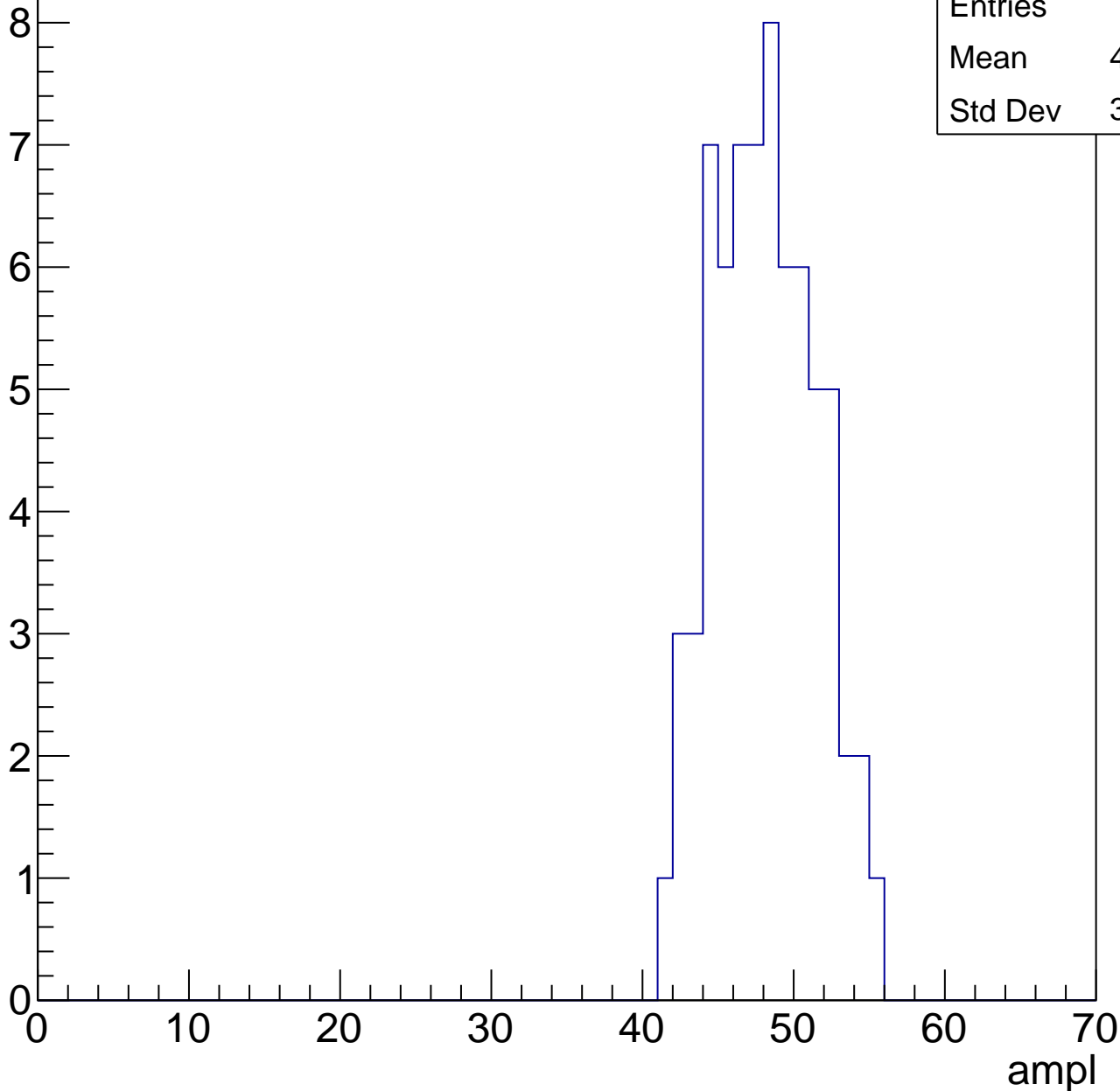


# B1L002S, U3-ch65, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

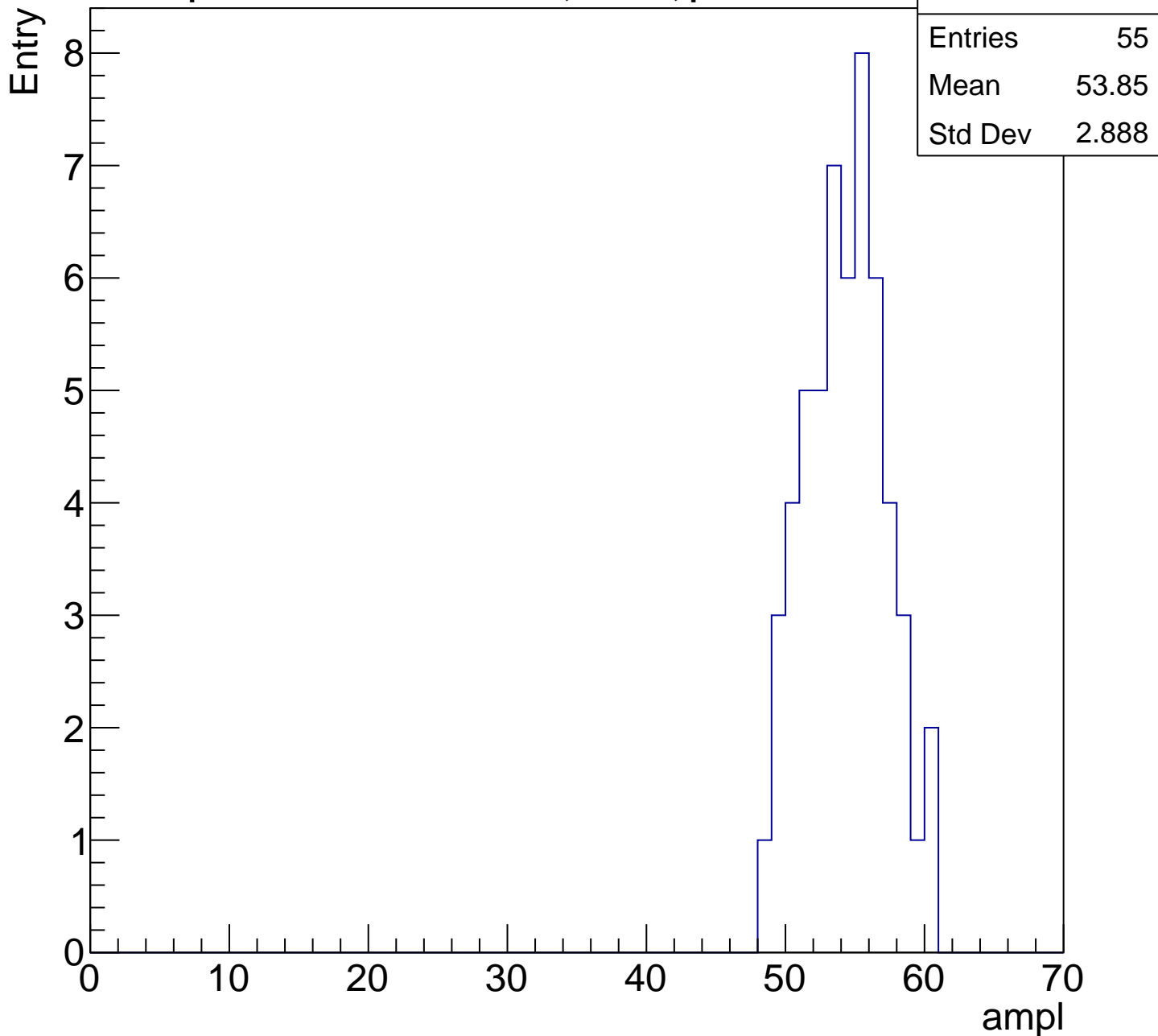
Entry

Entries	69
Mean	47.64
Std Dev	3.297



# B1L002S, U3-ch65, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch65, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

Entries 57

Mean 59.32

Std Dev 2.493

8

6

4

2

0

0

10

20

30

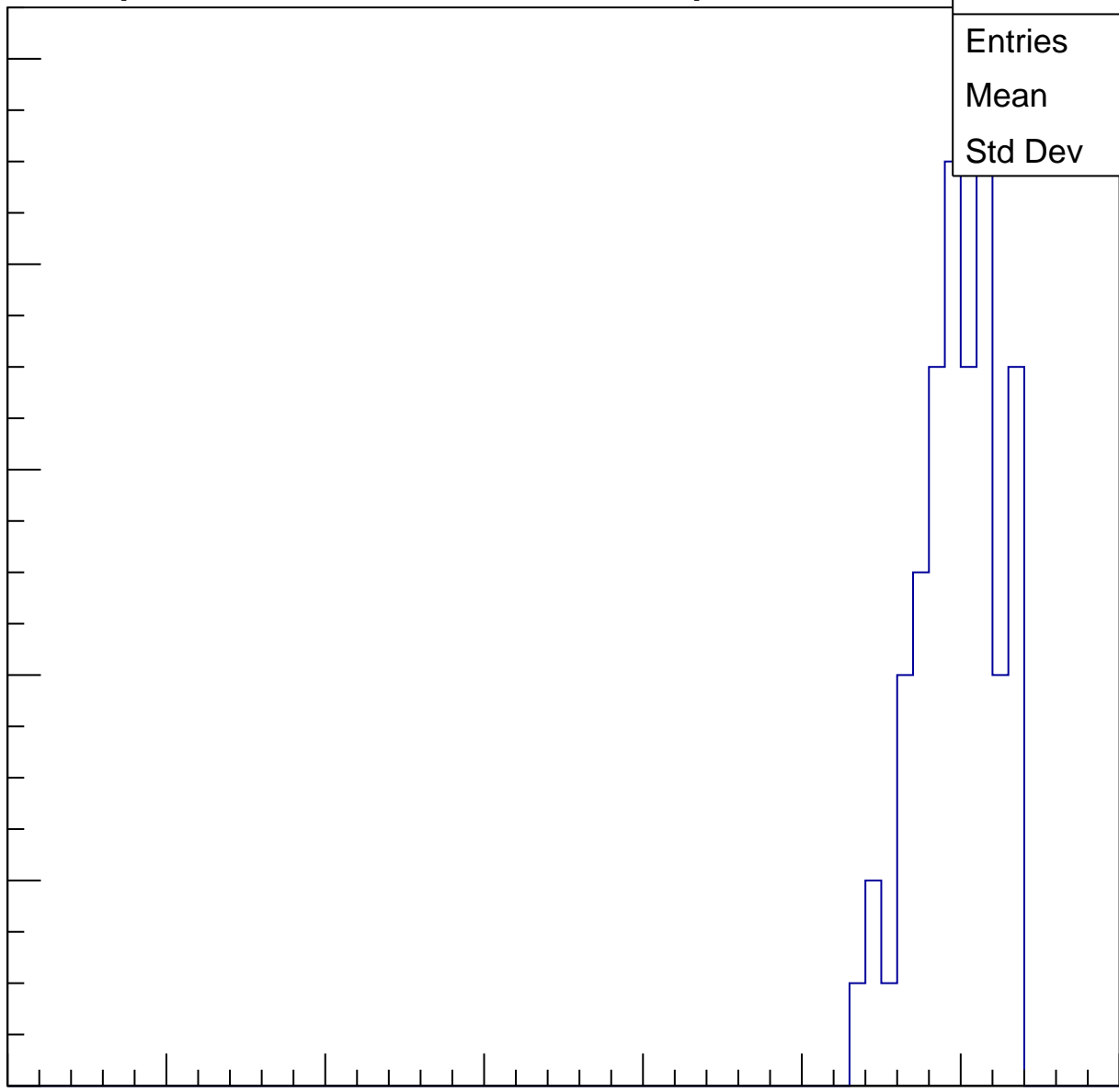
40

50

60

70

ampl

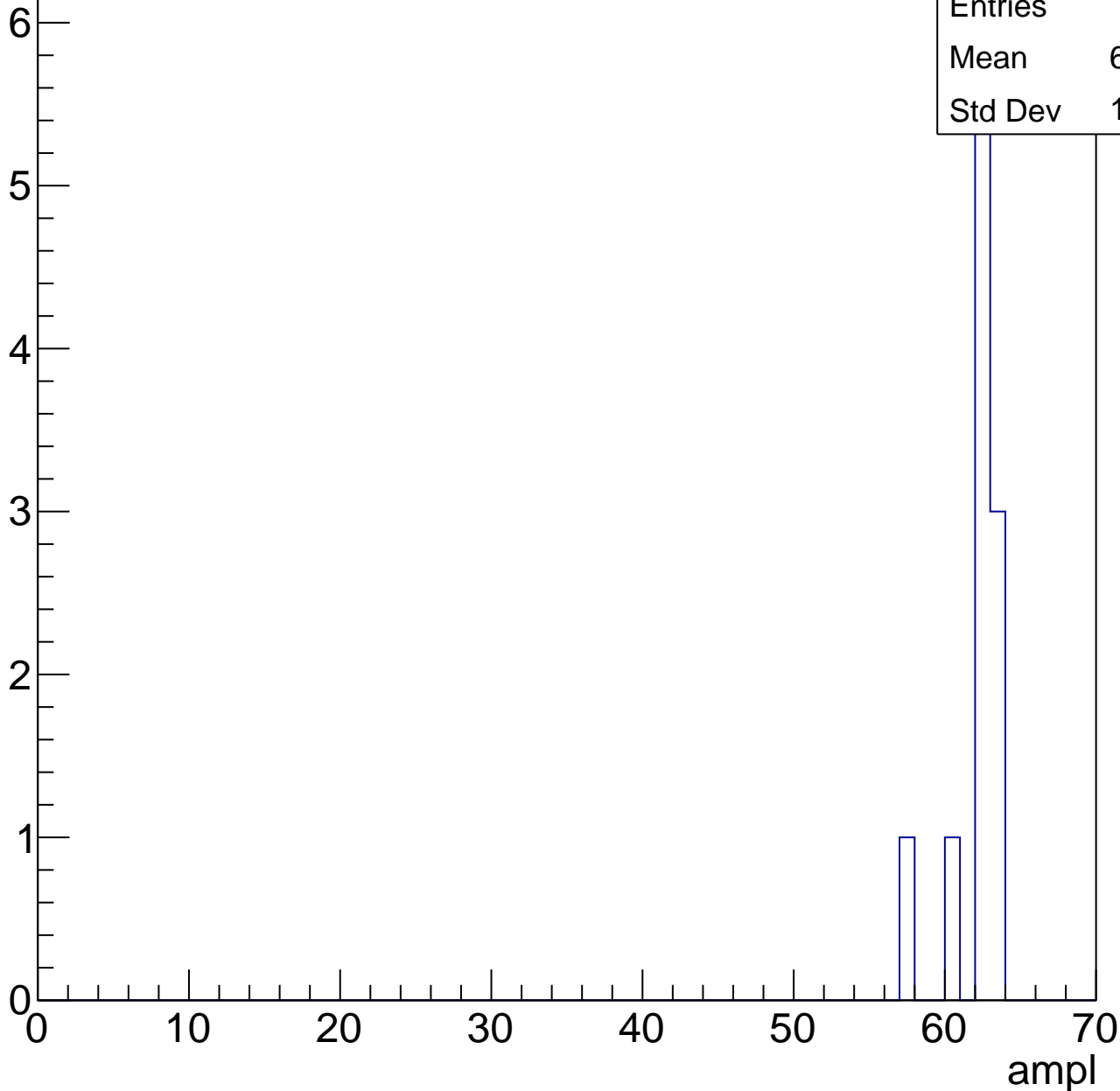


# B1L002S, U3-ch65, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	11
Mean	61.64
Std Dev	1.666





# B1L002S, U3-ch65, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch66, adc0

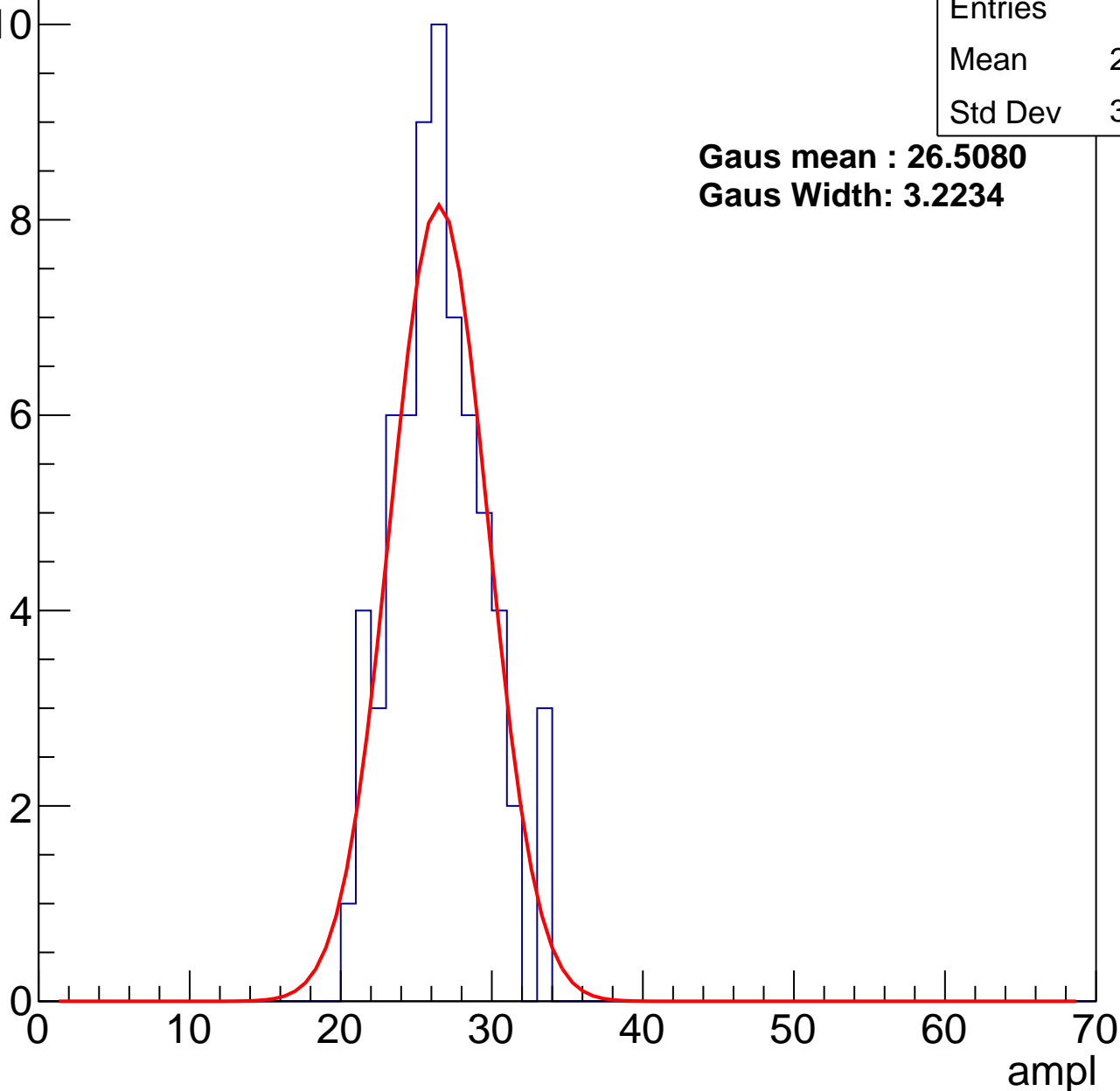
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	26.06
Std Dev	3.035

**Gaus mean : 26.5080**

**Gaus Width: 3.2234**



# B1L002S, U3-ch66, adc1

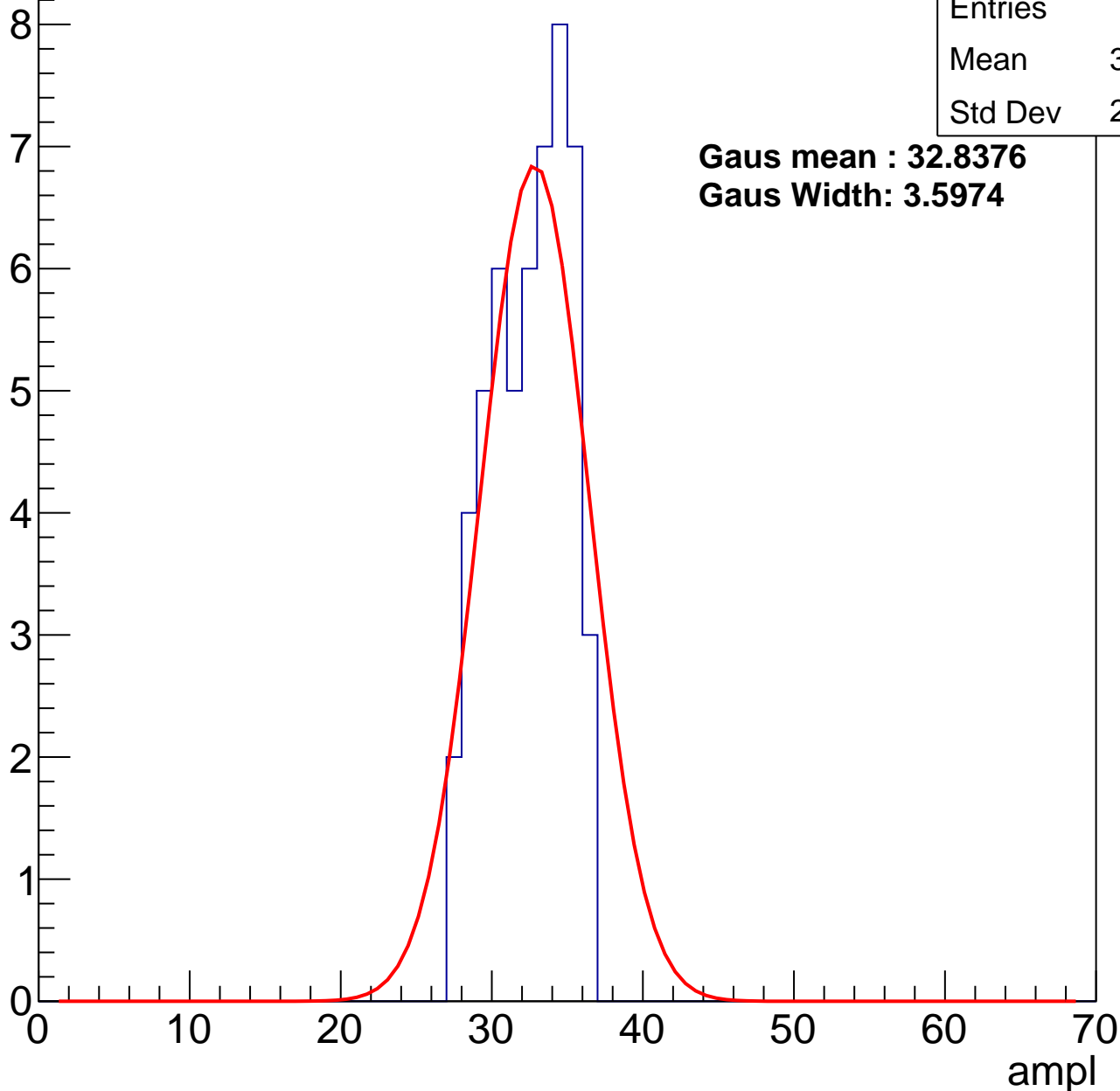
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	31.96
Std Dev	2.525

**Gaus mean : 32.8376**

**Gaus Width: 3.5974**



# B1L002S, U3-ch66, adc2

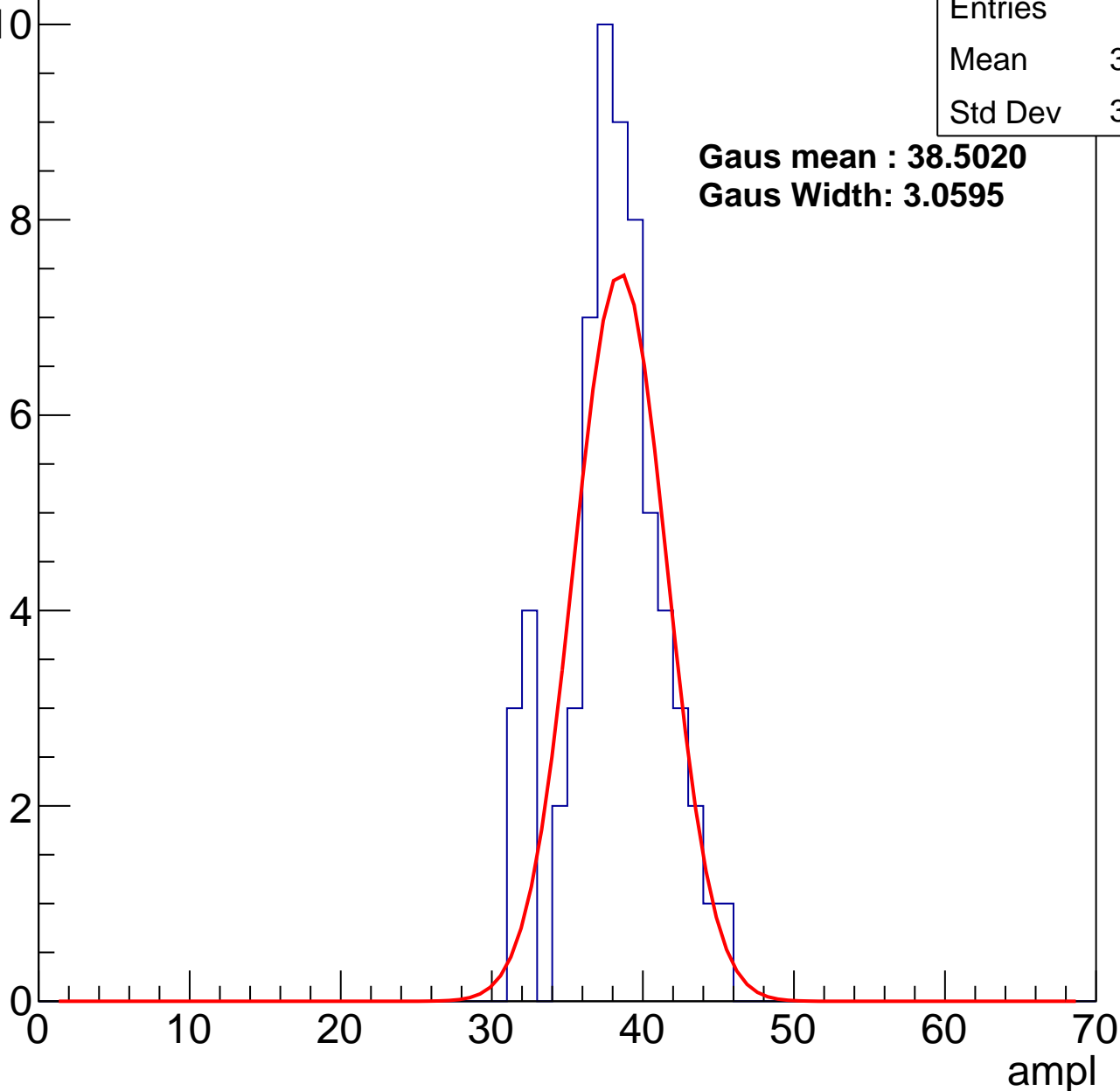
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	37.66
Std Dev	3.182

**Gaus mean : 38.5020**

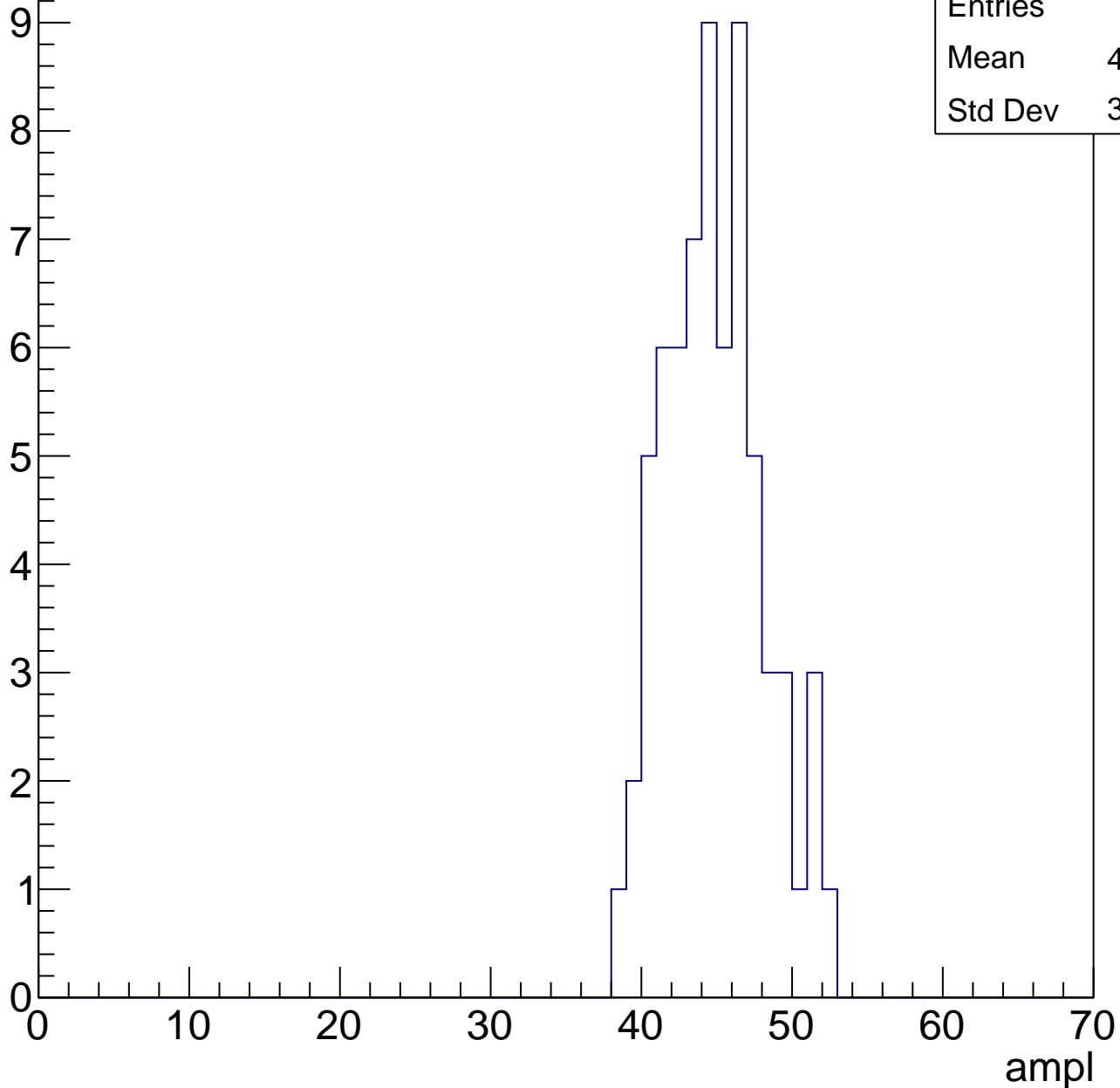
**Gaus Width: 3.0595**



# B1L002S, U3-ch66, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

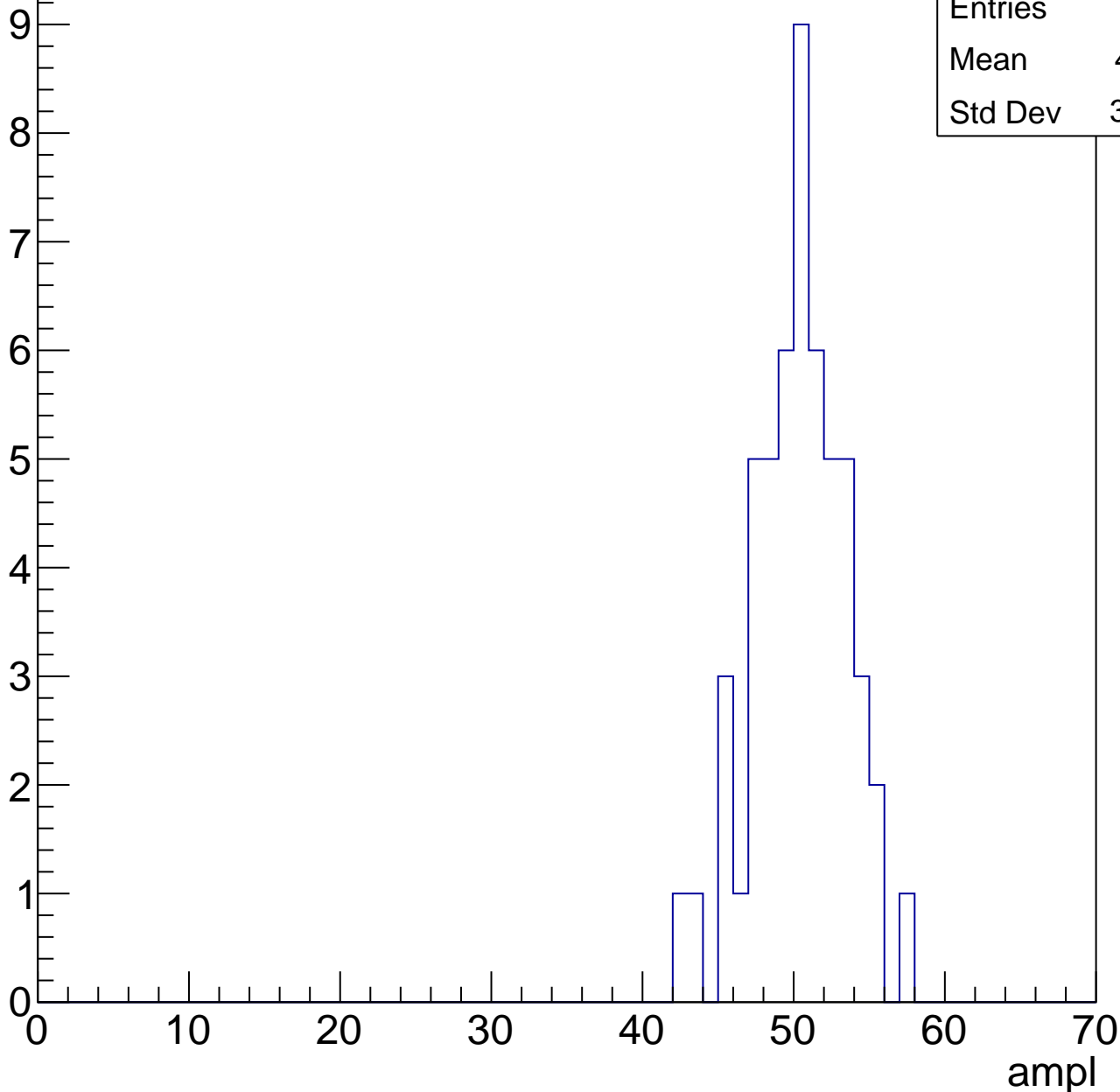


# B1L002S, U3-ch66, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

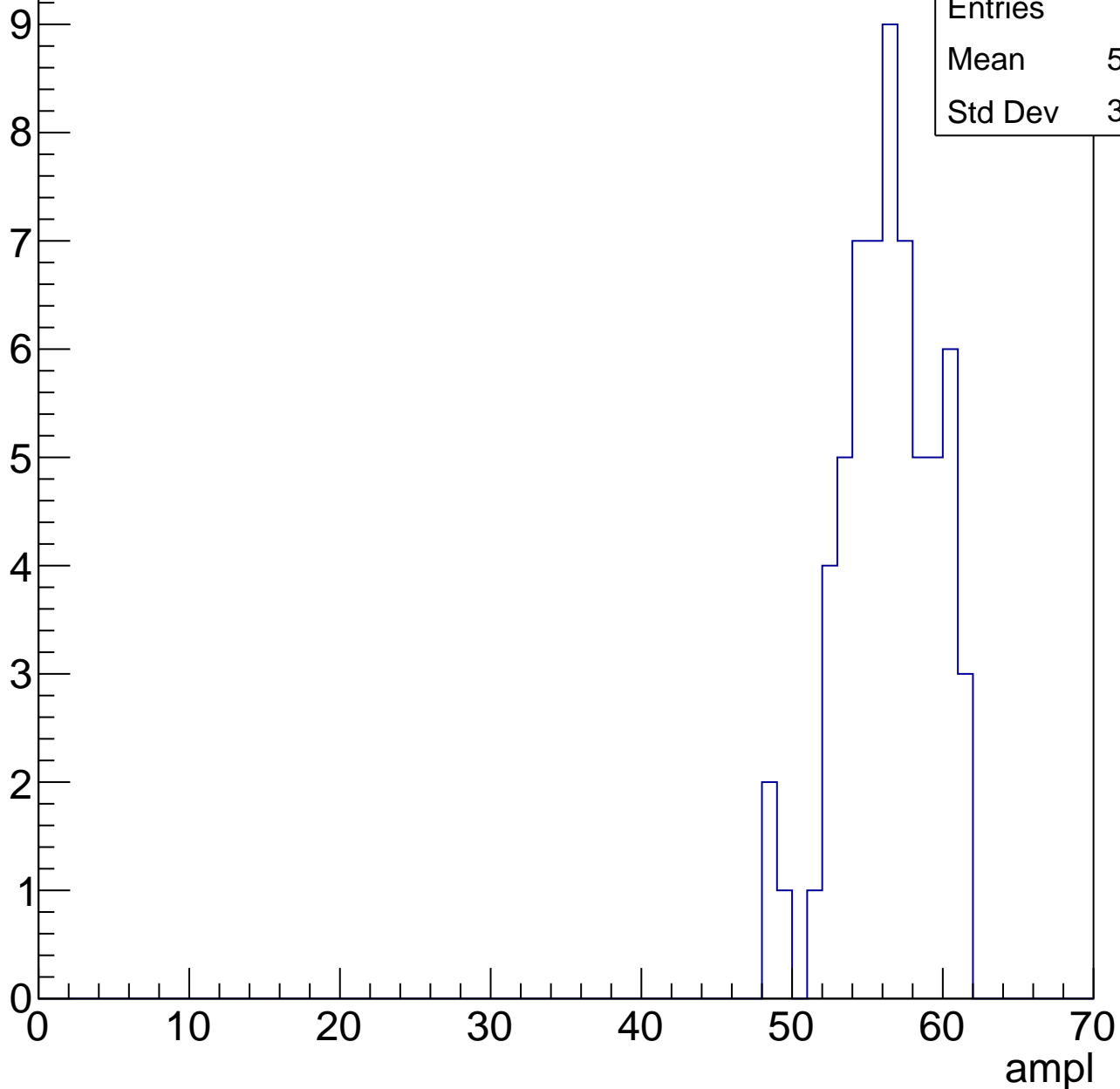
Entries	53
Mean	49.91
Std Dev	3.048



# B1L002S, U3-ch66, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

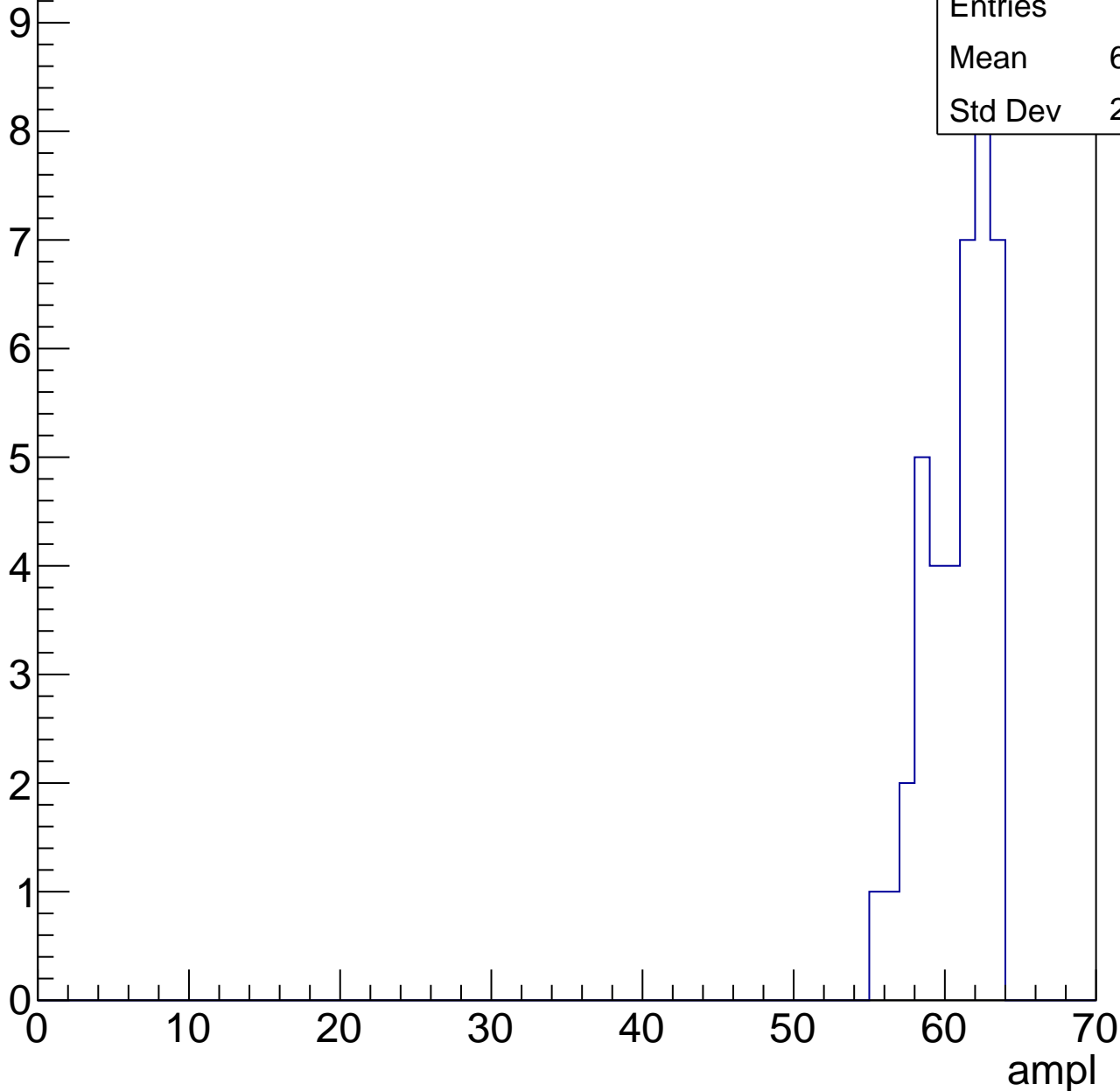


# B1L002S, U3-ch66, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	60.42
Std Dev	2.132

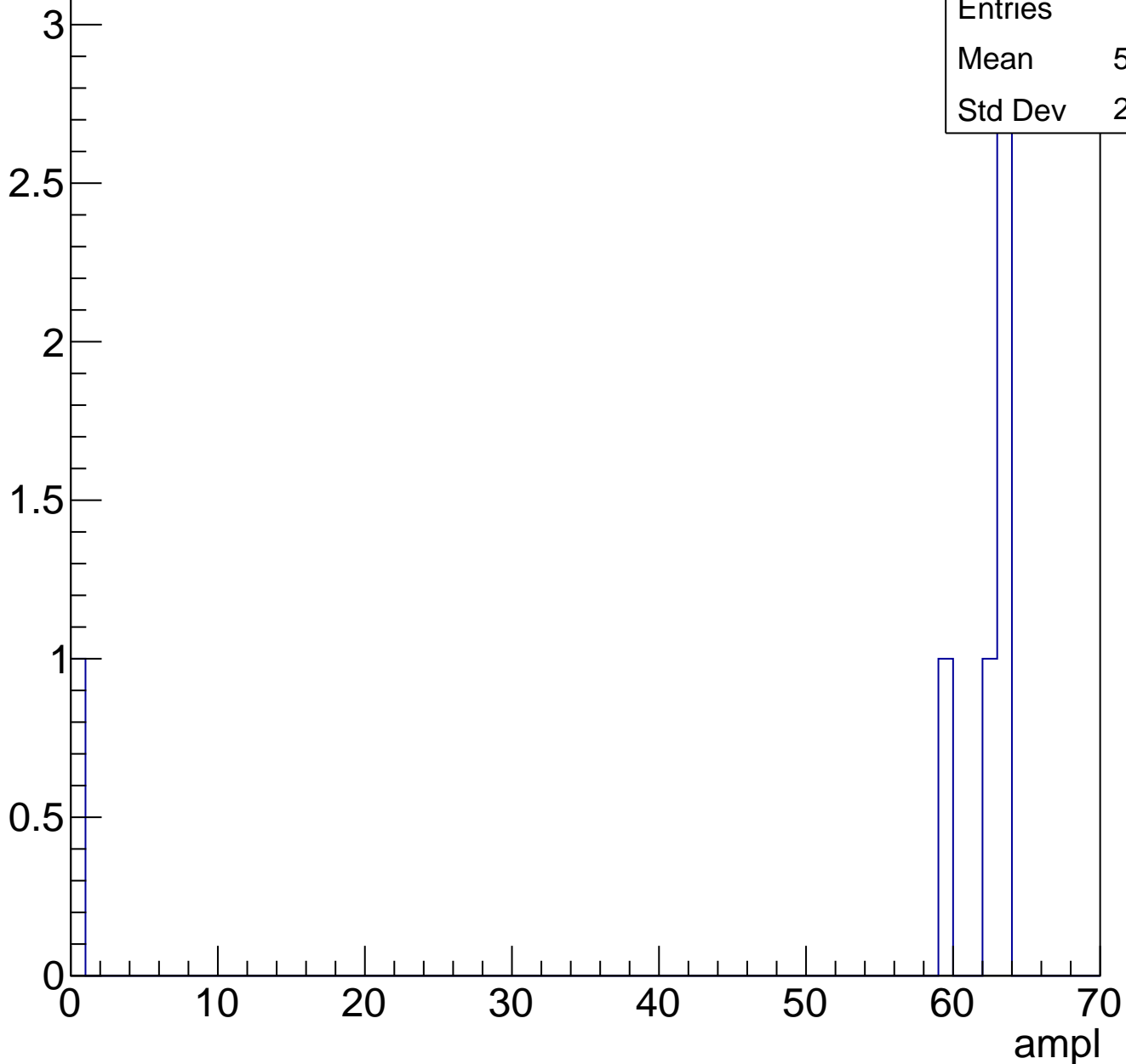




# B1L002S, U3-ch66, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch67, adc0

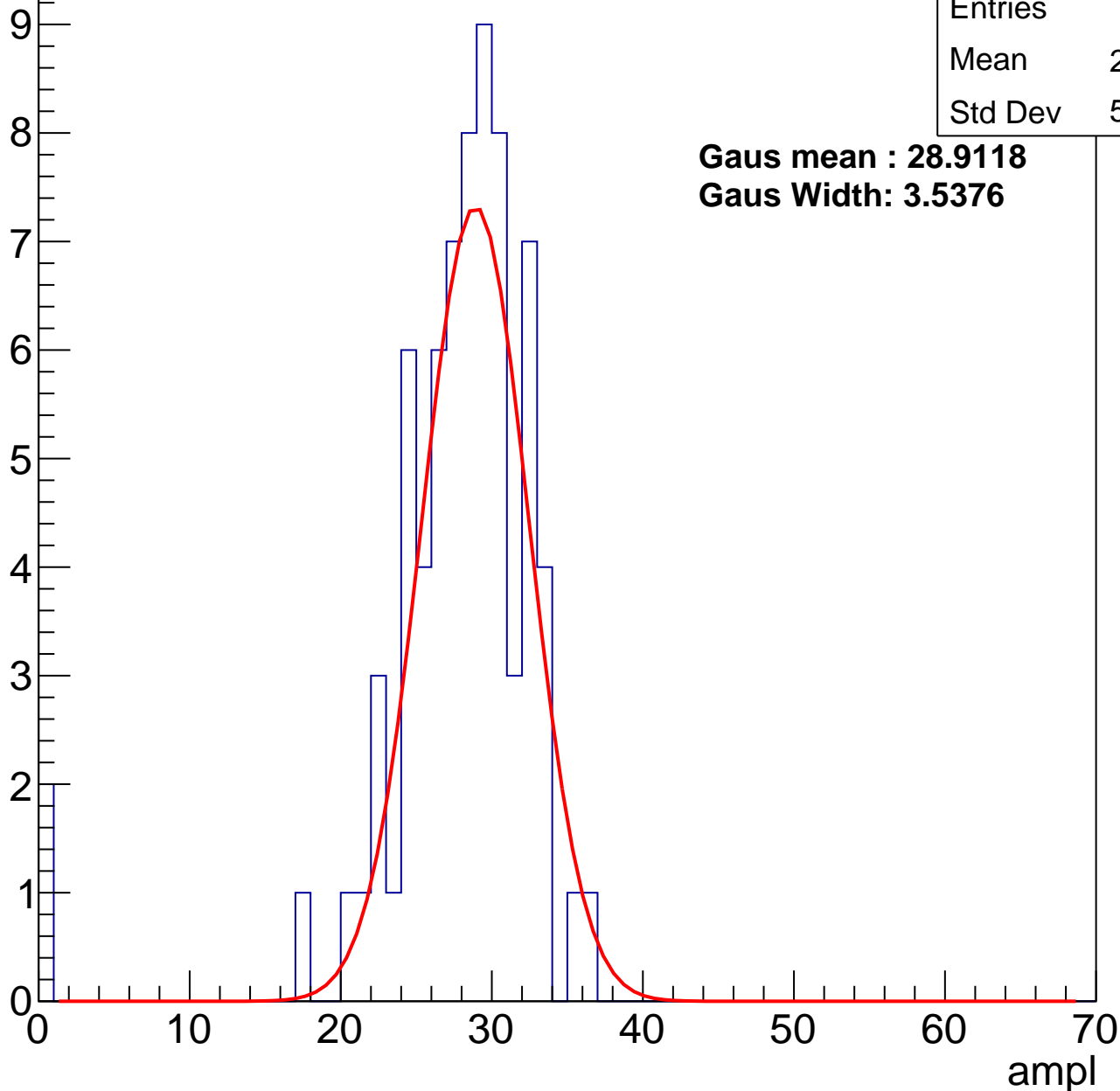
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	27.14
Std Dev	5.777

**Gaus mean : 28.9118**

**Gaus Width: 3.5376**



# B1L002S, U3-ch67, adc1

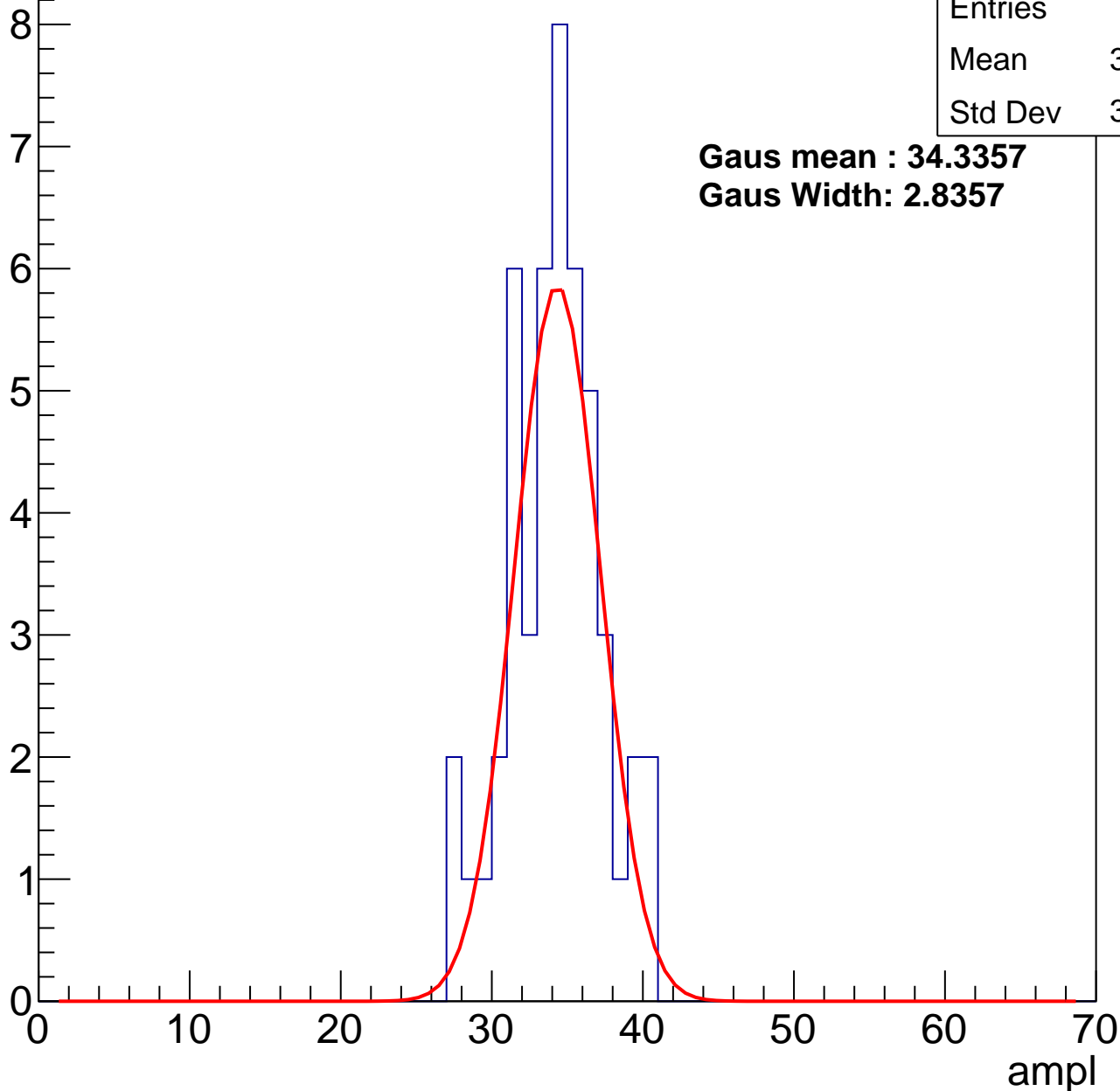
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	33.75
Std Dev	3.065

**Gaus mean : 34.3357**

**Gaus Width: 2.8357**



# B1L002S, U3-ch67, adc2

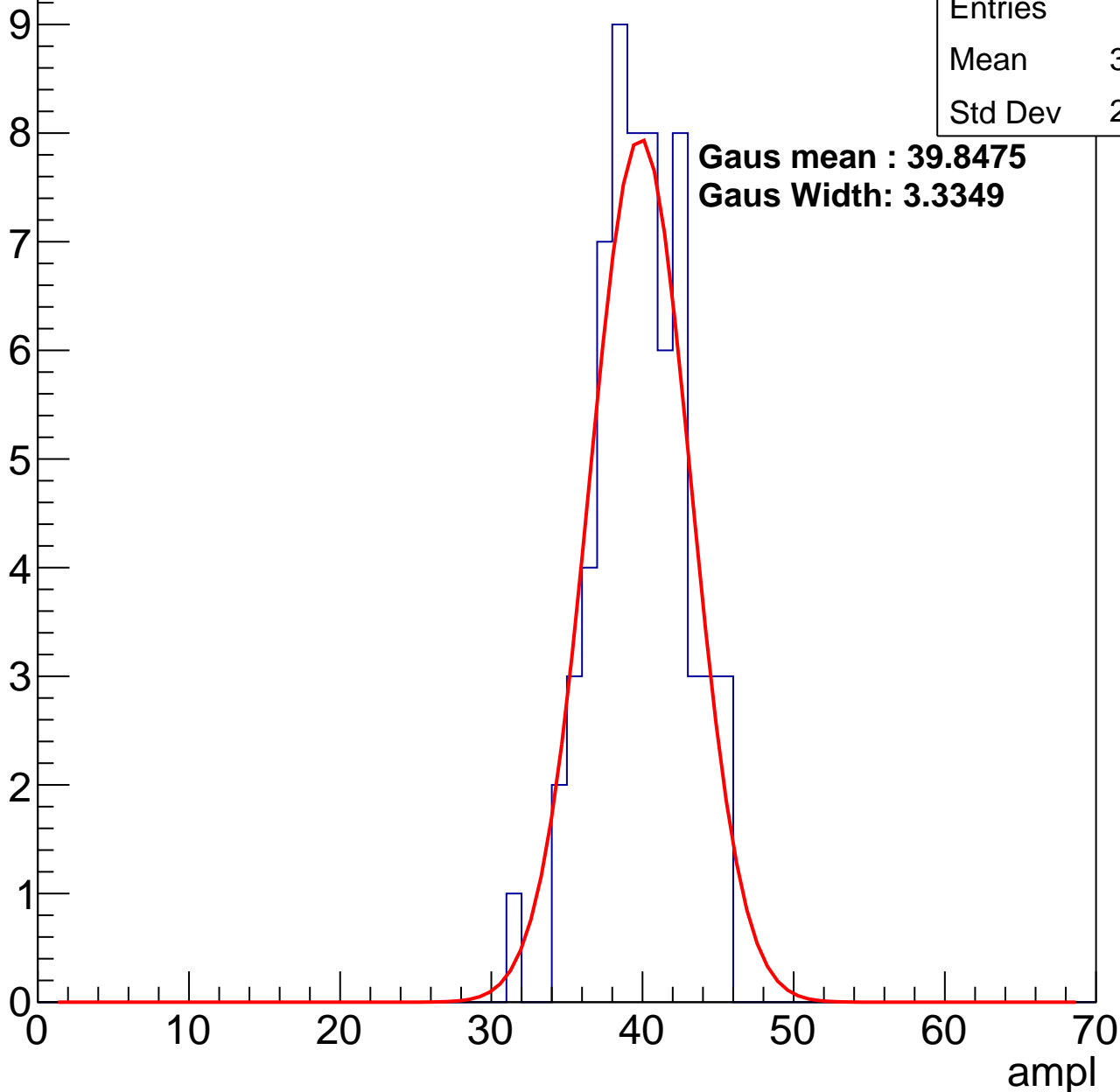
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	39.37
Std Dev	2.938

**Gaus mean : 39.8475**

**Gaus Width: 3.3349**

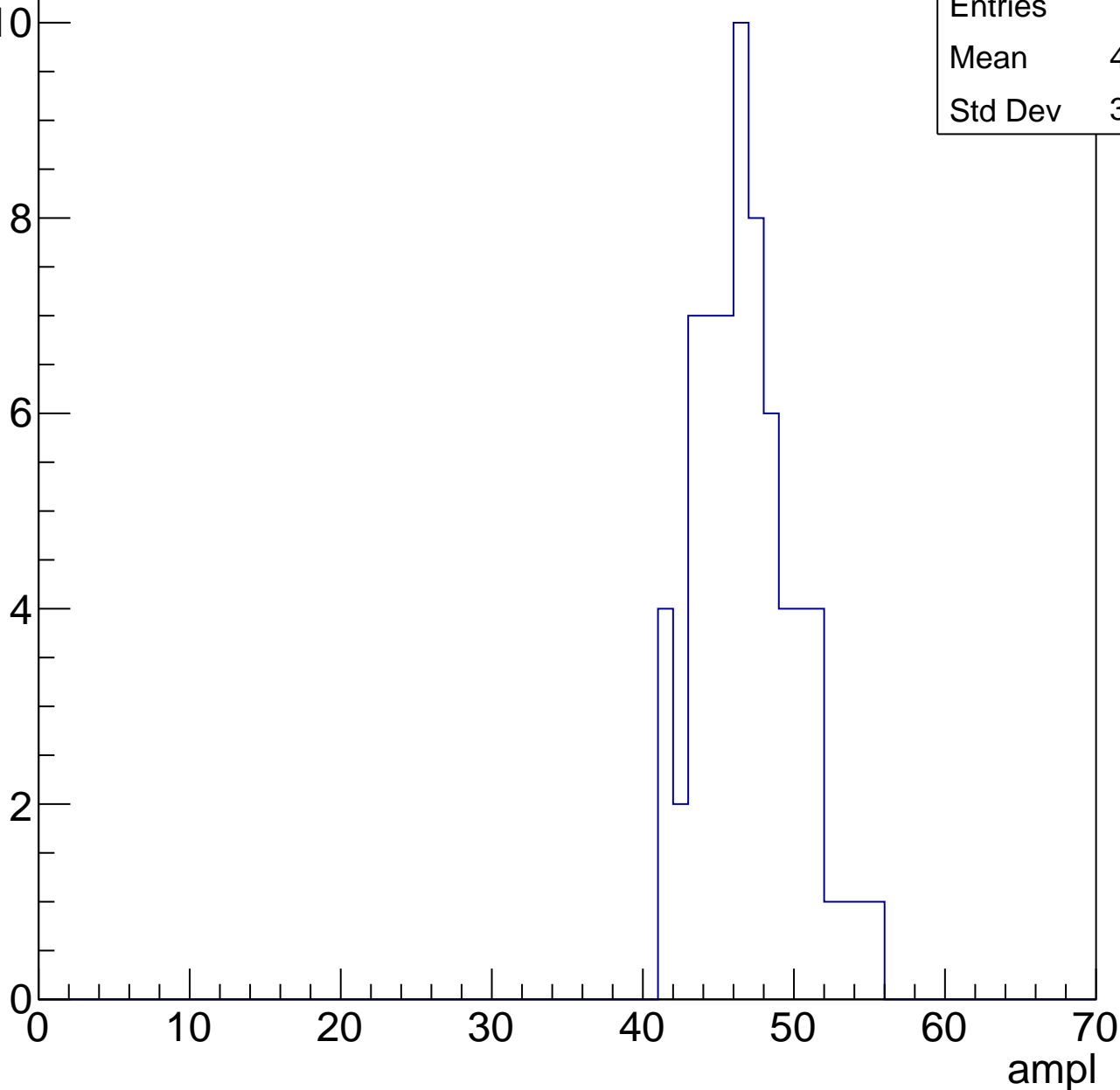


# B1L002S, U3-ch67, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

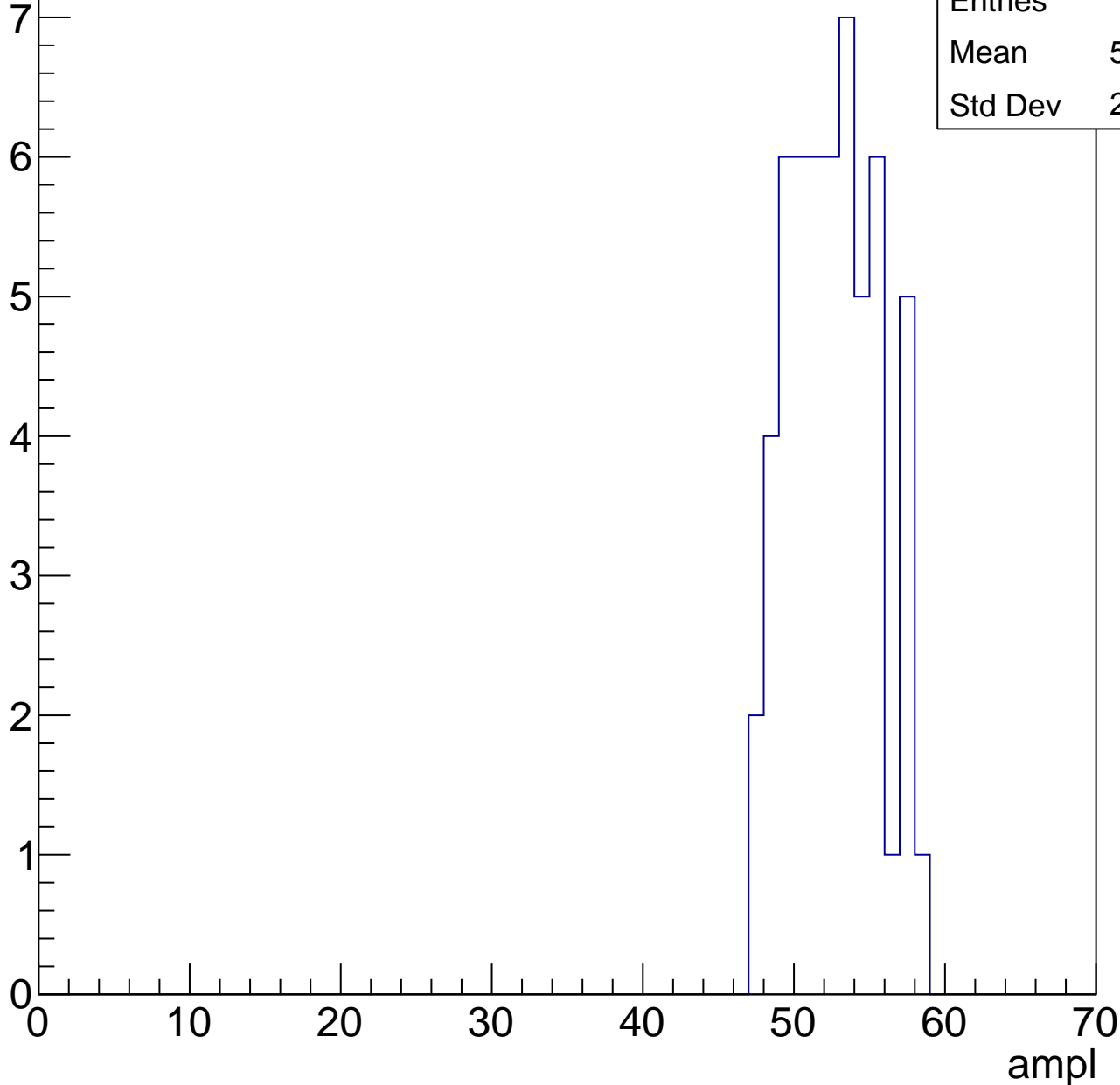
Entries	67
Mean	46.42
Std Dev	3.186



# B1L002S, U3-ch67, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



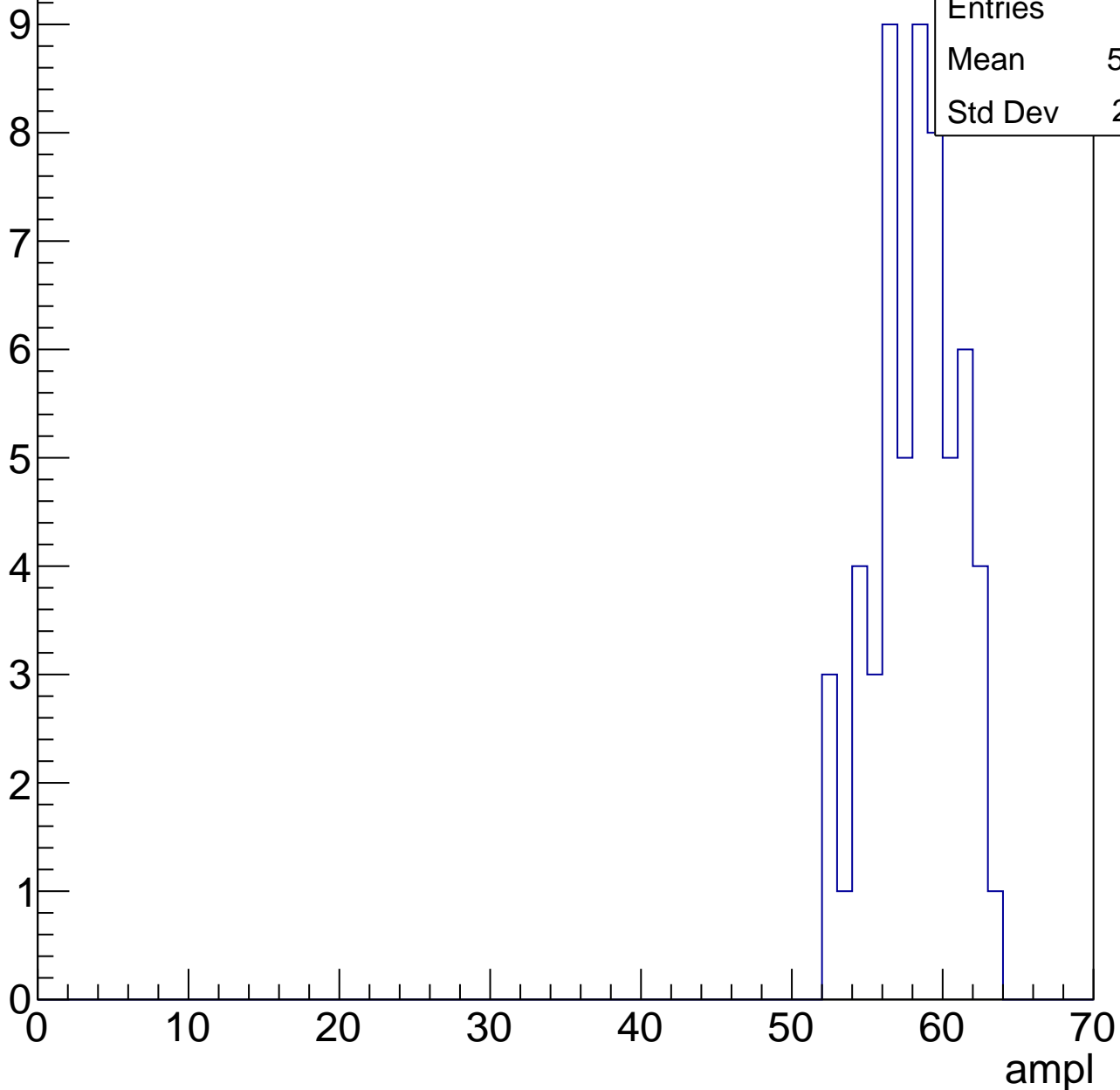
Entries	55
Mean	52.15
Std Dev	2.876

# B1L002S, U3-ch67, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.76
Std Dev	2.731

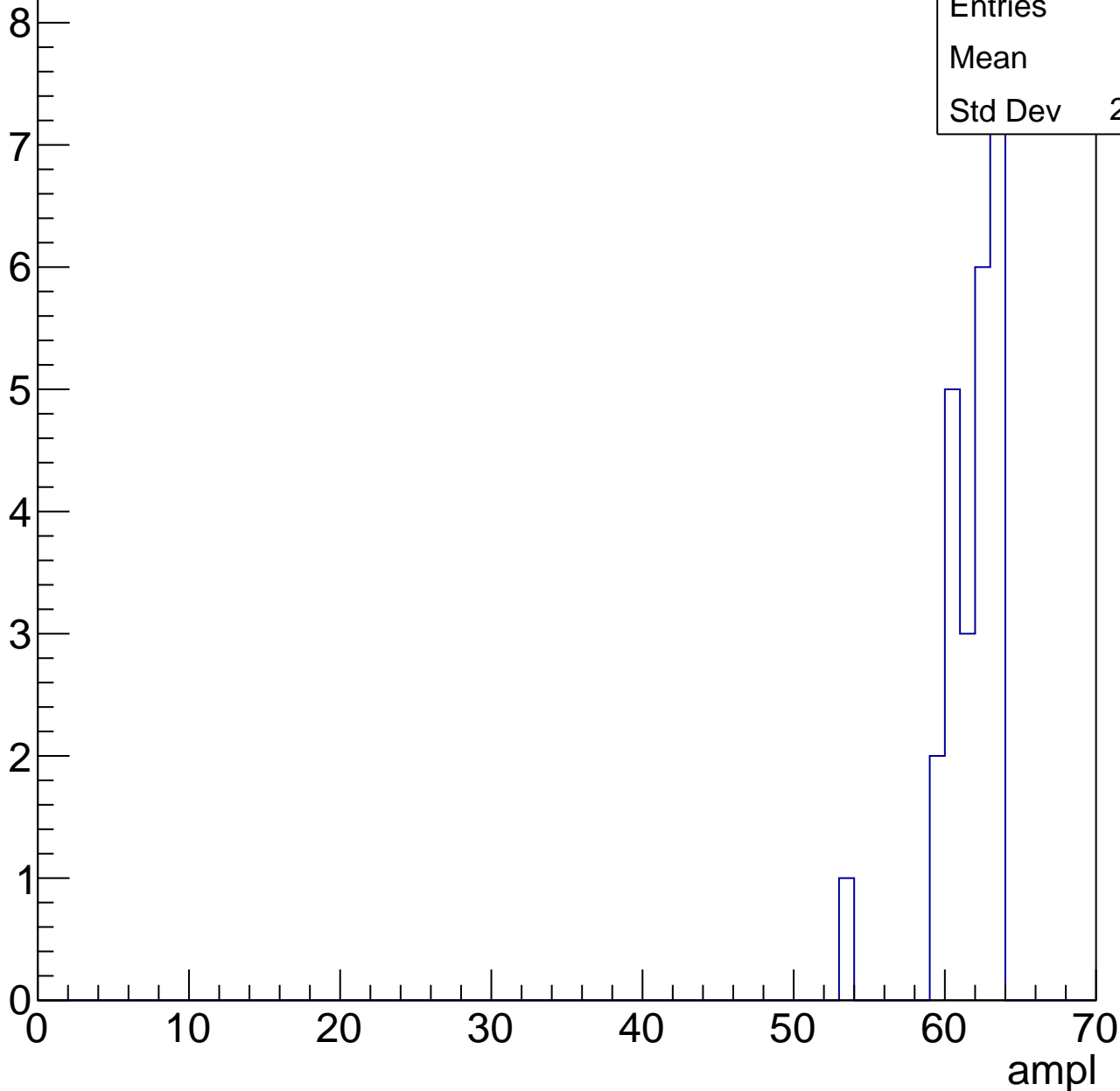


# B1L002S, U3-ch67, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	61.2
Std Dev	2.135

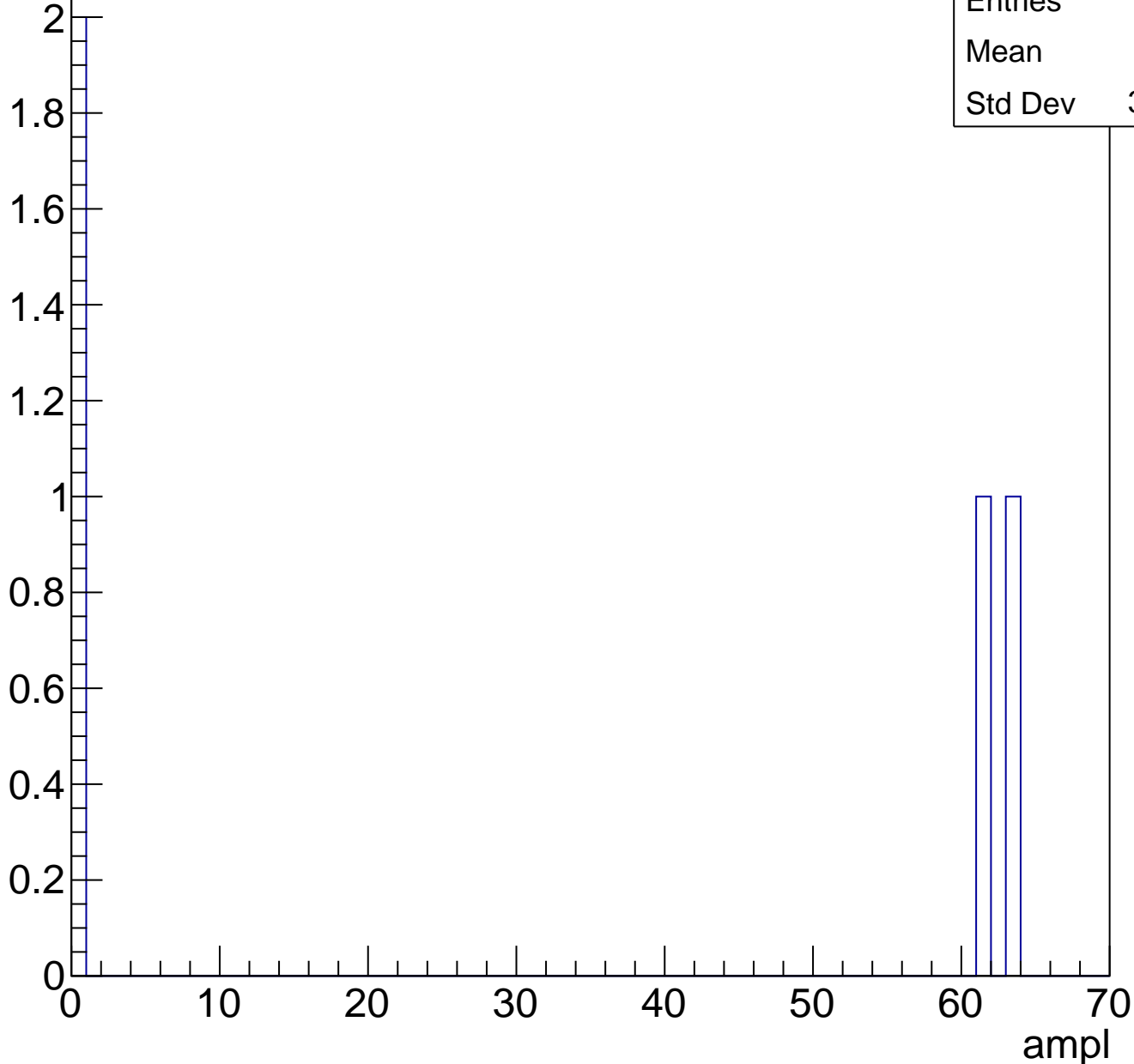




# B1L002S, U3-ch67, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch68, adc0

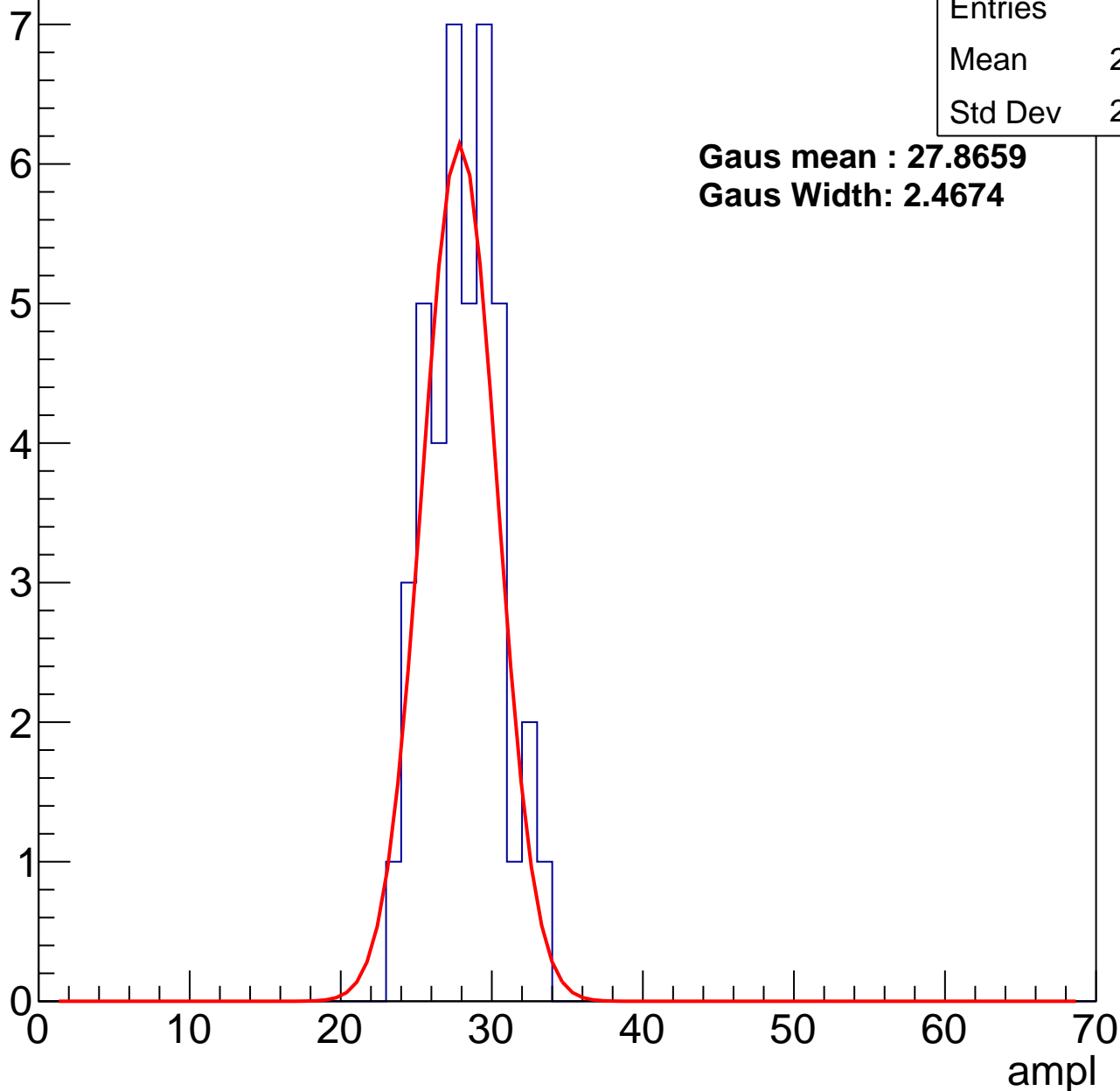
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	27.66
Std Dev	2.364

**Gaus mean : 27.8659**

**Gaus Width: 2.4674**



# B1L002S, U3-ch68, adc1

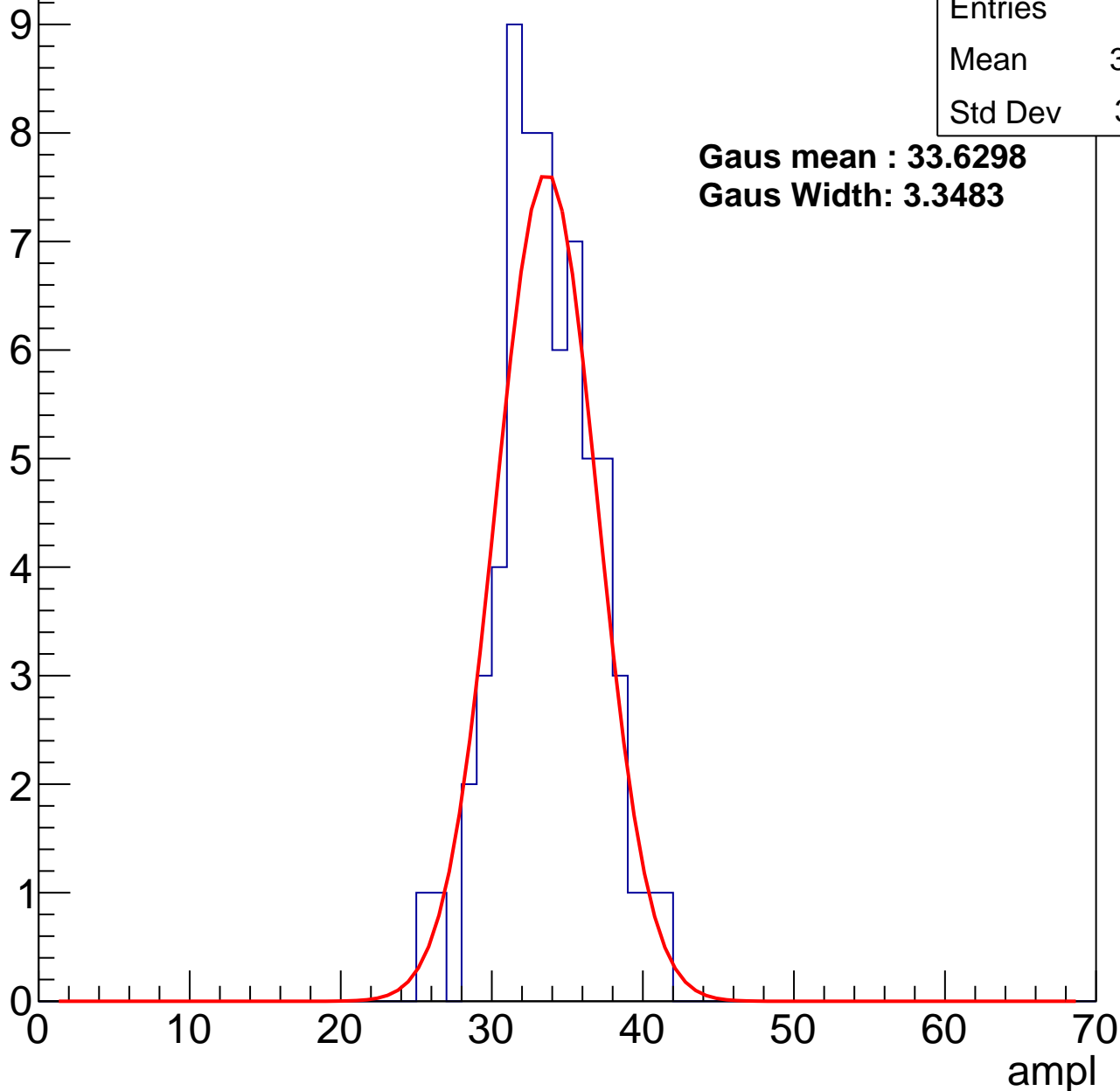
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	33.25
Std Dev	3.211

**Gaus mean : 33.6298**

**Gaus Width: 3.3483**



# B1L002S, U3-ch68, adc2

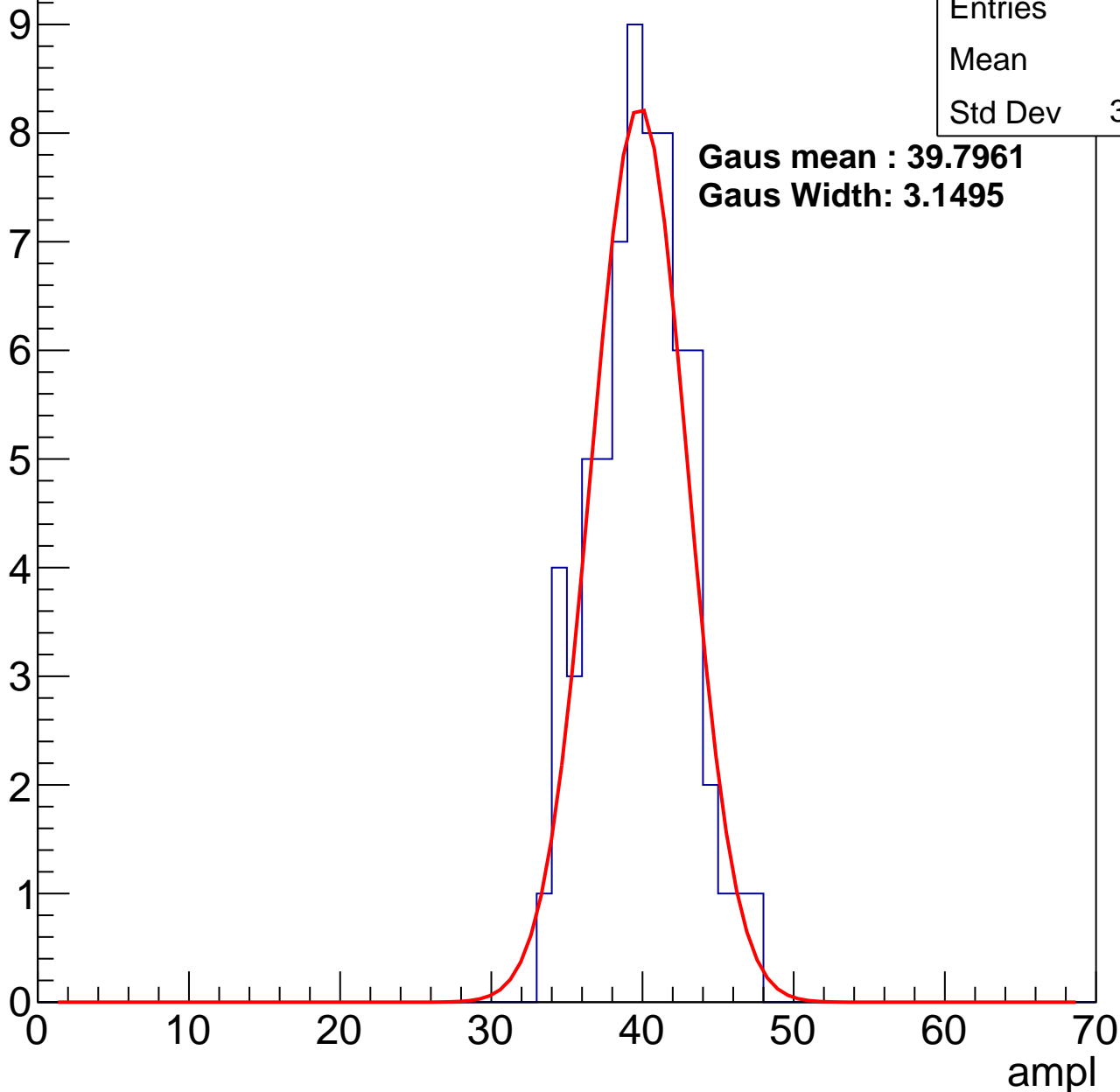
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	39.4
Std Dev	3.076

**Gaus mean : 39.7961**

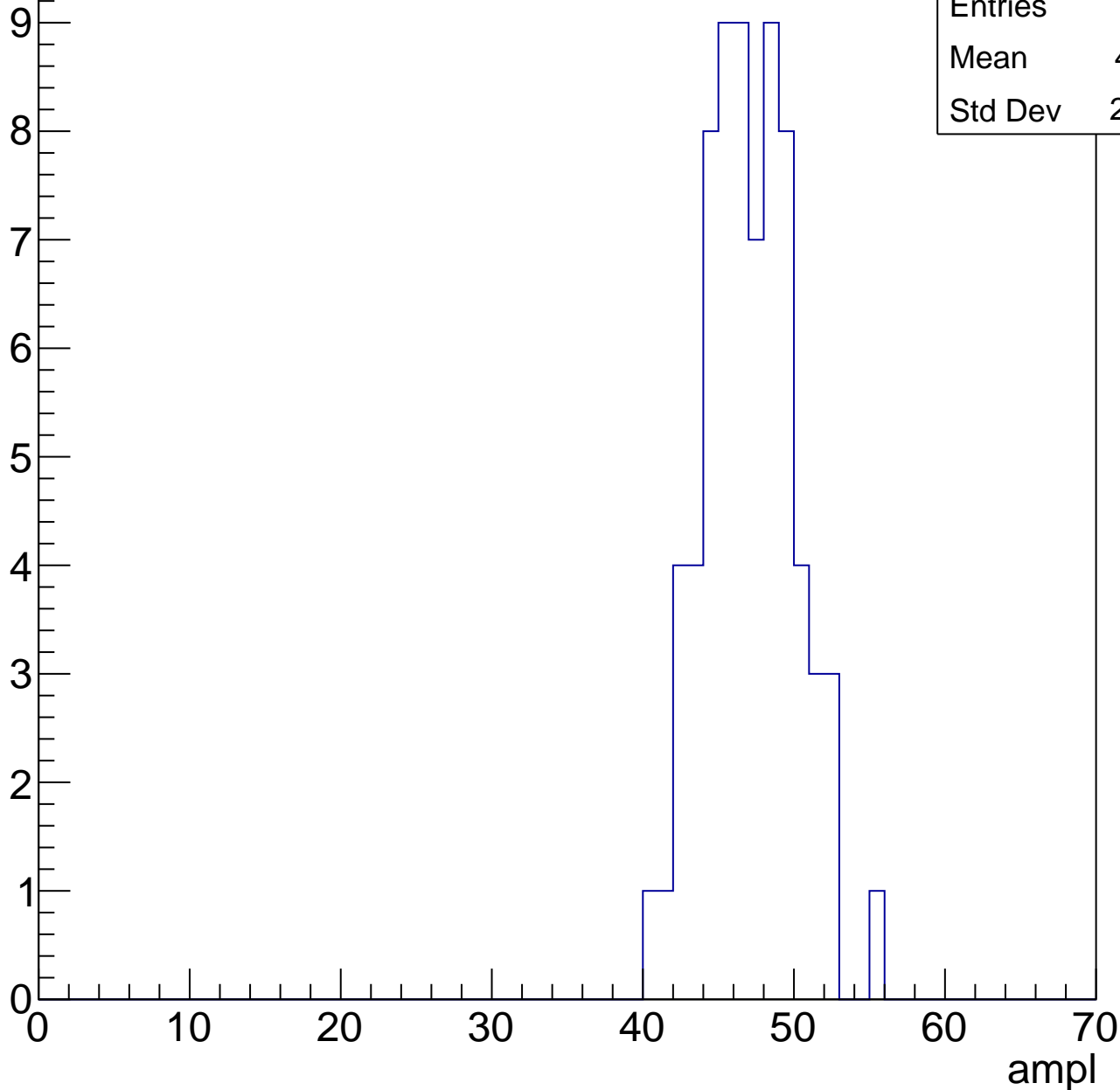
**Gaus Width: 3.1495**



# B1L002S, U3-ch68, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

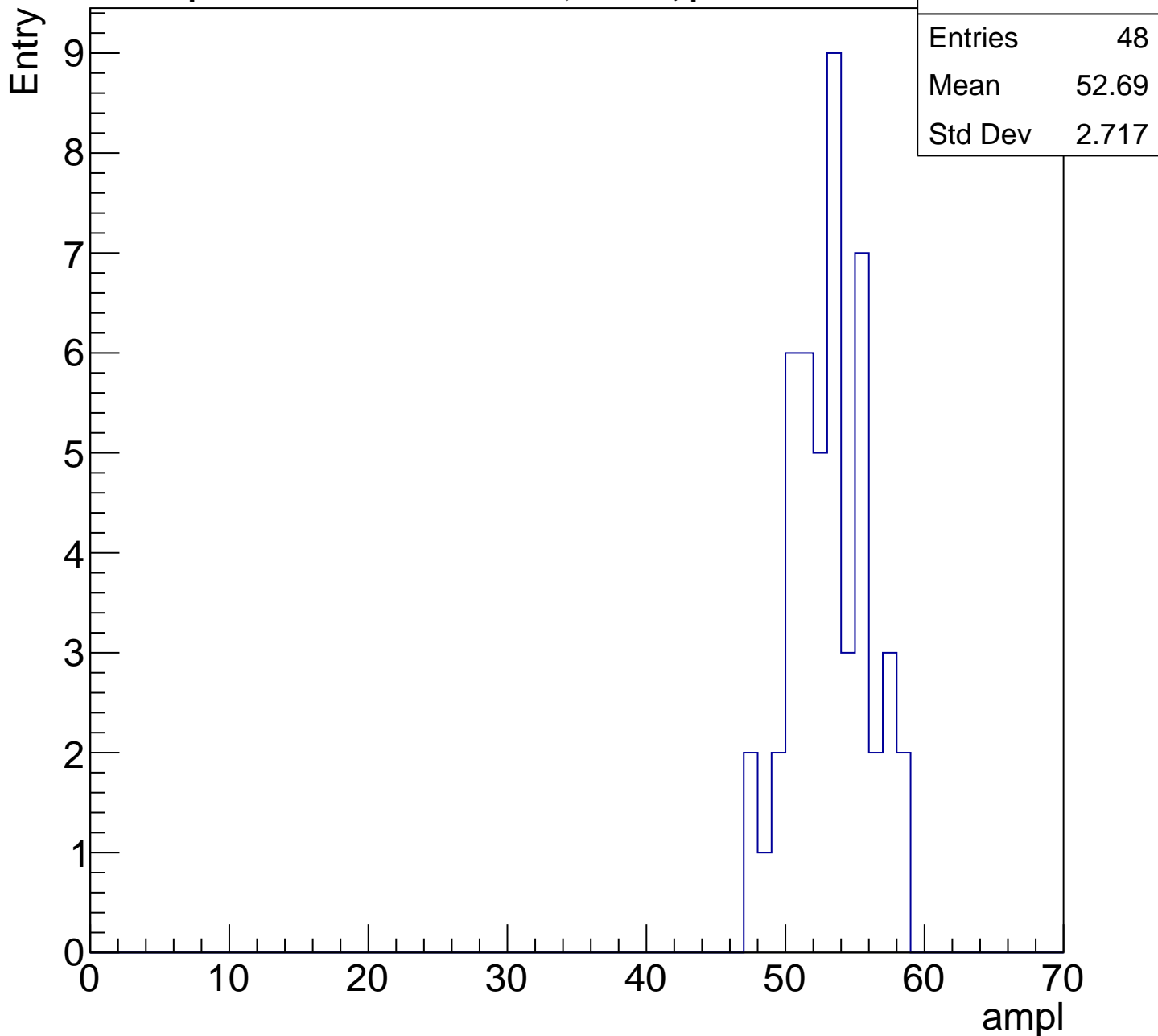
Entry



Entries	71
Mean	46.61
Std Dev	2.953

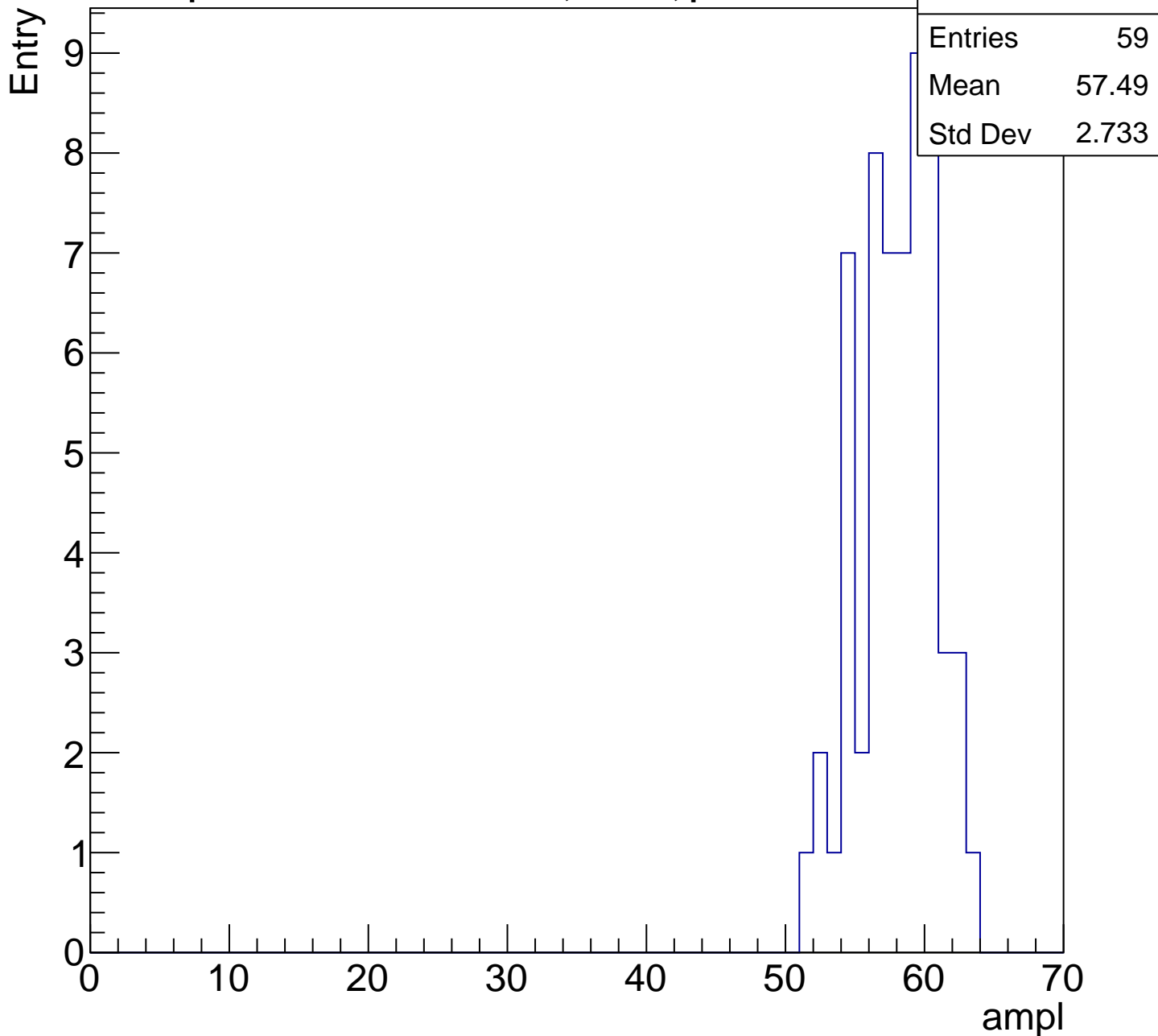
# B1L002S, U3-ch68, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch68, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

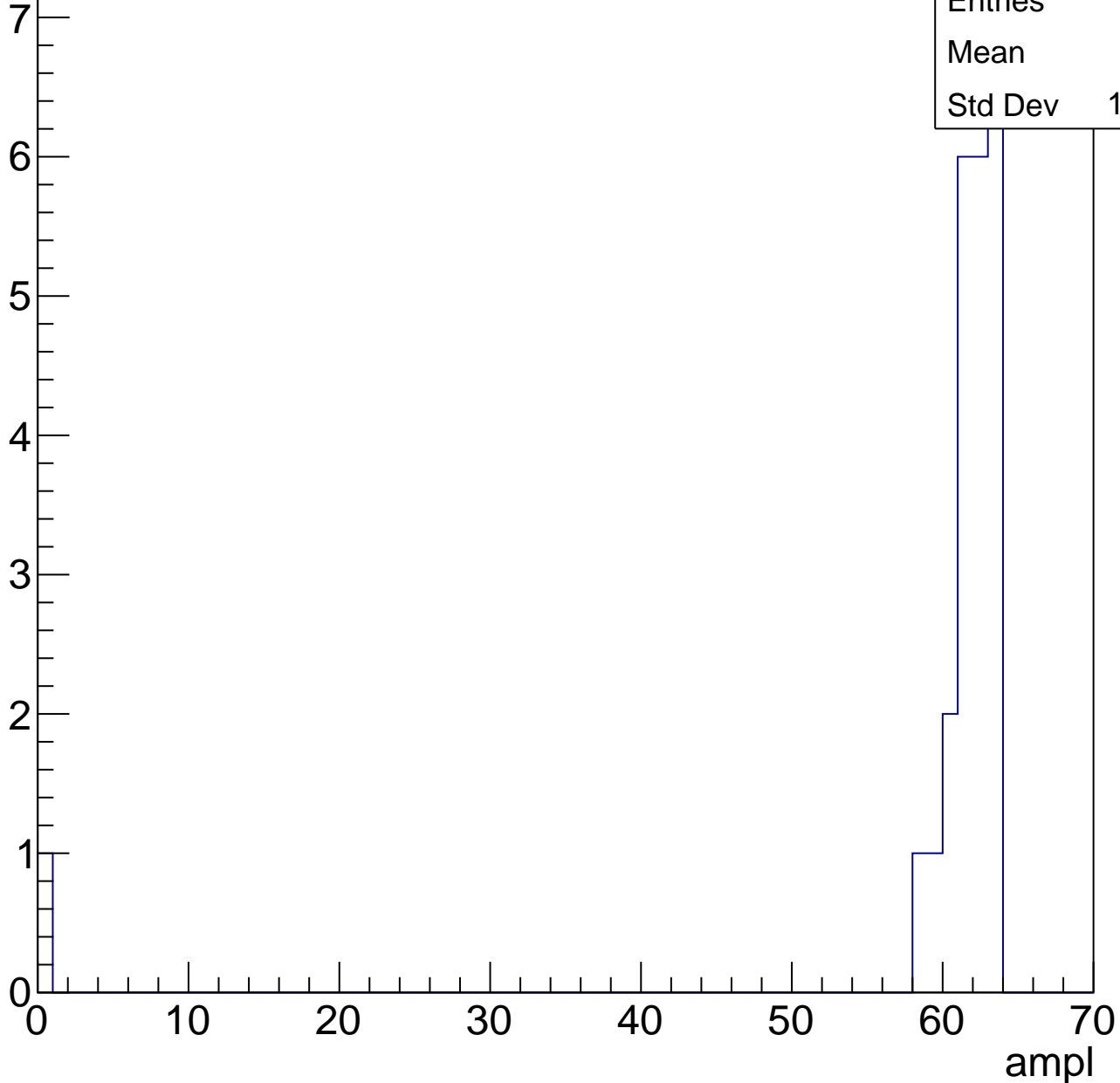


# B1L002S, U3-ch68, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	59
Std Dev	12.37

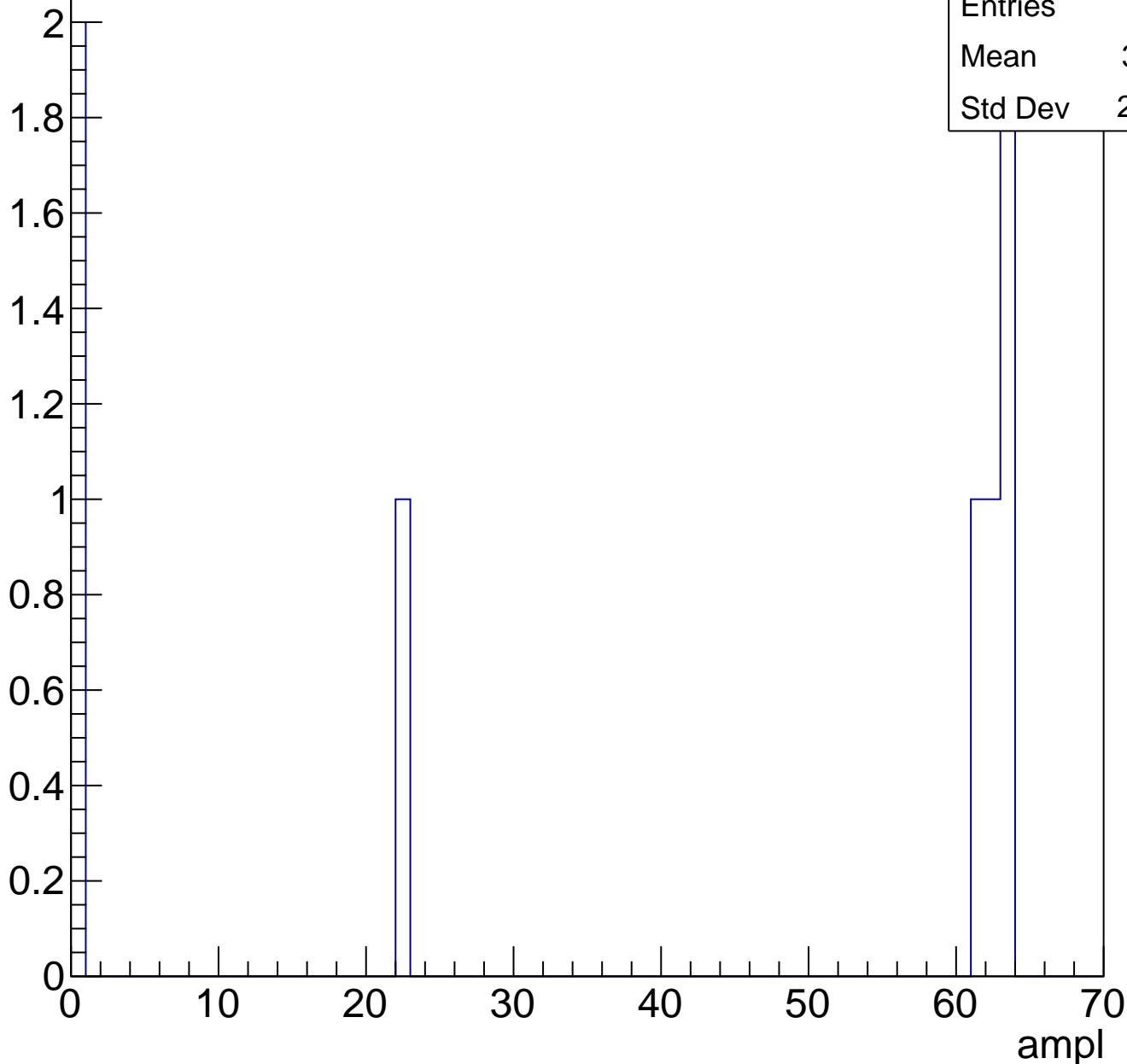




# B1L002S, U3-ch68, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	7
Mean	38.71
Std Dev	28.02

# B1L002S, U3-ch69, adc0

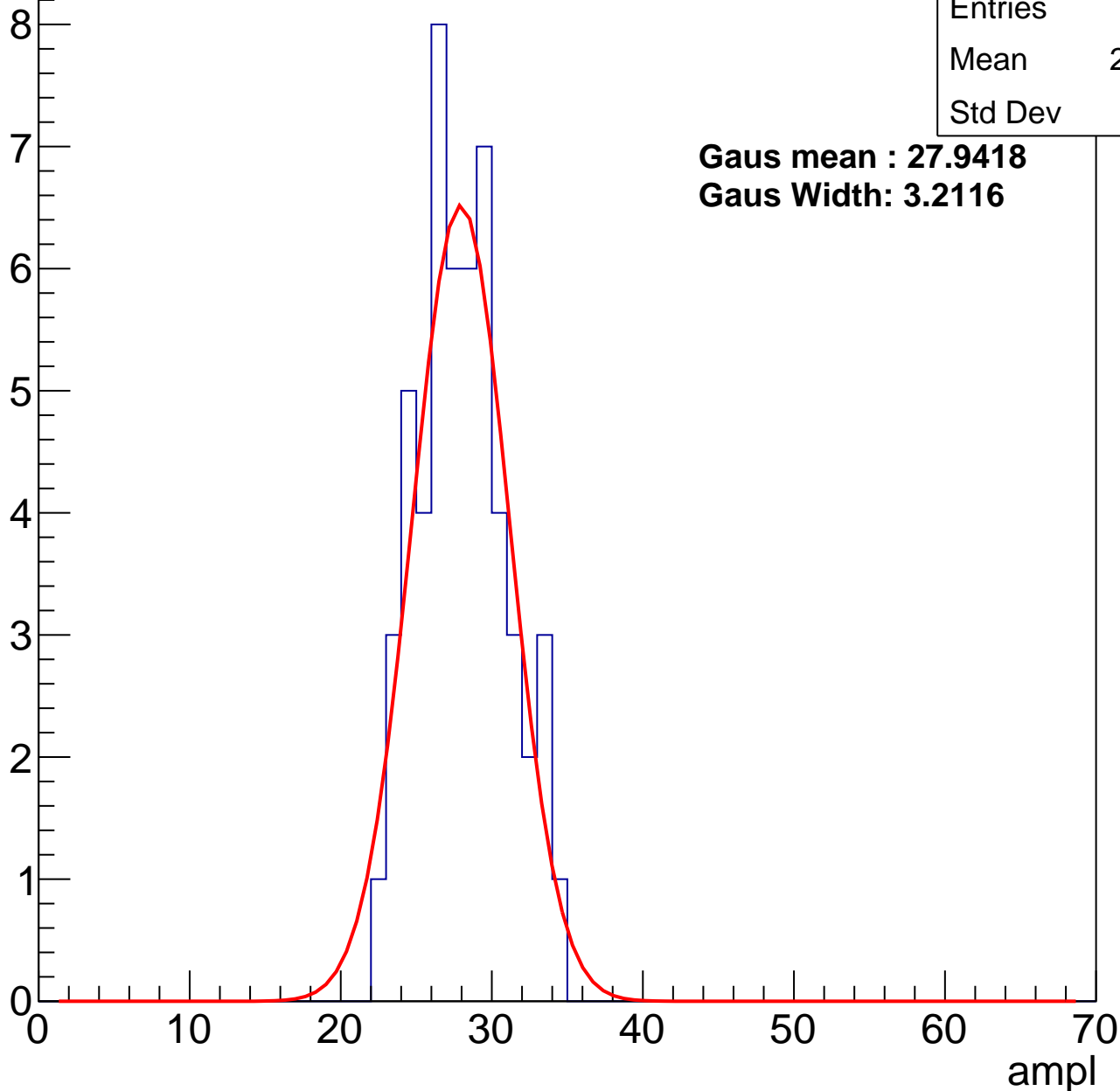
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	27.58
Std Dev	2.91

**Gaus mean : 27.9418**

**Gaus Width: 3.2116**



# B1L002S, U3-ch69, adc1

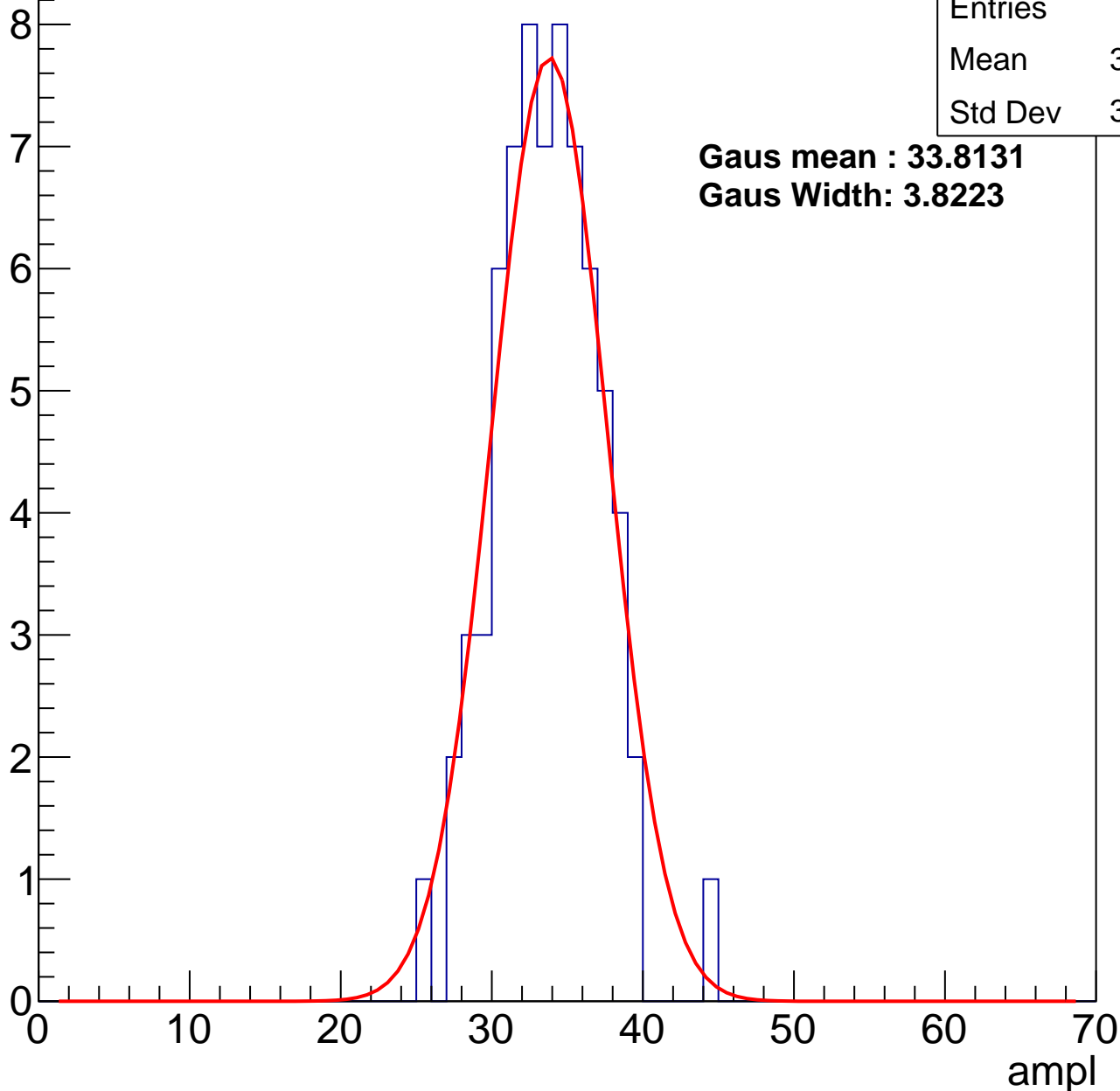
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	33.23
Std Dev	3.398

**Gaus mean : 33.8131**

**Gaus Width: 3.8223**



# B1L002S, U3-ch69, adc2

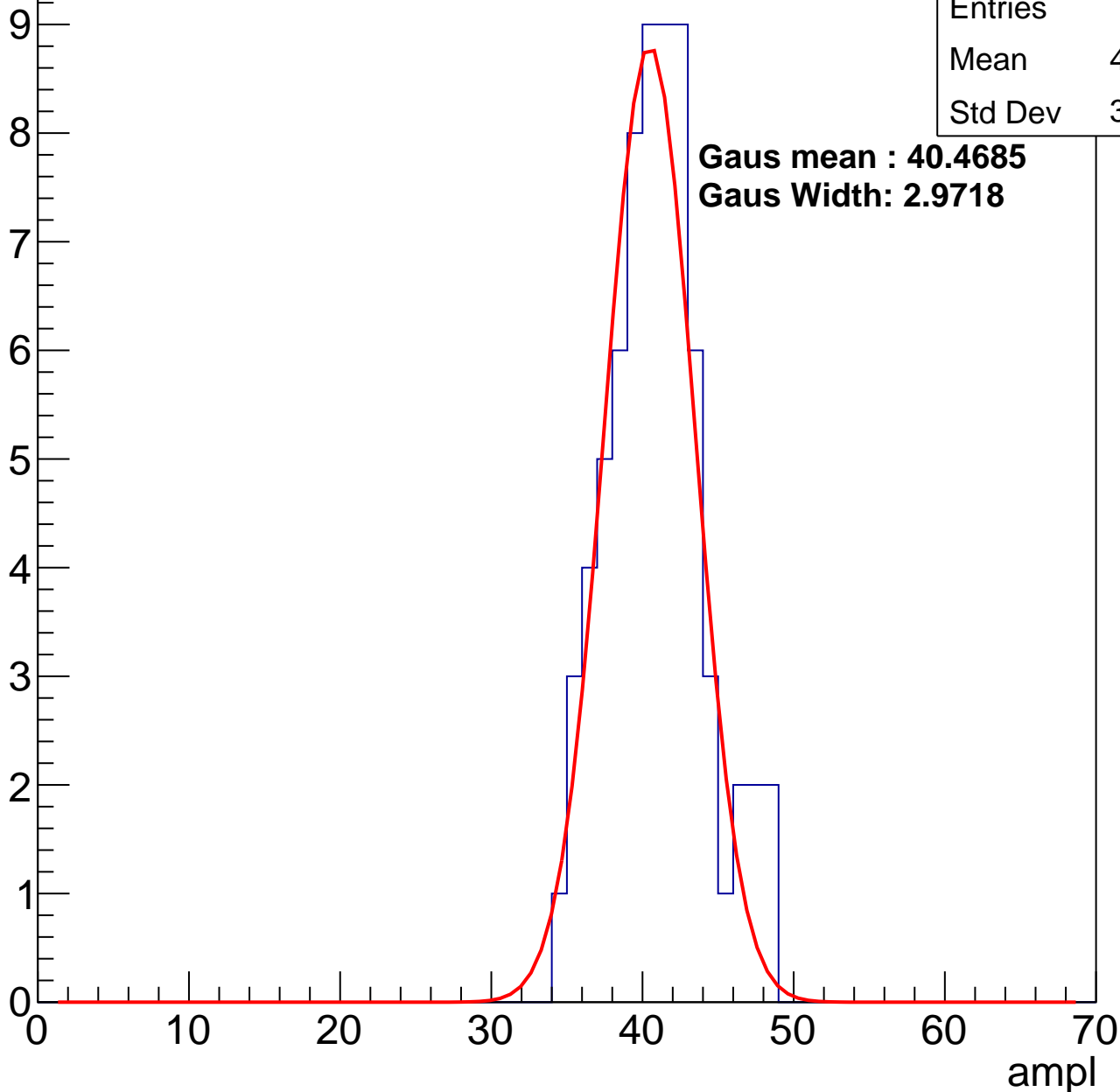
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	40.46
Std Dev	3.179

**Gaus mean : 40.4685**

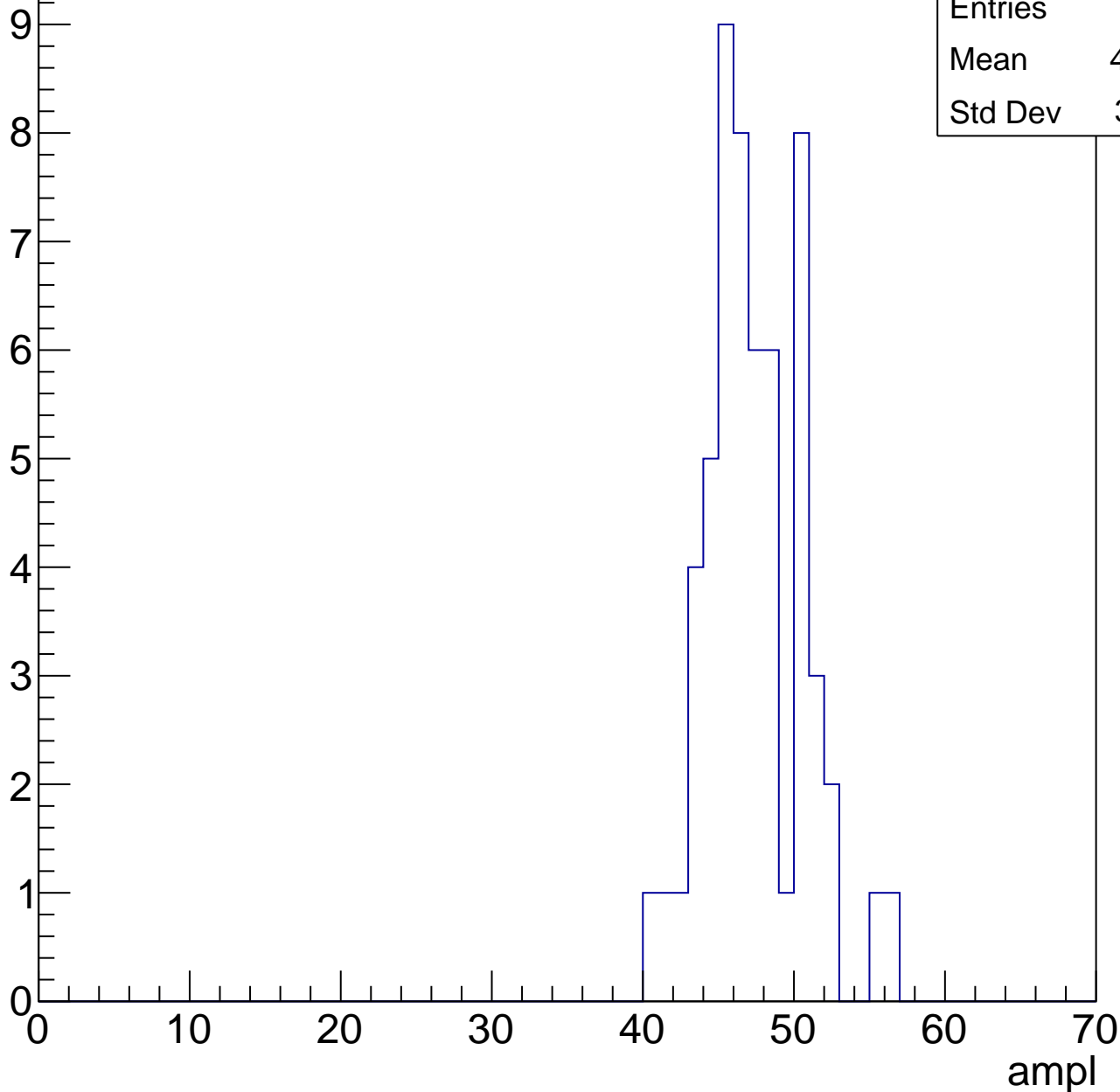
**Gaus Width: 2.9718**



# B1L002S, U3-ch69, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

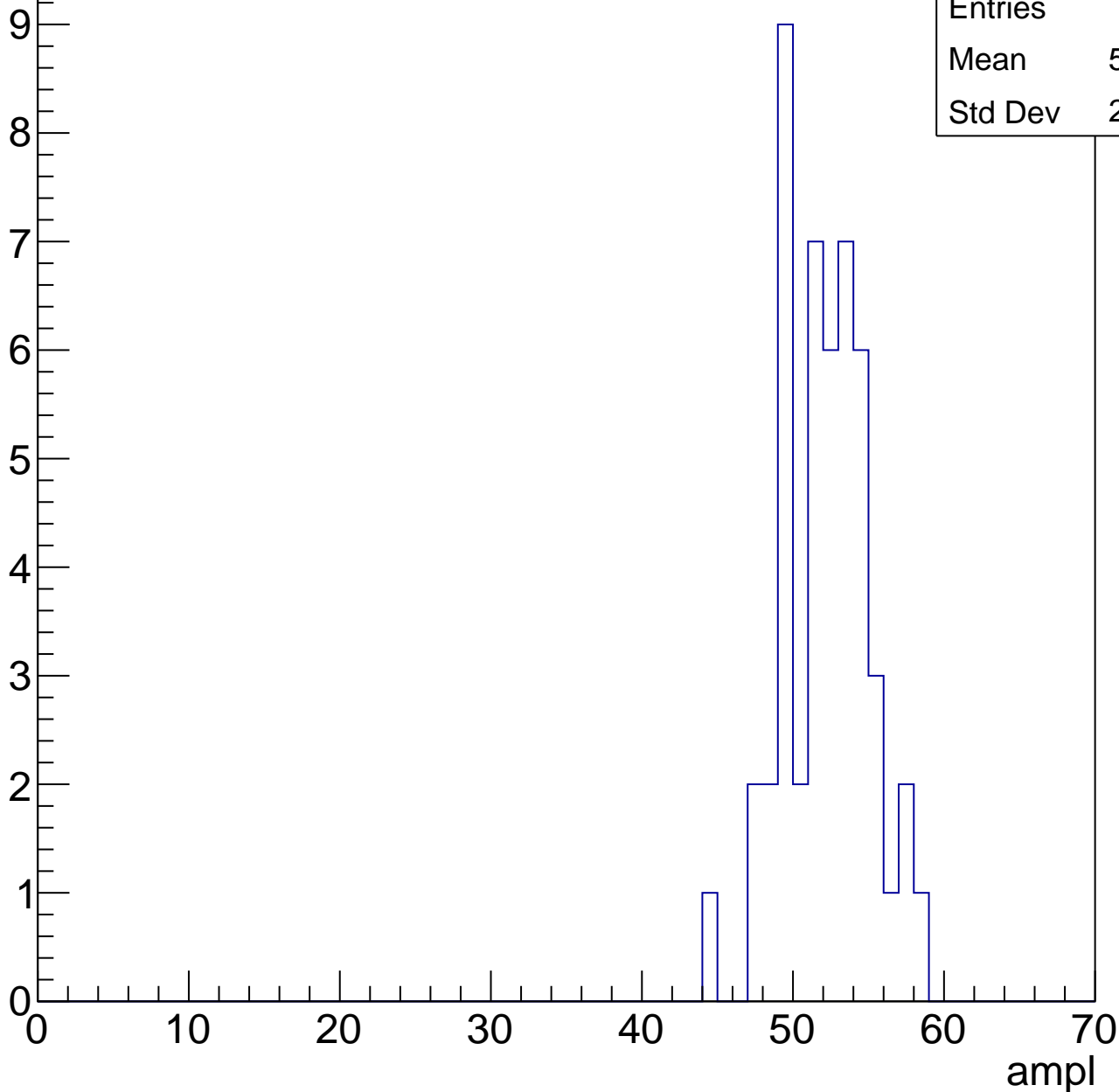


# B1L002S, U3-ch69, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	51.67
Std Dev	2.846

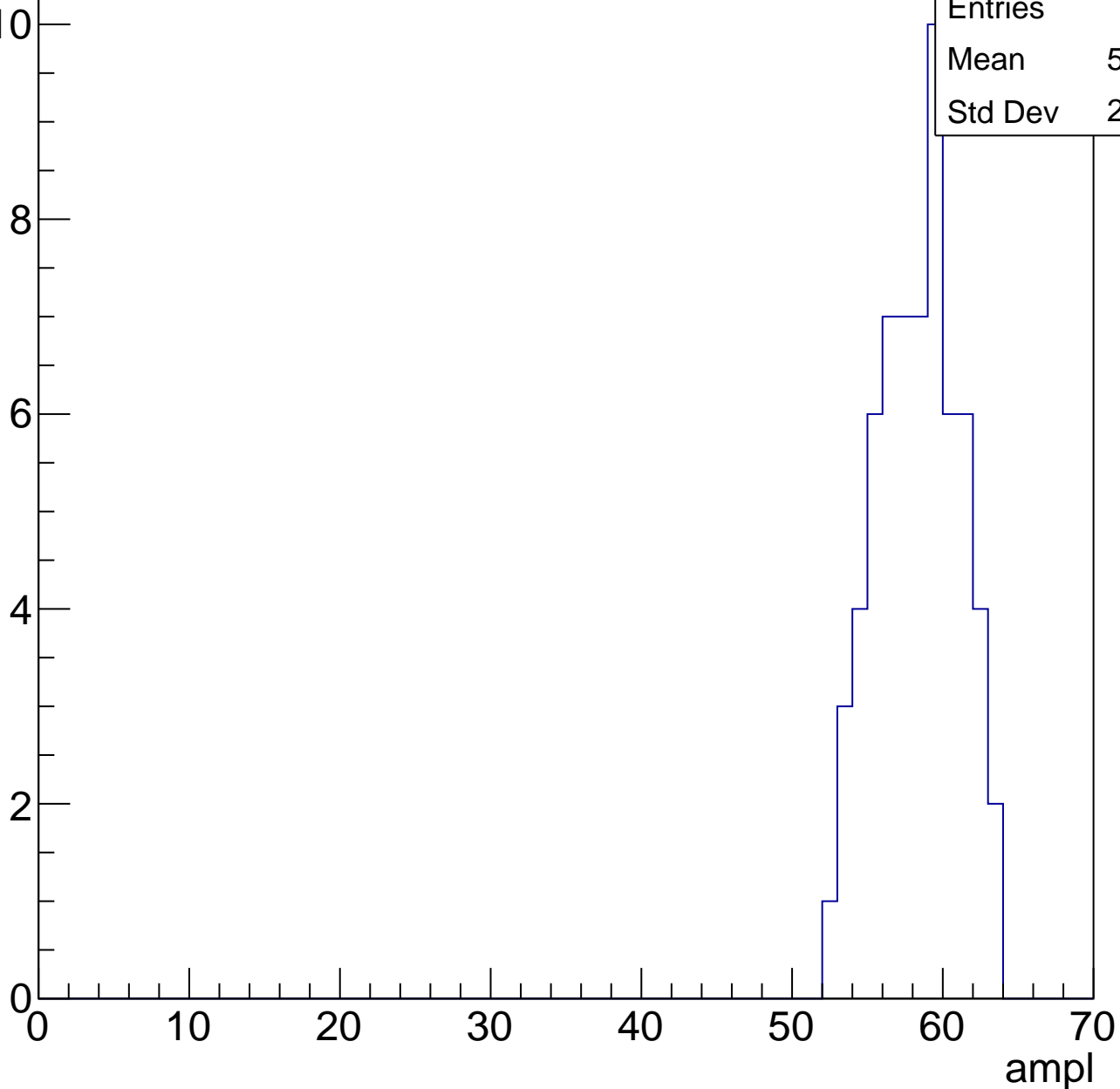


# B1L002S, U3-ch69, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	57.84
Std Dev	2.715

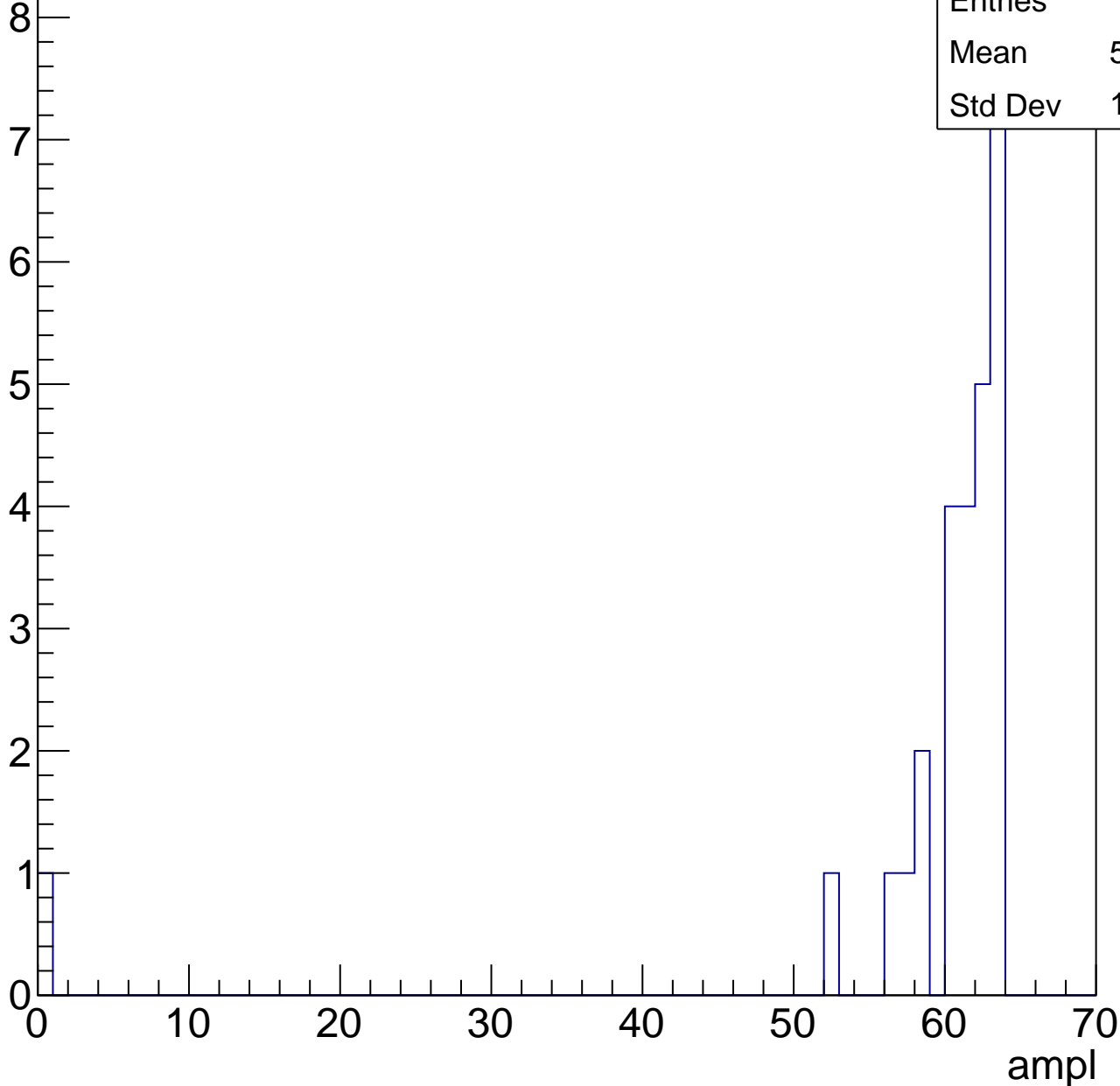


# B1L002S, U3-ch69, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.48
Std Dev	11.75

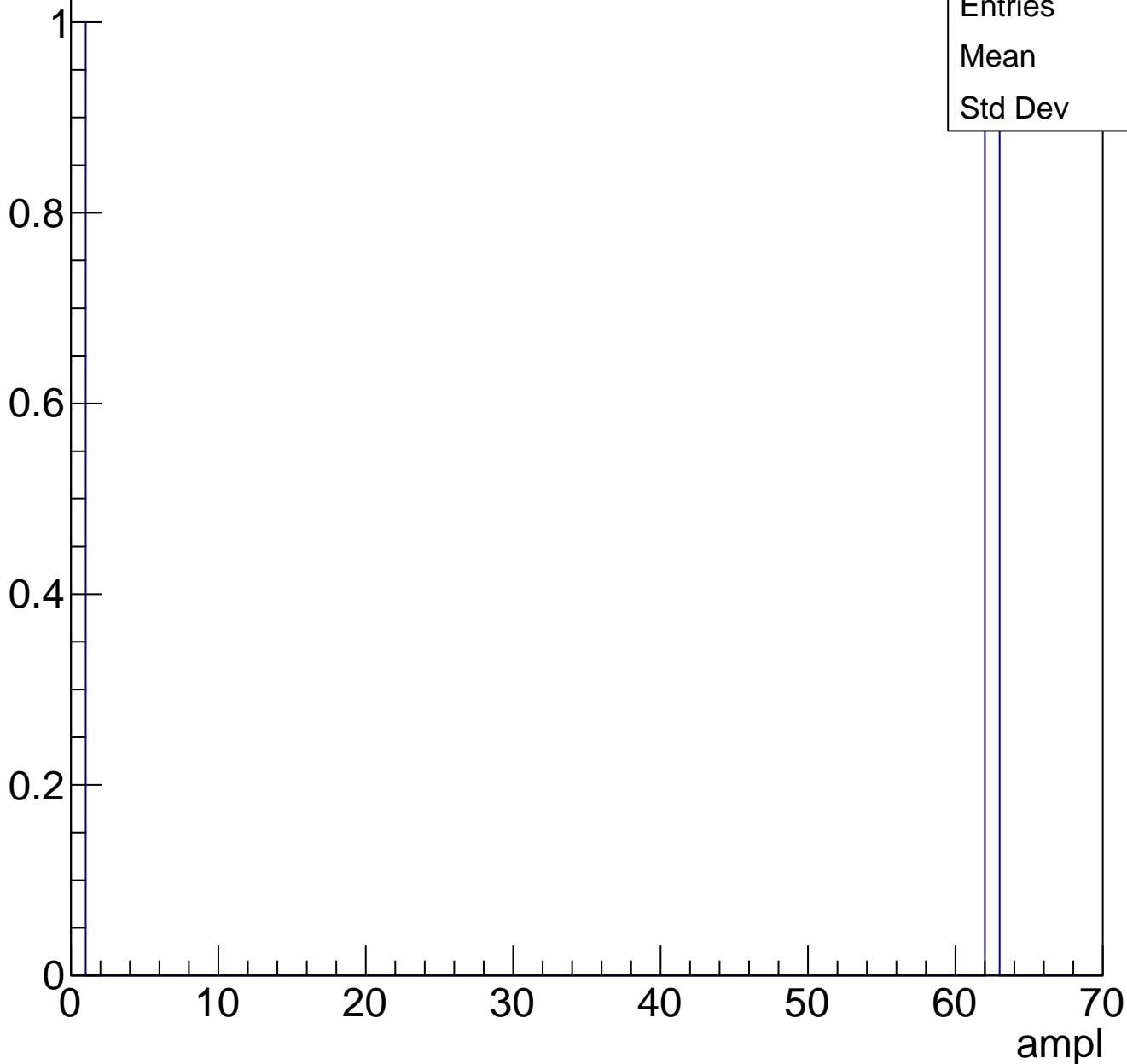




# B1L002S, U3-ch69, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch70, adc0

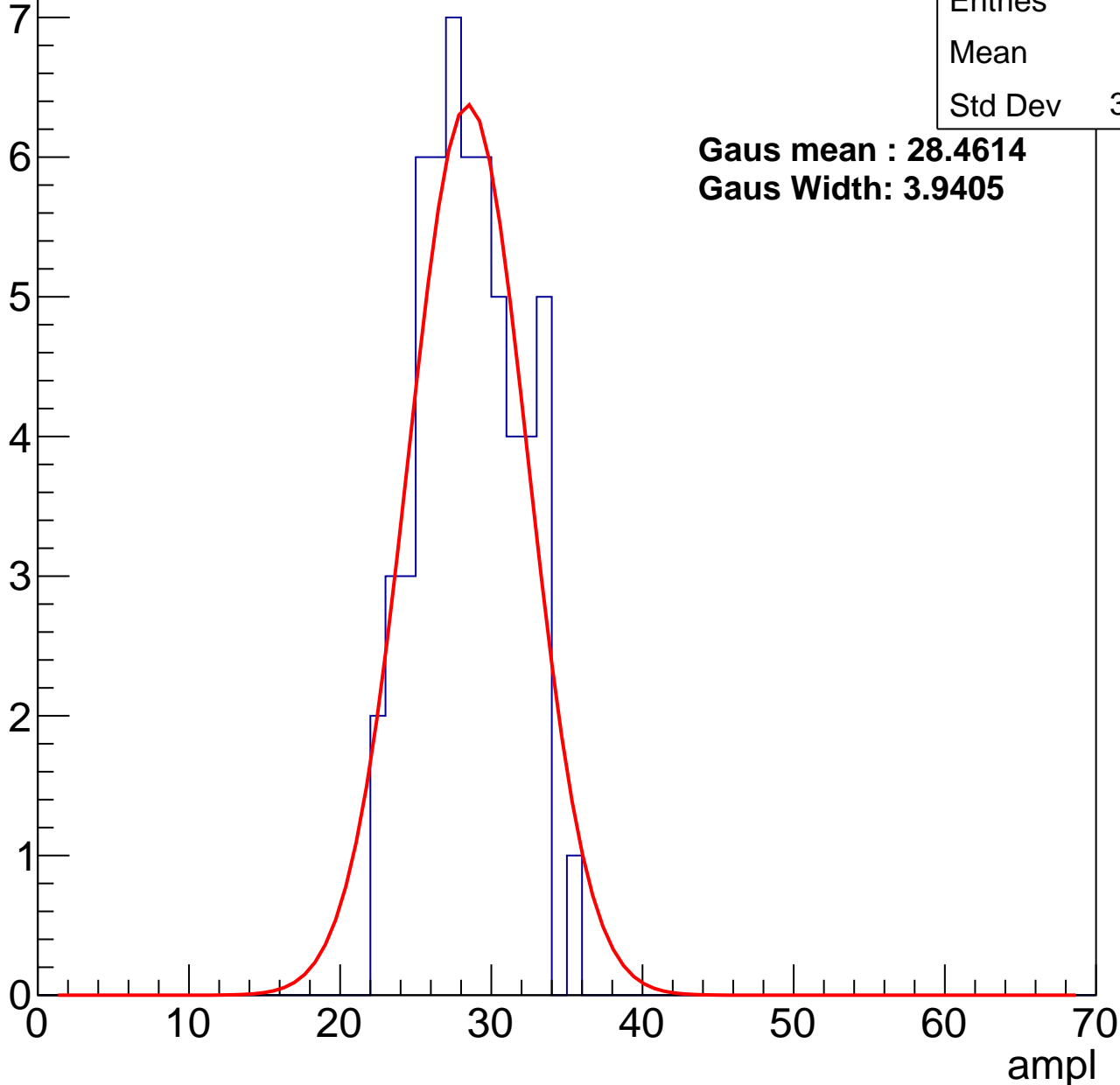
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	28
Std Dev	3.162

**Gaus mean : 28.4614**

**Gaus Width: 3.9405**



# B1L002S, U3-ch70, adc1

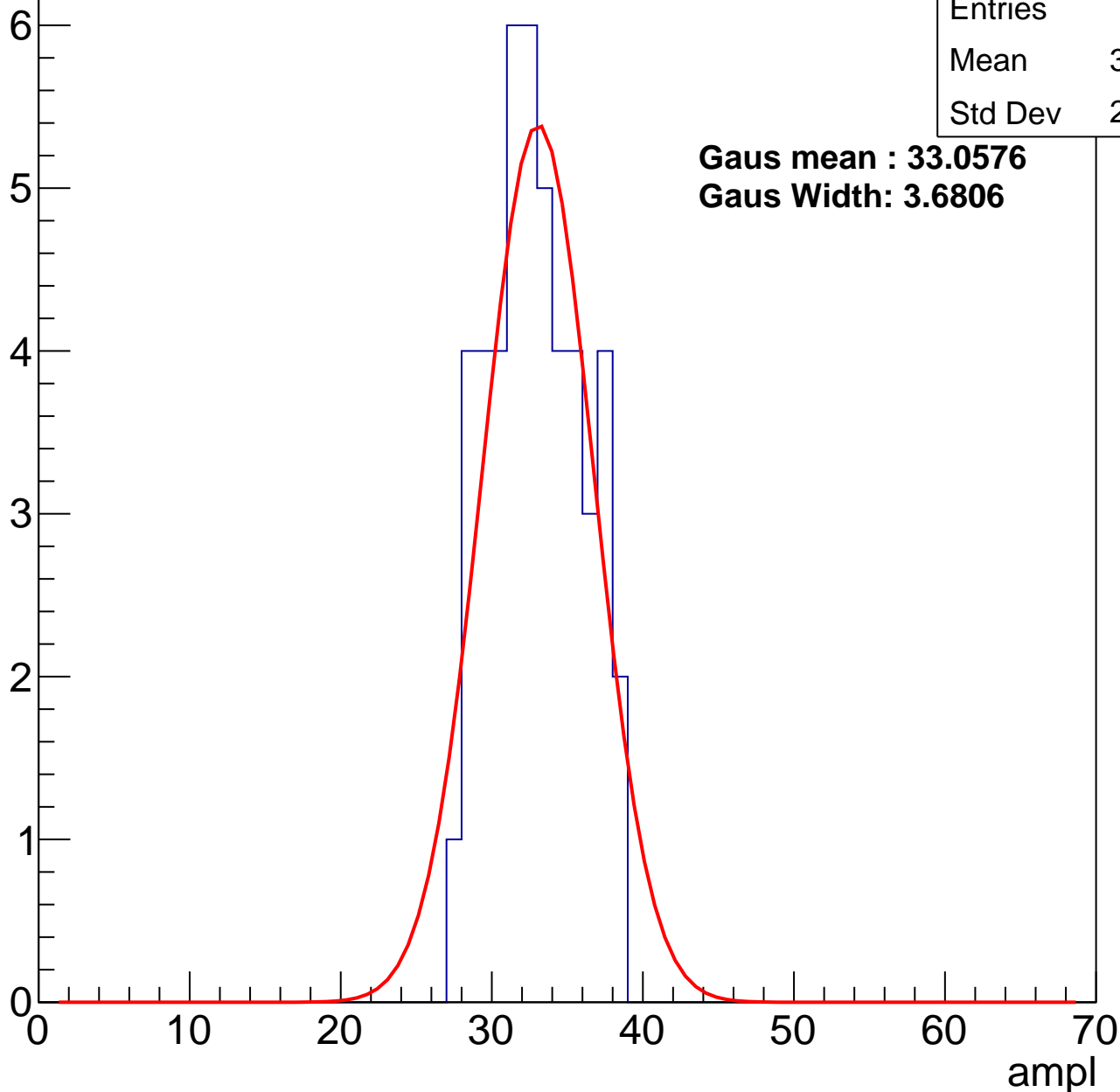
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	32.47
Std Dev	2.967

**Gaus mean : 33.0576**

**Gaus Width: 3.6806**



# B1L002S, U3-ch70, adc2

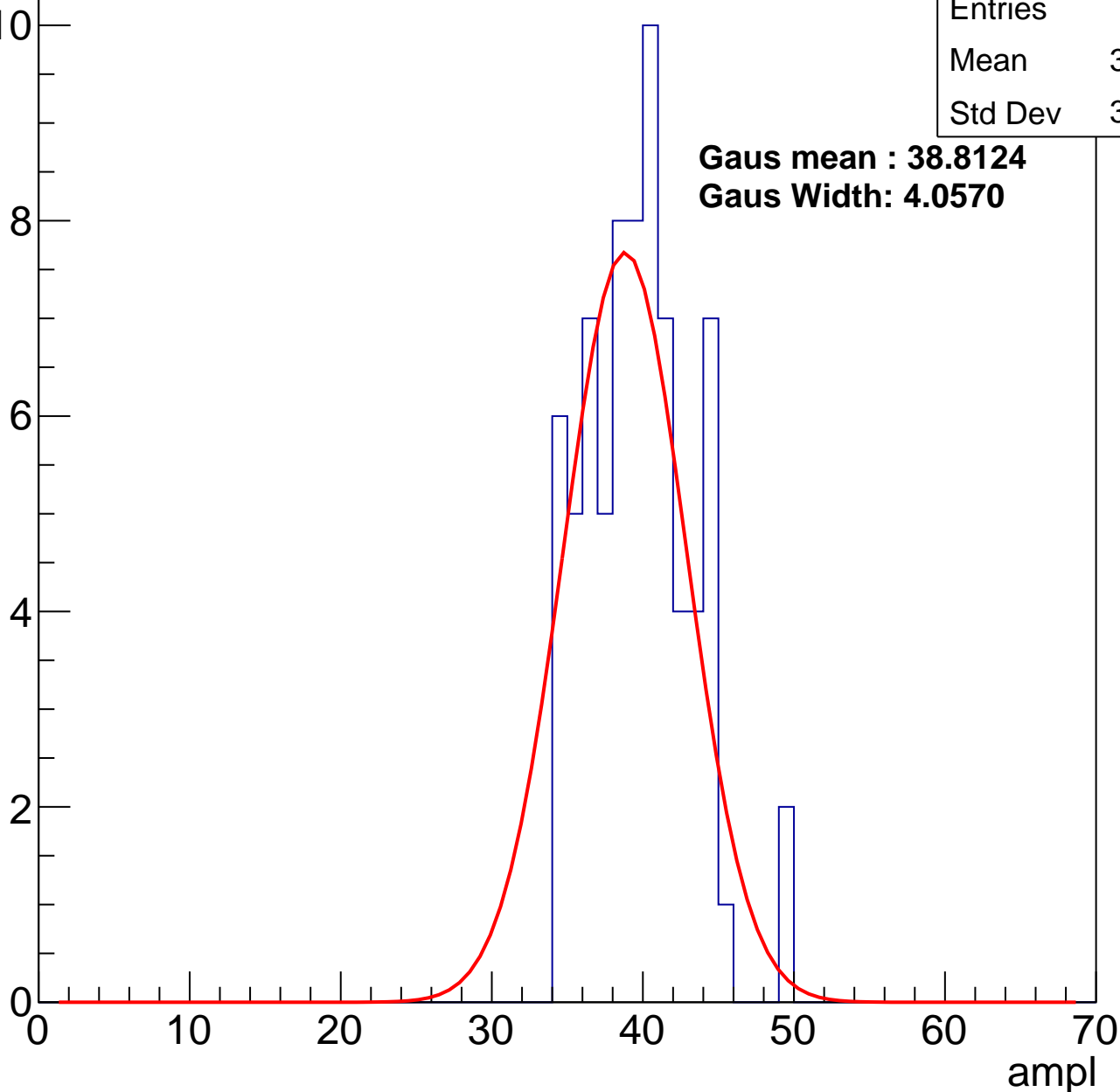
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	39.32
Std Dev	3.413

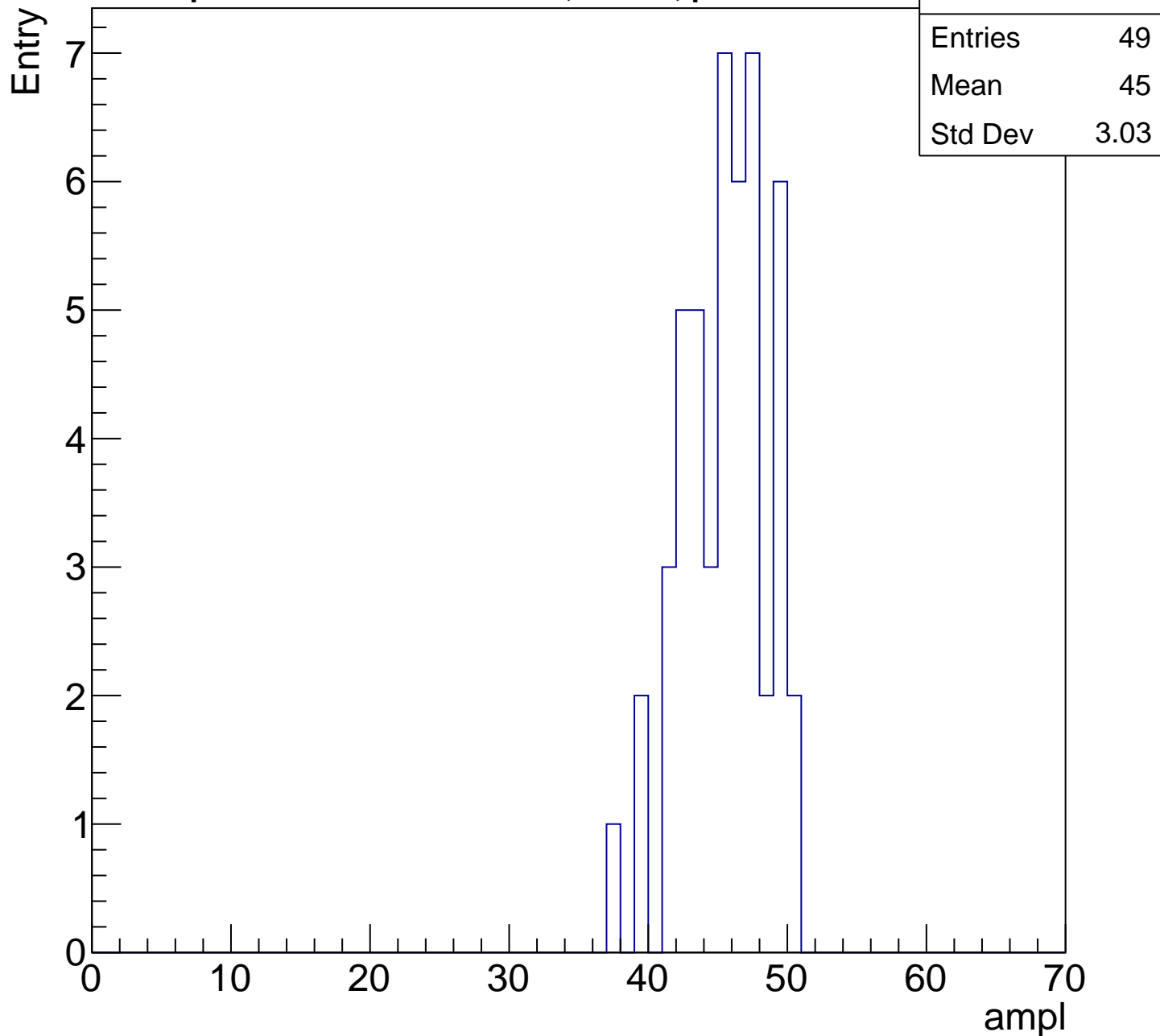
**Gaus mean : 38.8124**

**Gaus Width: 4.0570**



# B1L002S, U3-ch70, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch70, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	60
Mean	50.35
Std Dev	3.245

Entry

10

8

6

4

2

0

0

10

20

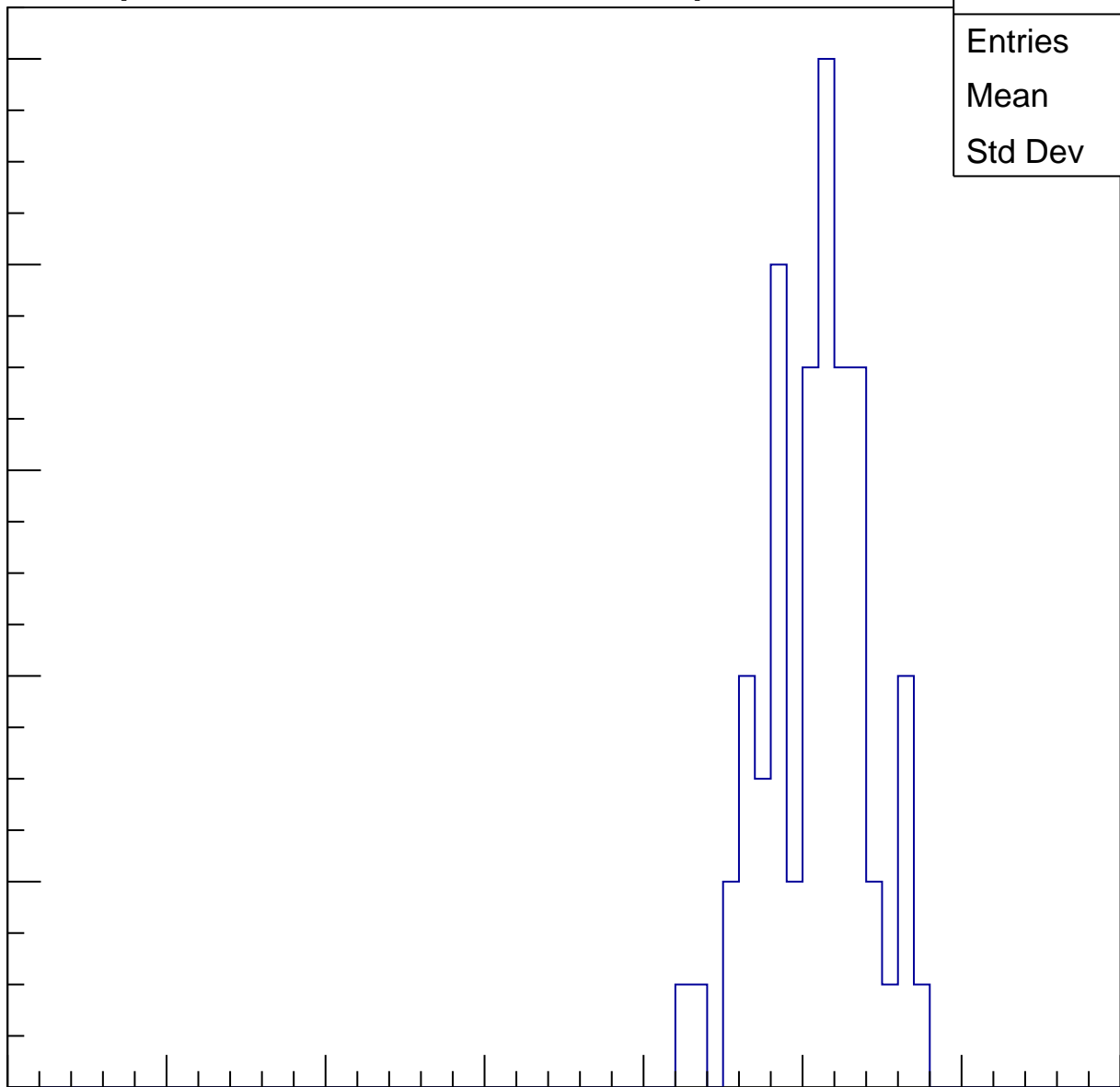
30

40

50

60

ampl

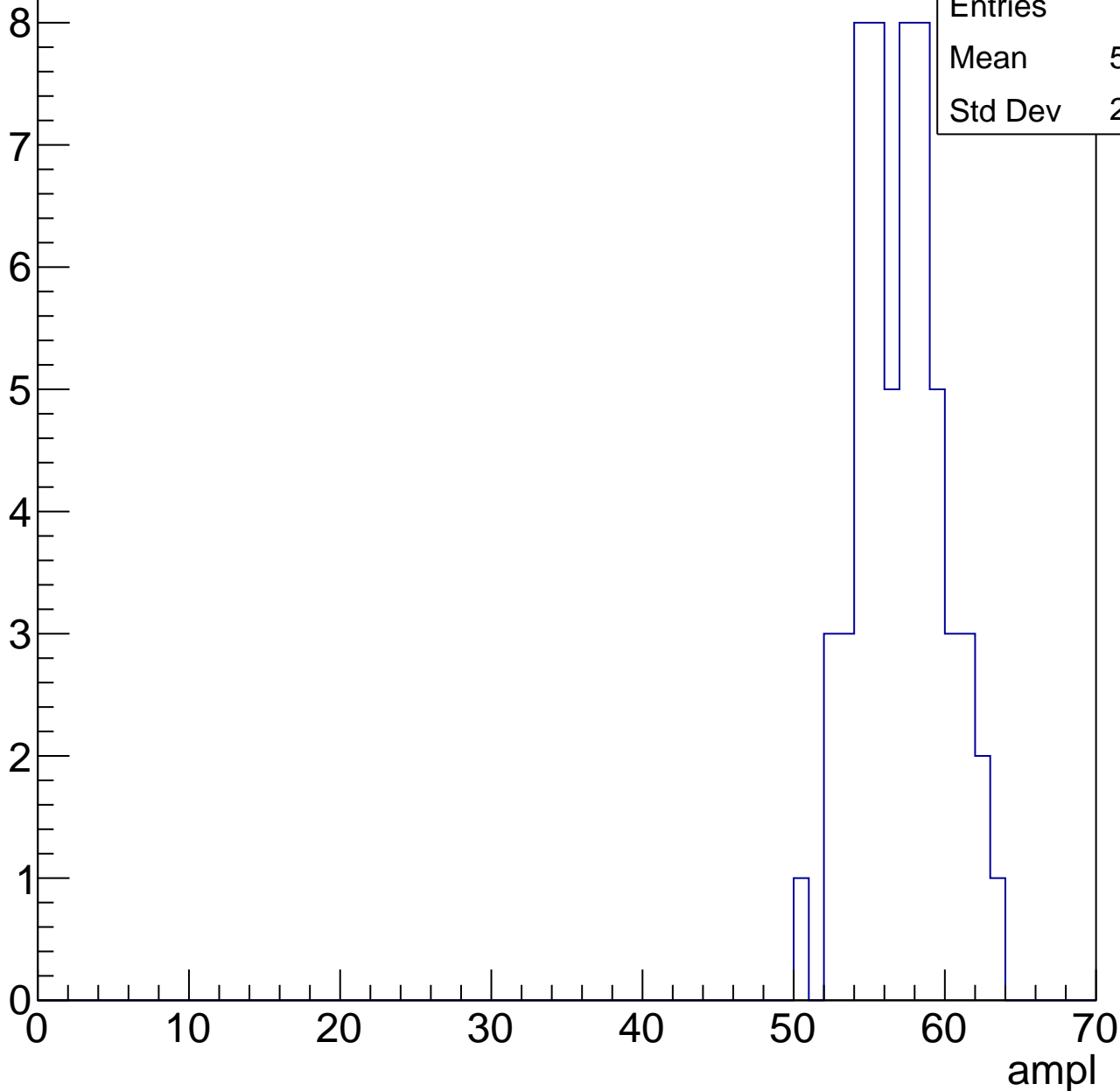


# B1L002S, U3-ch70, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

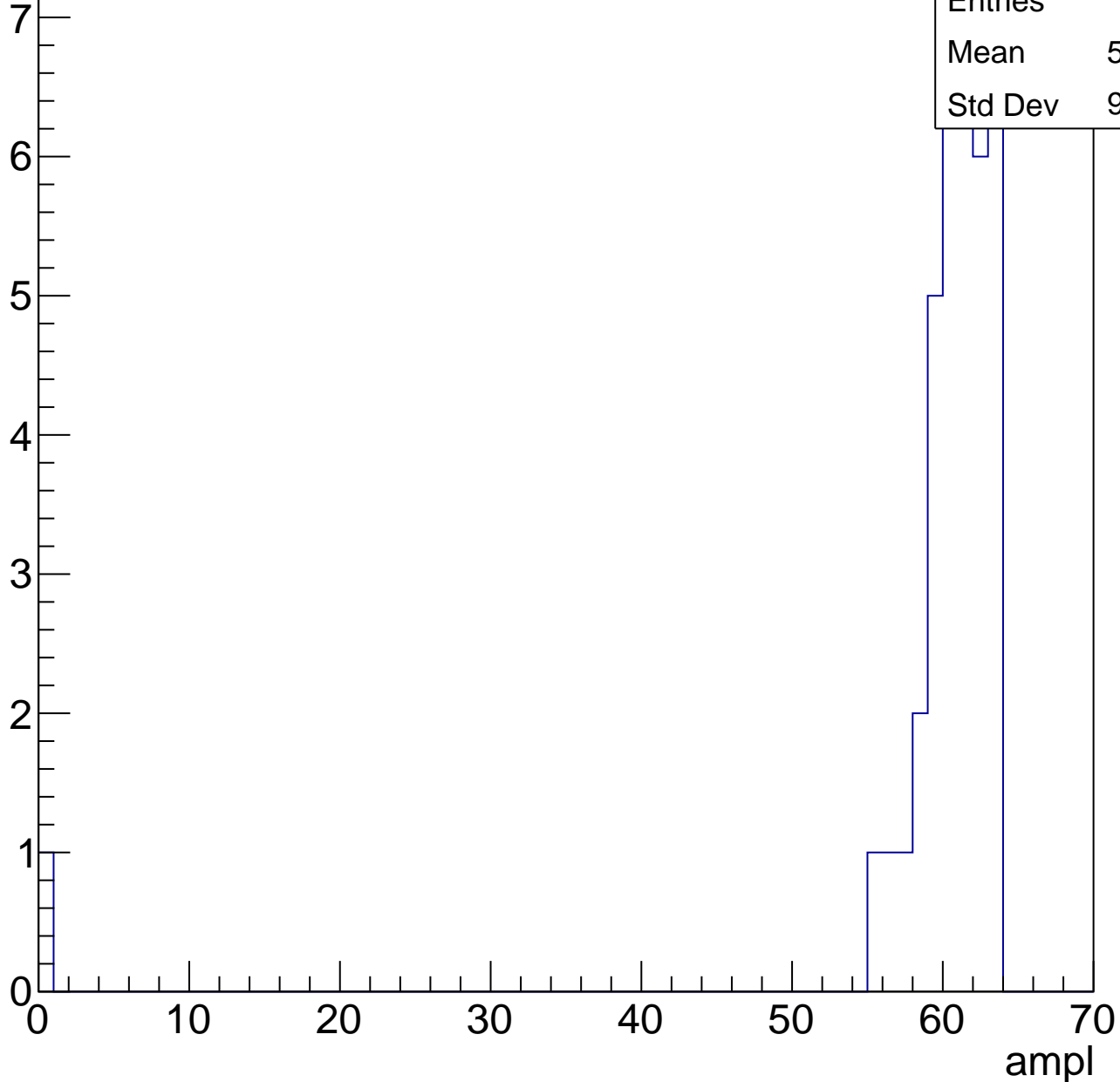
Entries	58
Mean	56.59
Std Dev	2.816



# B1L002S, U3-ch70, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

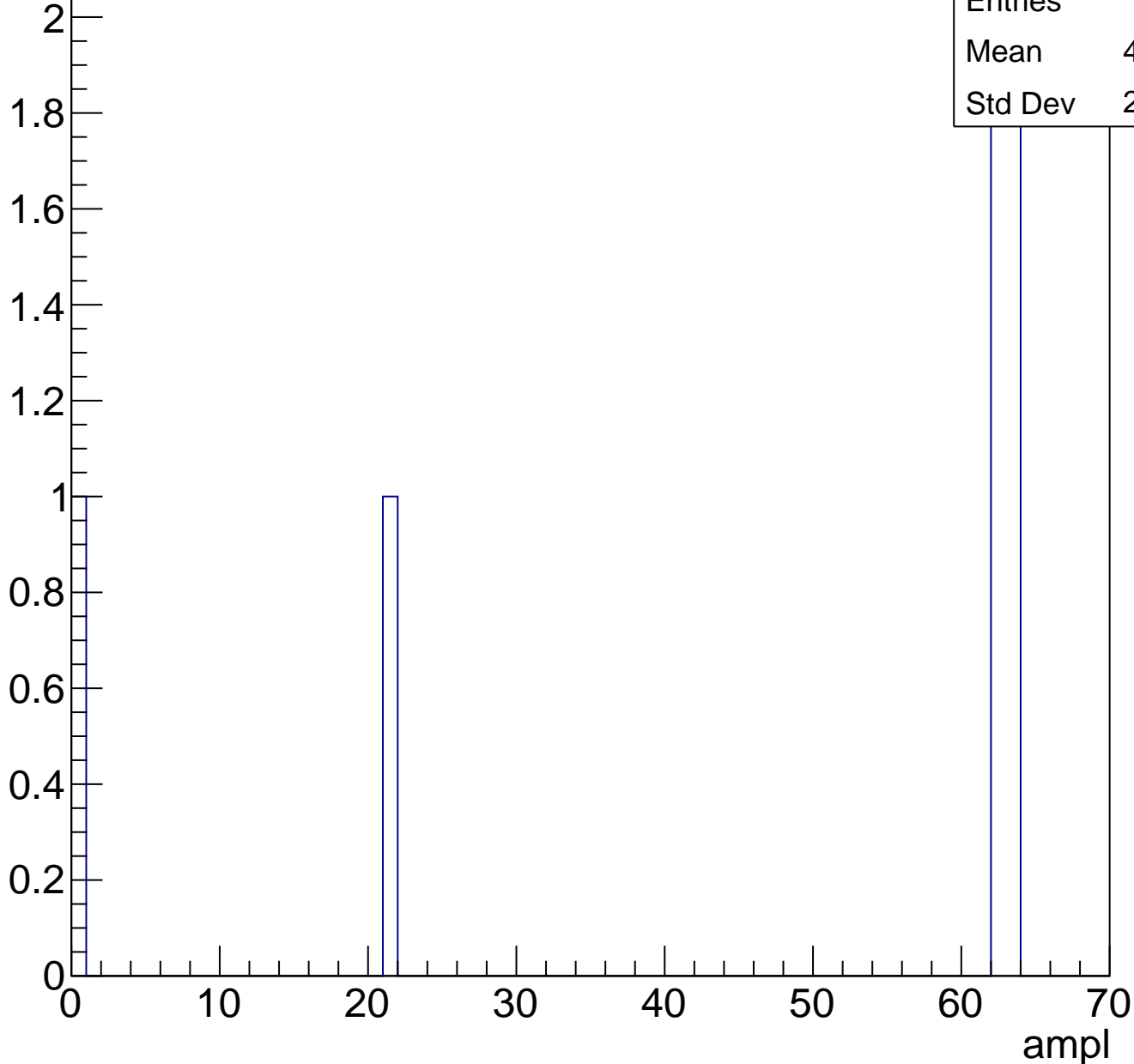




# B1L002S, U3-ch70, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch71, adc0

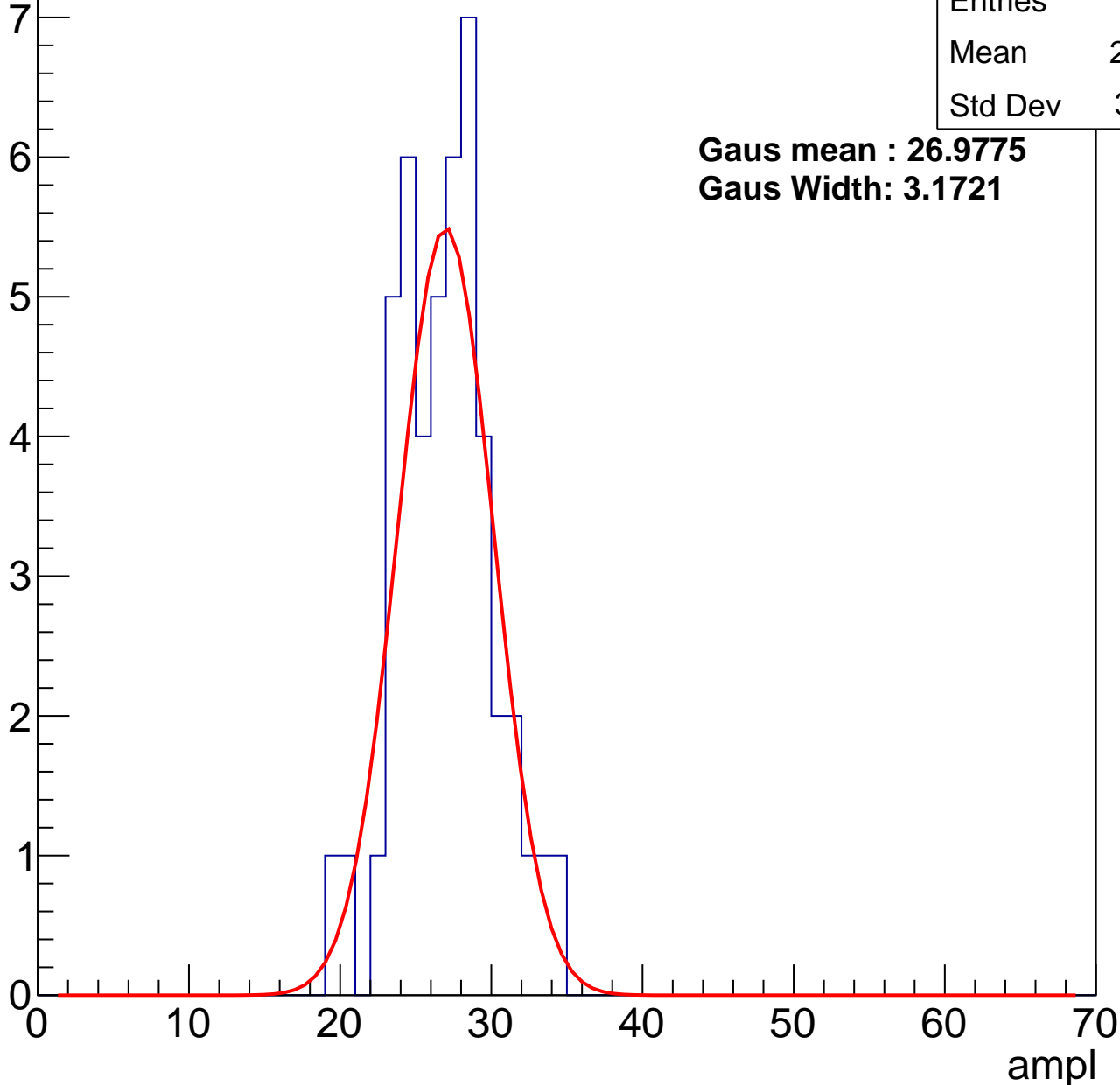
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	26.49
Std Dev	3.141

**Gaus mean : 26.9775**

**Gaus Width: 3.1721**



# B1L002S, U3-ch71, adc1

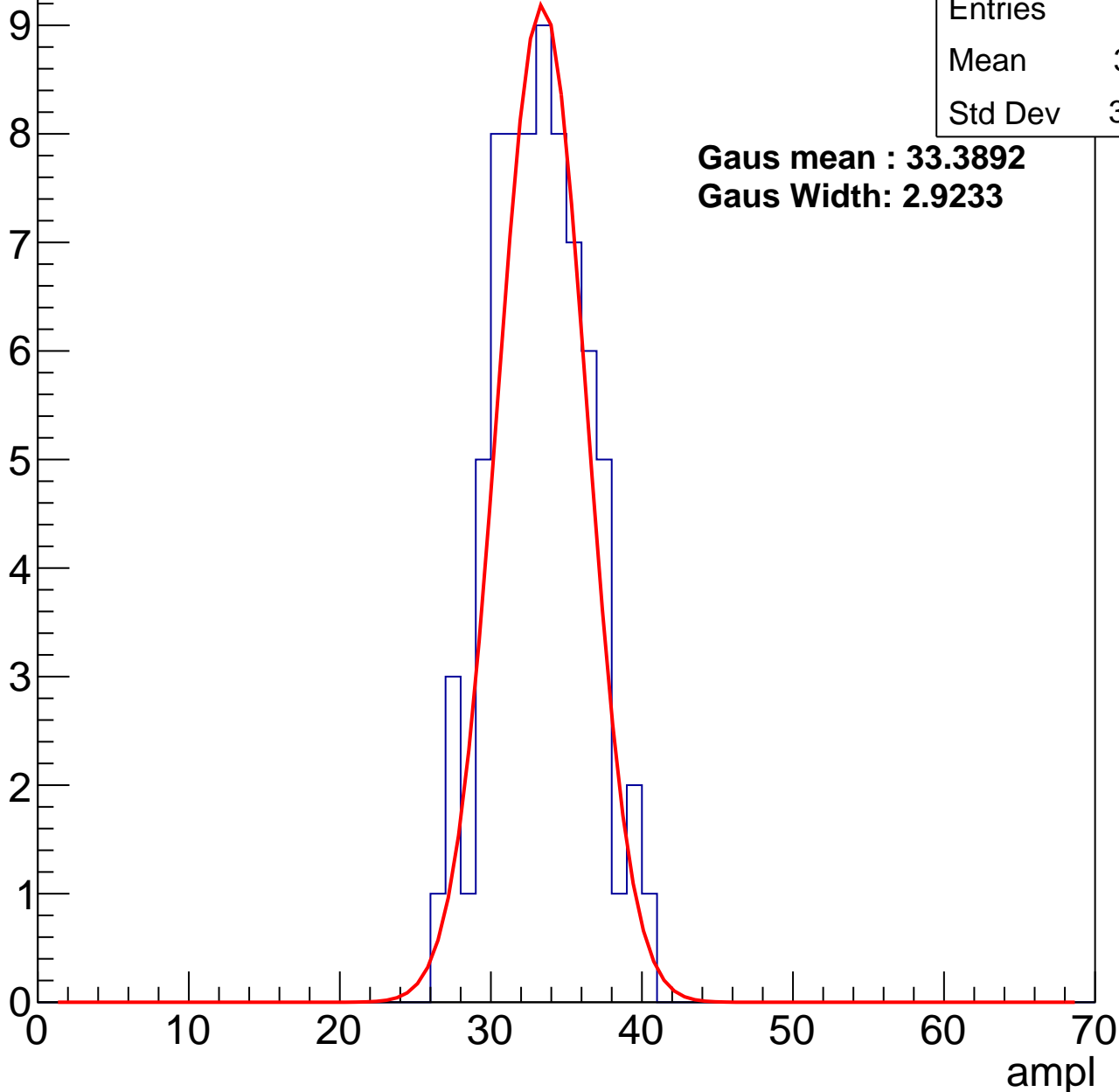
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	32.81
Std Dev	3.068

**Gaus mean : 33.3892**

**Gaus Width: 2.9233**



# B1L002S, U3-ch71, adc2

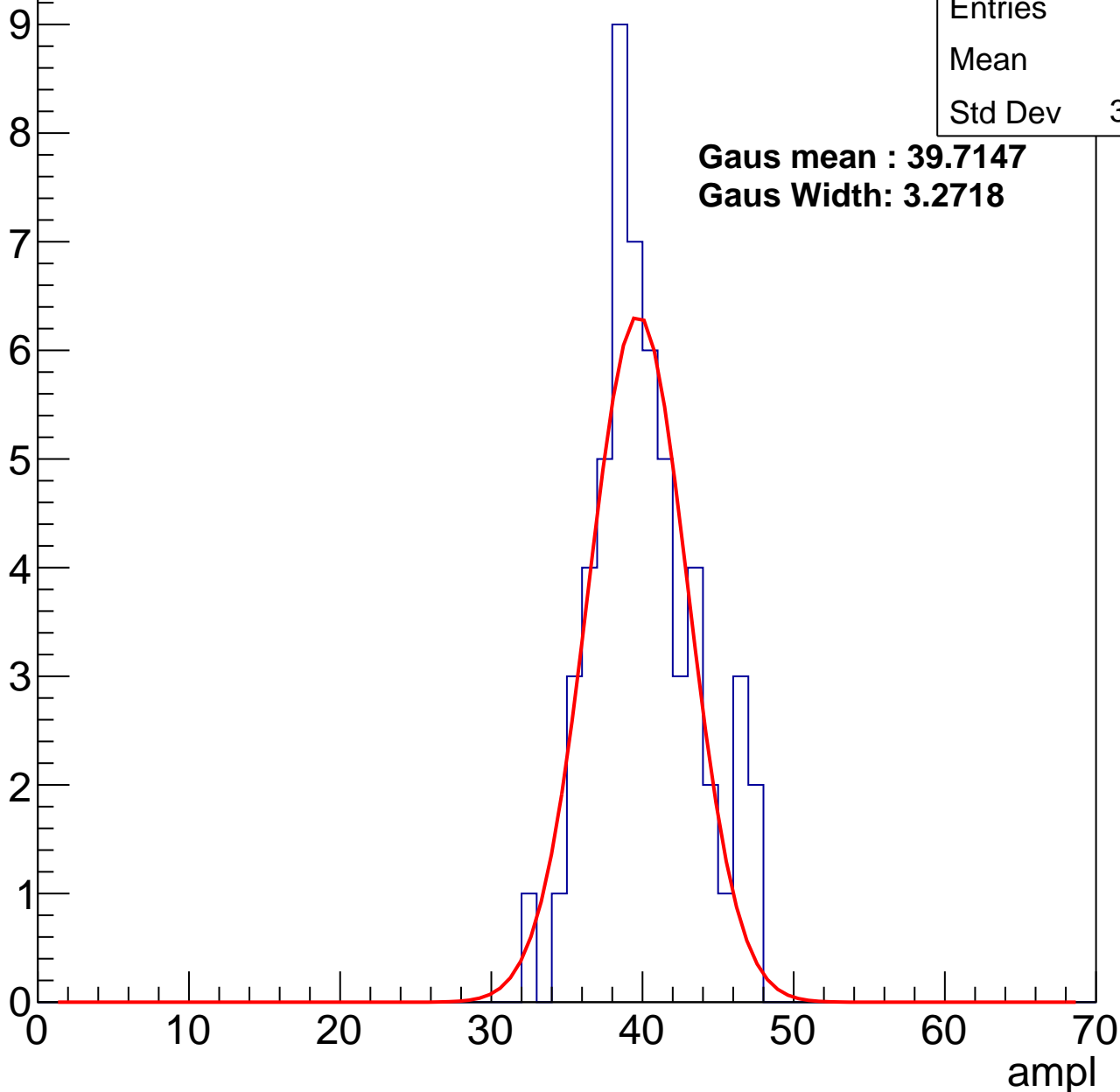
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	39.7
Std Dev	3.375

**Gaus mean : 39.7147**

**Gaus Width: 3.2718**



# B1L002S, U3-ch71, adc3

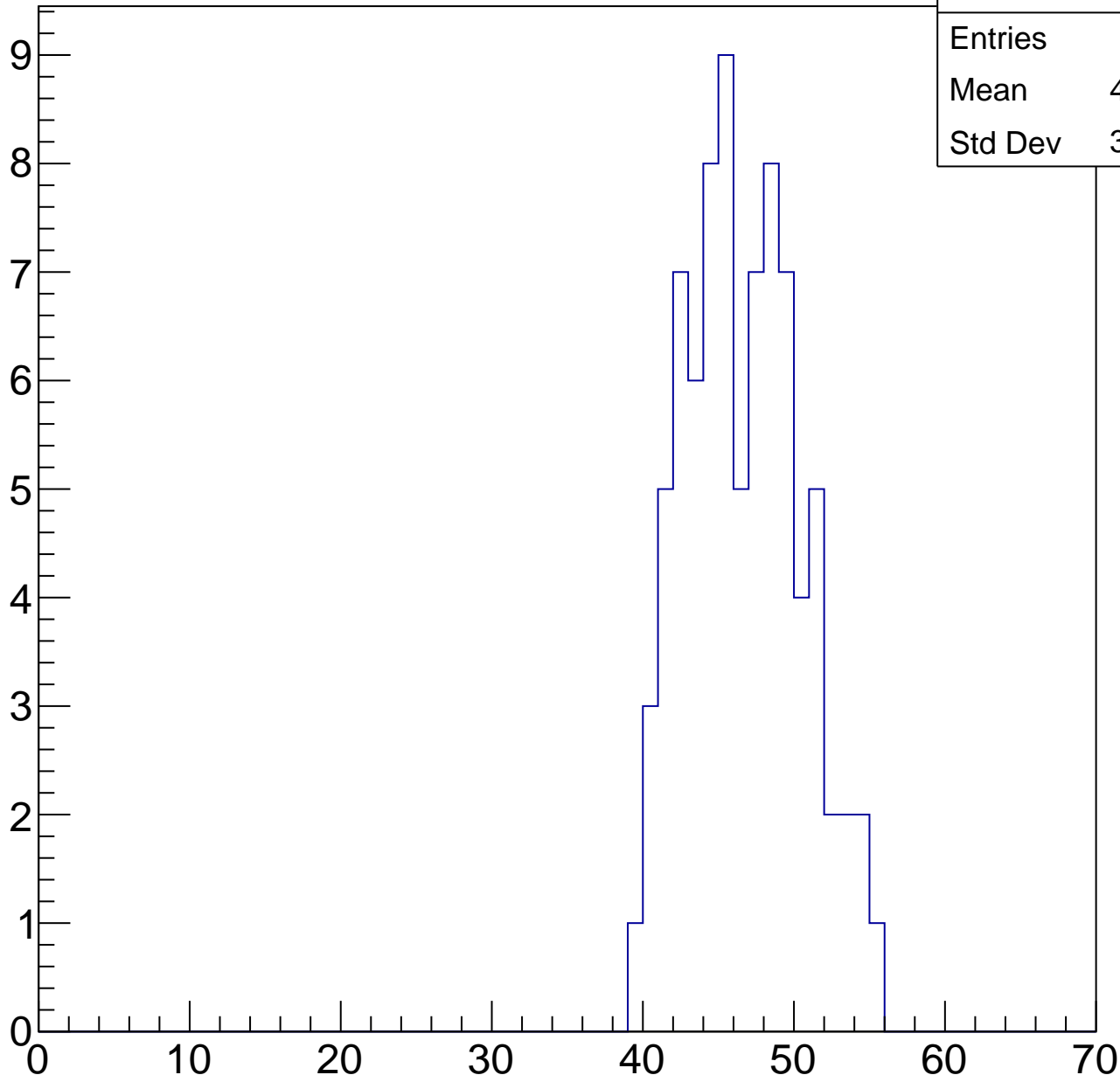
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	82
Mean	46.18
Std Dev	3.755

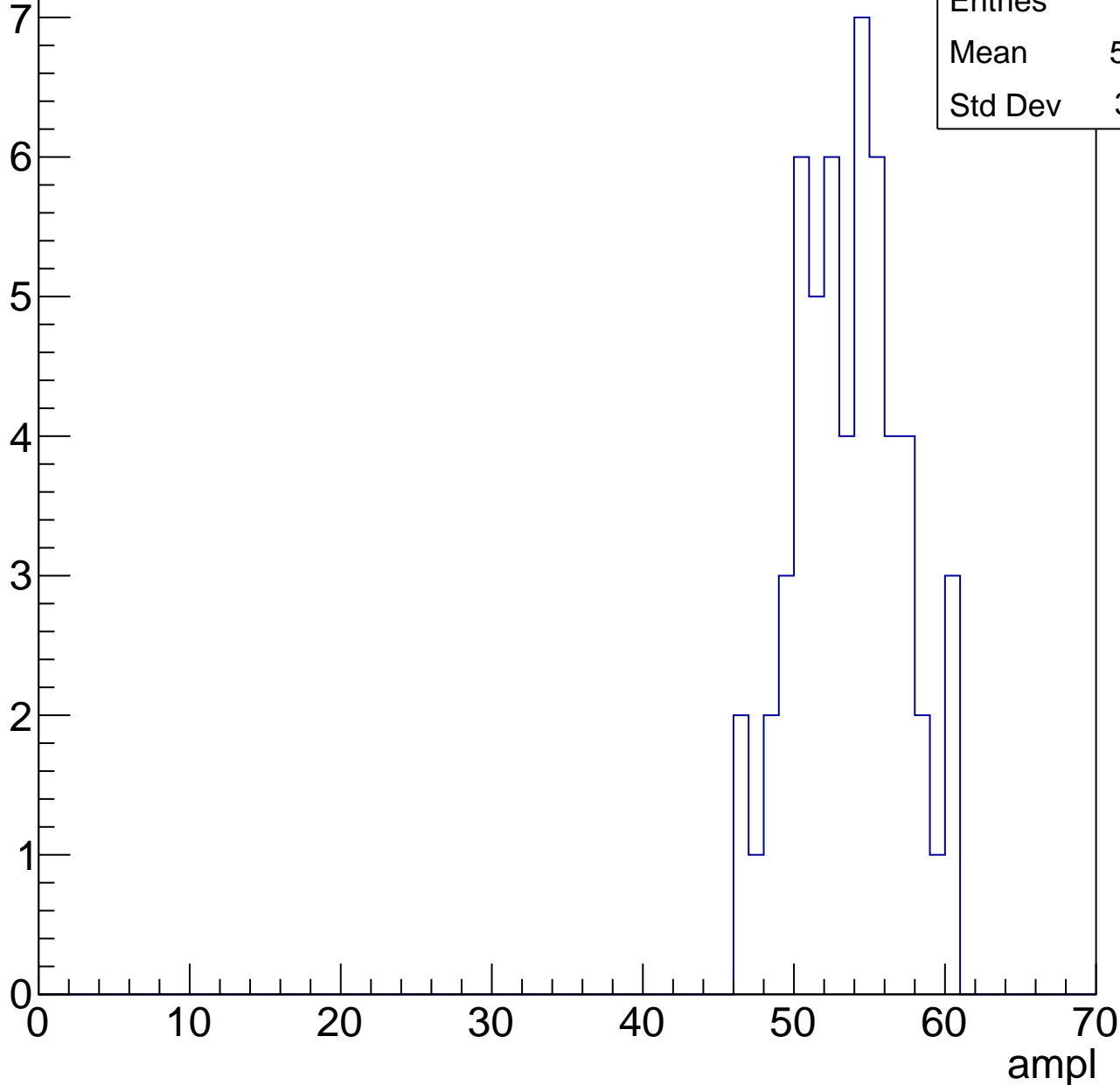
ampl



# B1L002S, U3-ch71, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch71, adc5

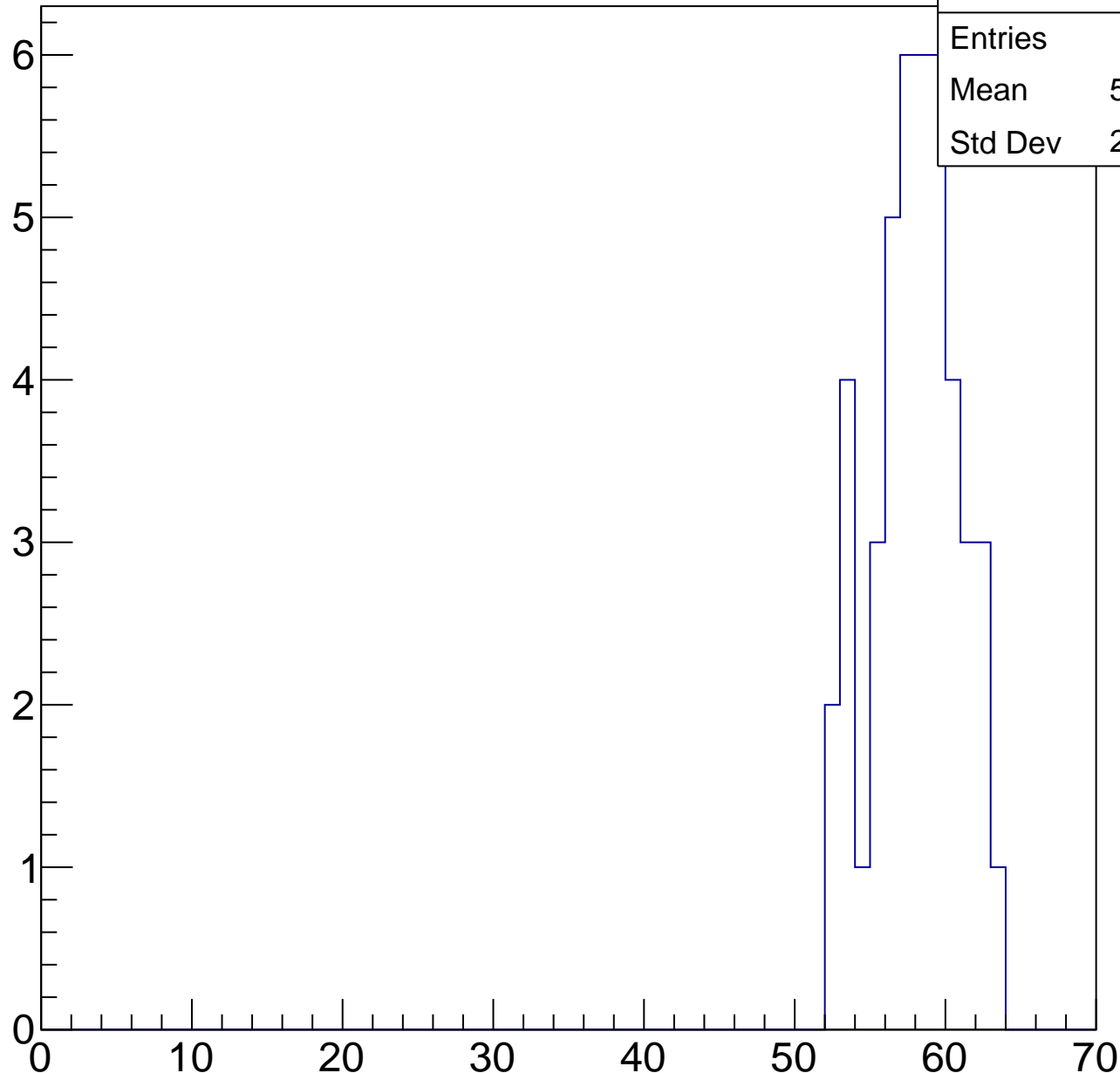
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

6  
5  
4  
3  
2  
1  
0

Entries	44
Mean	57.52
Std Dev	2.832

ampl

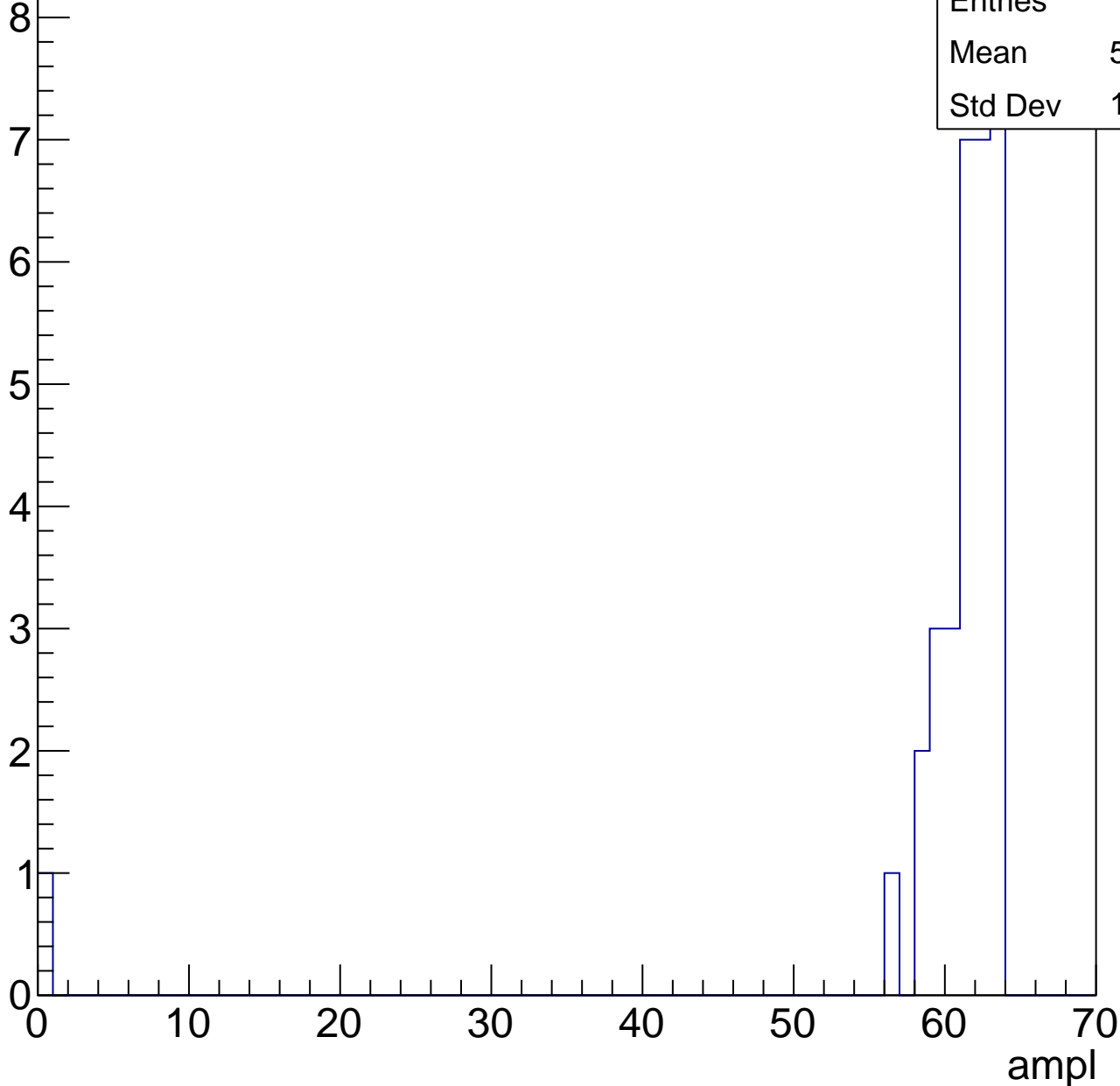


# B1L002S, U3-ch71, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.19
Std Dev	10.77





# B1L002S, U3-ch71, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch72, adc0

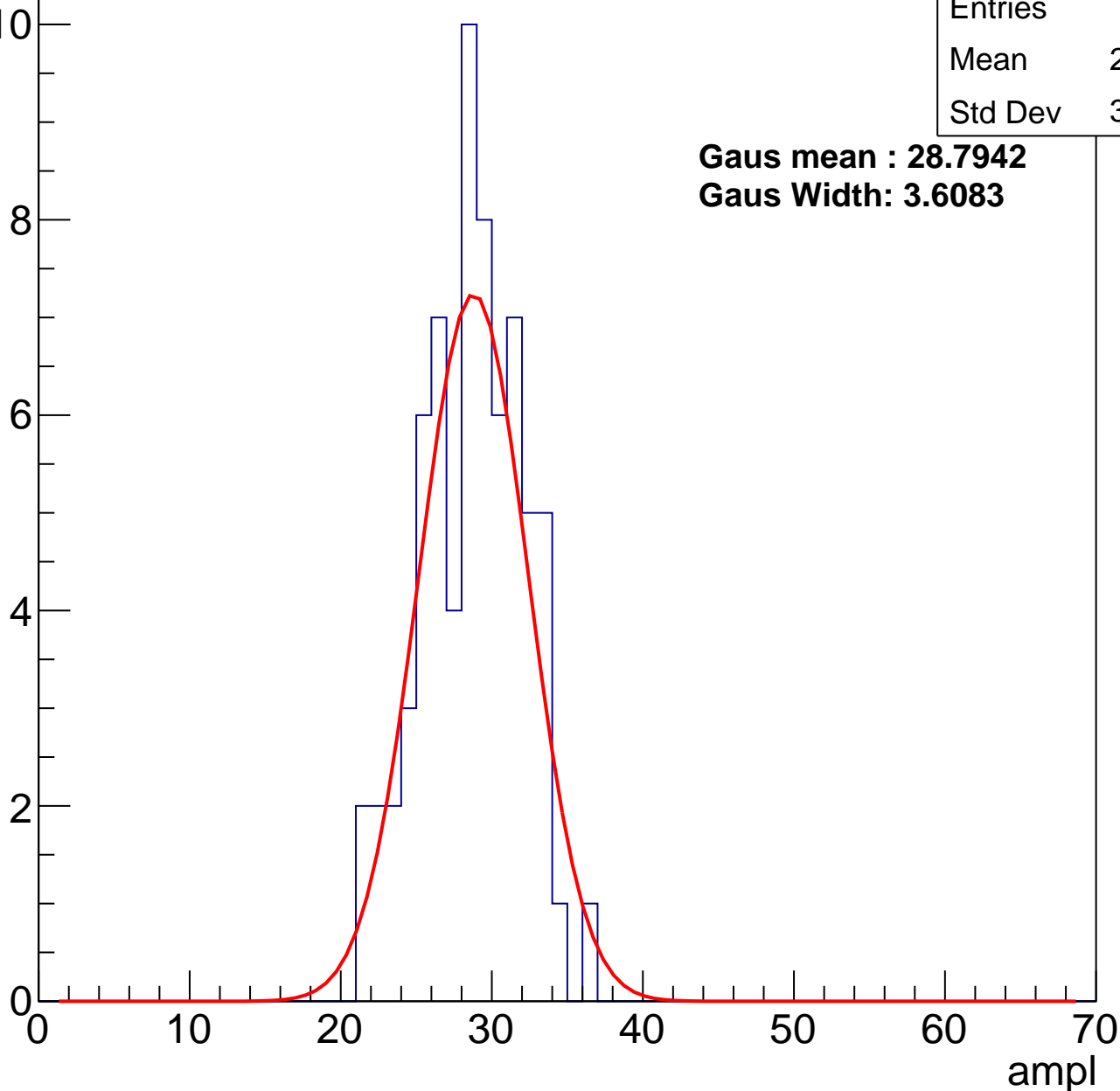
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	28.23
Std Dev	3.298

**Gaus mean : 28.7942**

**Gaus Width: 3.6083**



# B1L002S, U3-ch72, adc1

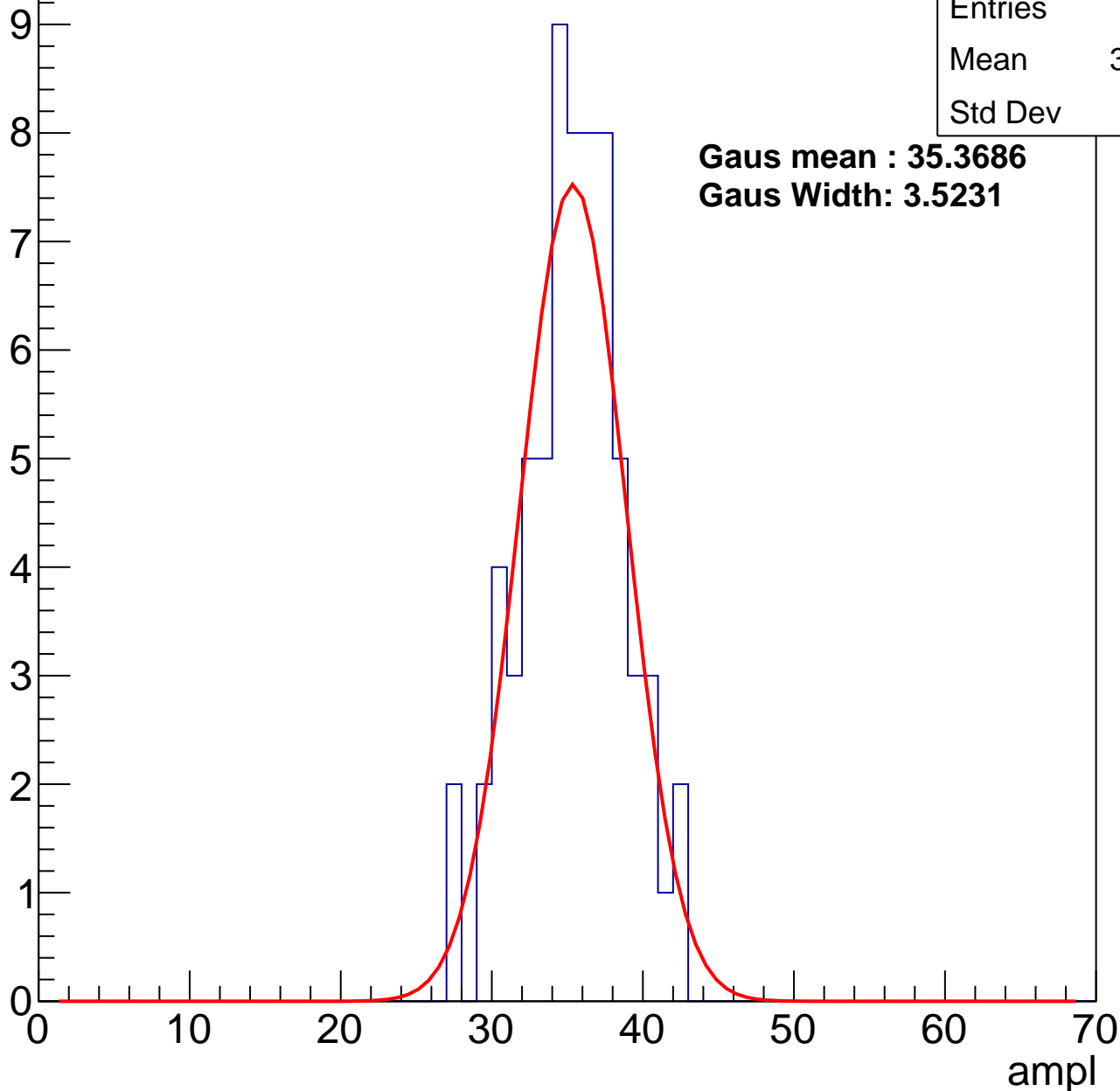
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	34.88
Std Dev	3.35

**Gaus mean : 35.3686**

**Gaus Width: 3.5231**



# B1L002S, U3-ch72, adc2

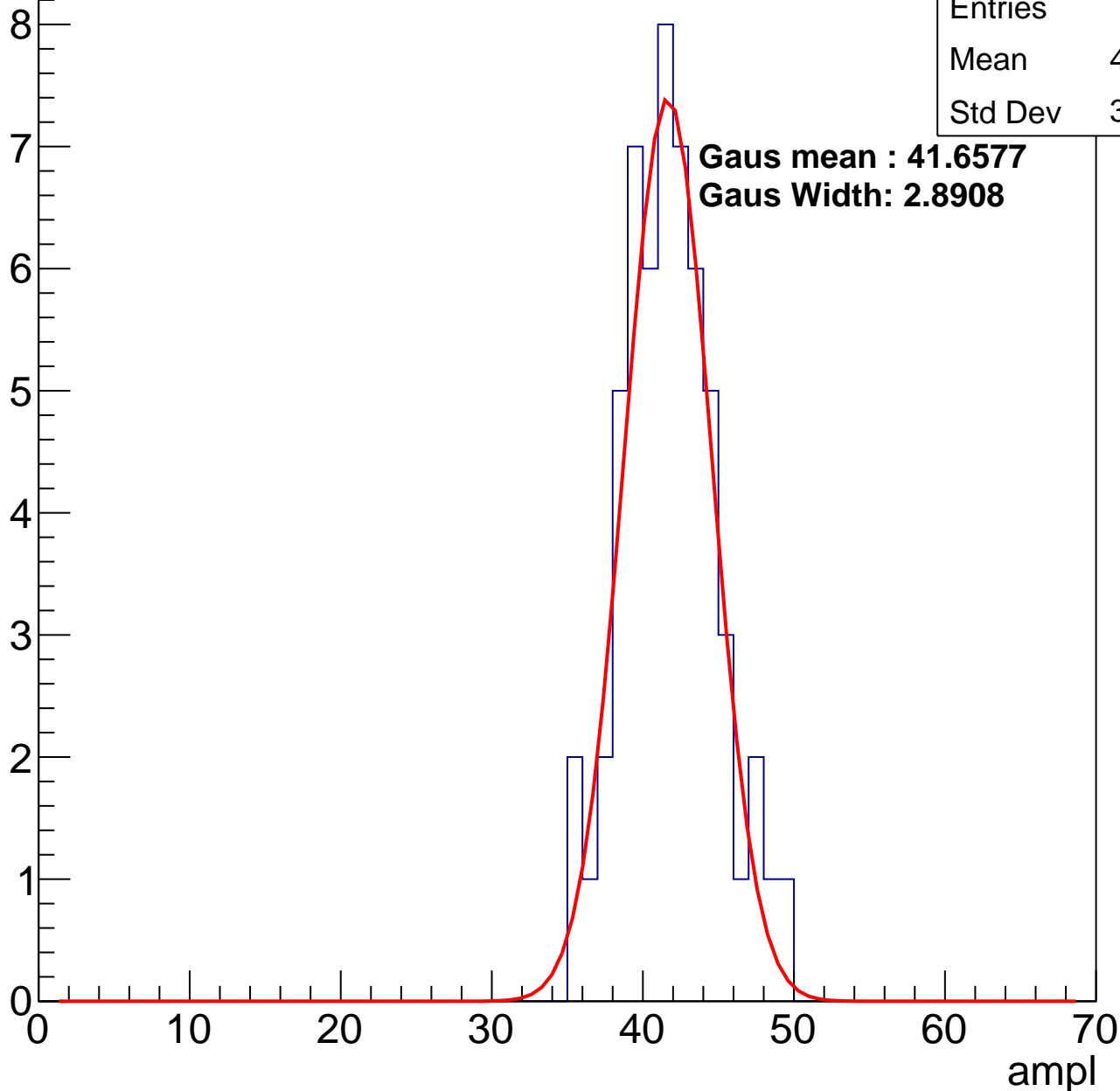
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.32
Std Dev	3.067

**Gaus mean : 41.6577**

**Gaus Width: 2.8908**

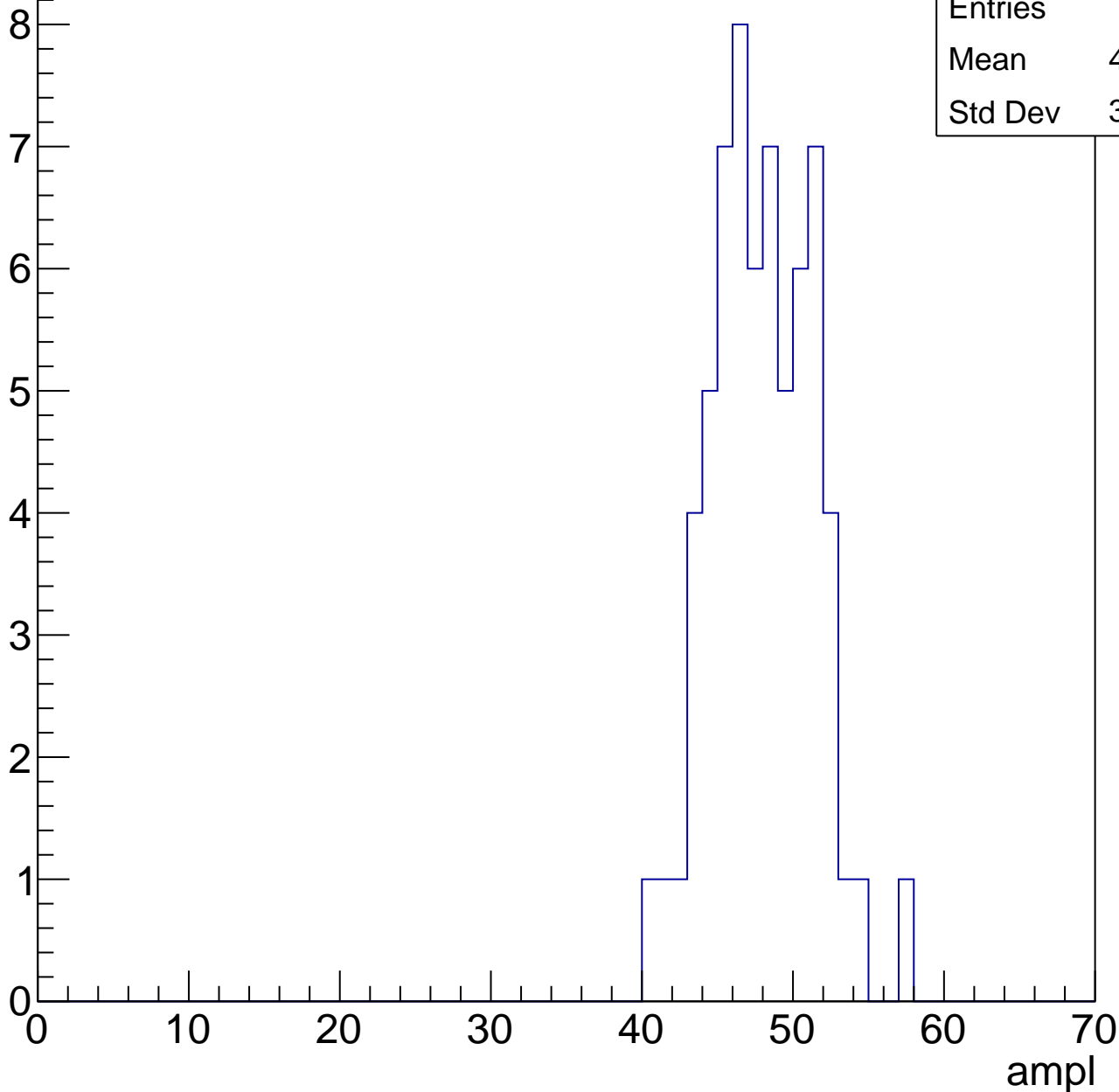


# B1L002S, U3-ch72, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	47.54
Std Dev	3.315

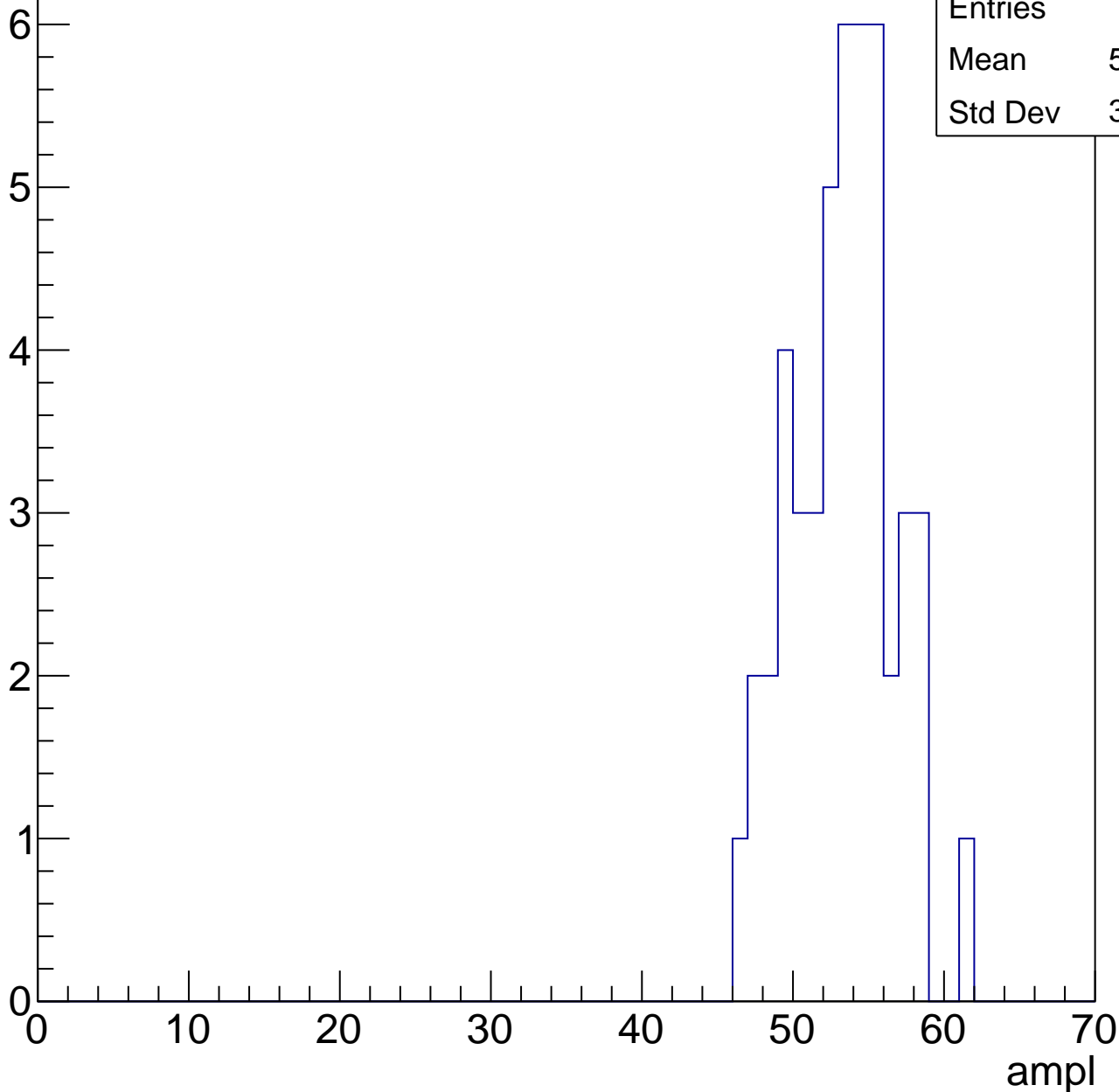


# B1L002S, U3-ch72, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	52.87
Std Dev	3.305

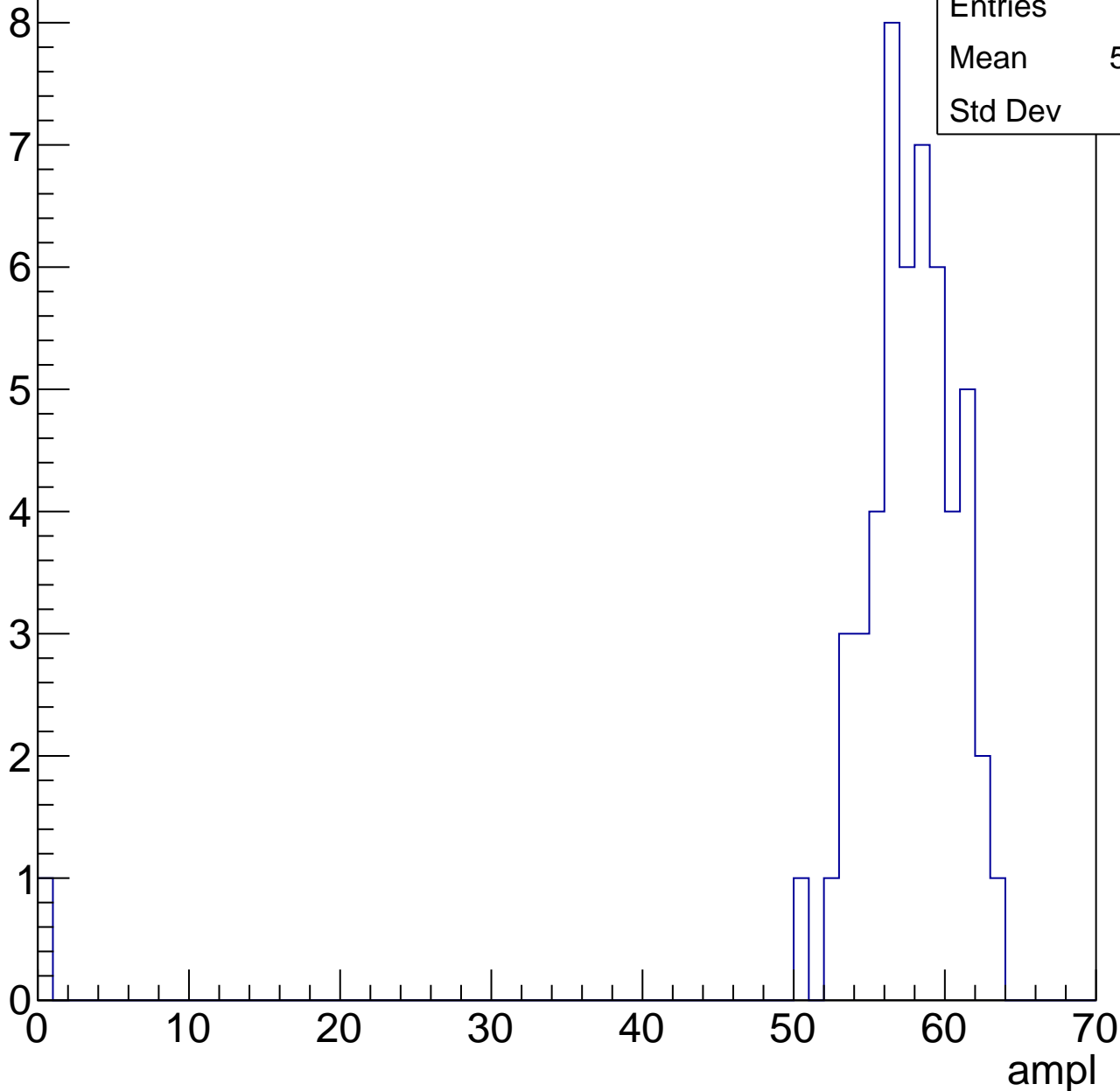


# B1L002S, U3-ch72, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

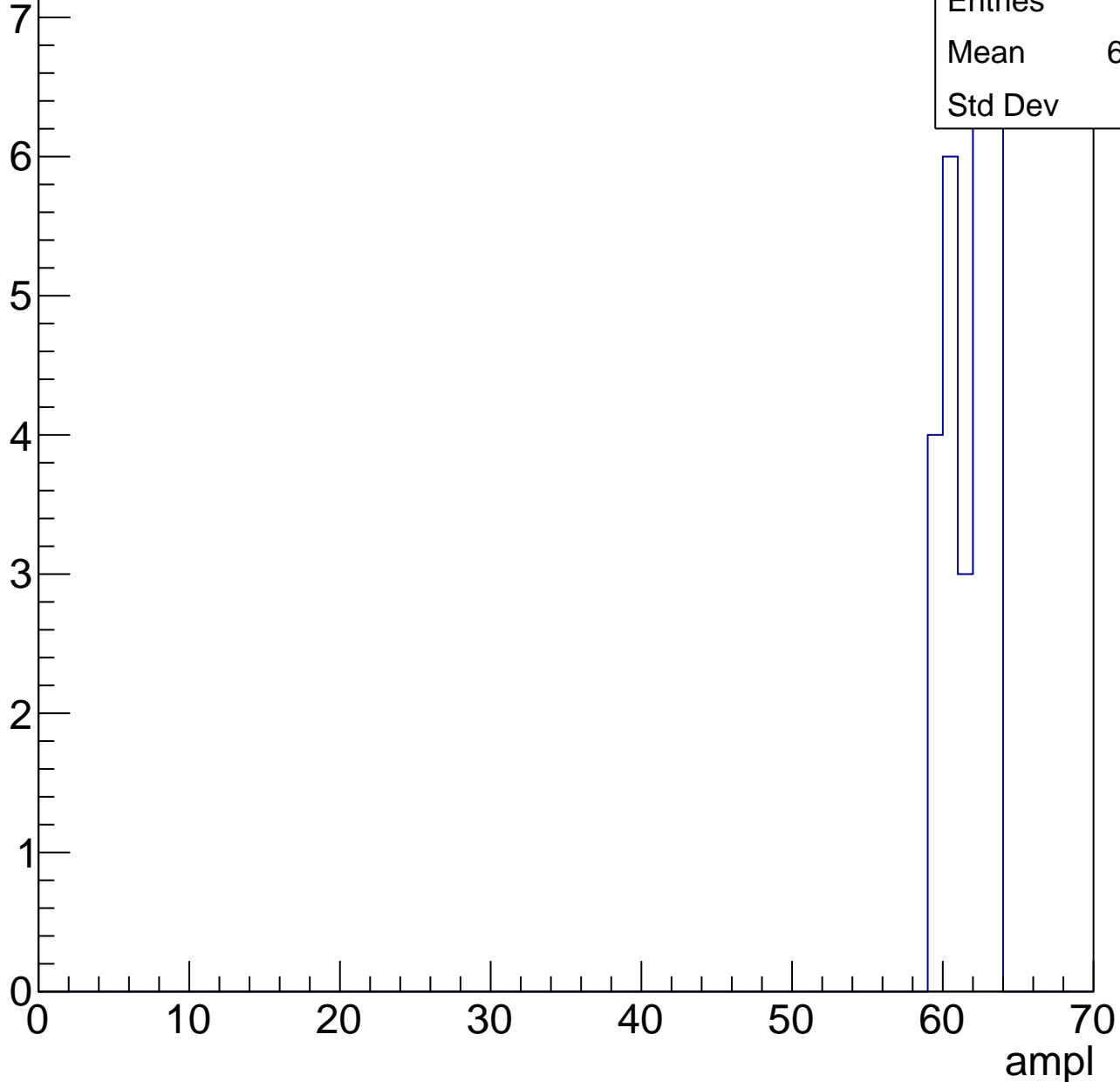
Entries	52
Mean	56.25
Std Dev	8.35



# B1L002S, U3-ch72, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

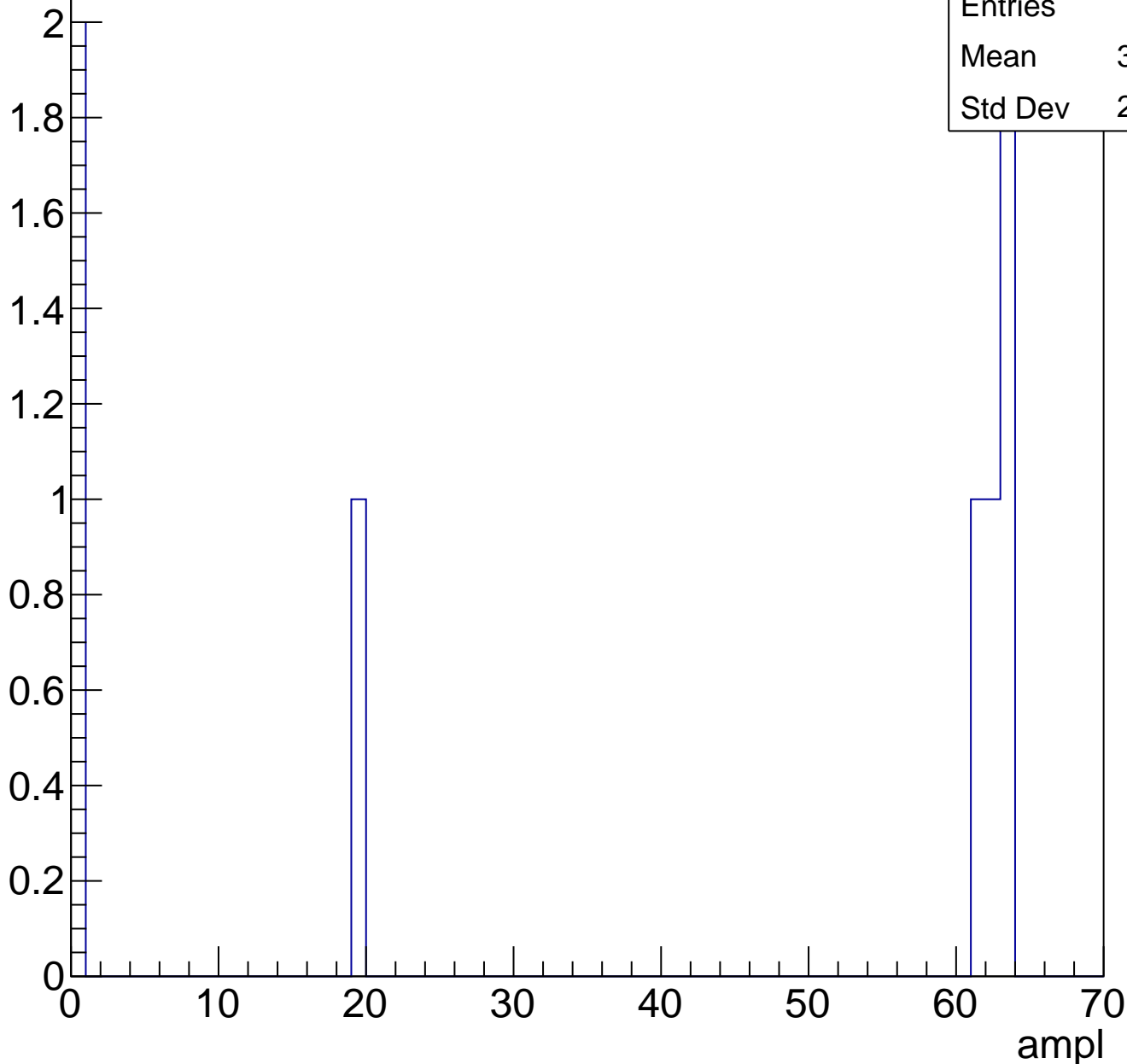




# B1L002S, U3-ch72, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch73, adc0

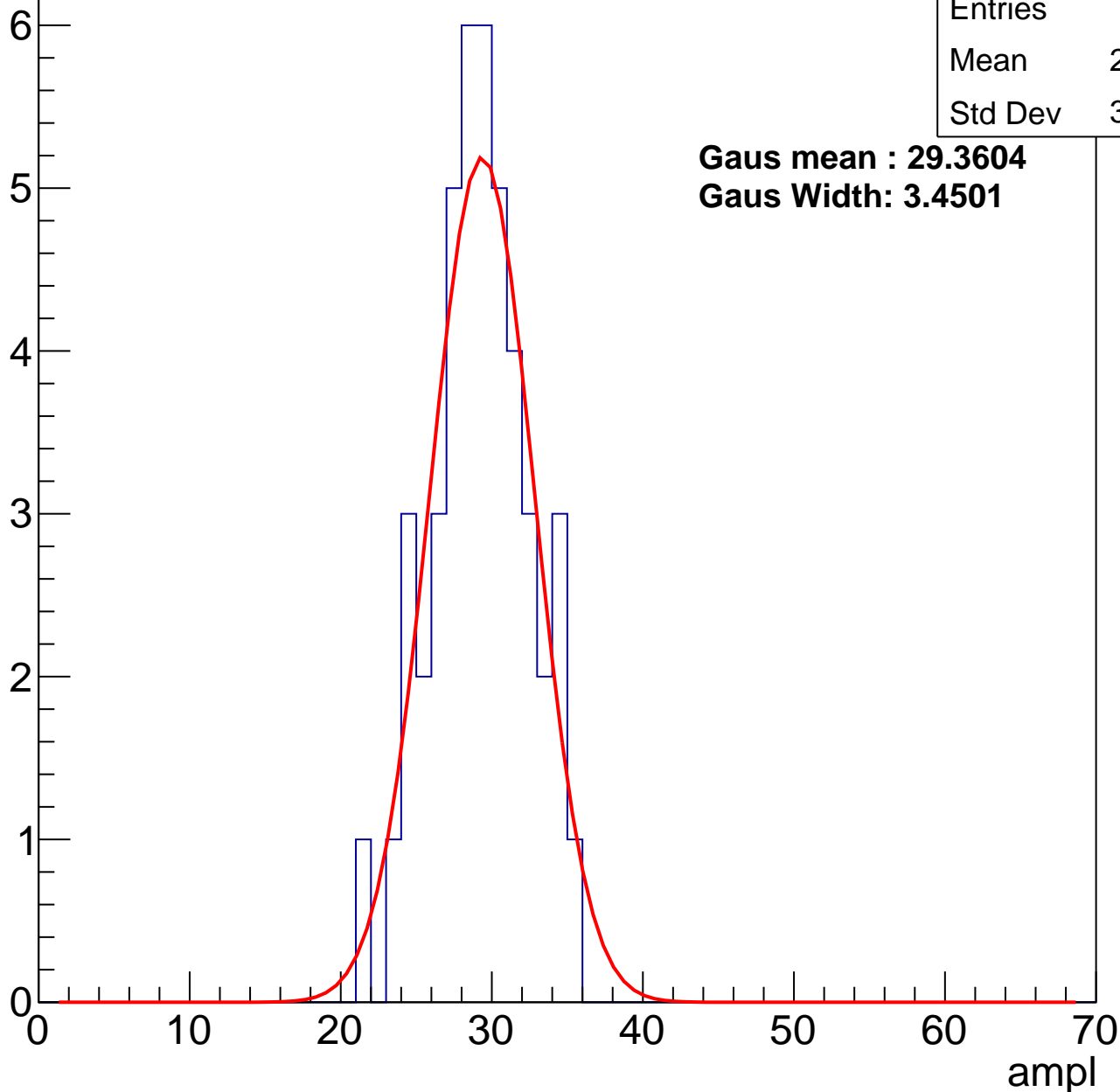
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	28.76
Std Dev	3.156

**Gaus mean : 29.3604**

**Gaus Width: 3.4501**



# B1L002S, U3-ch73, adc1

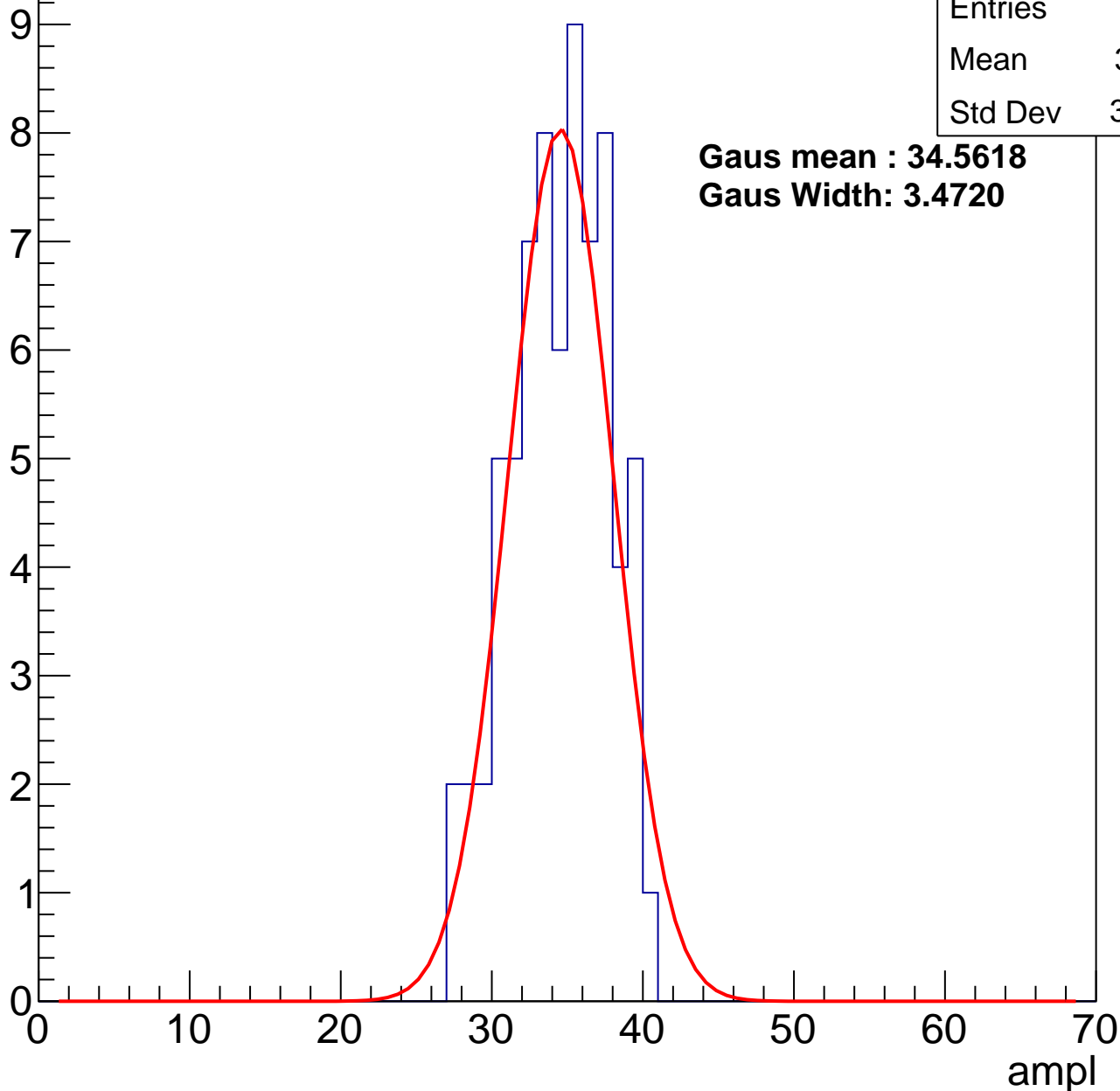
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.01
Std Dev	3.173

**Gaus mean : 34.5618**

**Gaus Width: 3.4720**



# B1L002S, U3-ch73, adc2

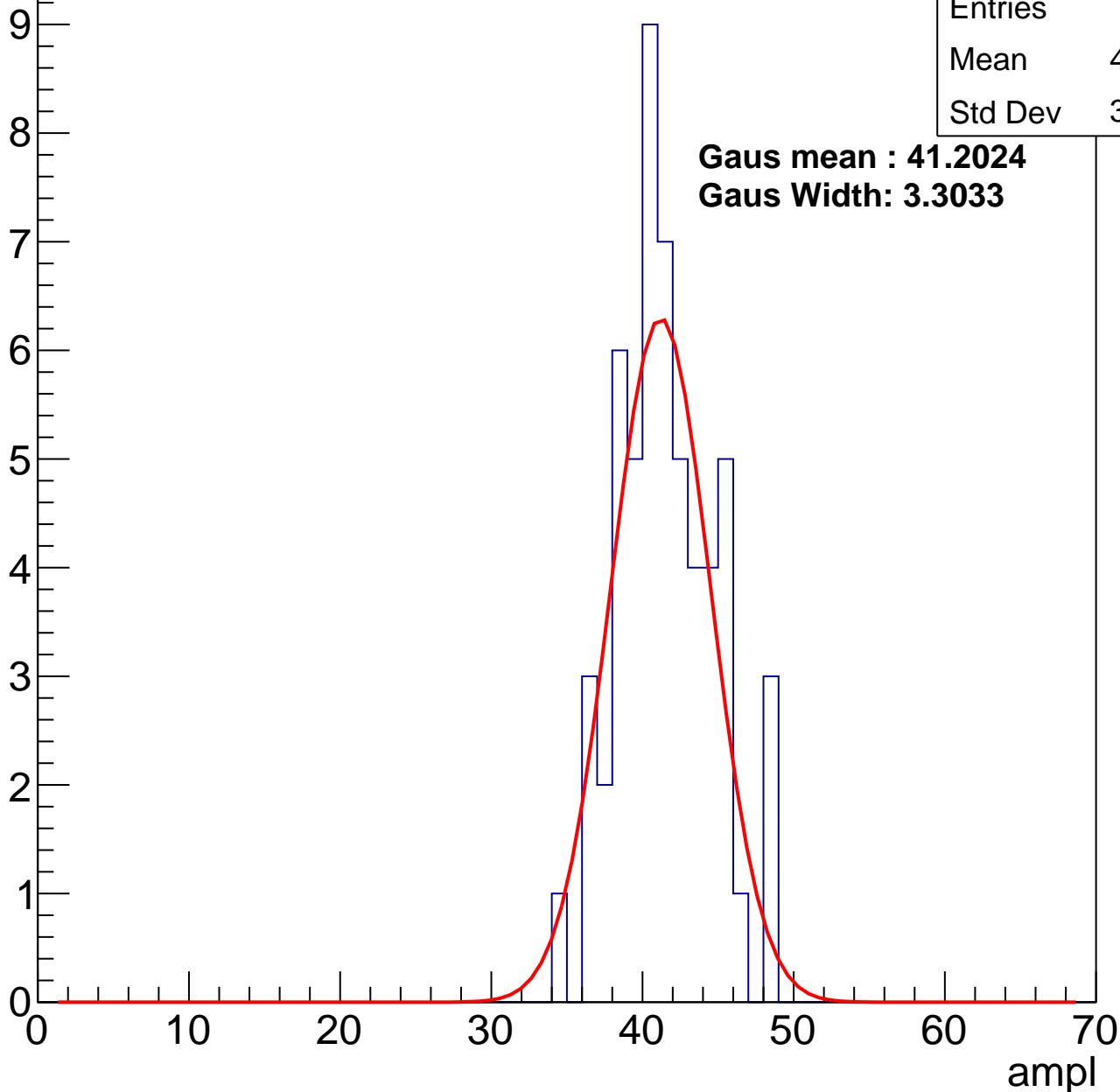
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	41.07
Std Dev	3.156

**Gaus mean : 41.2024**

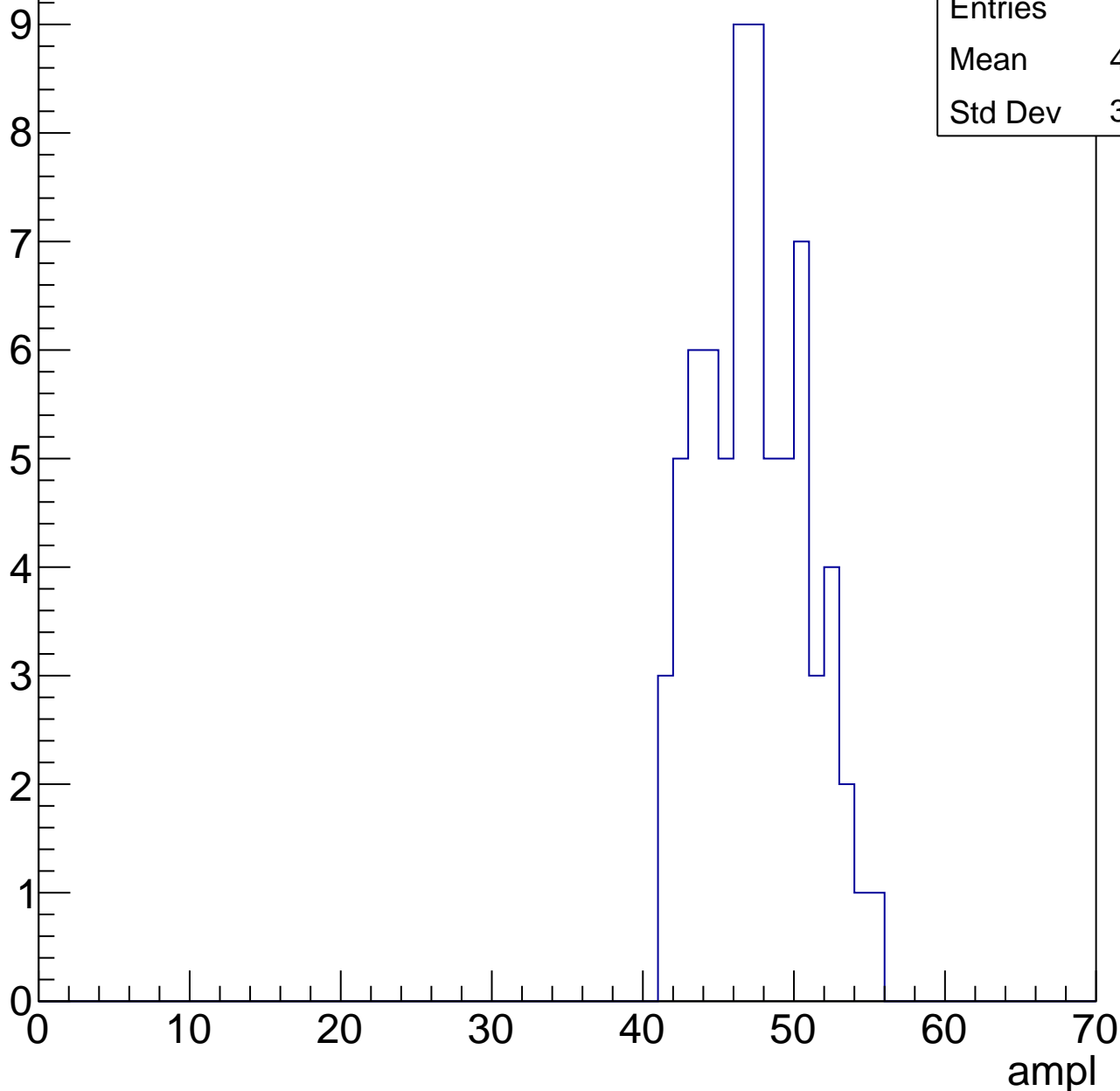
**Gaus Width: 3.3033**



# B1L002S, U3-ch73, adc3

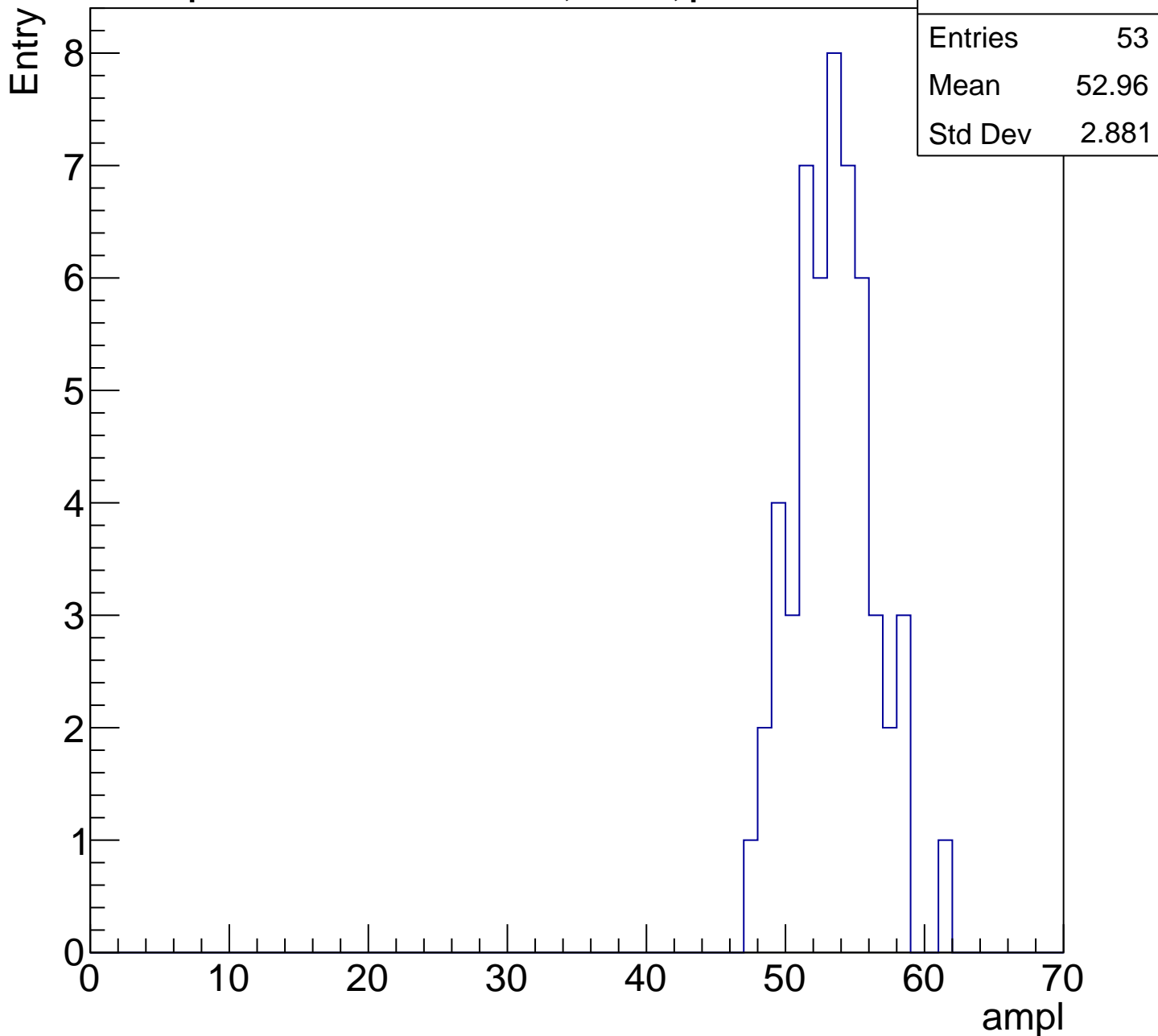
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch73, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch73, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	63
Mean	58.81
Std Dev	2.759

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8

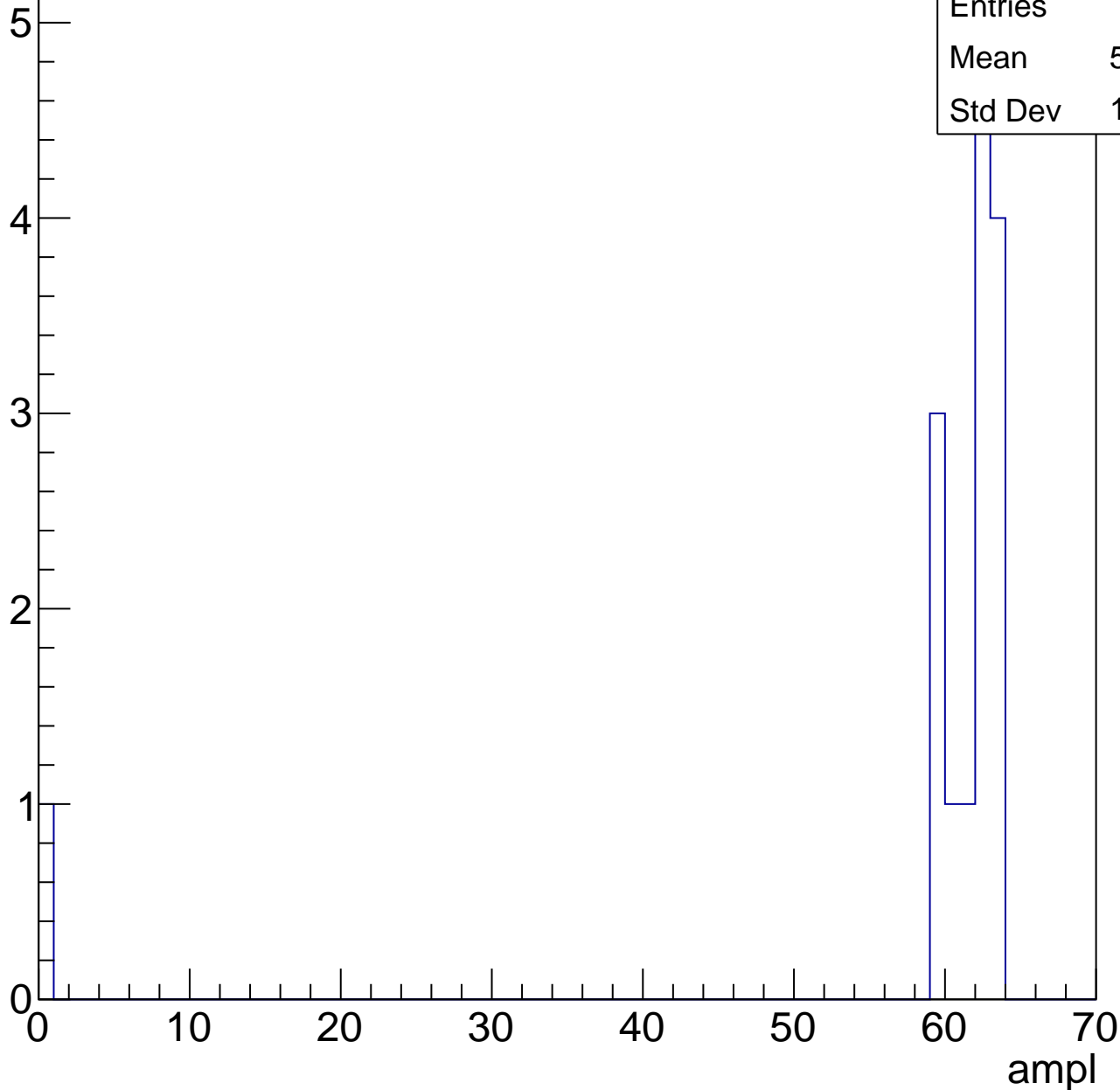
9

# B1L002S, U3-ch73, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	57.33
Std Dev	15.39





# B1L002S, U3-ch73, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U3-ch74, adc0

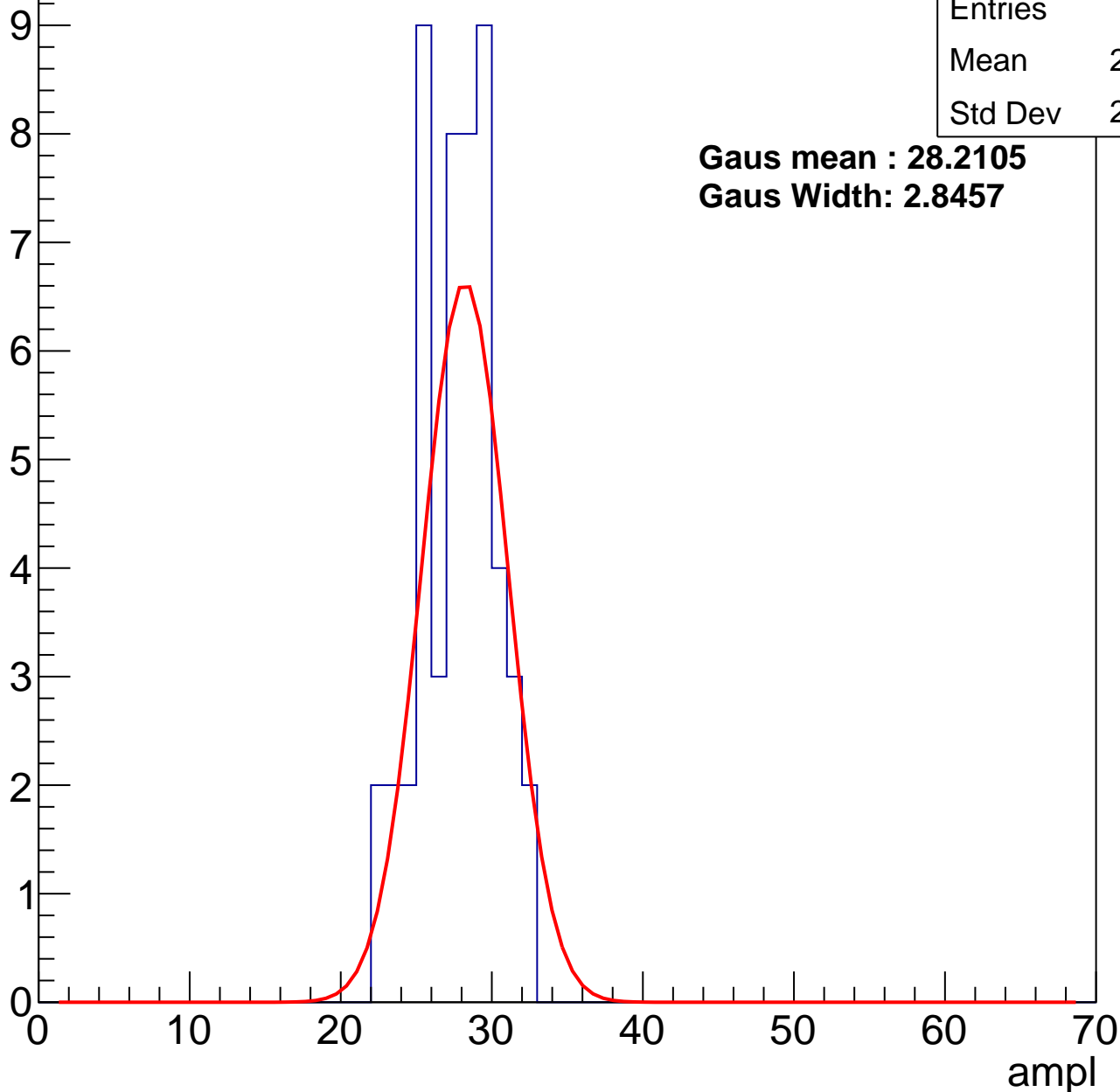
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	27.29
Std Dev	2.452

**Gaus mean : 28.2105**

**Gaus Width: 2.8457**



# B1L002S, U3-ch74, adc1

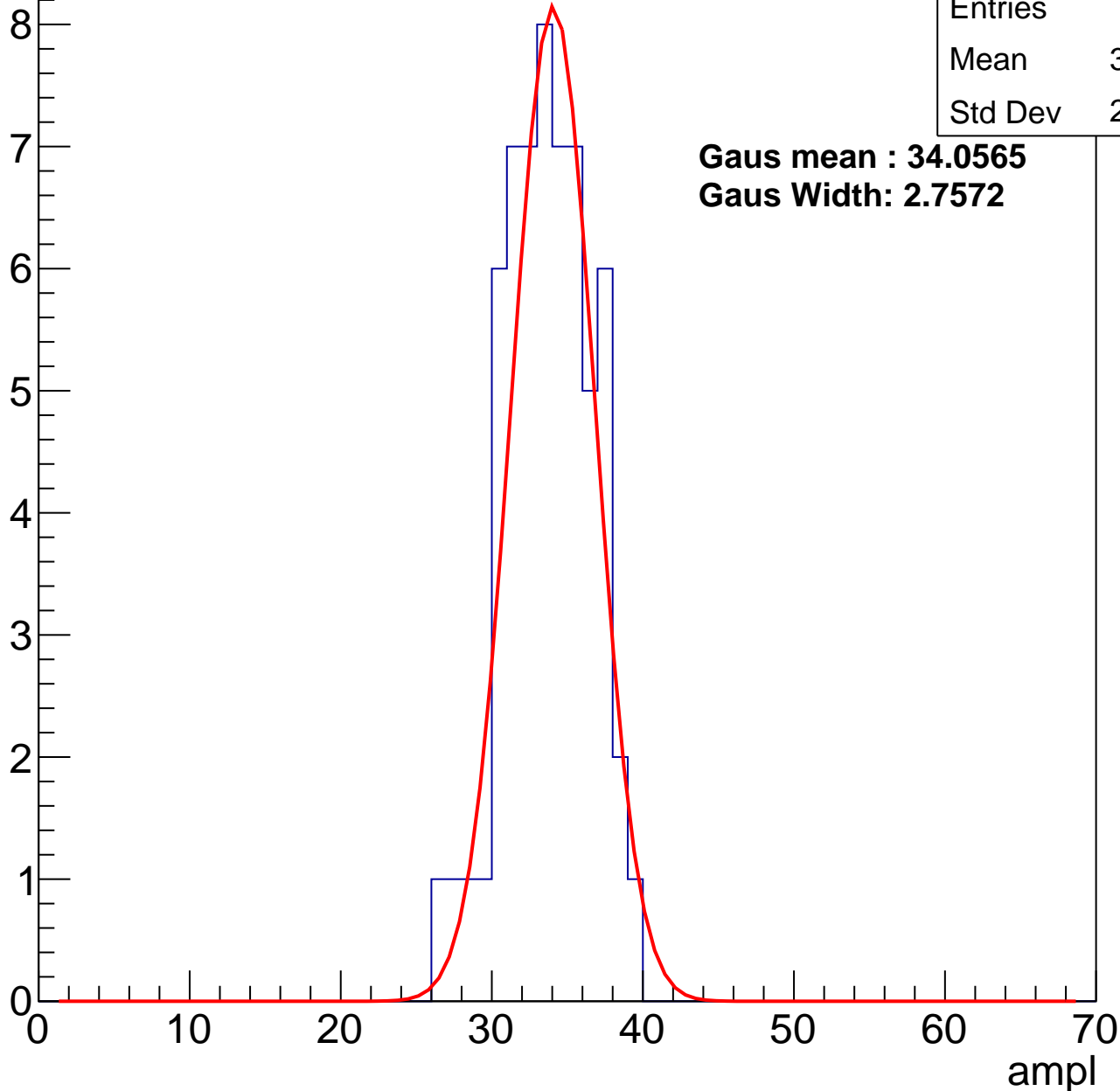
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.25
Std Dev	2.808

**Gaus mean : 34.0565**

**Gaus Width: 2.7572**



# B1L002S, U3-ch74, adc2

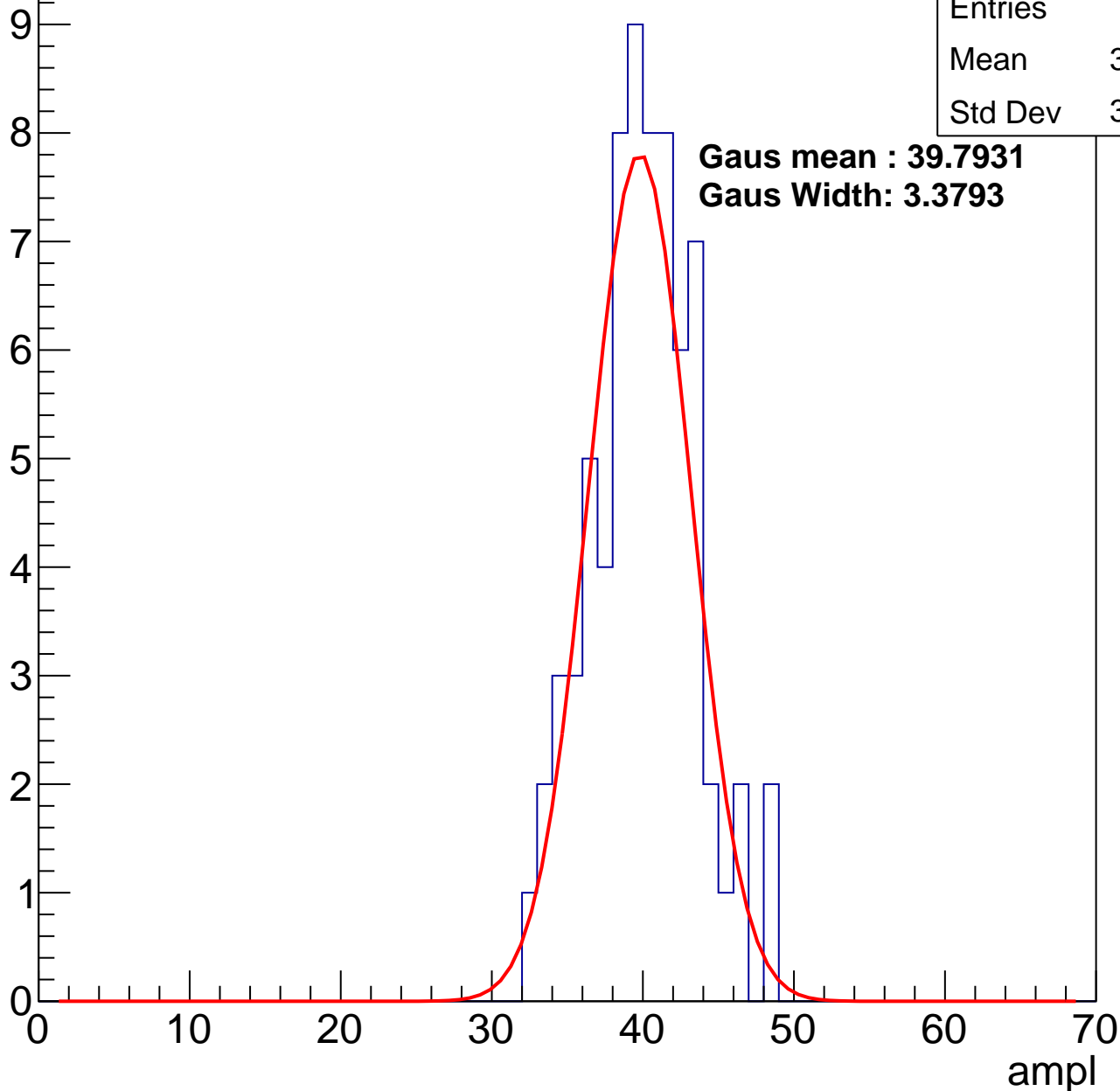
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	39.58
Std Dev	3.442

**Gaus mean : 39.7931**

**Gaus Width: 3.3793**

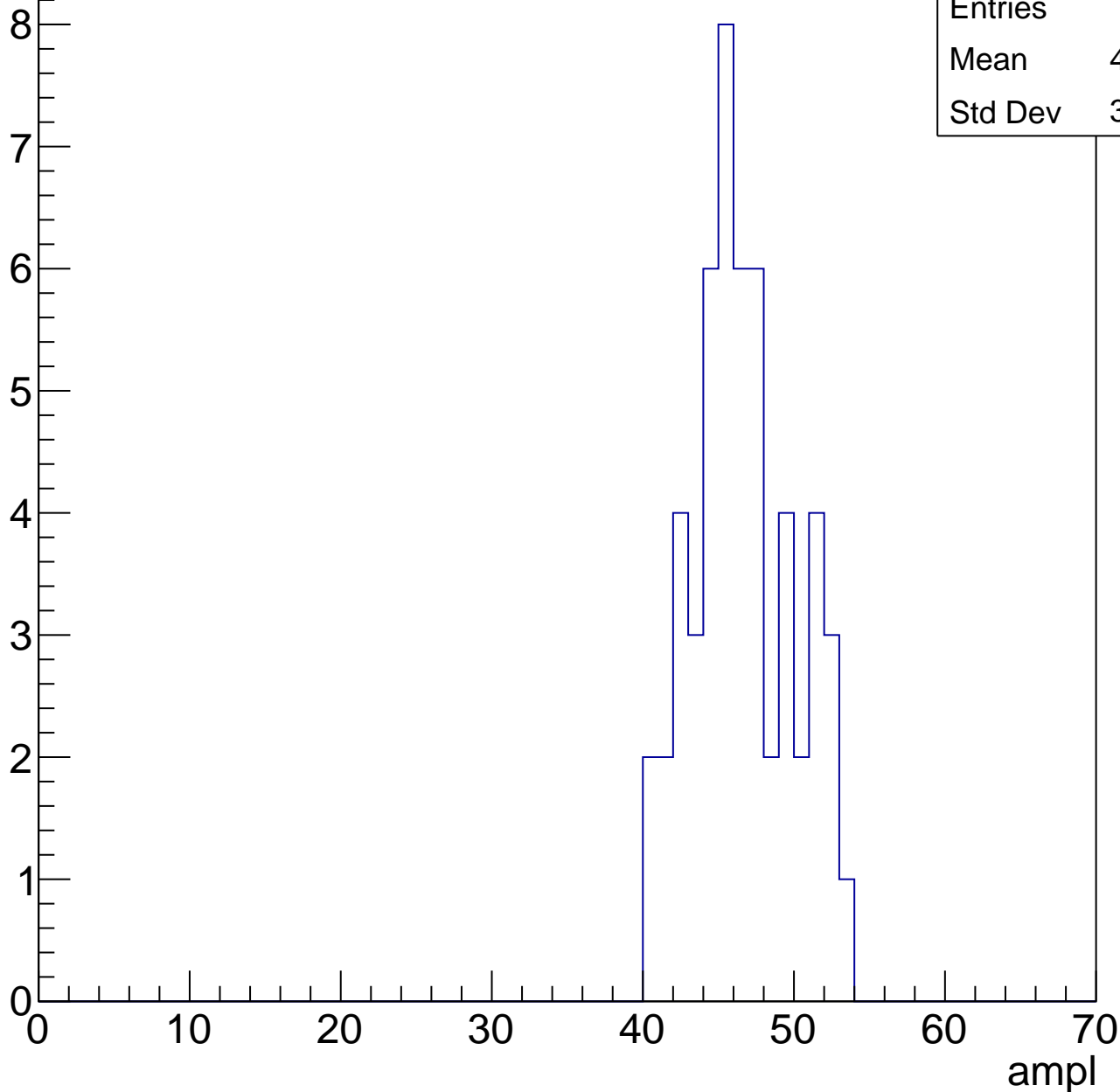


# B1L002S, U3-ch74, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

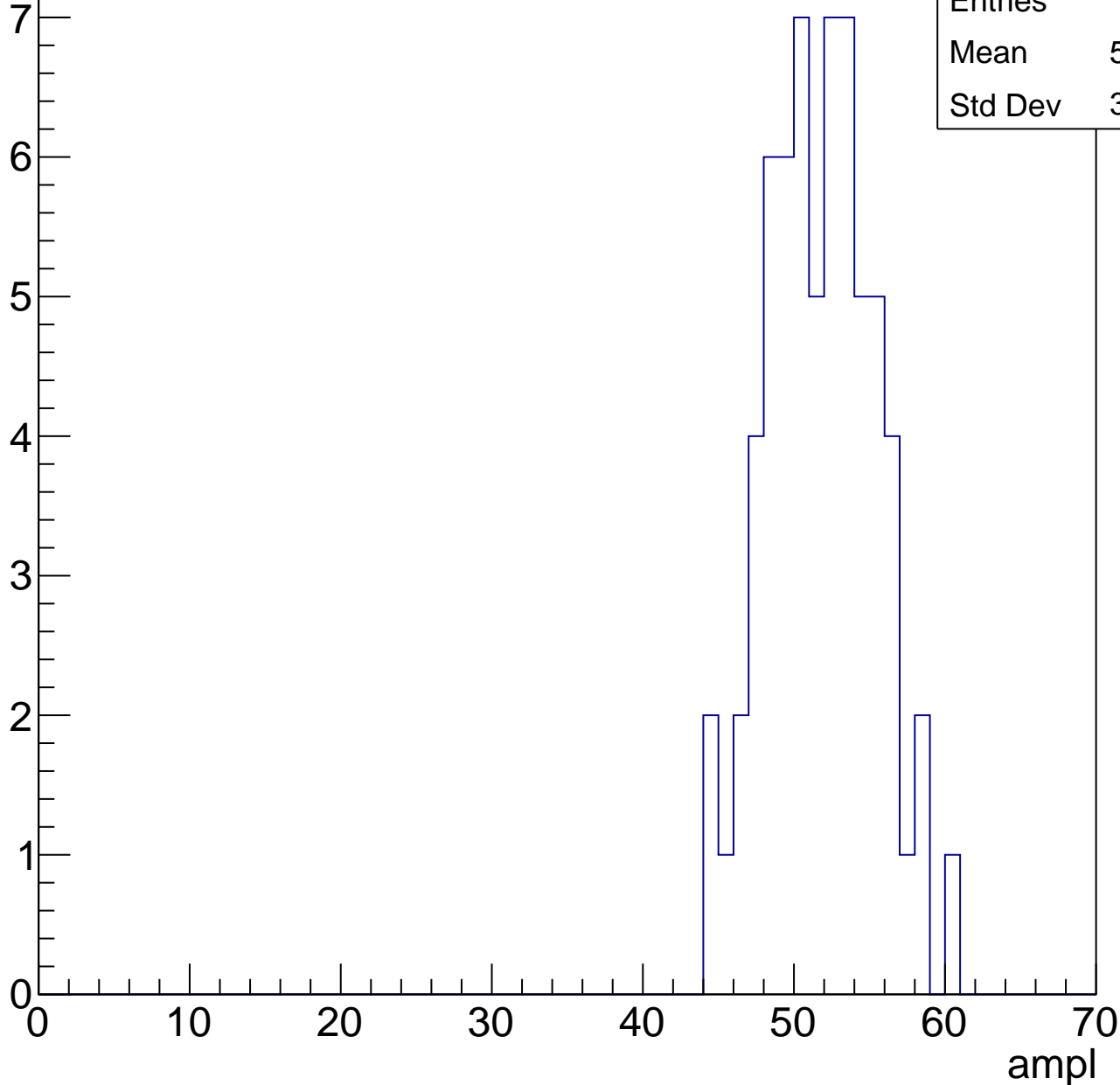
Entries	53
Mean	46.15
Std Dev	3.316



# B1L002S, U3-ch74, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

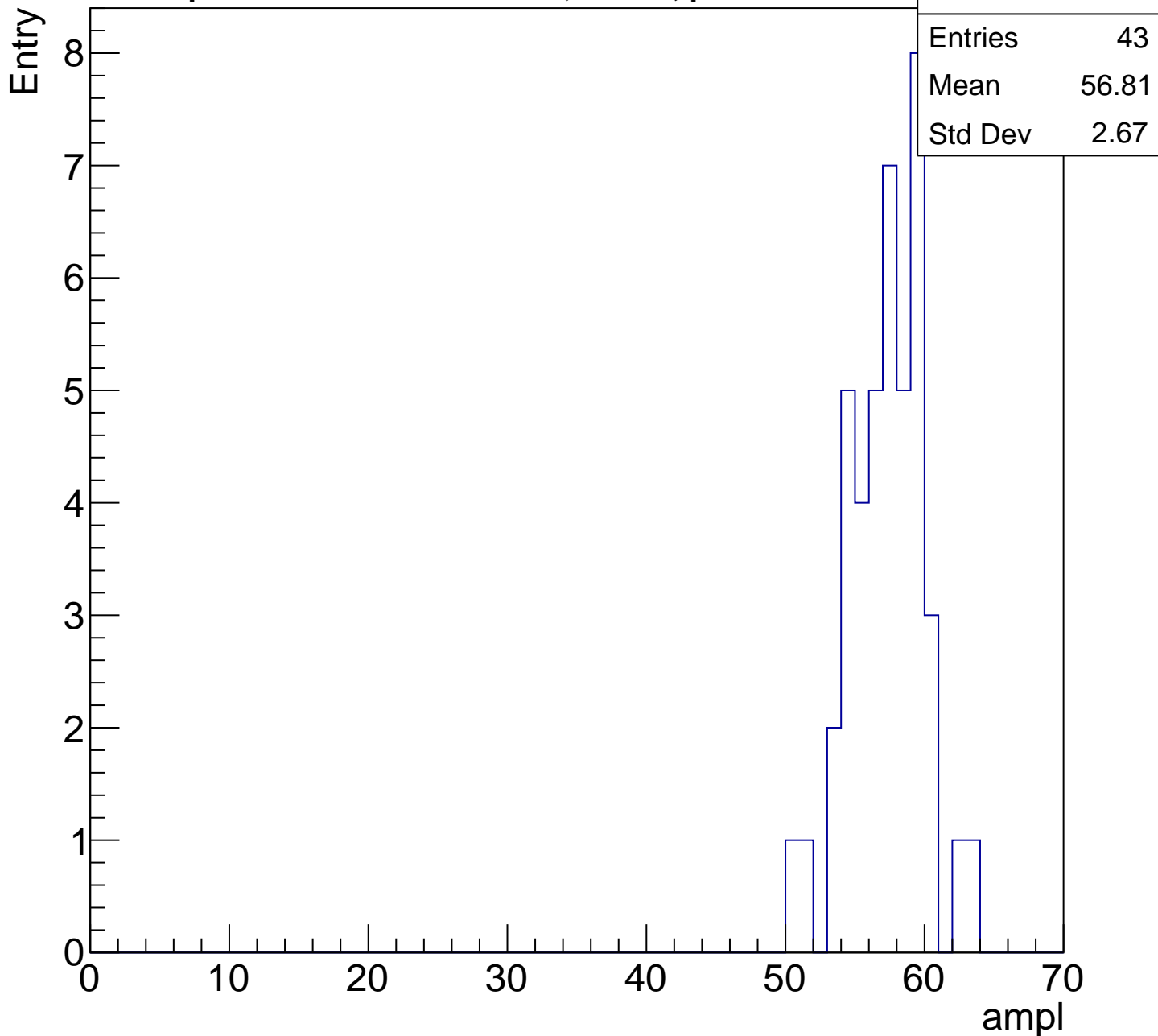
Entry



Entries	65
Mean	51.34
Std Dev	3.509

# B1L002S, U3-ch74, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

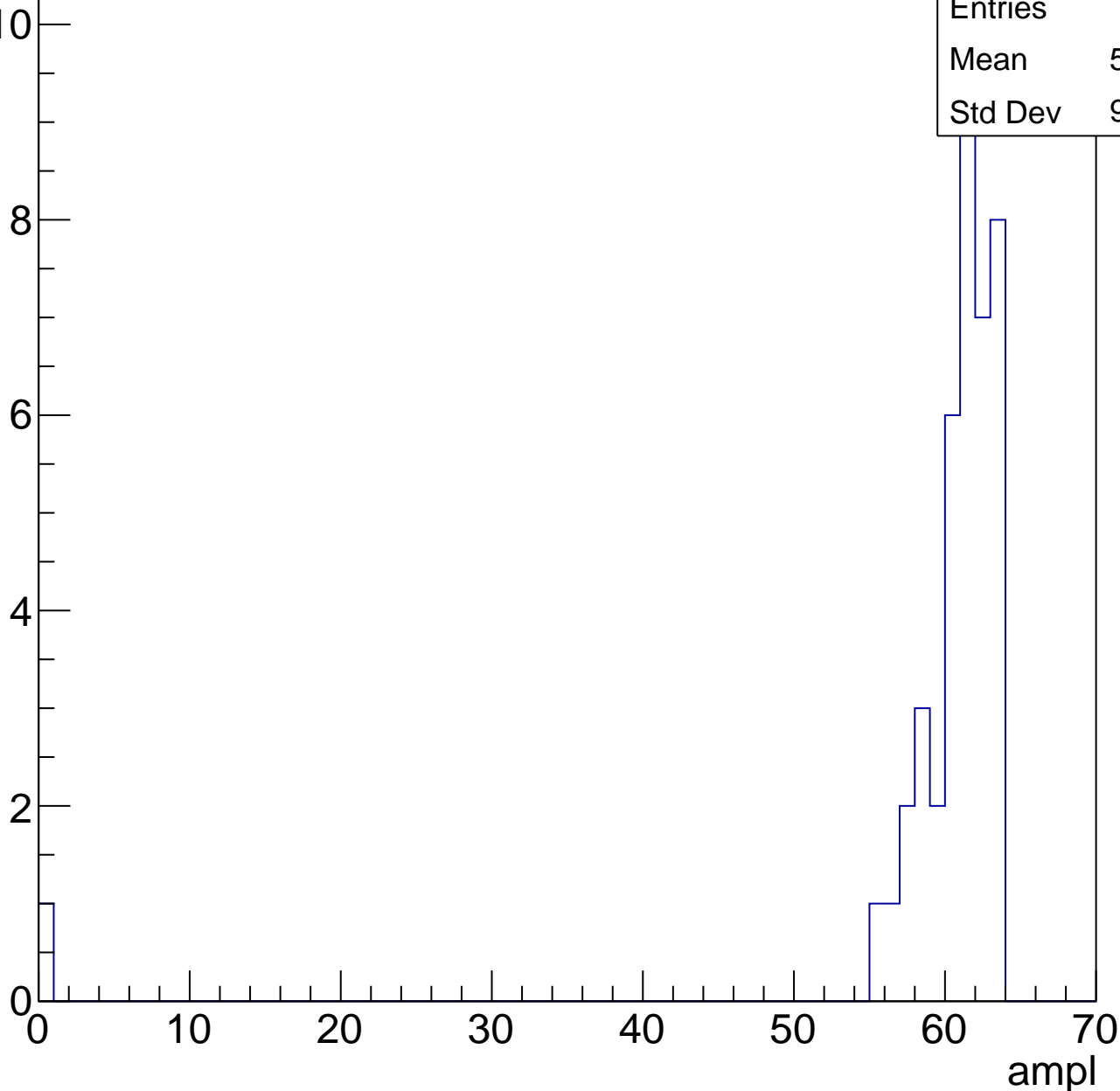


# B1L002S, U3-ch74, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	59.15
Std Dev	9.568

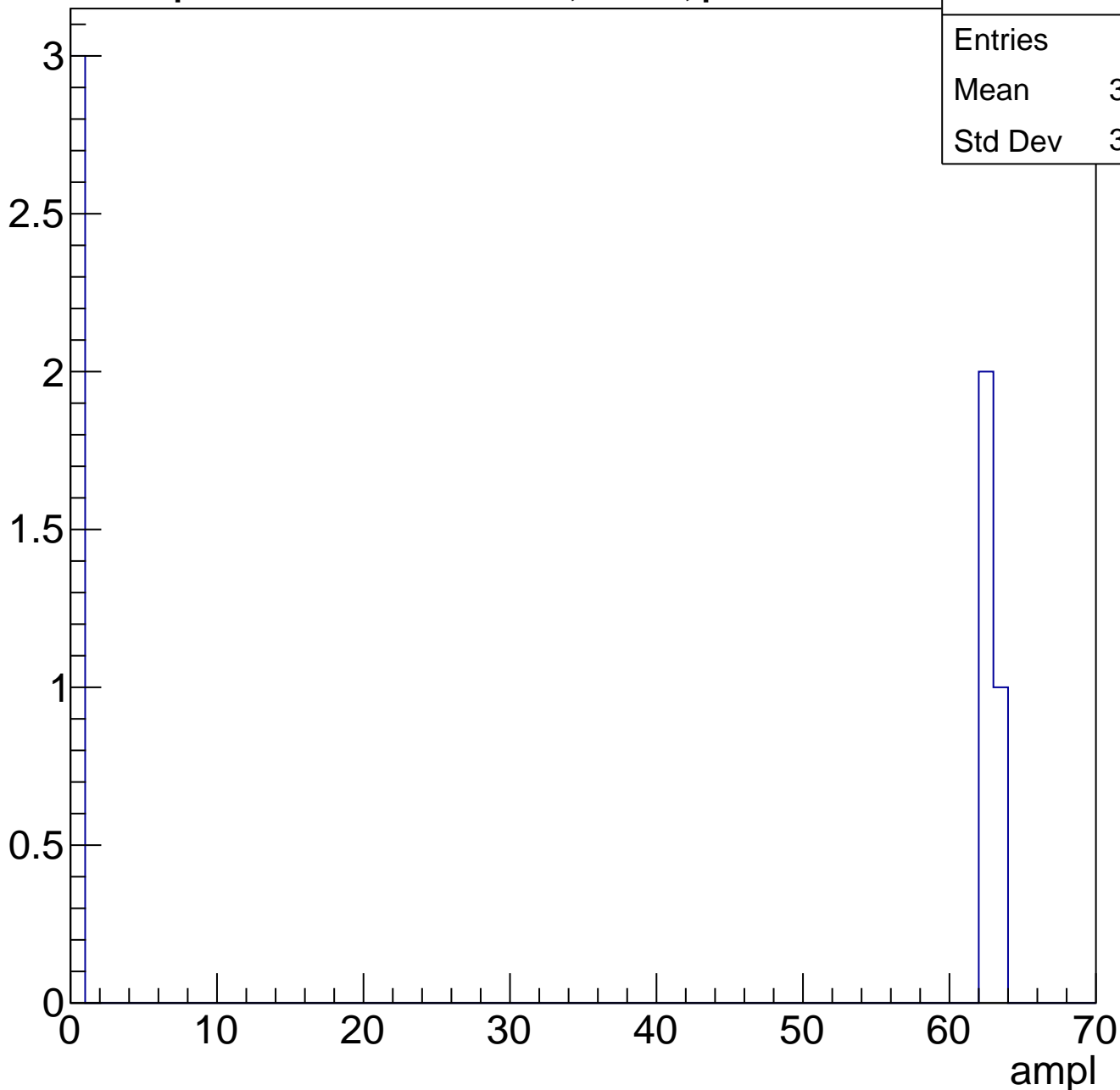




# B1L002S, U3-ch74, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	31.17
Std Dev	31.17

# B1L002S, U3-ch75, adc0

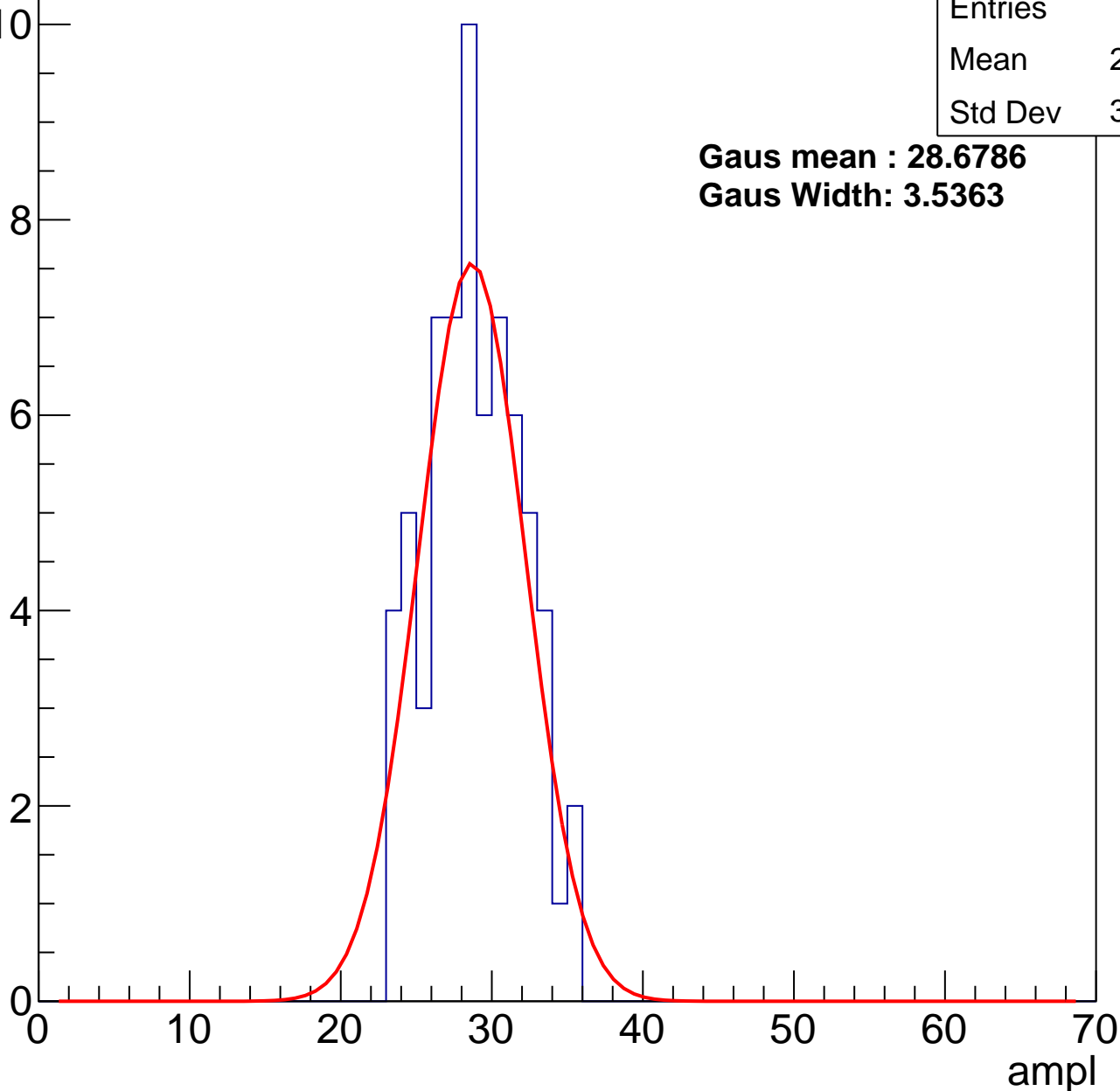
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	28.42
Std Dev	3.072

**Gaus mean : 28.6786**

**Gaus Width: 3.5363**



# B1L002S, U3-ch75, adc1

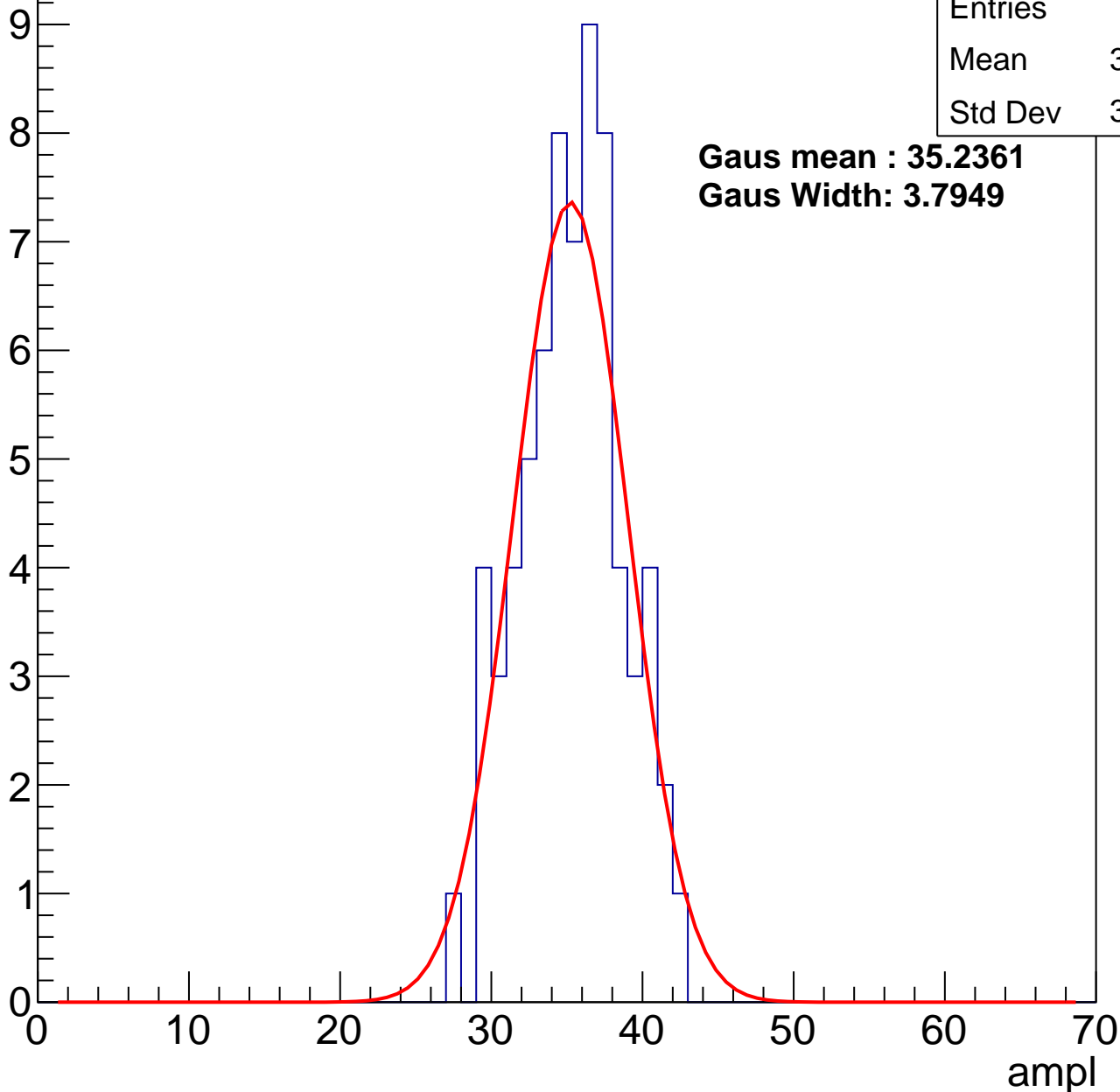
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	34.86
Std Dev	3.337

**Gaus mean : 35.2361**

**Gaus Width: 3.7949**



# B1L002S, U3-ch75, adc2

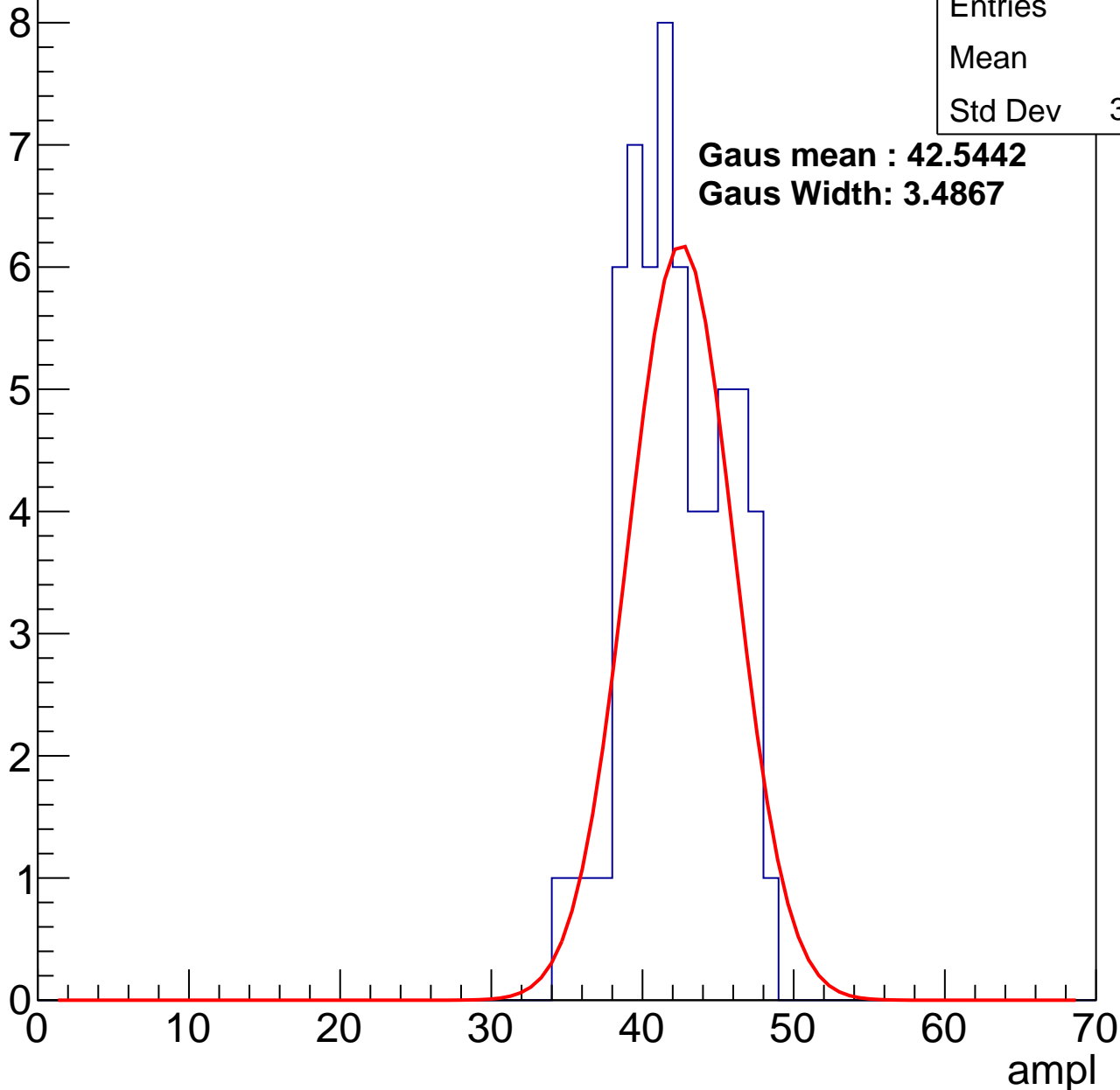
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	41.7
Std Dev	3.268

**Gaus mean : 42.5442**

**Gaus Width: 3.4867**

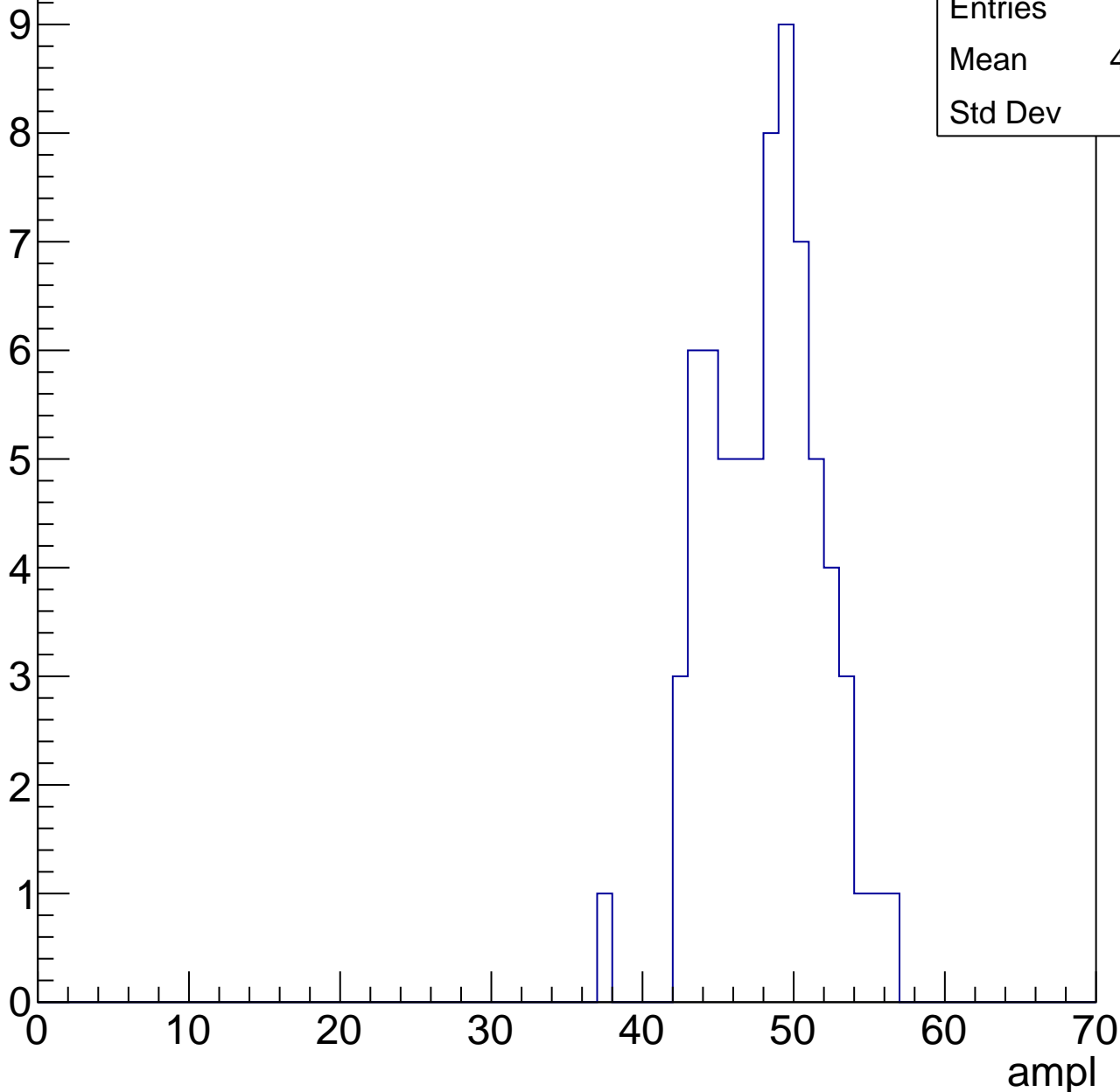


# B1L002S, U3-ch75, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

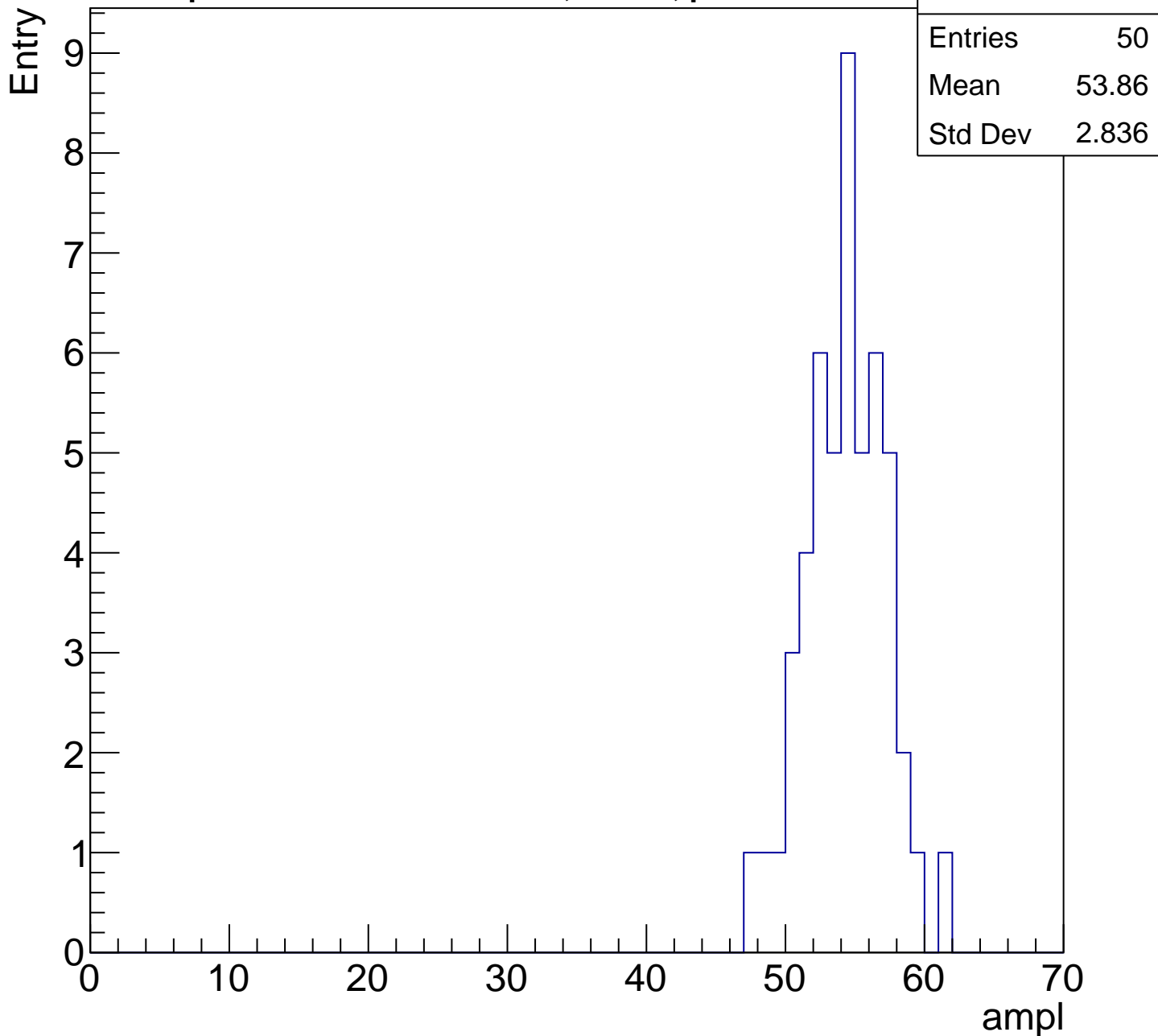
Entry

Entries	70
Mean	47.67
Std Dev	3.6



# B1L002S, U3-ch75, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

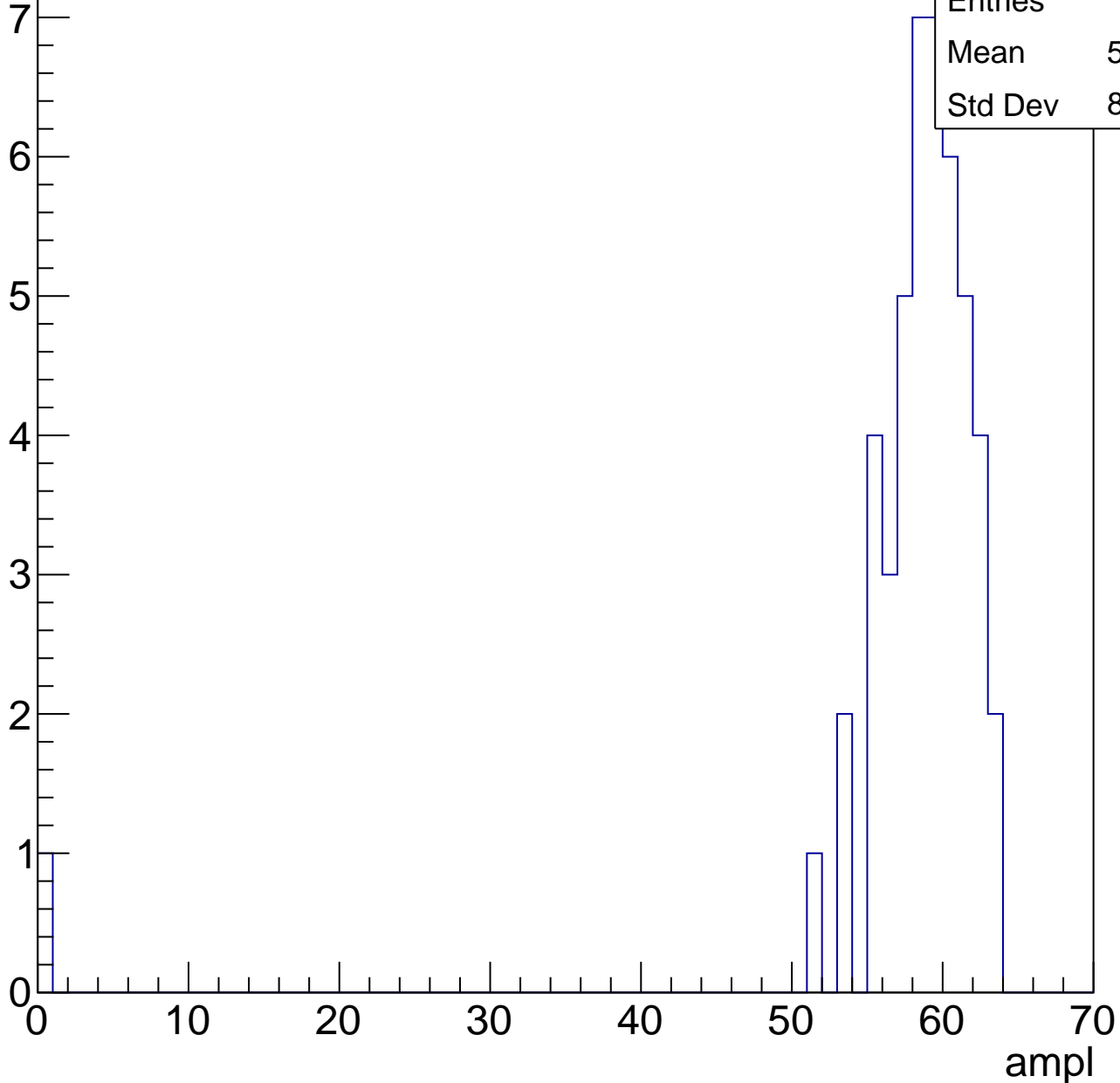


# B1L002S, U3-ch75, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

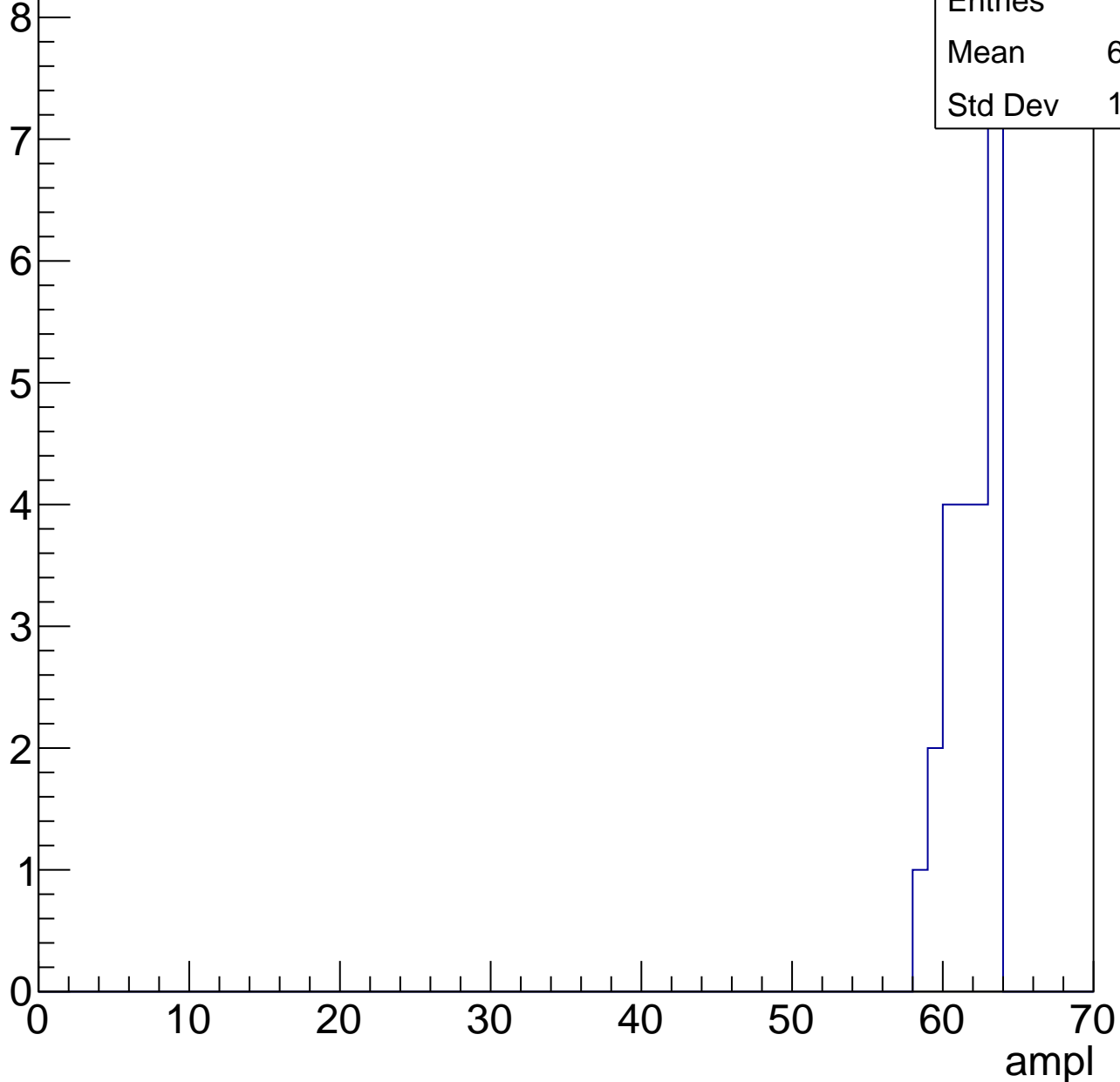
Entries	47
Mean	57.19
Std Dev	8.843



# B1L002S, U3-ch75, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

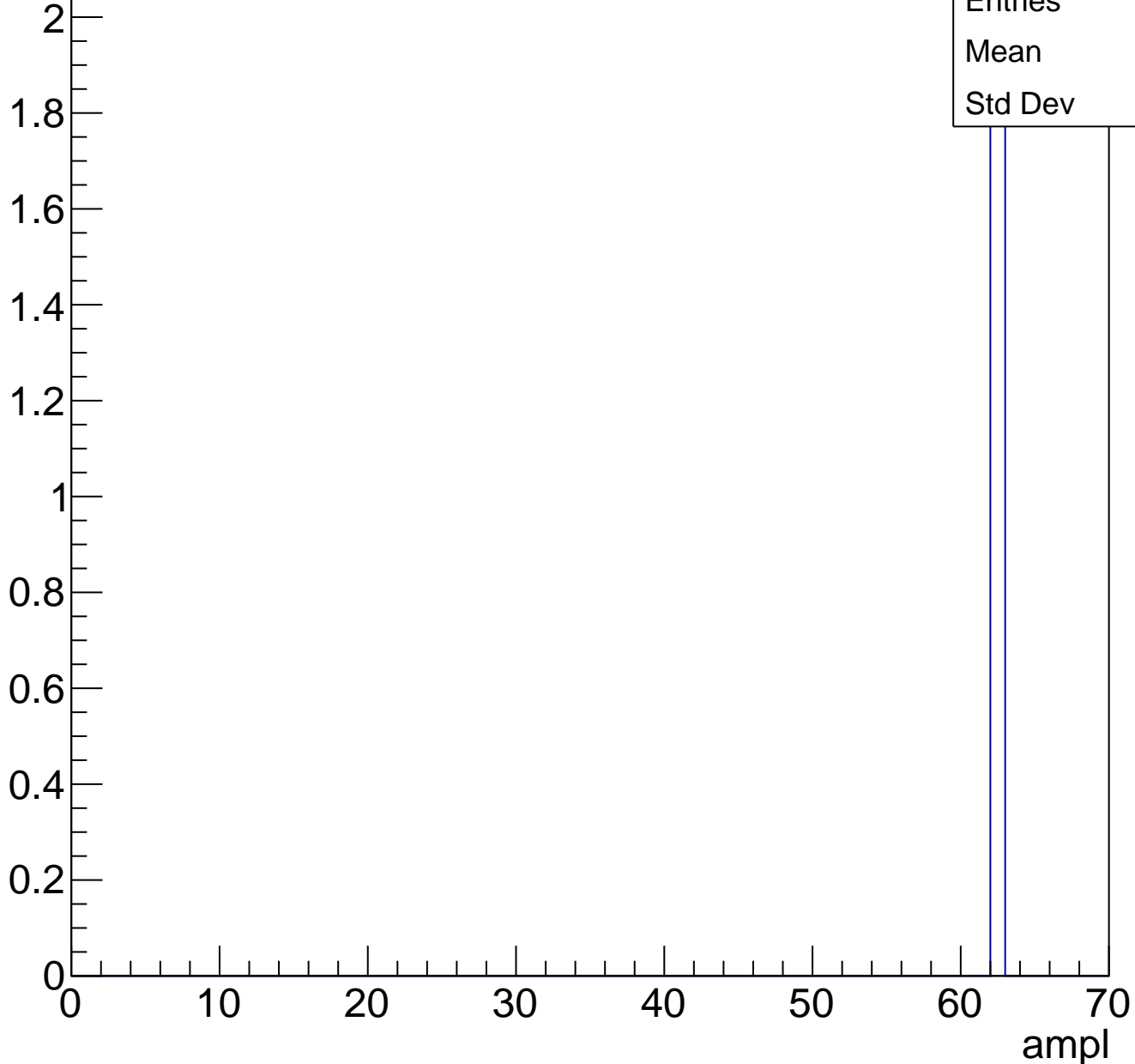




# B1L002S, U3-ch75, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch76, adc0

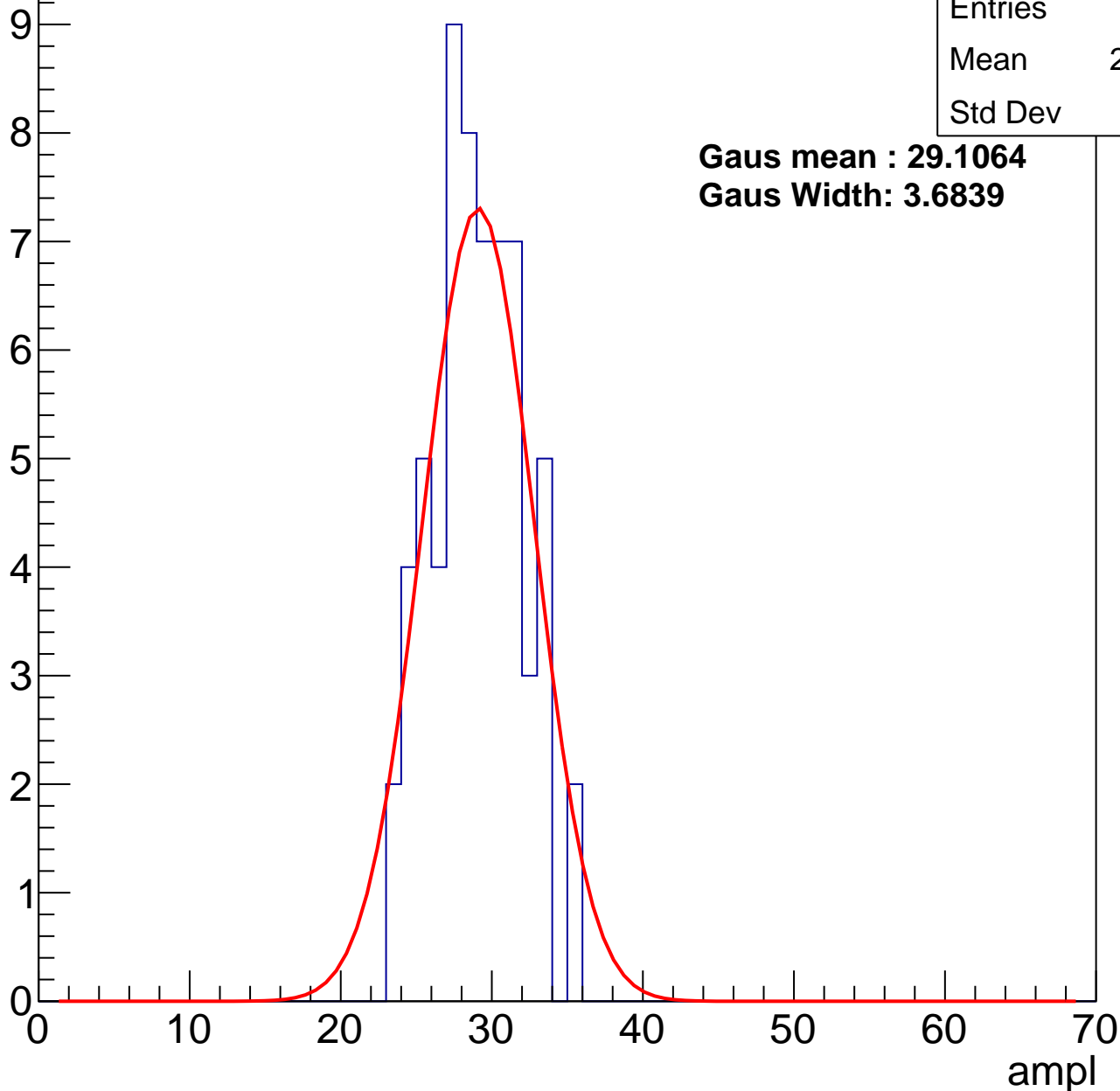
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.56
Std Dev	2.91

**Gaus mean : 29.1064**

**Gaus Width: 3.6839**



# B1L002S, U3-ch76, adc1

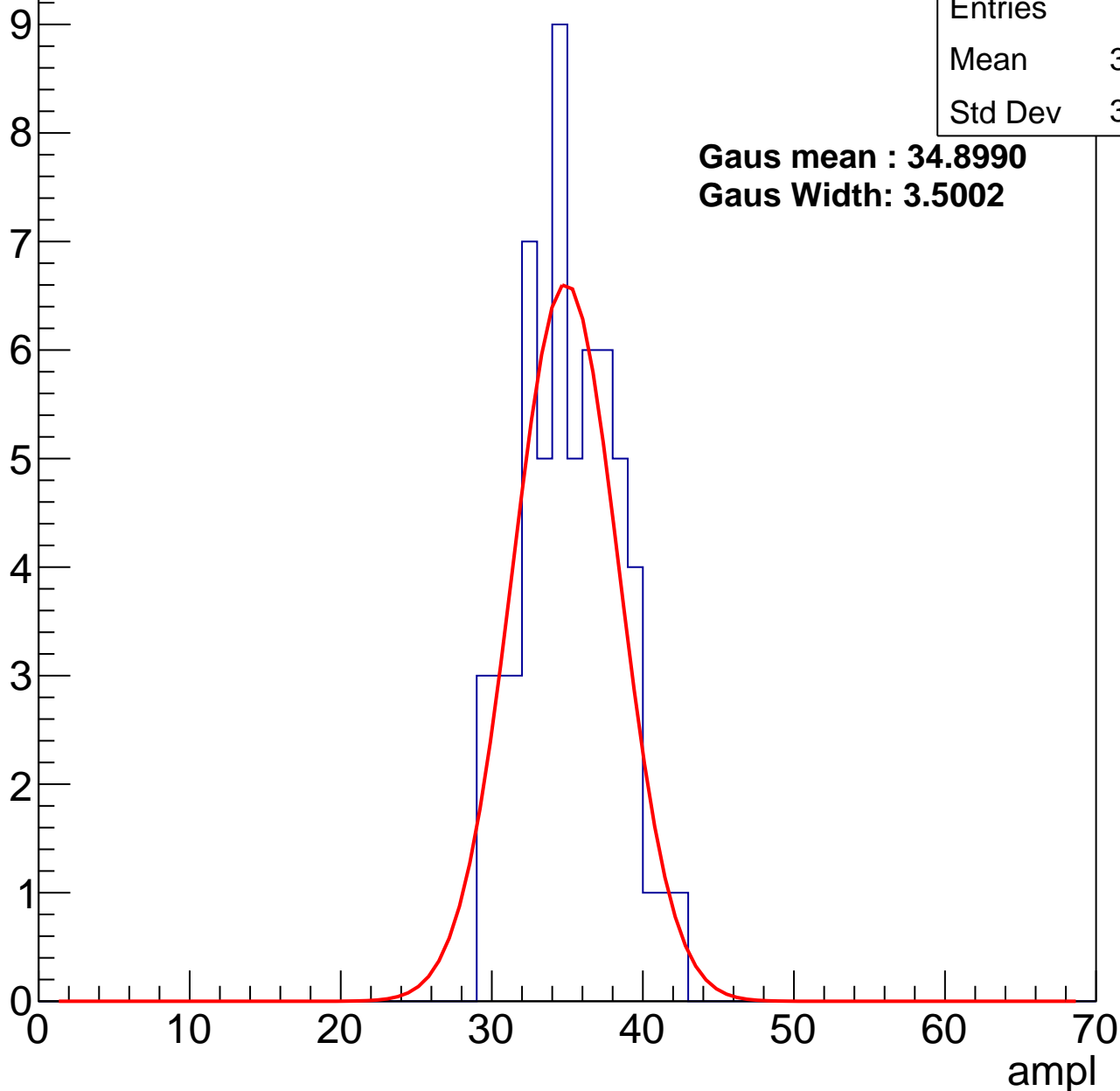
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.69
Std Dev	3.099

**Gaus mean : 34.8990**

**Gaus Width: 3.5002**



# B1L002S, U3-ch76, adc2

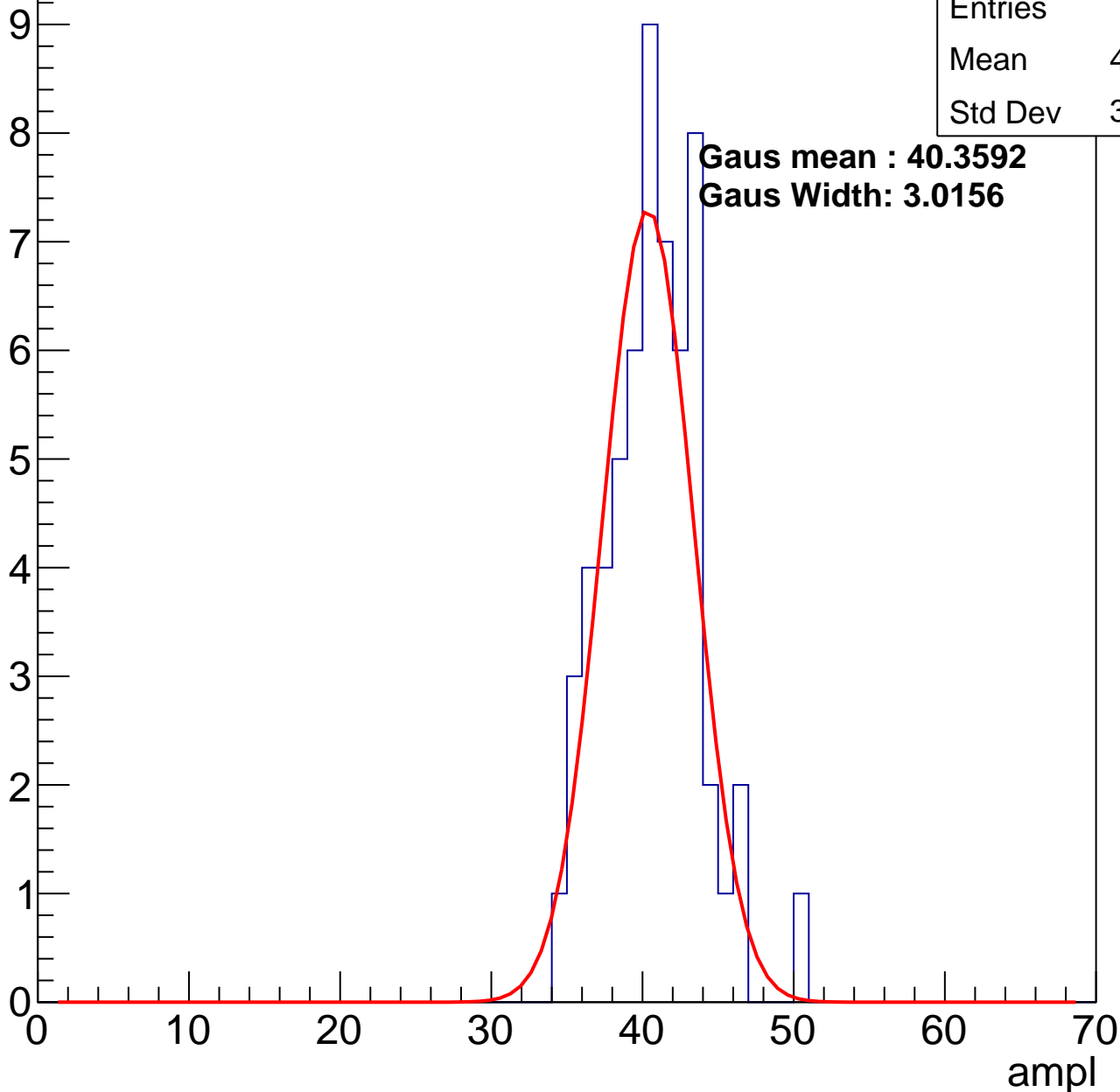
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.22
Std Dev	3.098

**Gaus mean : 40.3592**

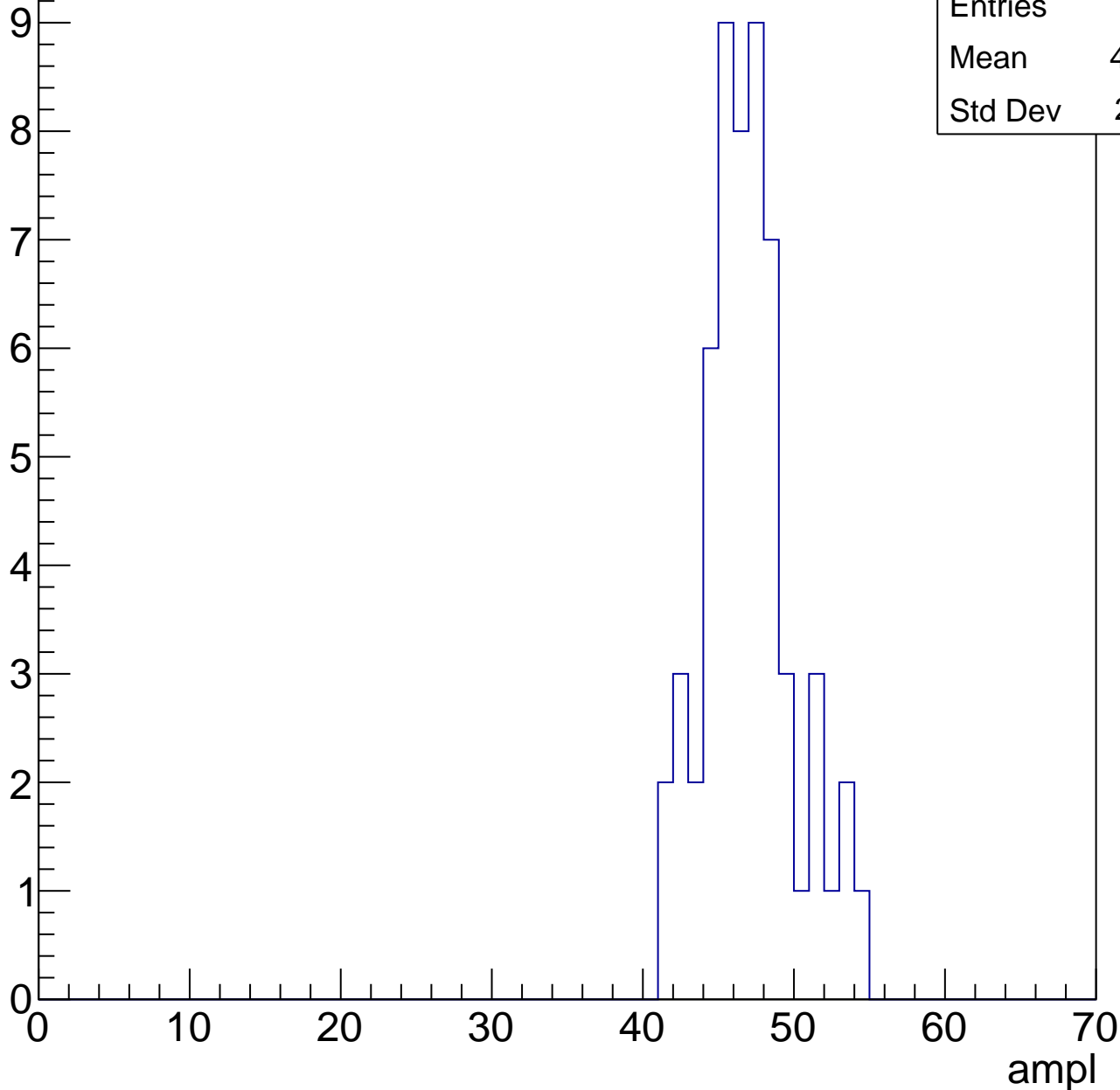
**Gaus Width: 3.0156**



# B1L002S, U3-ch76, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	57
Mean	46.53
Std Dev	2.921

# B1L002S, U3-ch76, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	53
Mean	51.58
Std Dev	2.942

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

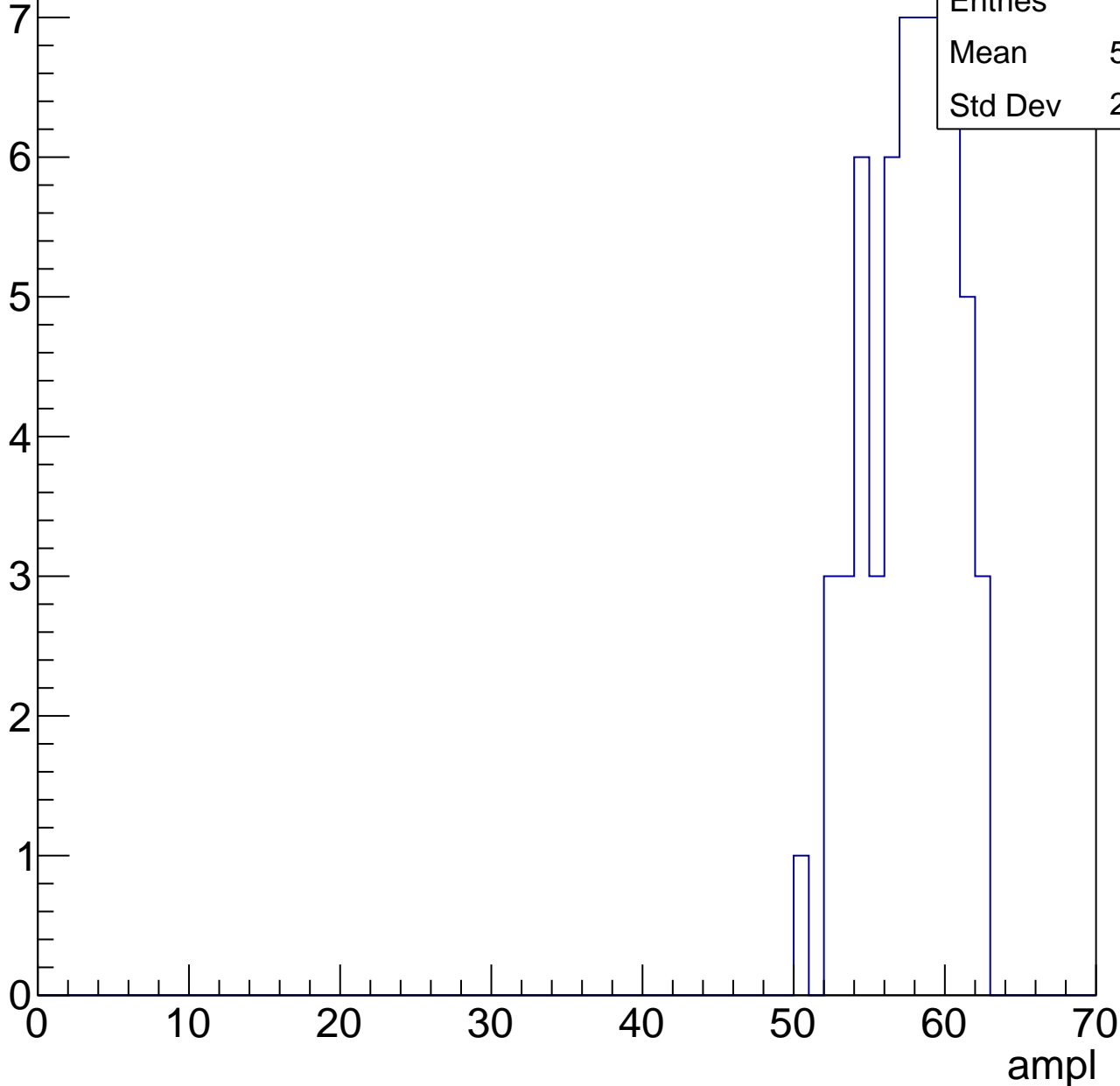
7

# B1L002S, U3-ch76, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.22
Std Dev	2.919

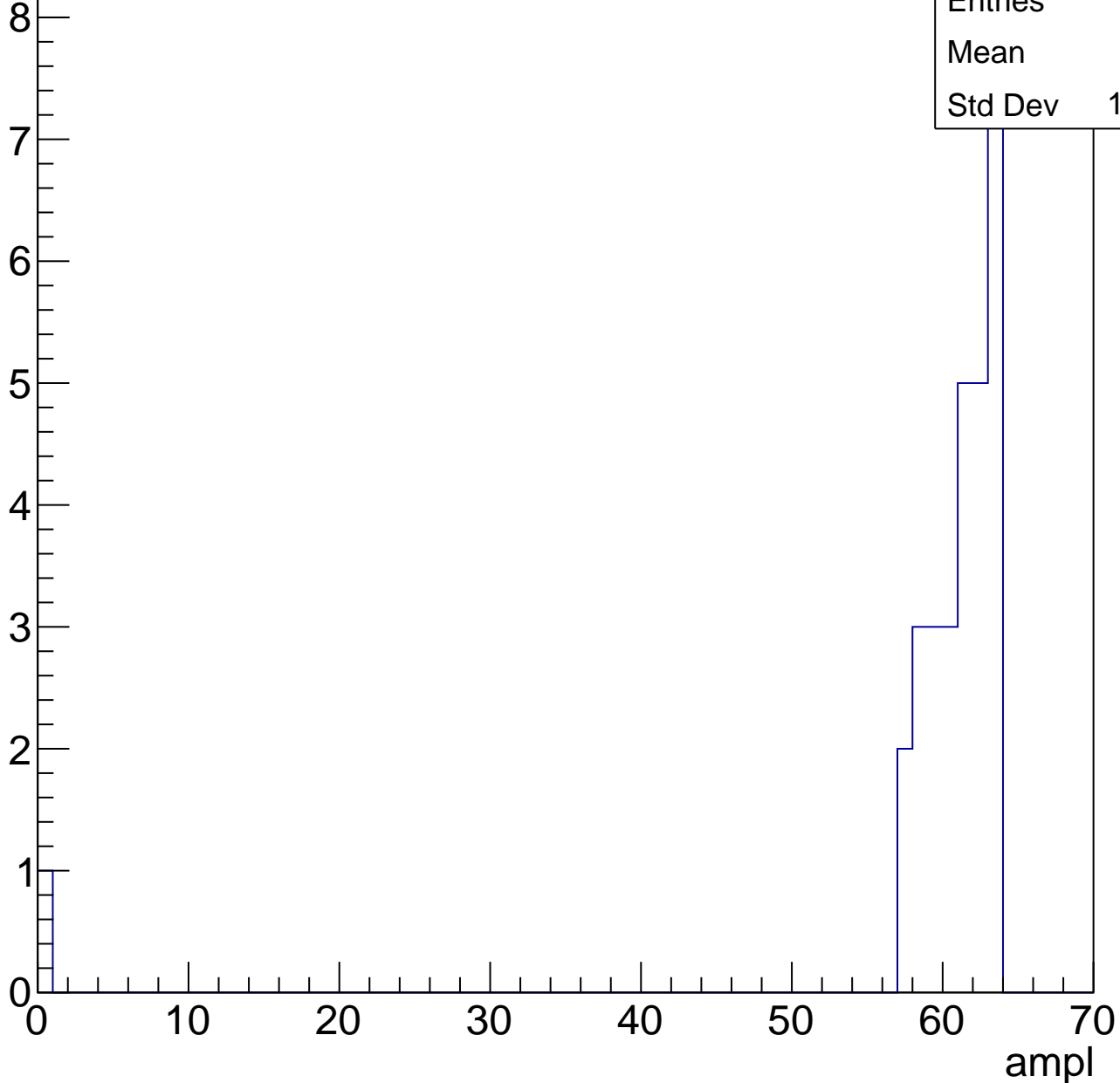


# B1L002S, U3-ch76, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.8
Std Dev	11.09

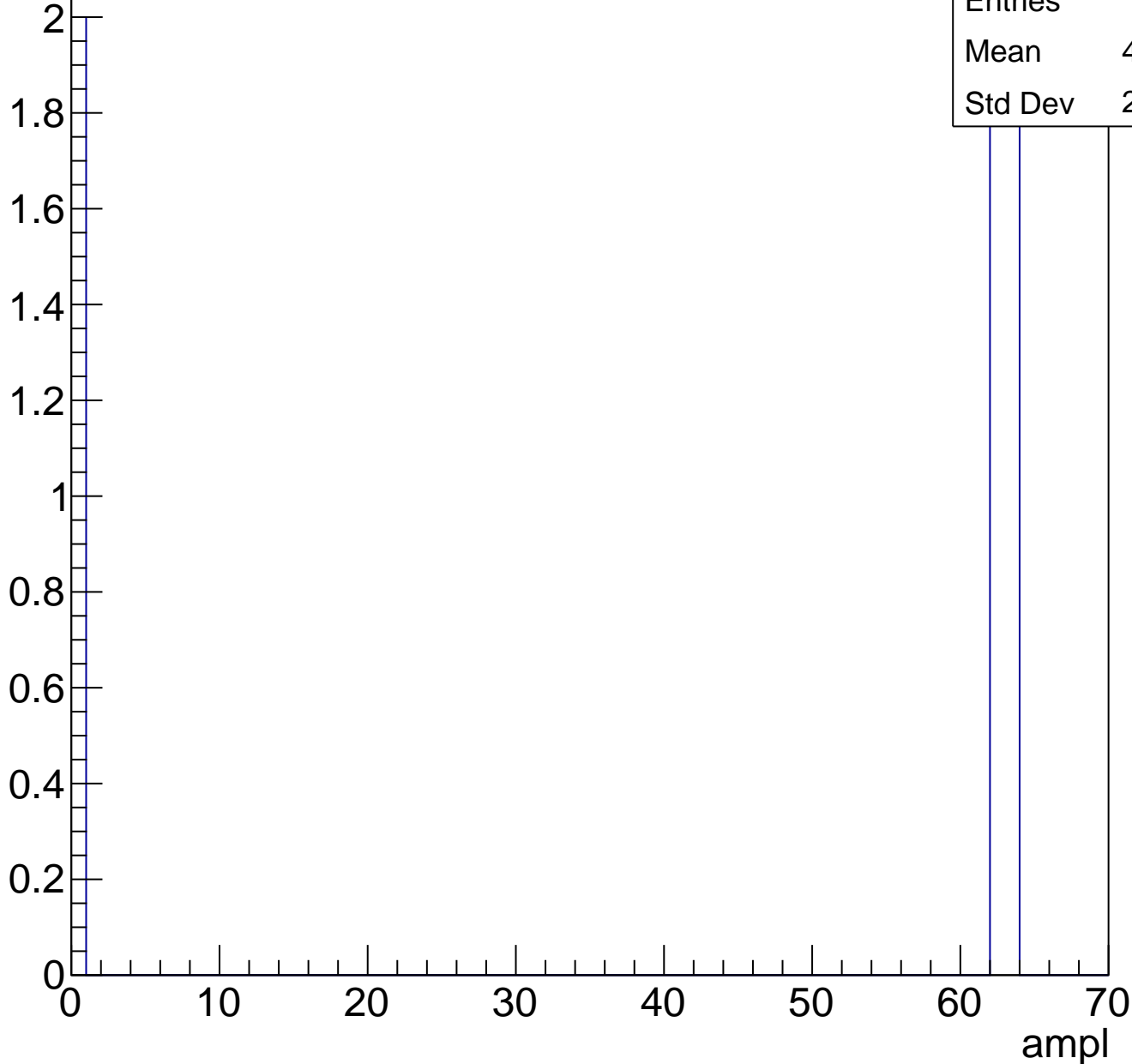




# B1L002S, U3-ch76, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch77, adc0

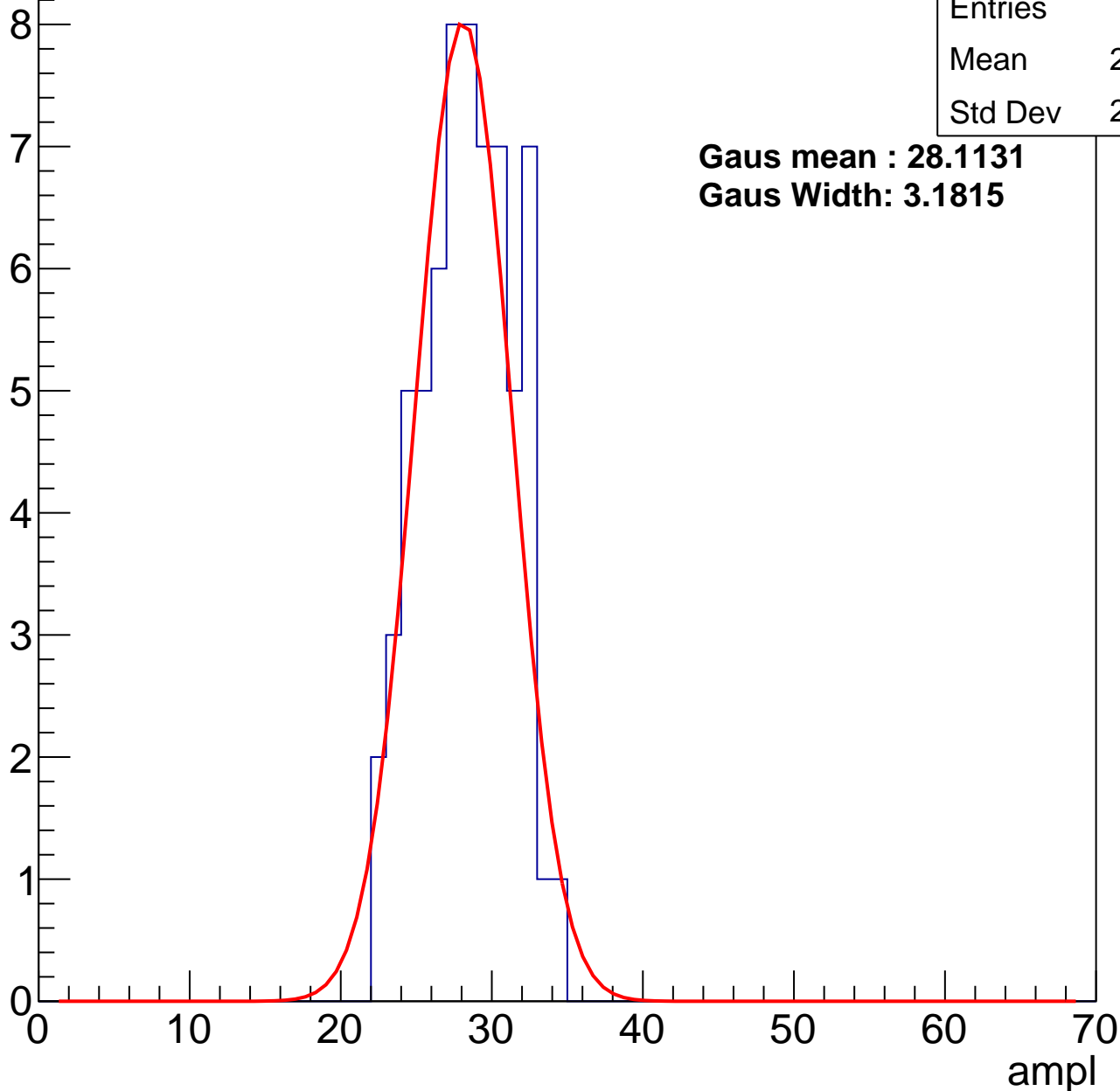
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	27.89
Std Dev	2.925

**Gaus mean : 28.1131**

**Gaus Width: 3.1815**



# B1L002S, U3-ch77, adc1

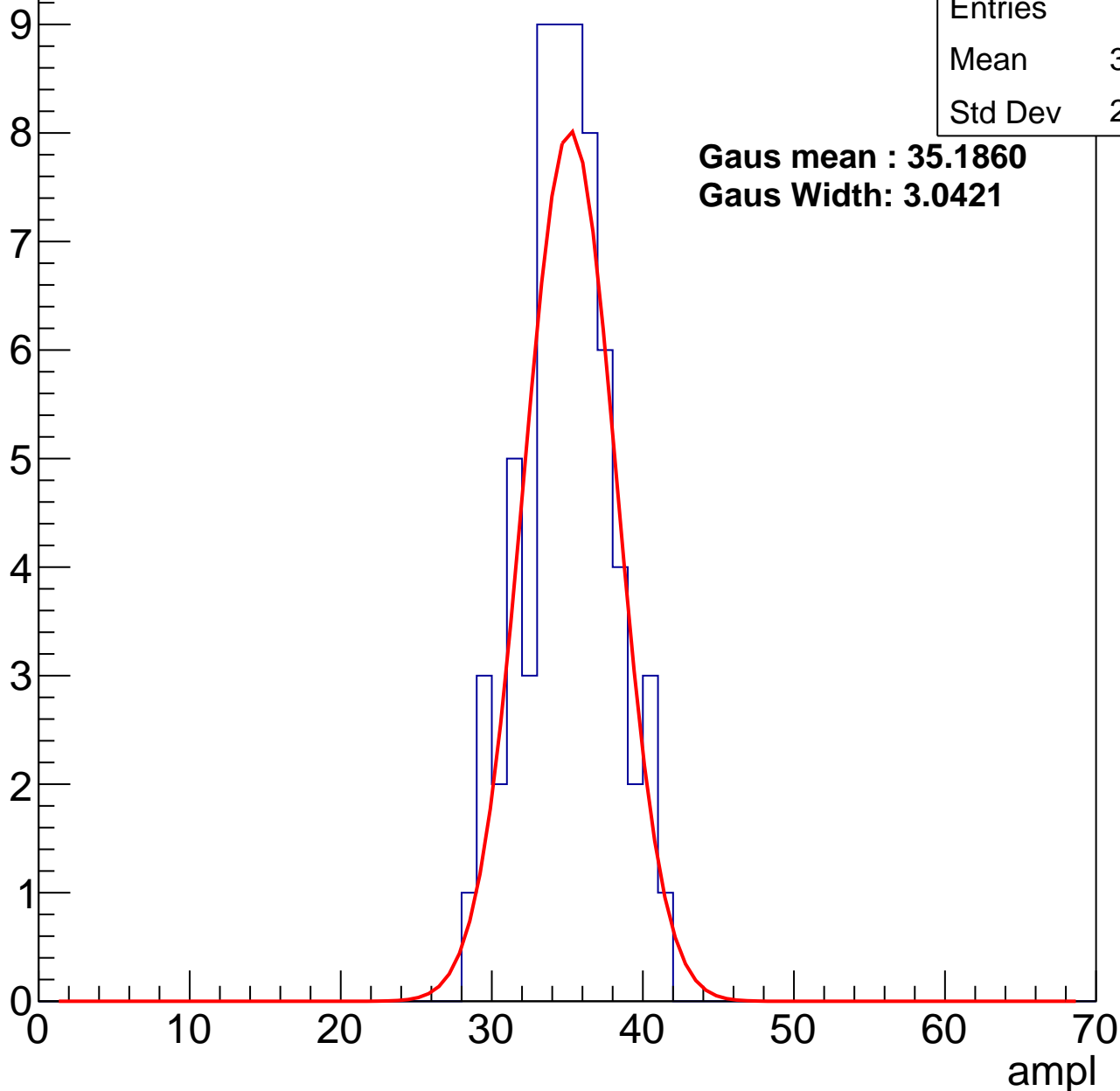
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.54
Std Dev	2.925

**Gaus mean : 35.1860**

**Gaus Width: 3.0421**



# B1L002S, U3-ch77, adc2

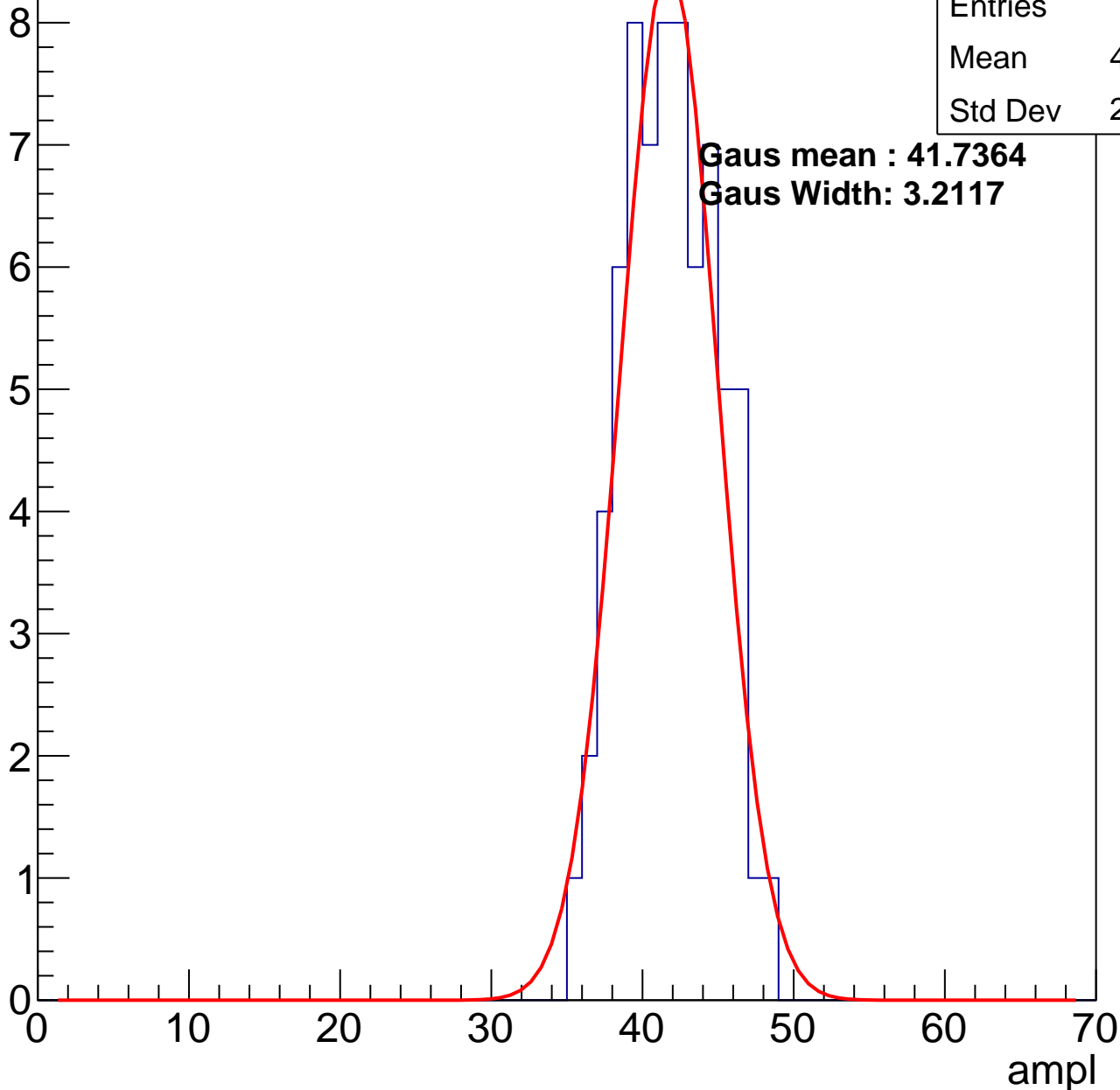
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	41.38
Std Dev	2.993

**Gaus mean : 41.7364**

**Gaus Width: 3.2117**

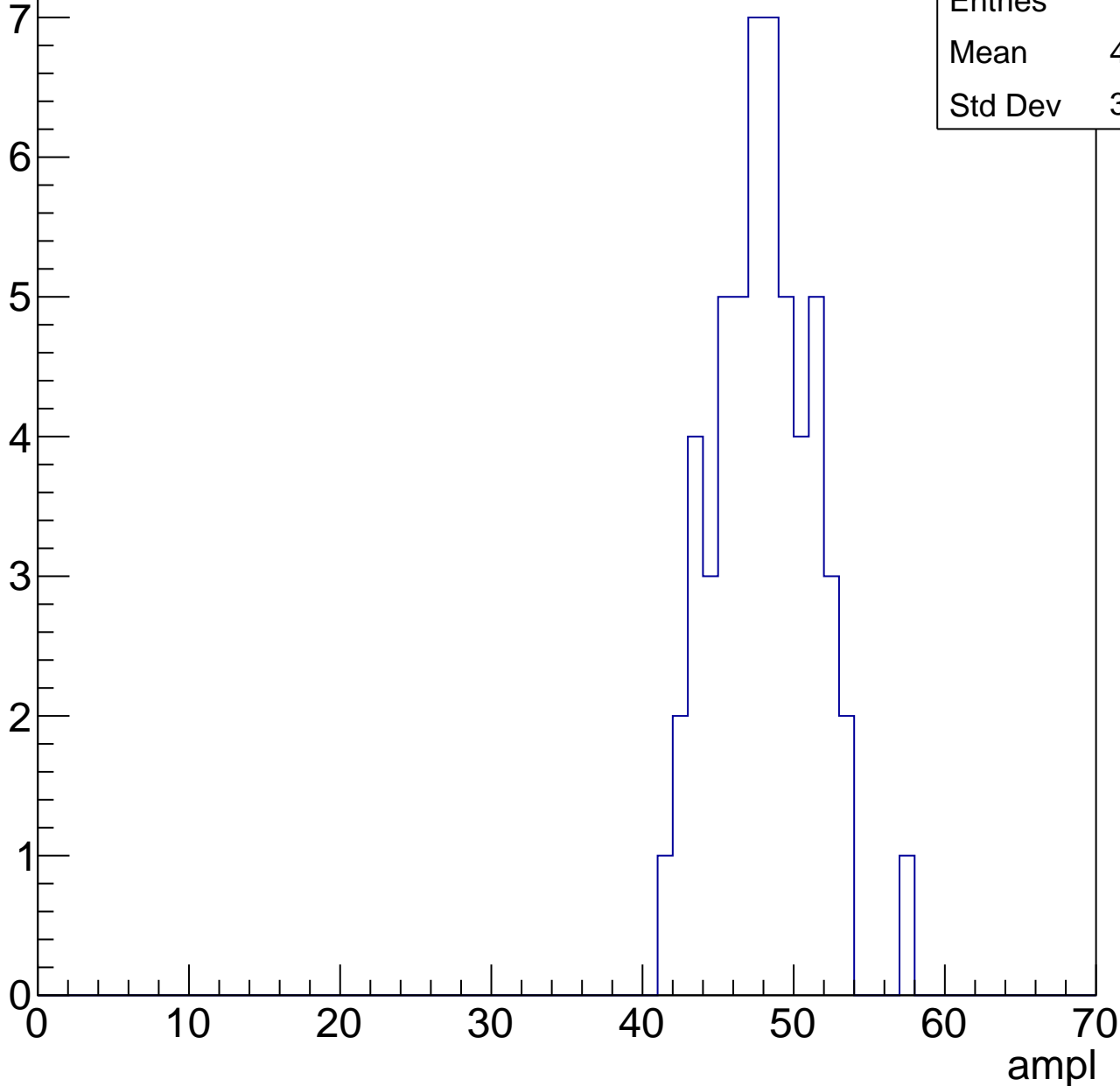


# B1L002S, U3-ch77, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

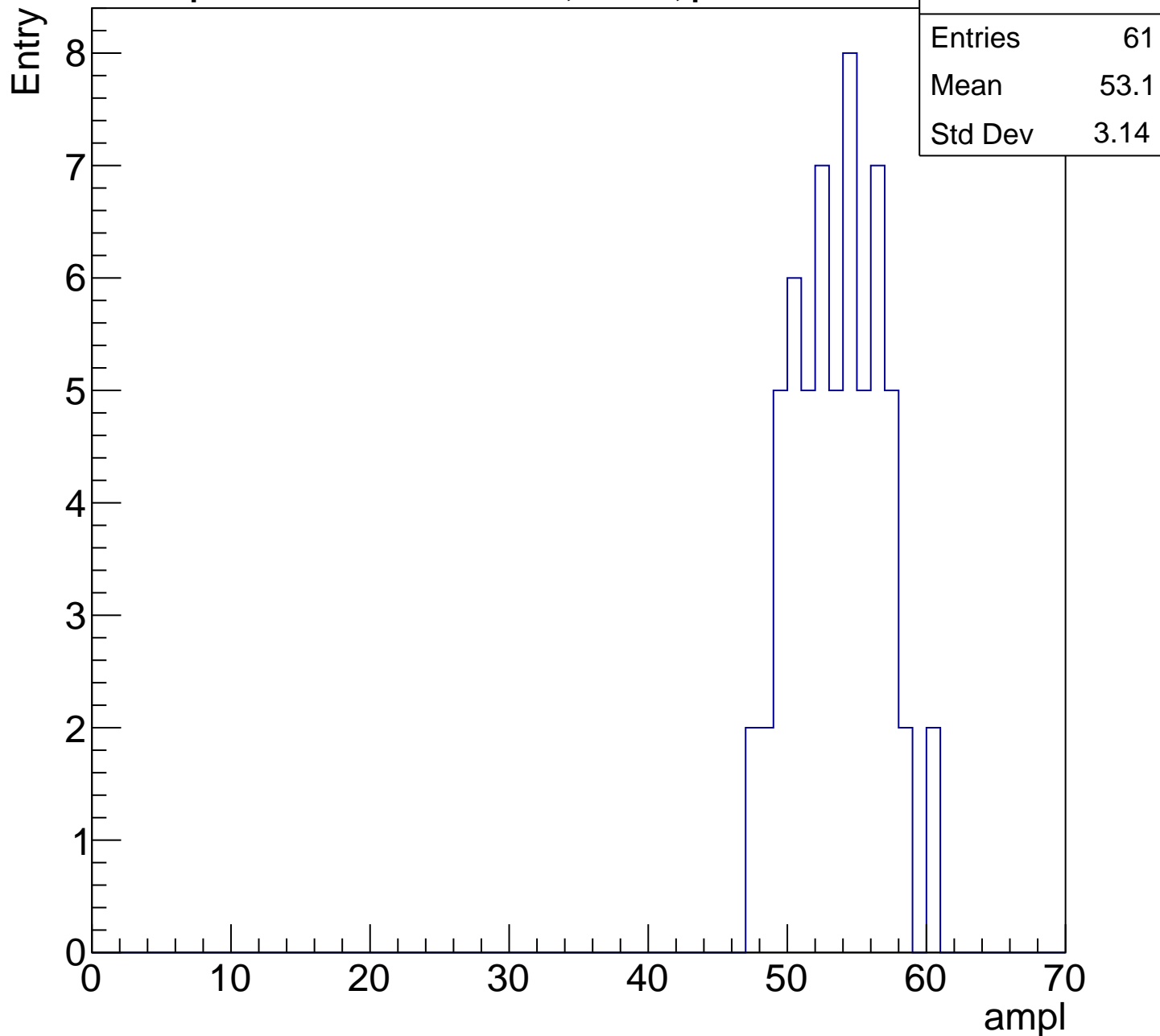
Entry

Entries	54
Mean	47.56
Std Dev	3.264



# B1L002S, U3-ch77, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

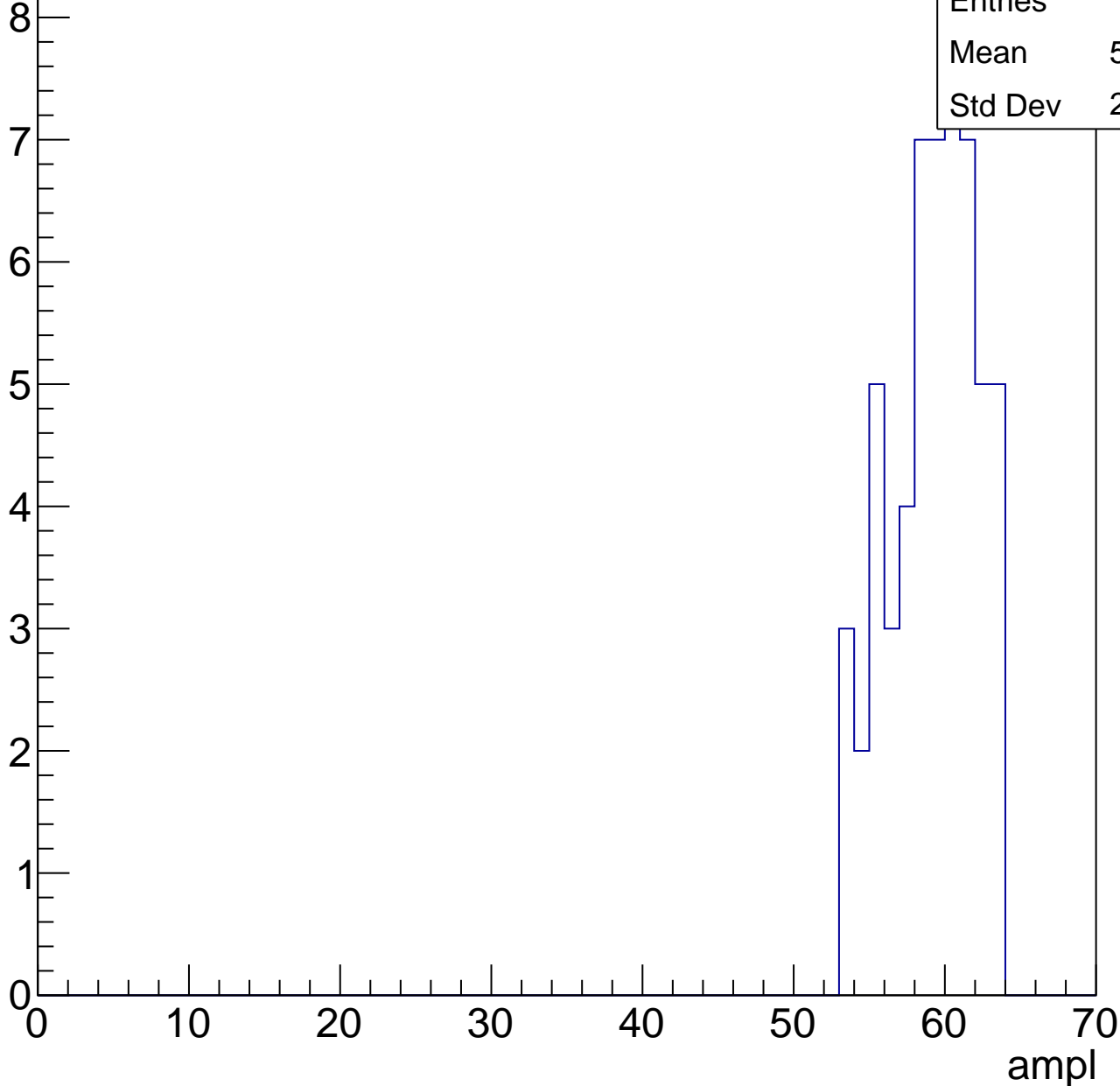


# B1L002S, U3-ch77, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

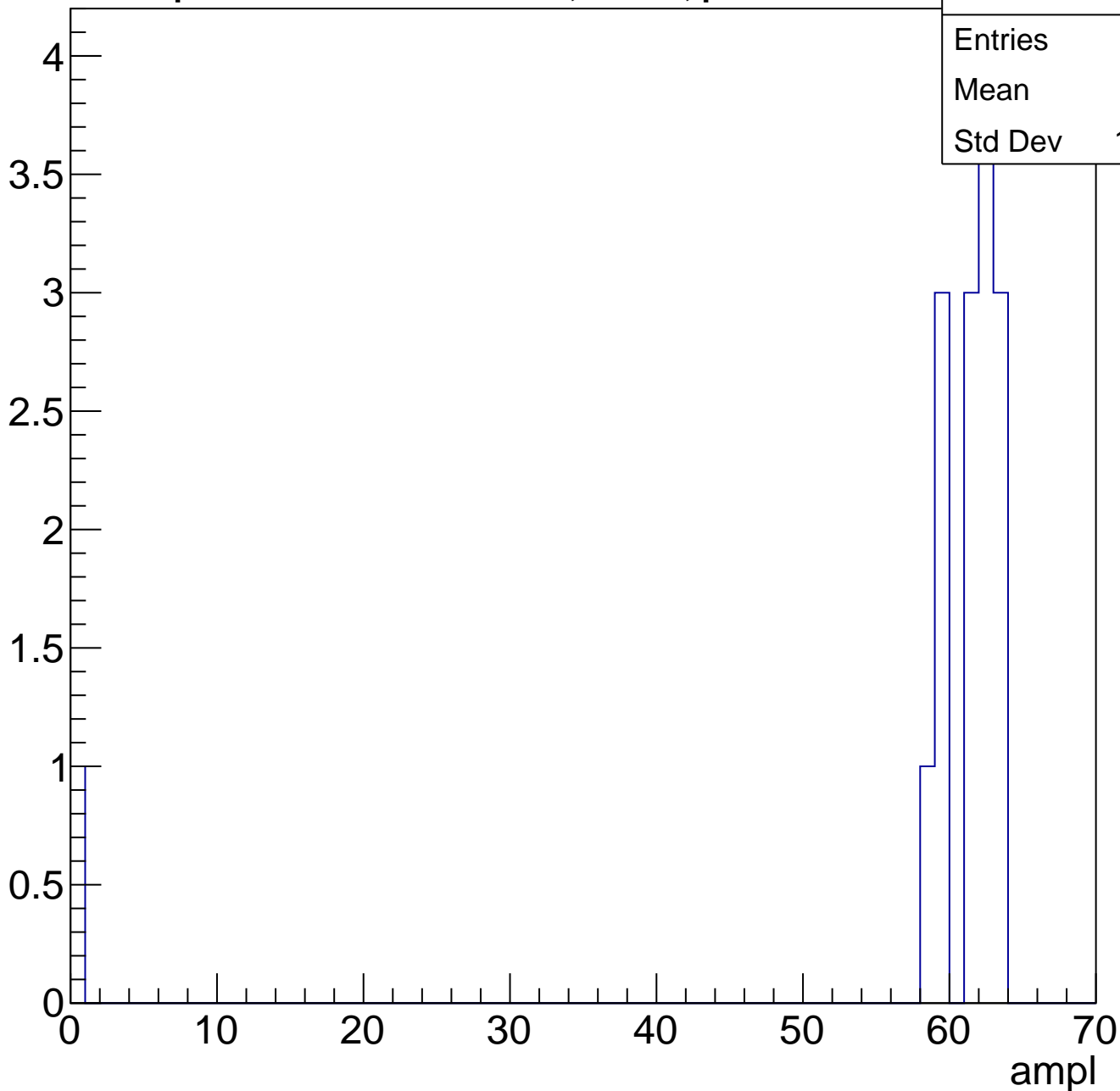
Entries	56
Mean	58.73
Std Dev	2.819



# B1L002S, U3-ch77, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

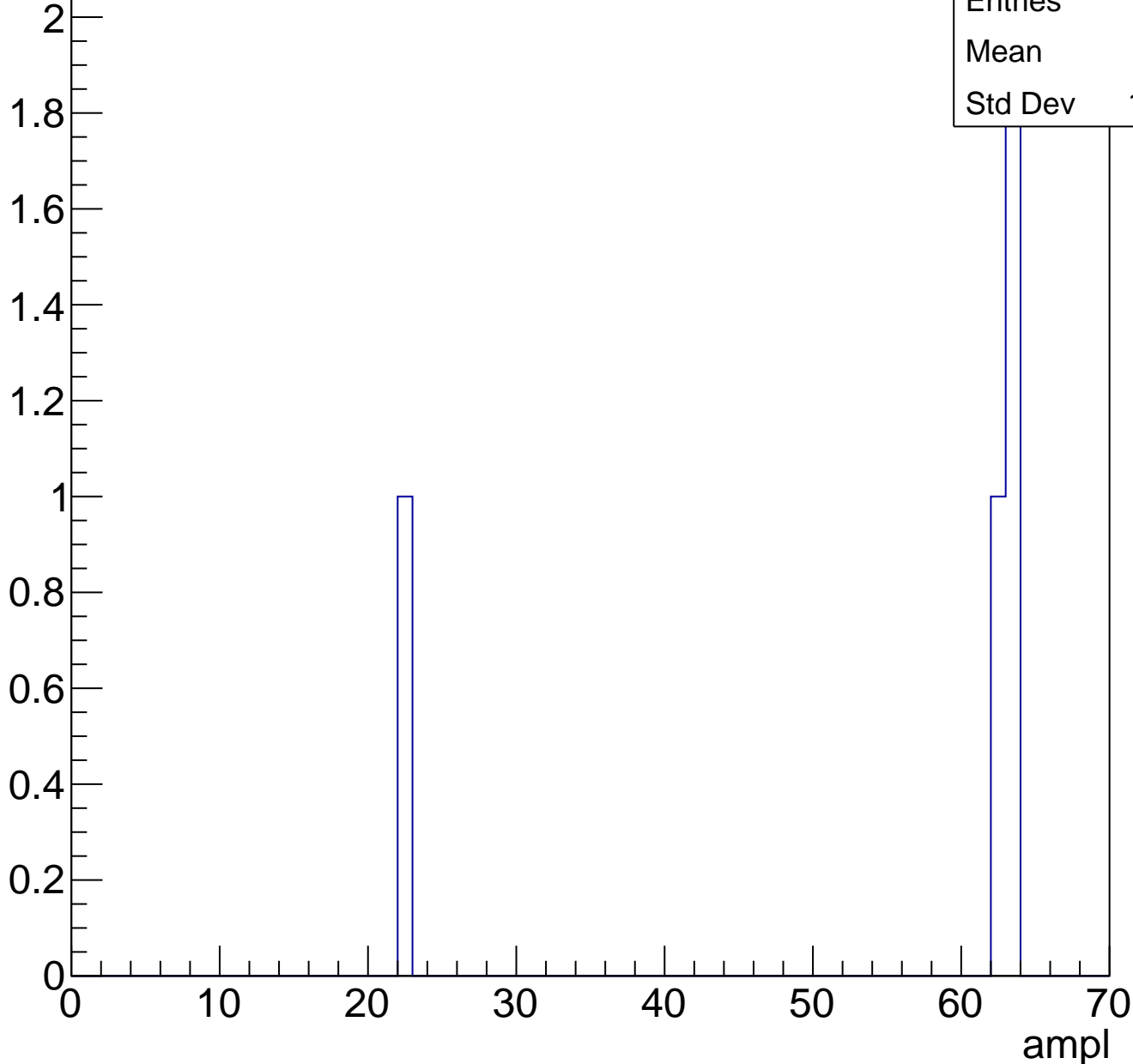




# B1L002S, U3-ch77, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	52.5
Std Dev	17.61

# B1L002S, U3-ch78, adc0

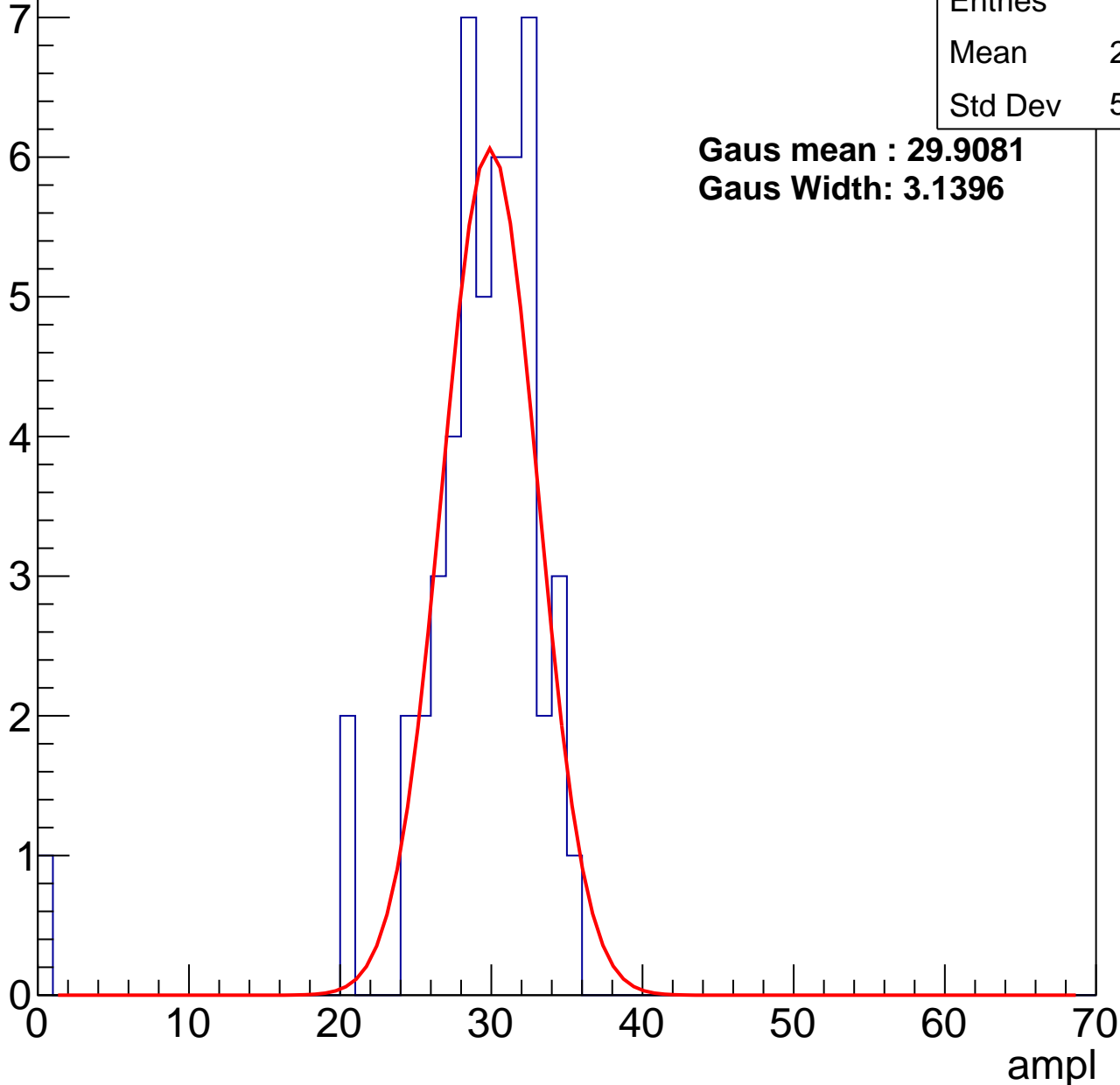
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	28.59
Std Dev	5.168

**Gaus mean : 29.9081**

**Gaus Width: 3.1396**



# B1L002S, U3-ch78, adc1

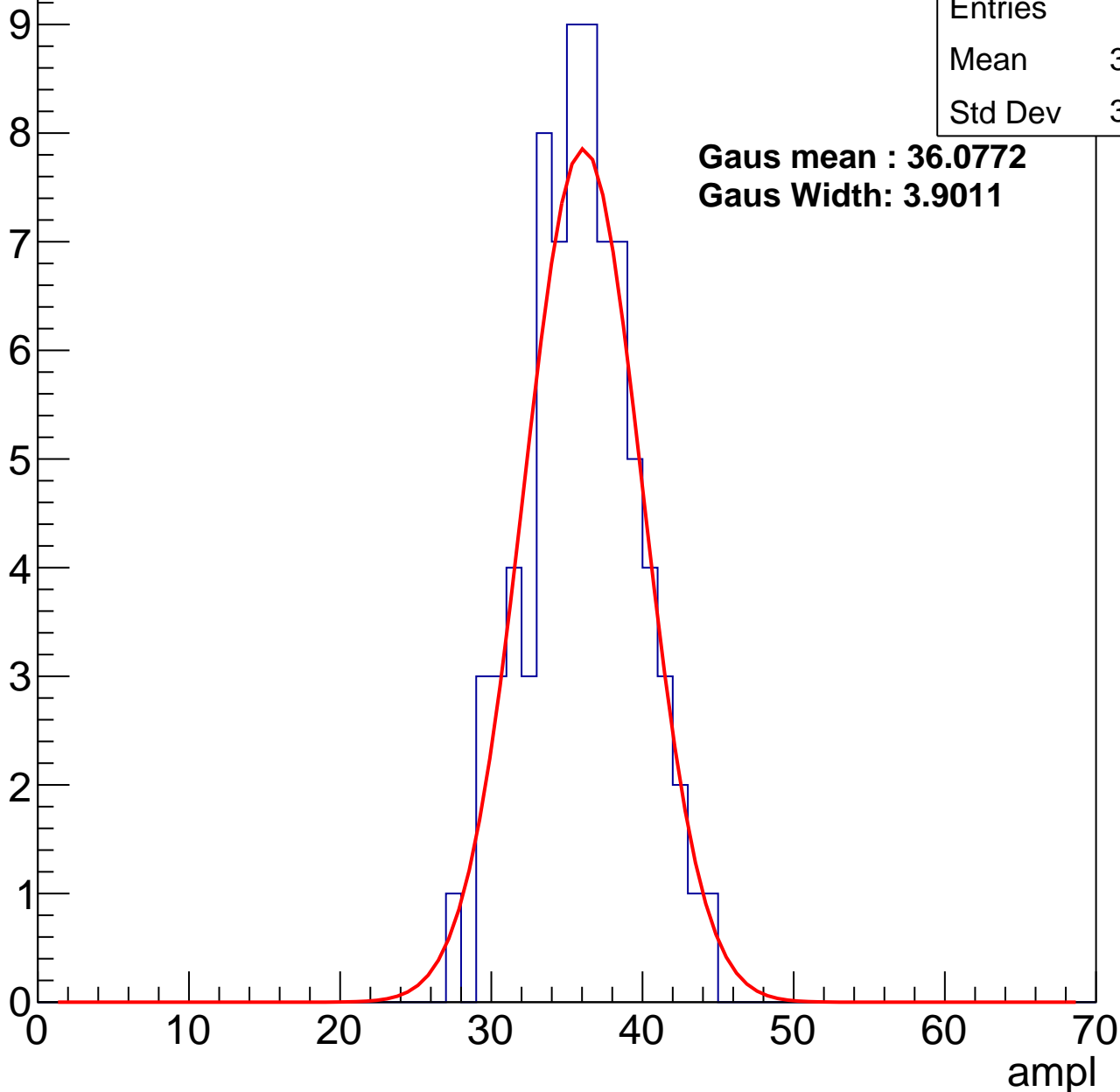
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	35.57
Std Dev	3.573

**Gaus mean : 36.0772**

**Gaus Width: 3.9011**



# B1L002S, U3-ch78, adc2

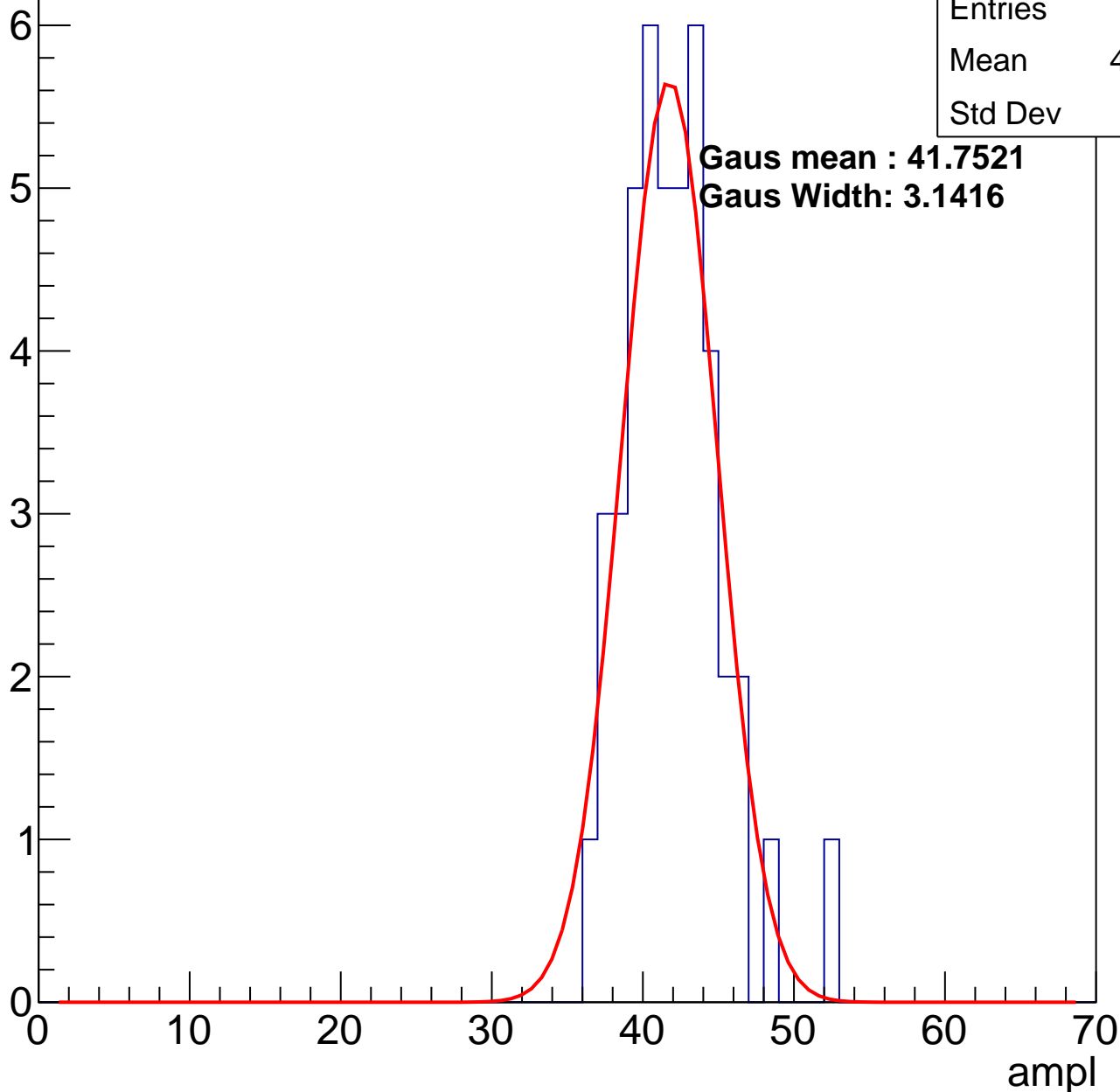
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	41.52
Std Dev	3.13

**Gaus mean : 41.7521**

**Gaus Width: 3.1416**

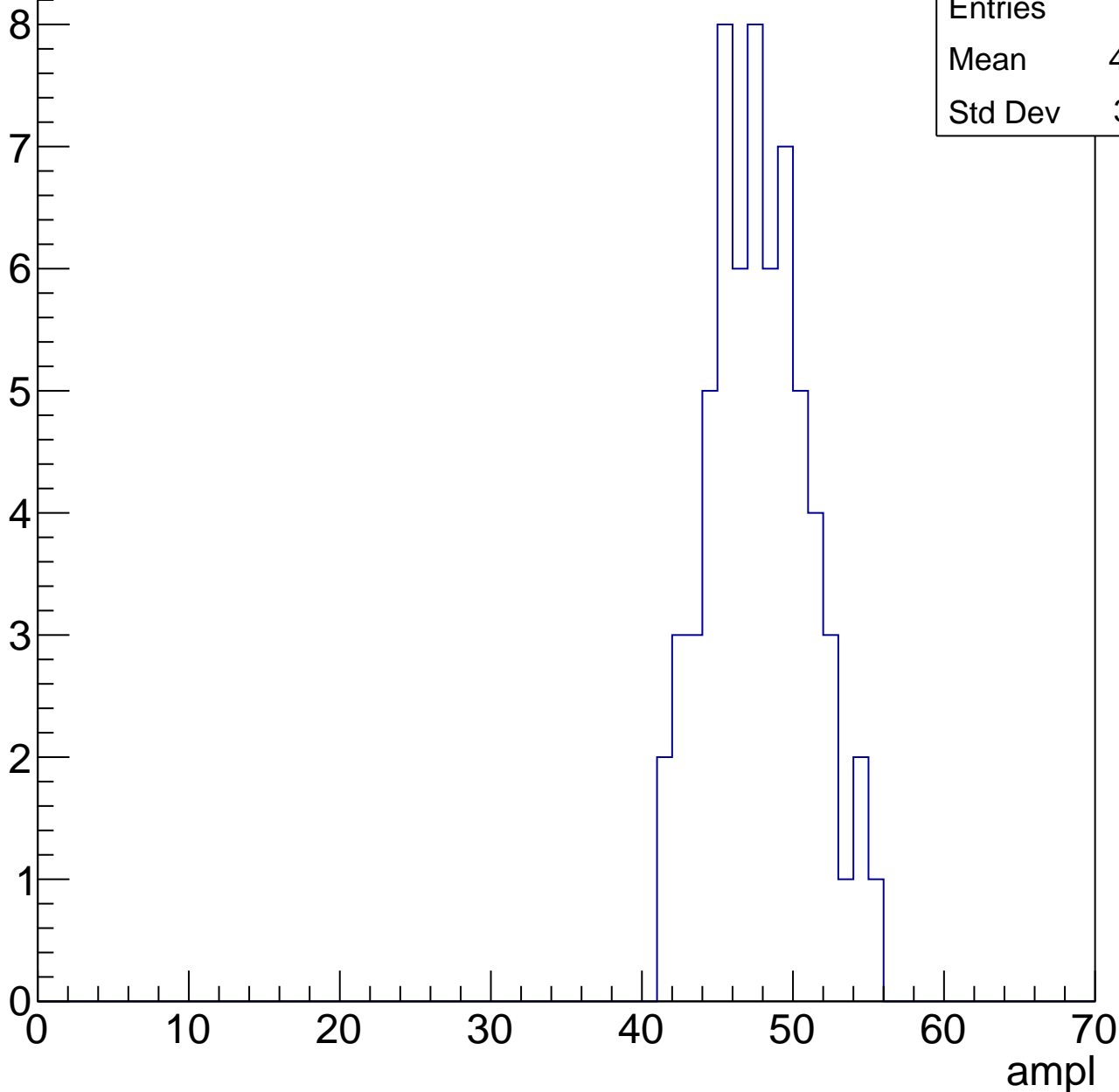


# B1L002S, U3-ch78, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

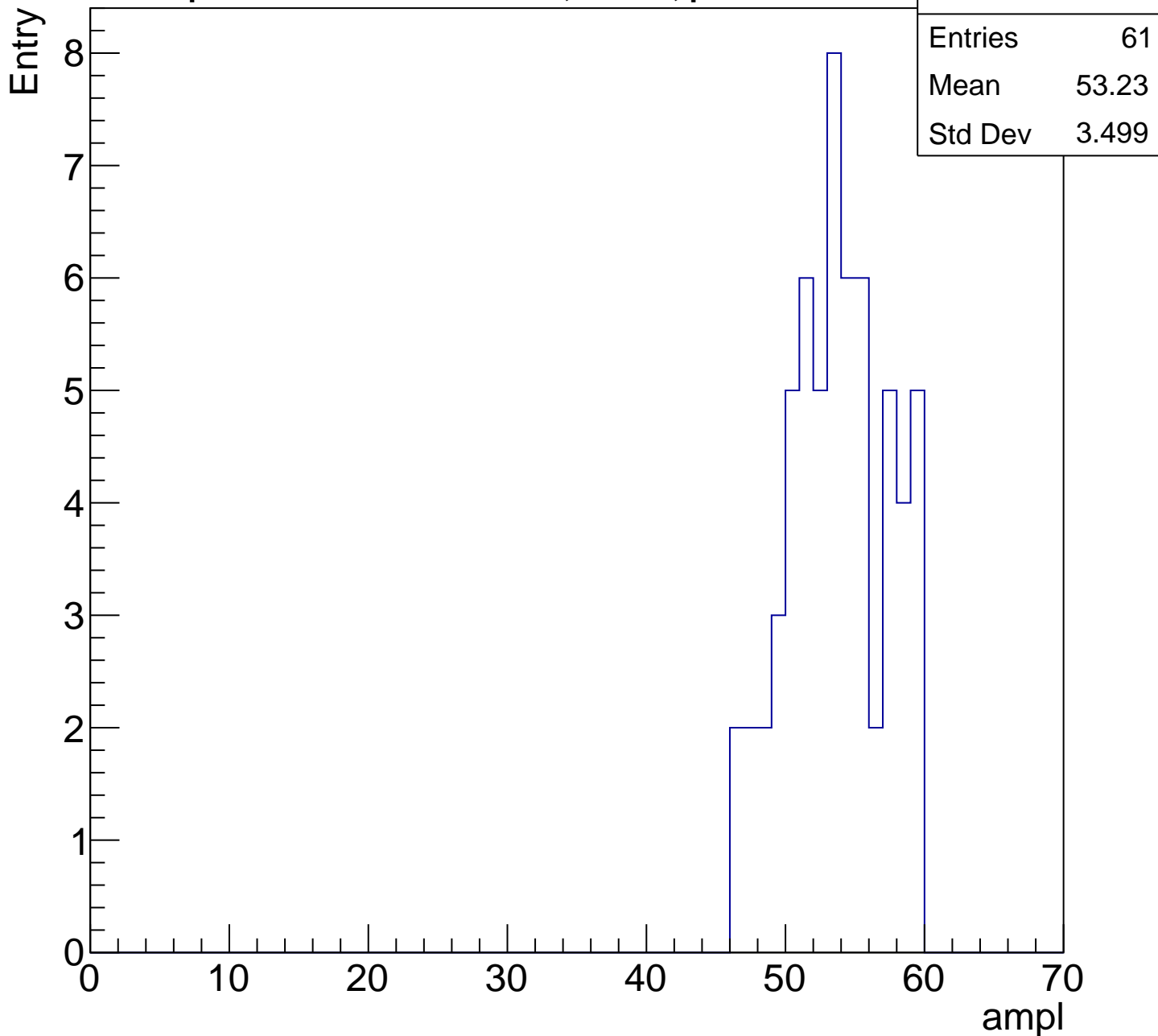
Entry

Entries	64
Mean	47.28
Std Dev	3.281



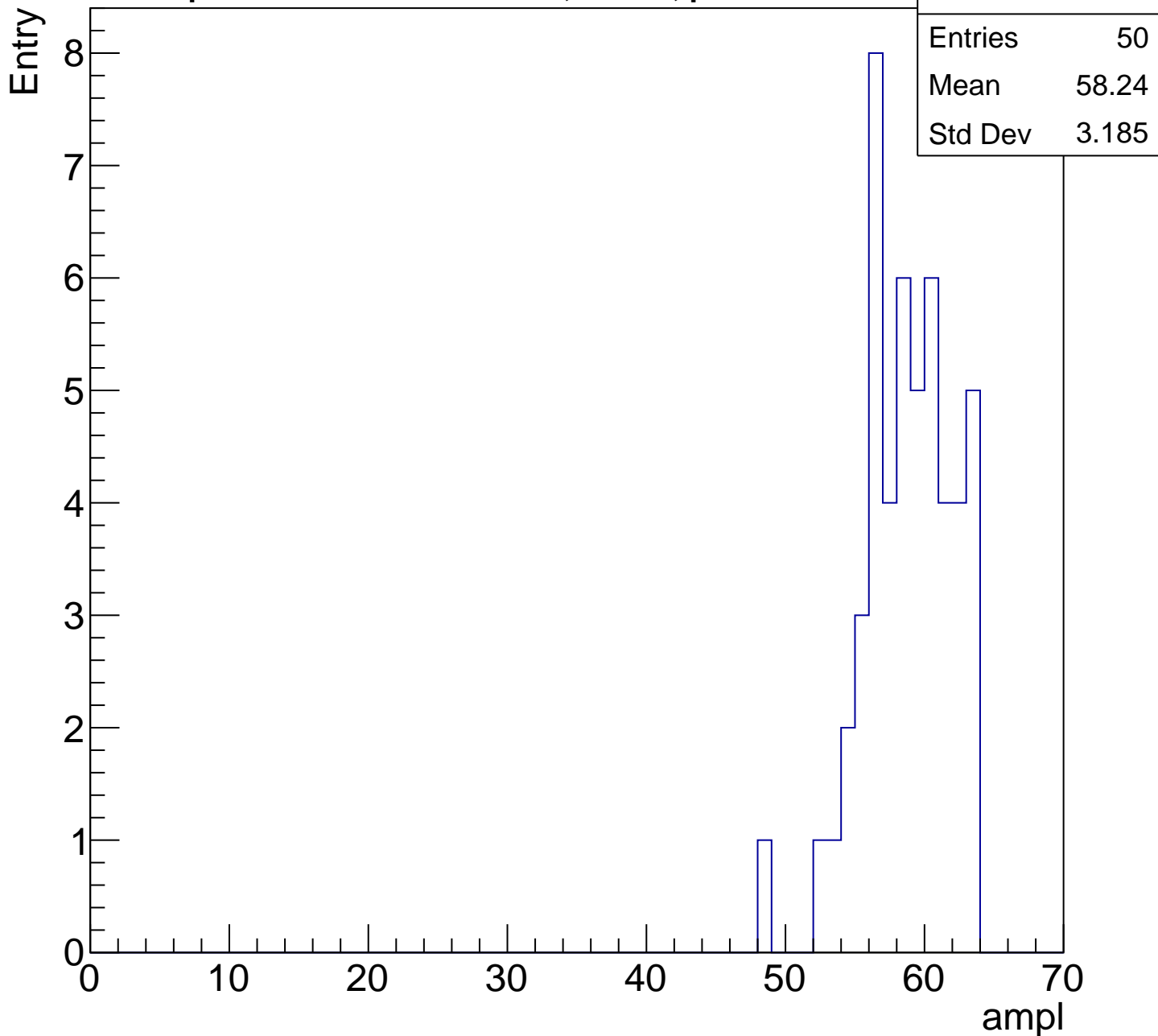
# B1L002S, U3-ch78, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch78, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

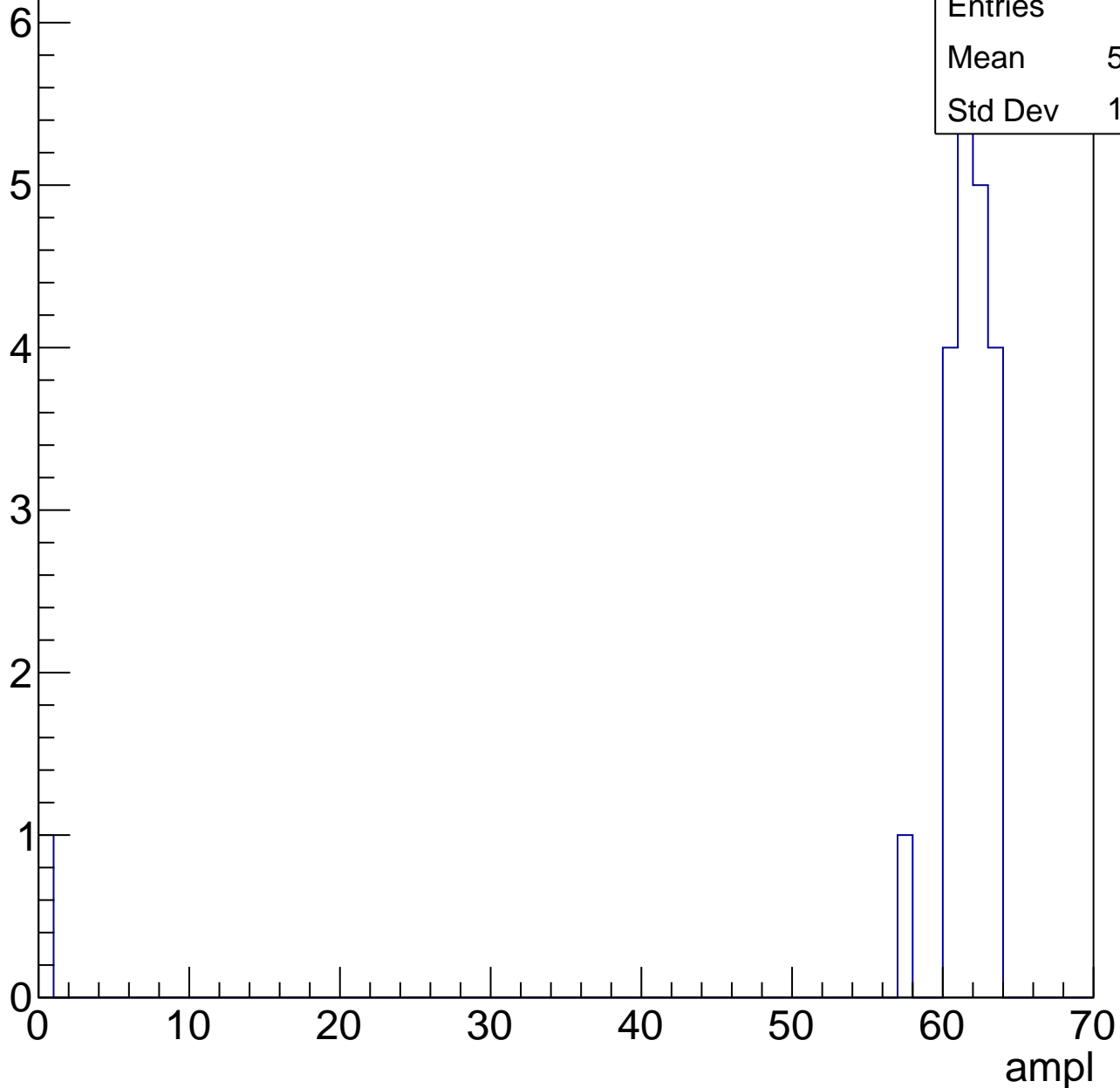


# B1L002S, U3-ch78, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.33
Std Dev	13.12





# B1L002S, U3-ch78, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	31.25
Std Dev	31.25

# B1L002S, U3-ch79, adc0

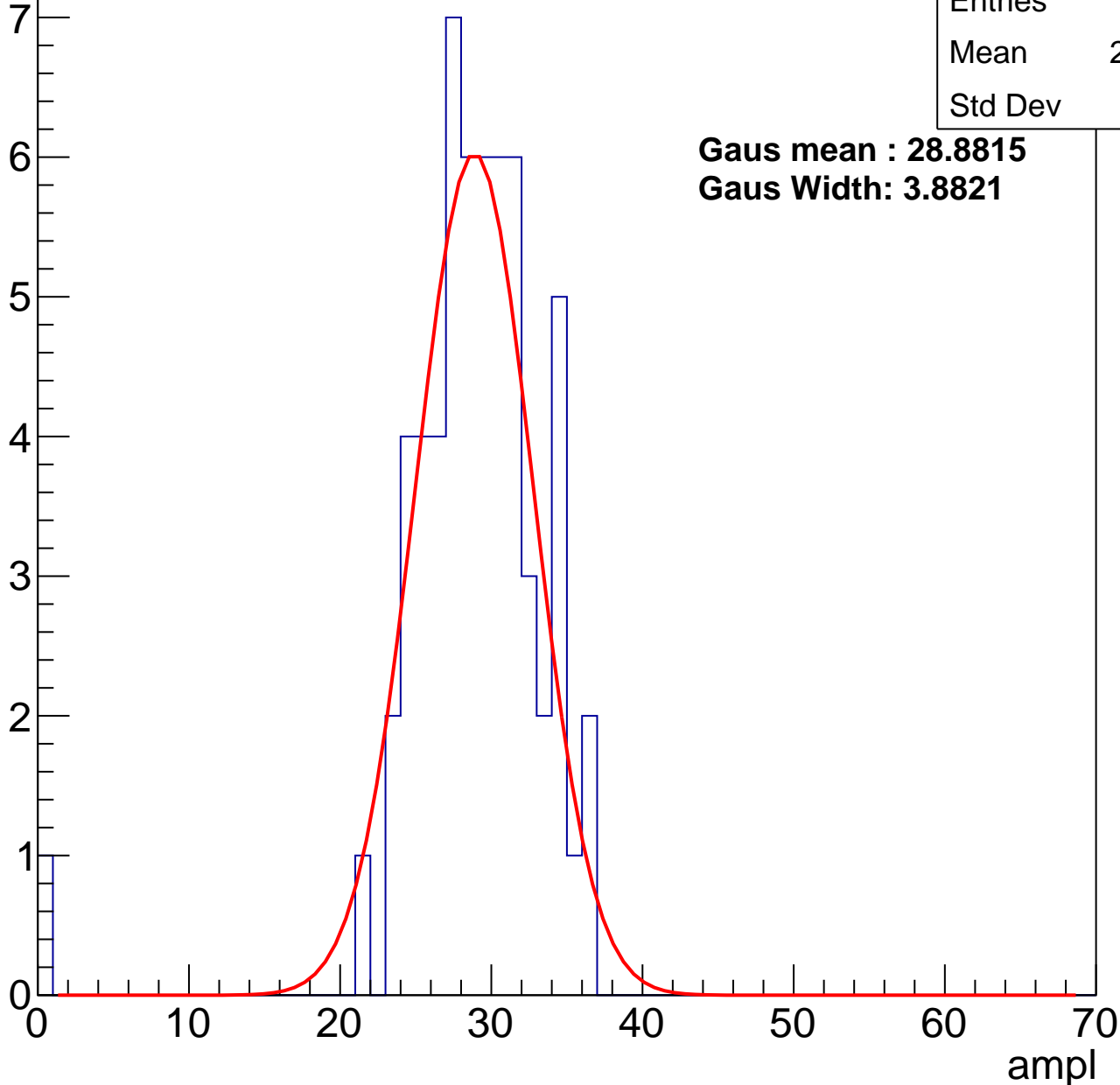
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.38
Std Dev	5.06

**Gaus mean : 28.8815**

**Gaus Width: 3.8821**



# B1L002S, U3-ch79, adc1

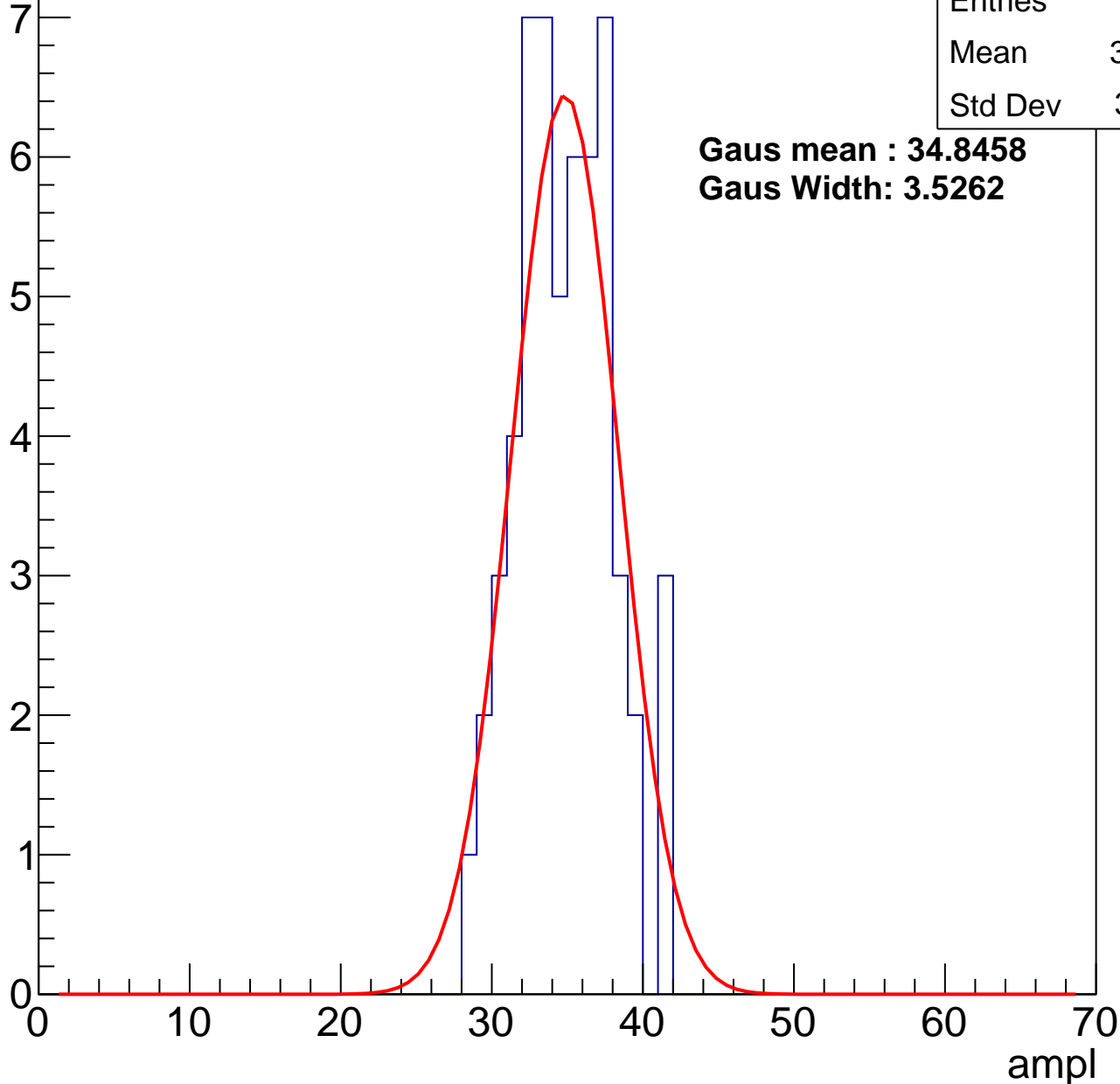
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.38
Std Dev	3.091

**Gaus mean : 34.8458**

**Gaus Width: 3.5262**



# B1L002S, U3-ch79, adc2

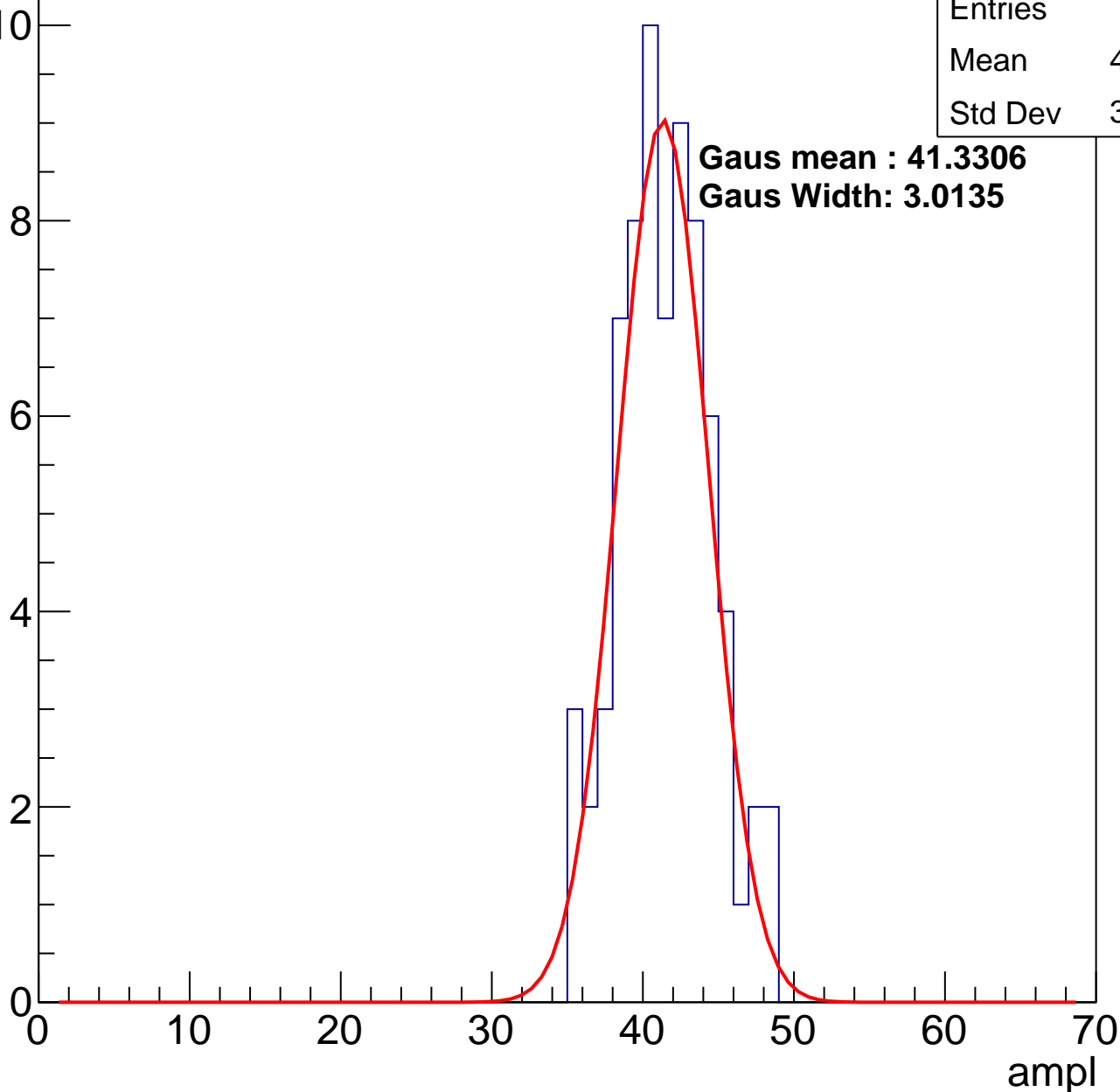
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	41.04
Std Dev	3.039

**Gaus mean : 41.3306**

**Gaus Width: 3.0135**

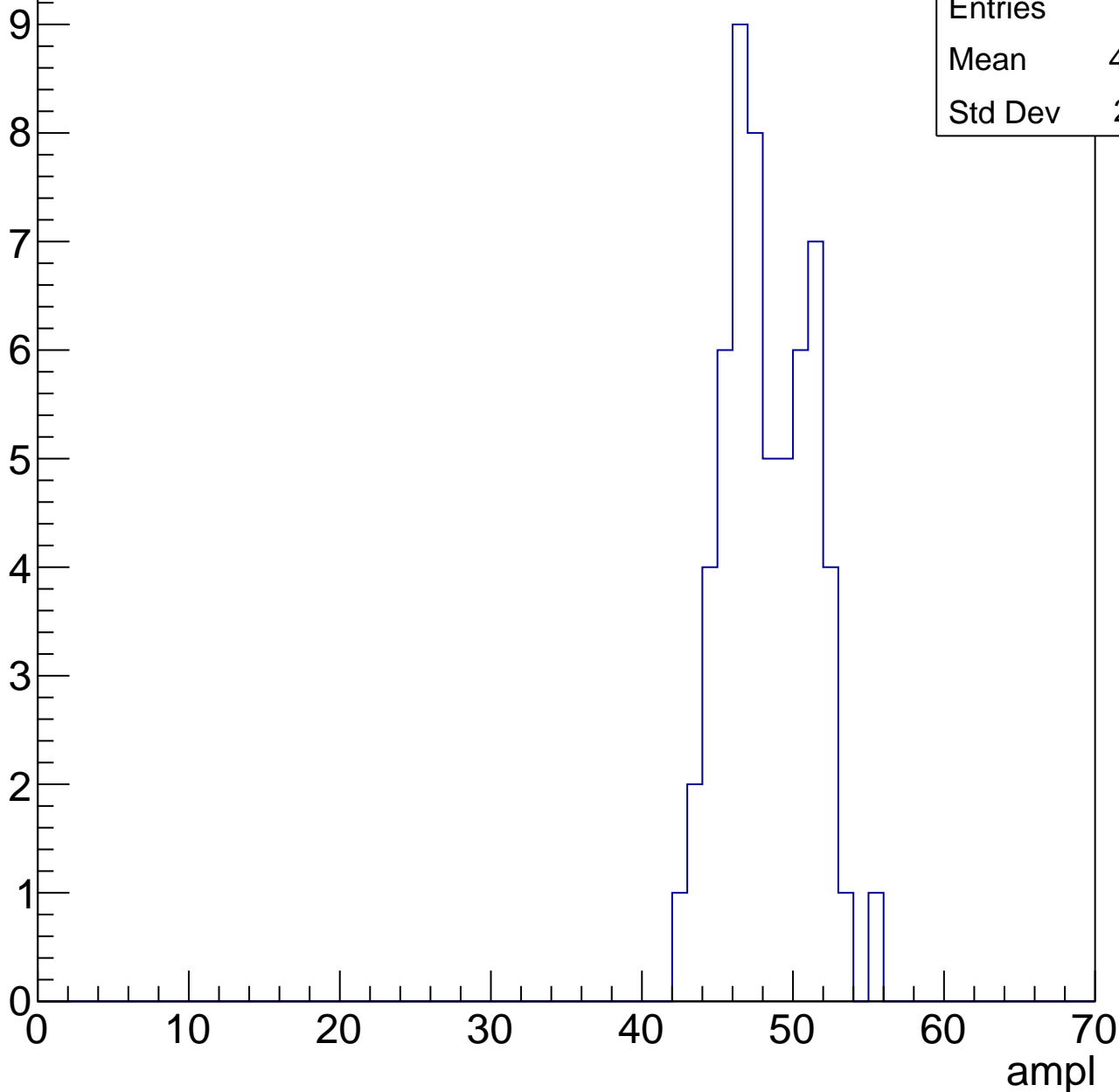


# B1L002S, U3-ch79, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	47.83
Std Dev	2.841

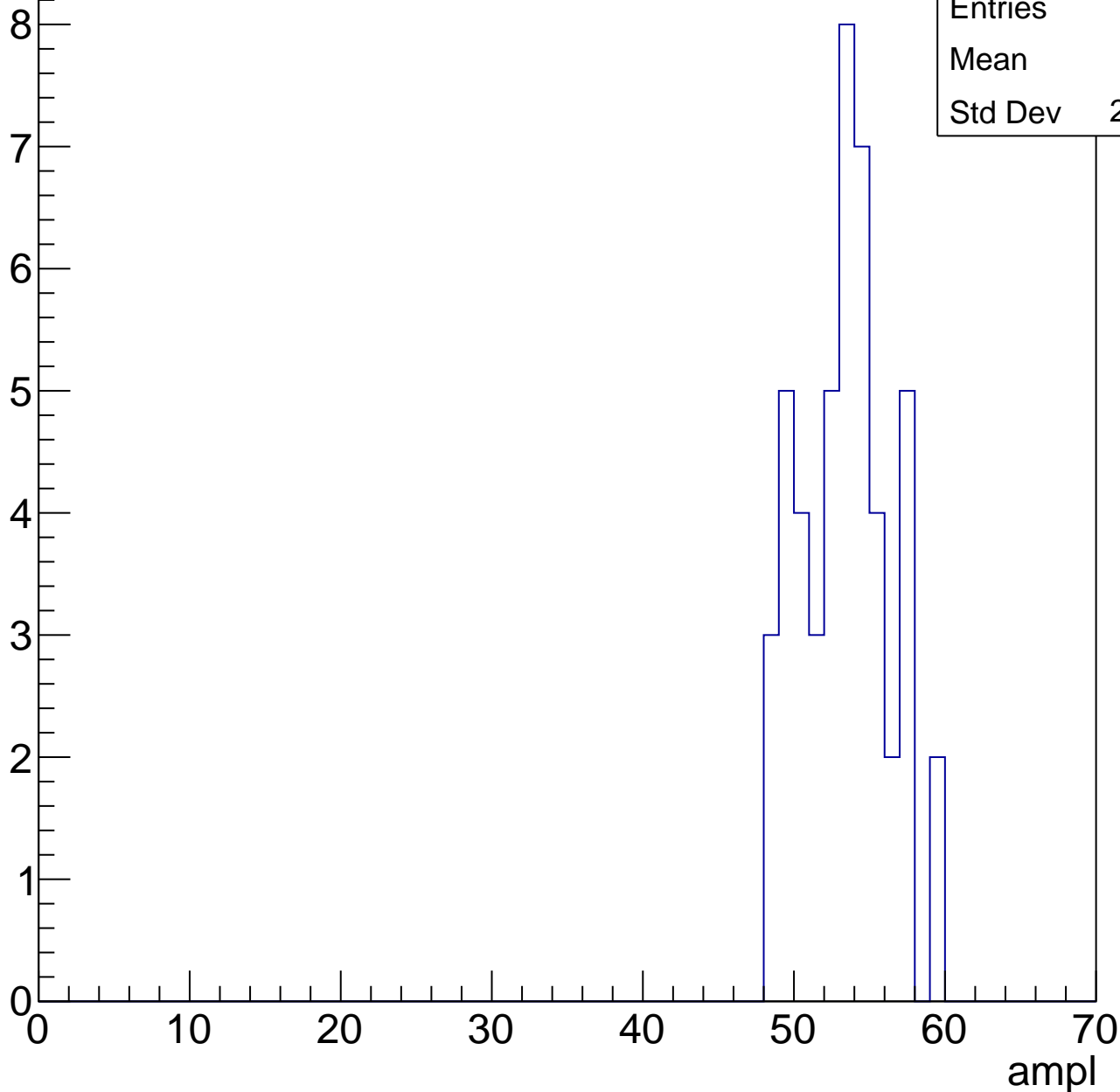


# B1L002S, U3-ch79, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	52.9
Std Dev	2.888

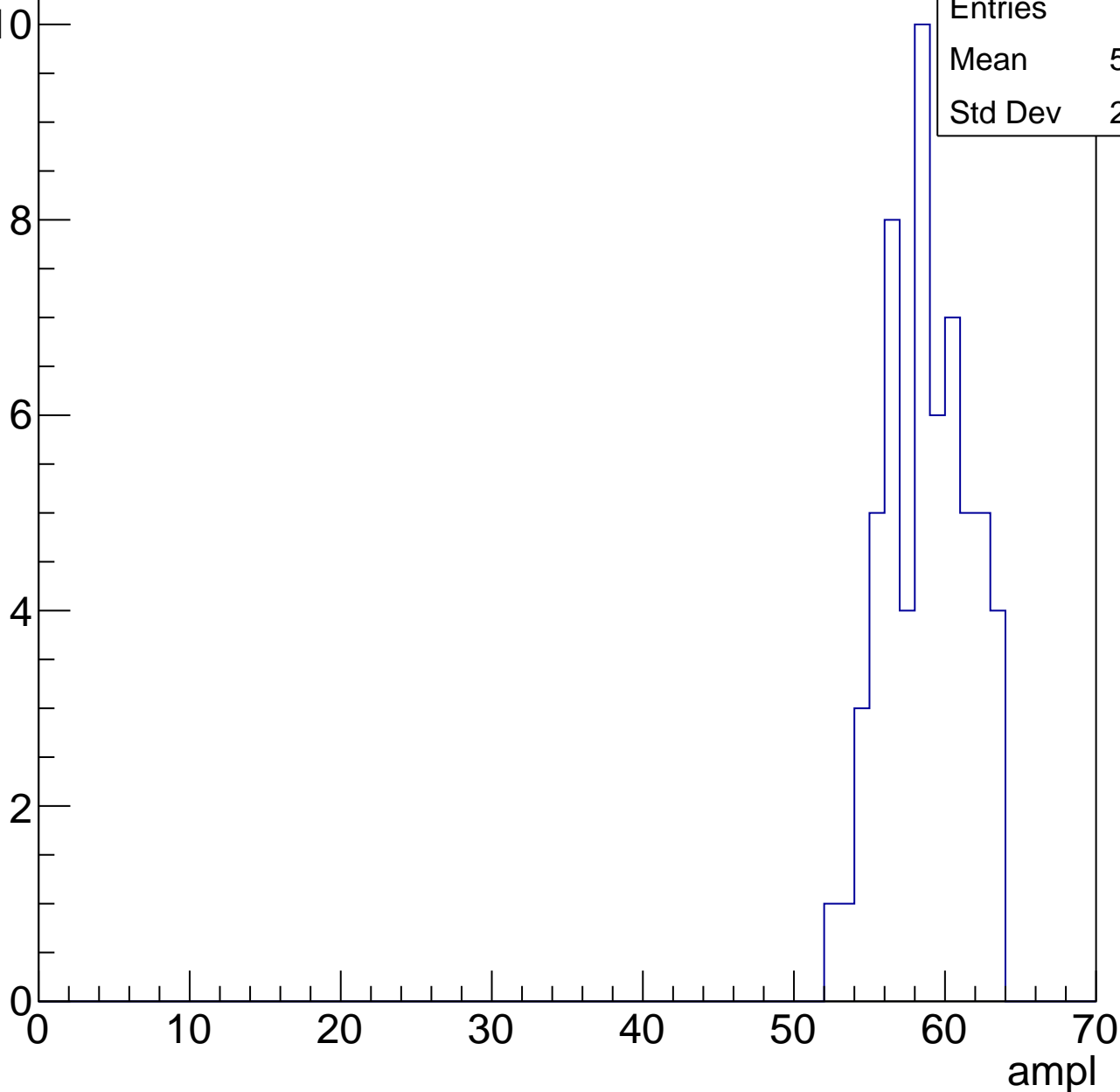


# B1L002S, U3-ch79, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	58.29
Std Dev	2.744

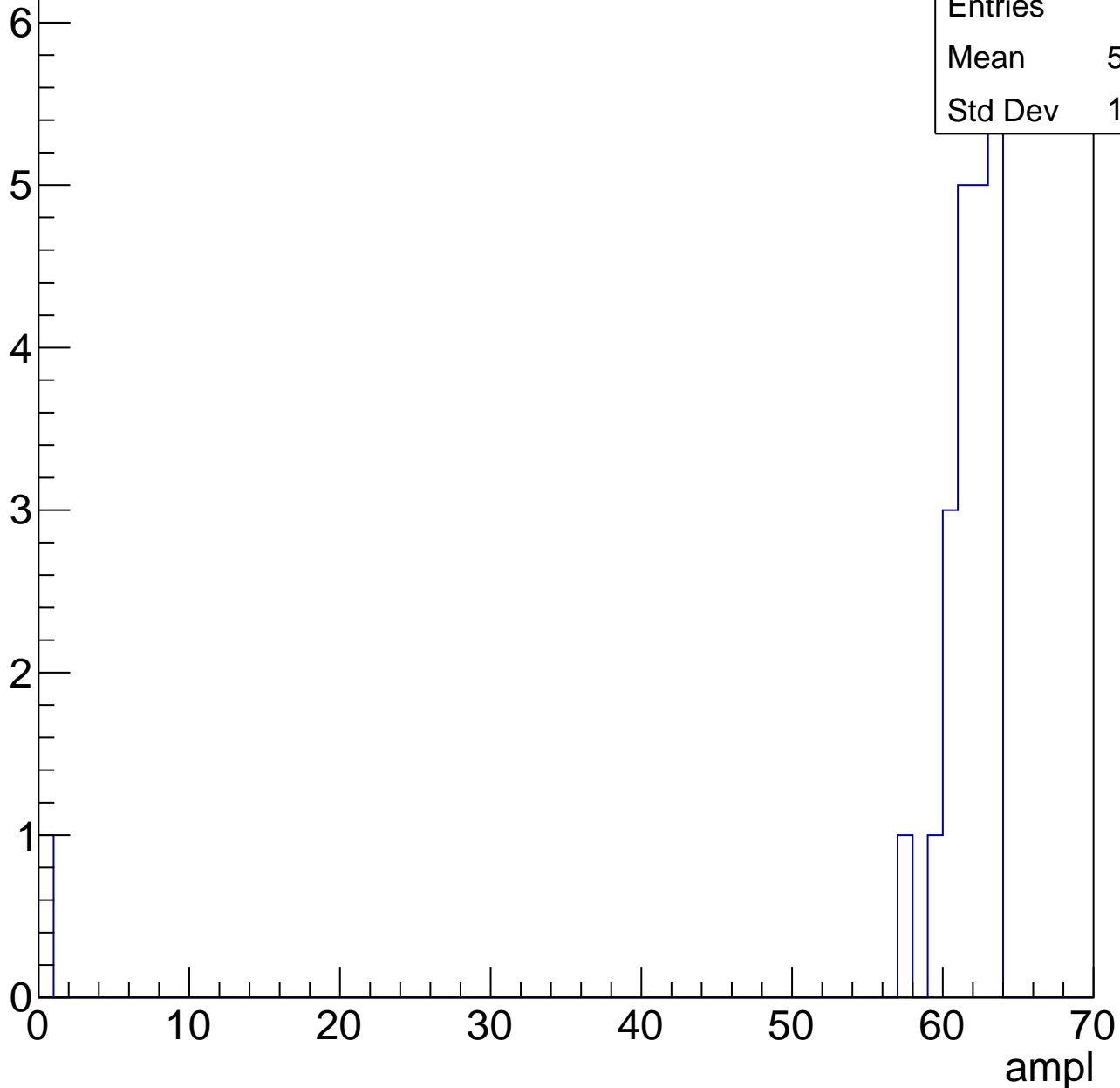


# B1L002S, U3-ch79, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.59
Std Dev	12.87

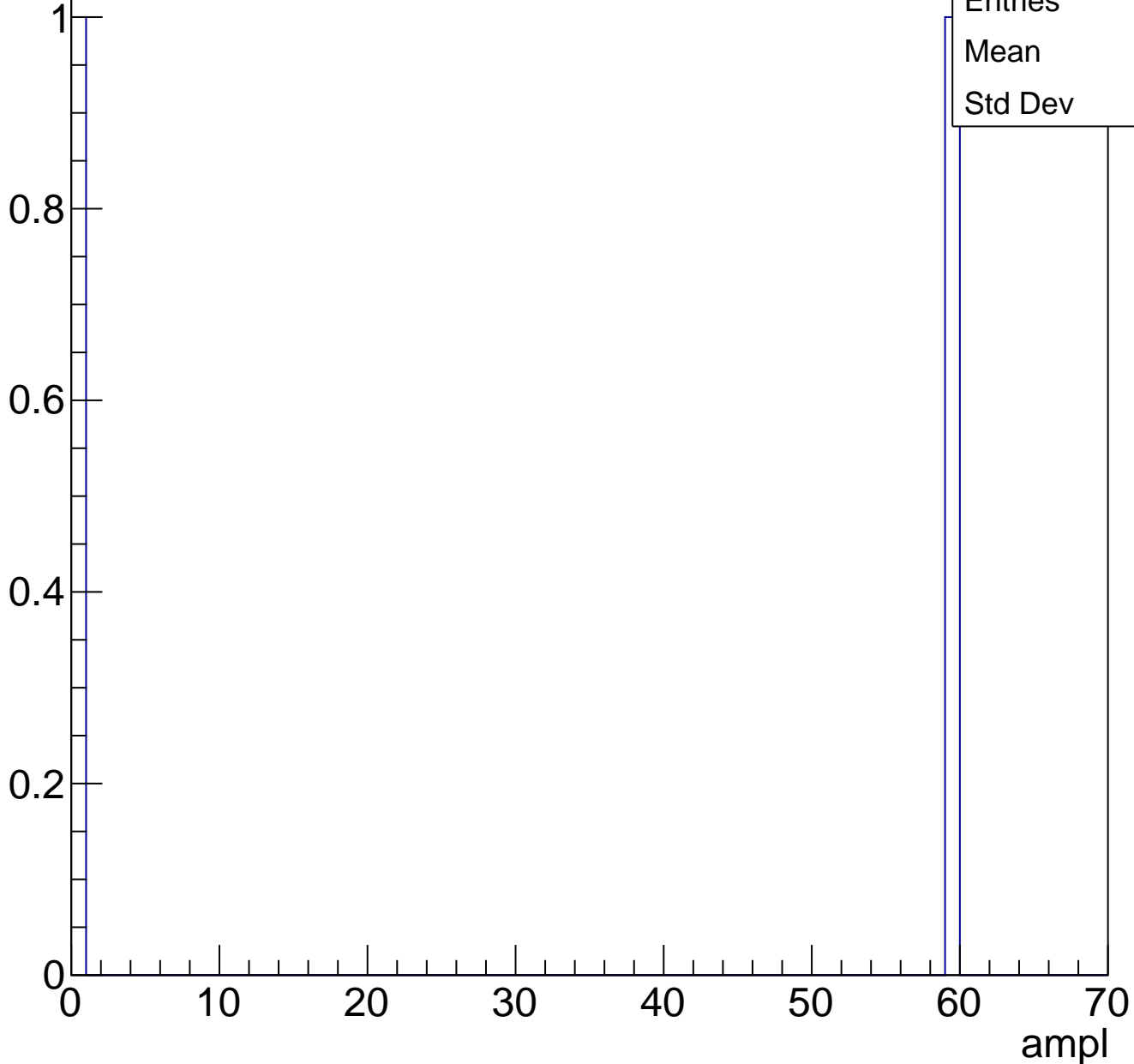




# B1L002S, U3-ch79, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch80, adc0

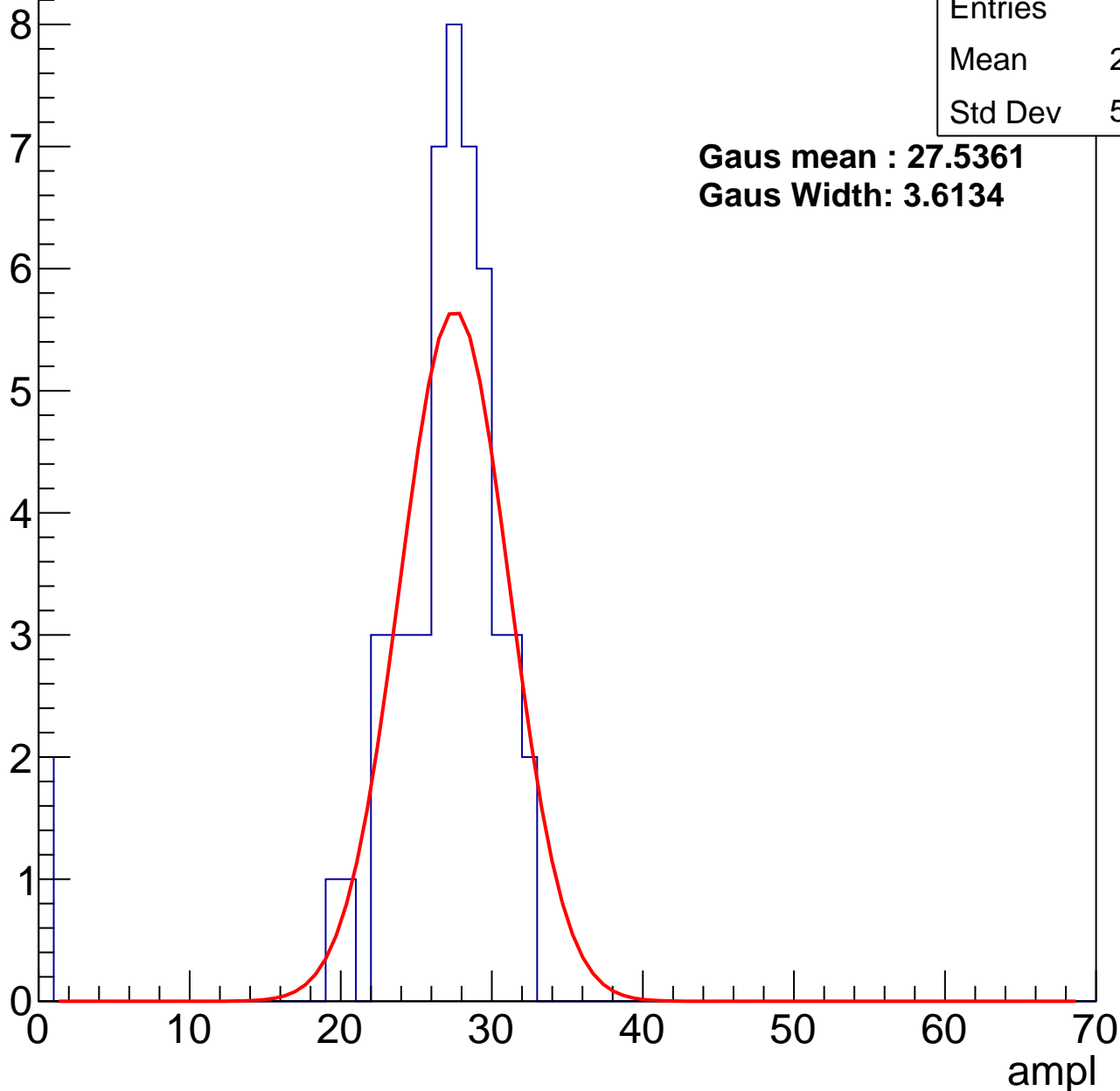
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	25.69
Std Dev	5.895

**Gaus mean : 27.5361**

**Gaus Width: 3.6134**



# B1L002S, U3-ch80, adc1

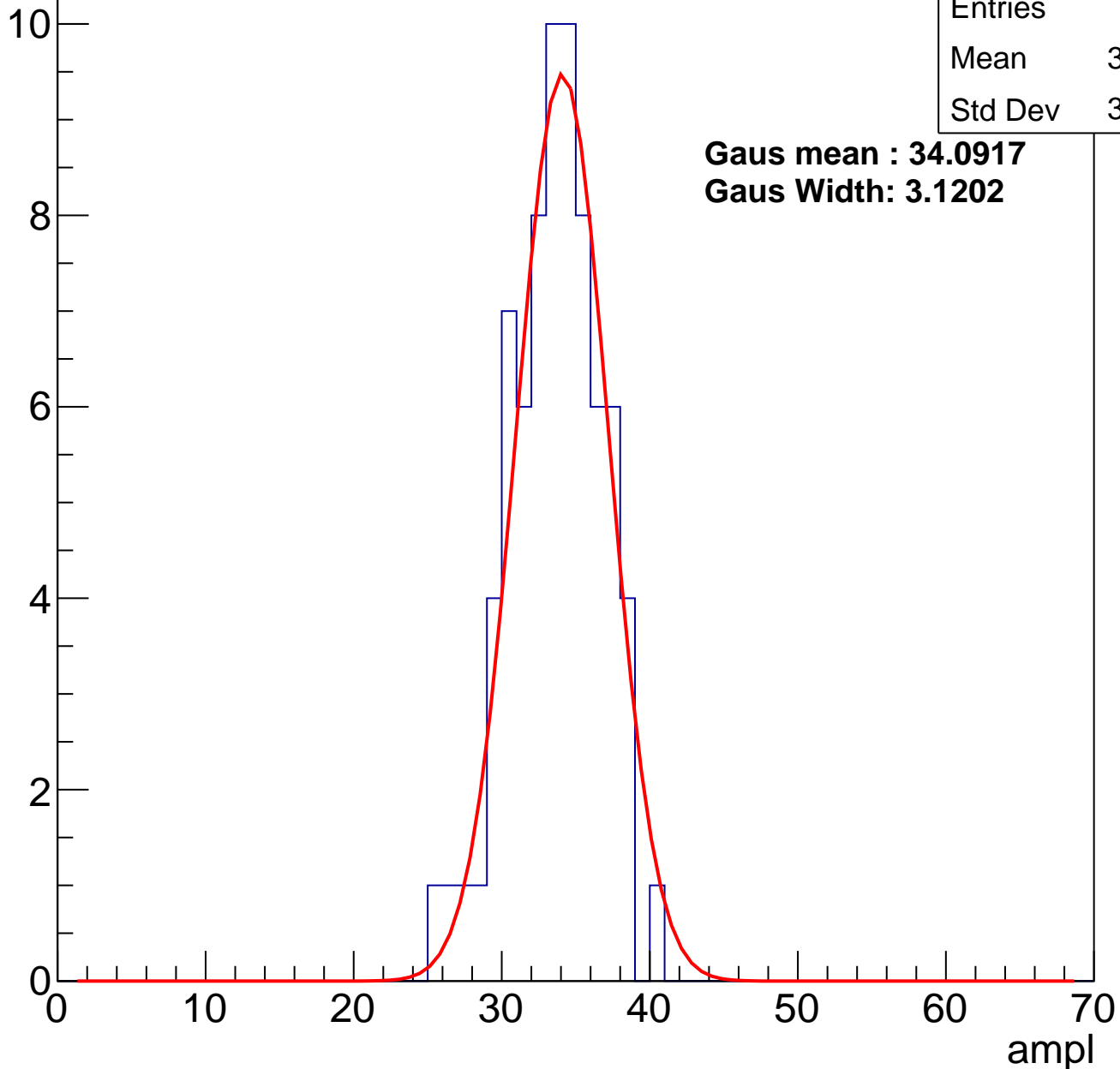
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	33.16
Std Dev	3.014

**Gaus mean : 34.0917**

**Gaus Width: 3.1202**

Entry



# B1L002S, U3-ch80, adc2

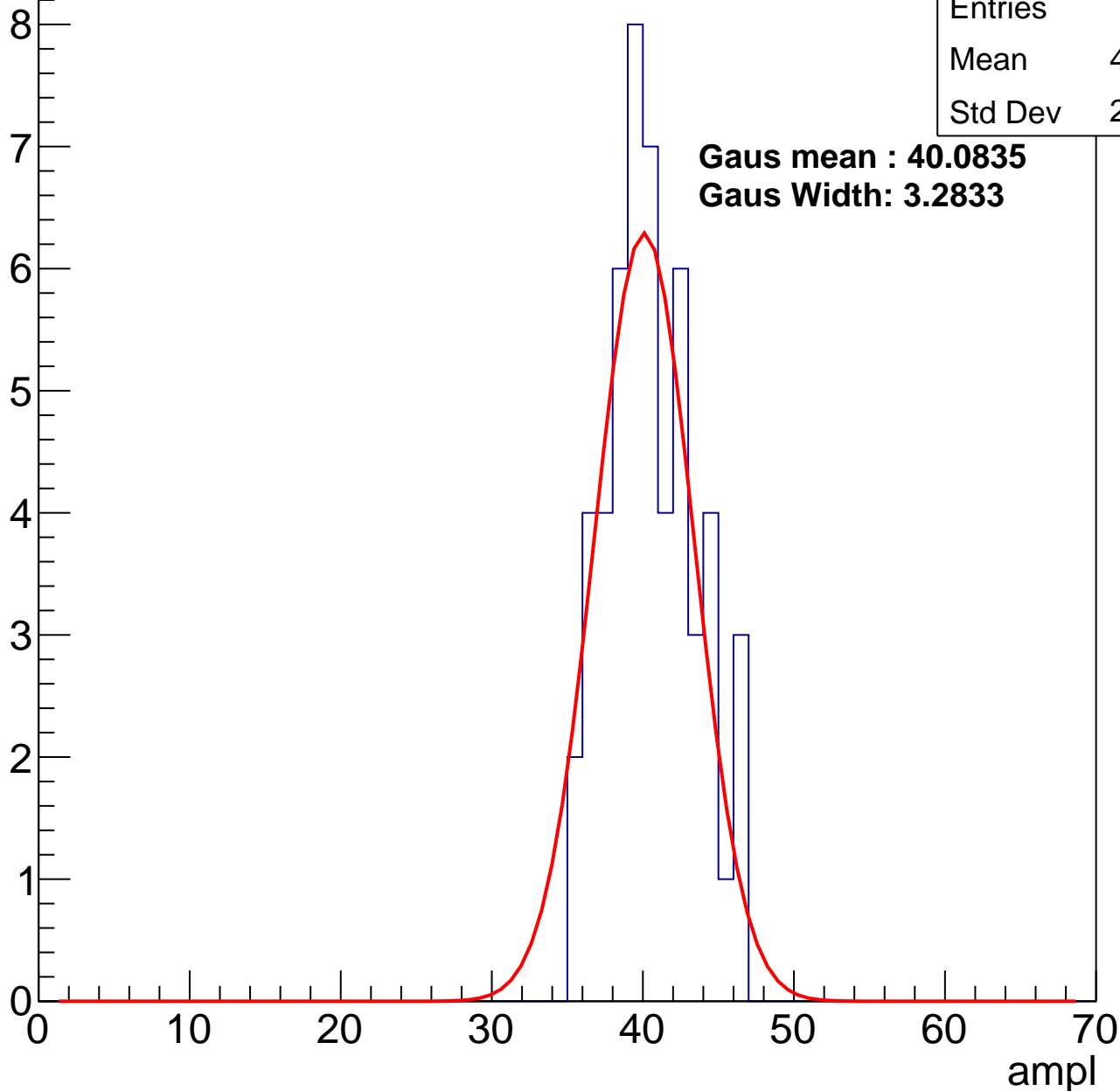
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.12
Std Dev	2.887

**Gaus mean : 40.0835**

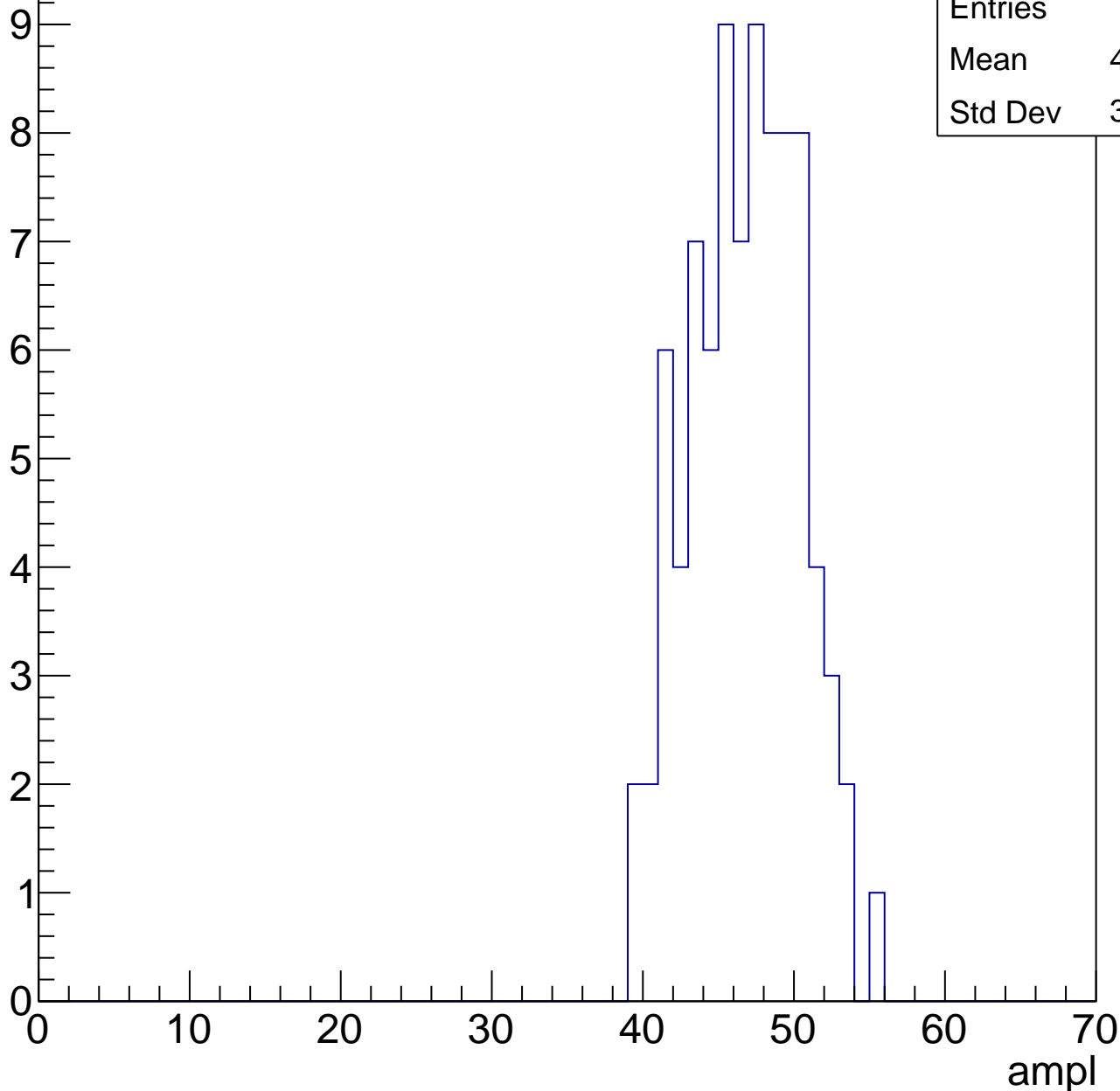
**Gaus Width: 3.2833**



# B1L002S, U3-ch80, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

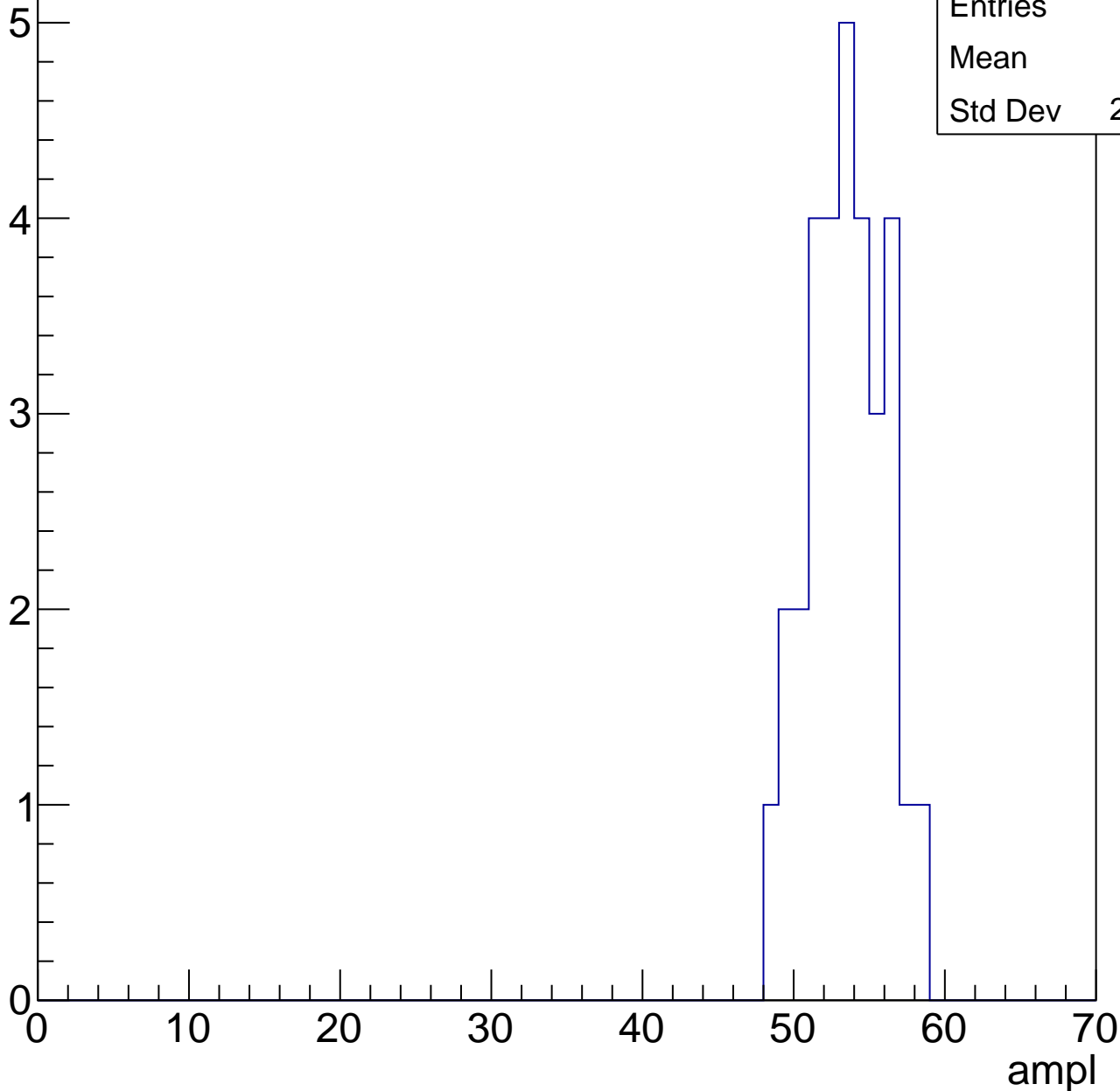


# B1L002S, U3-ch80, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

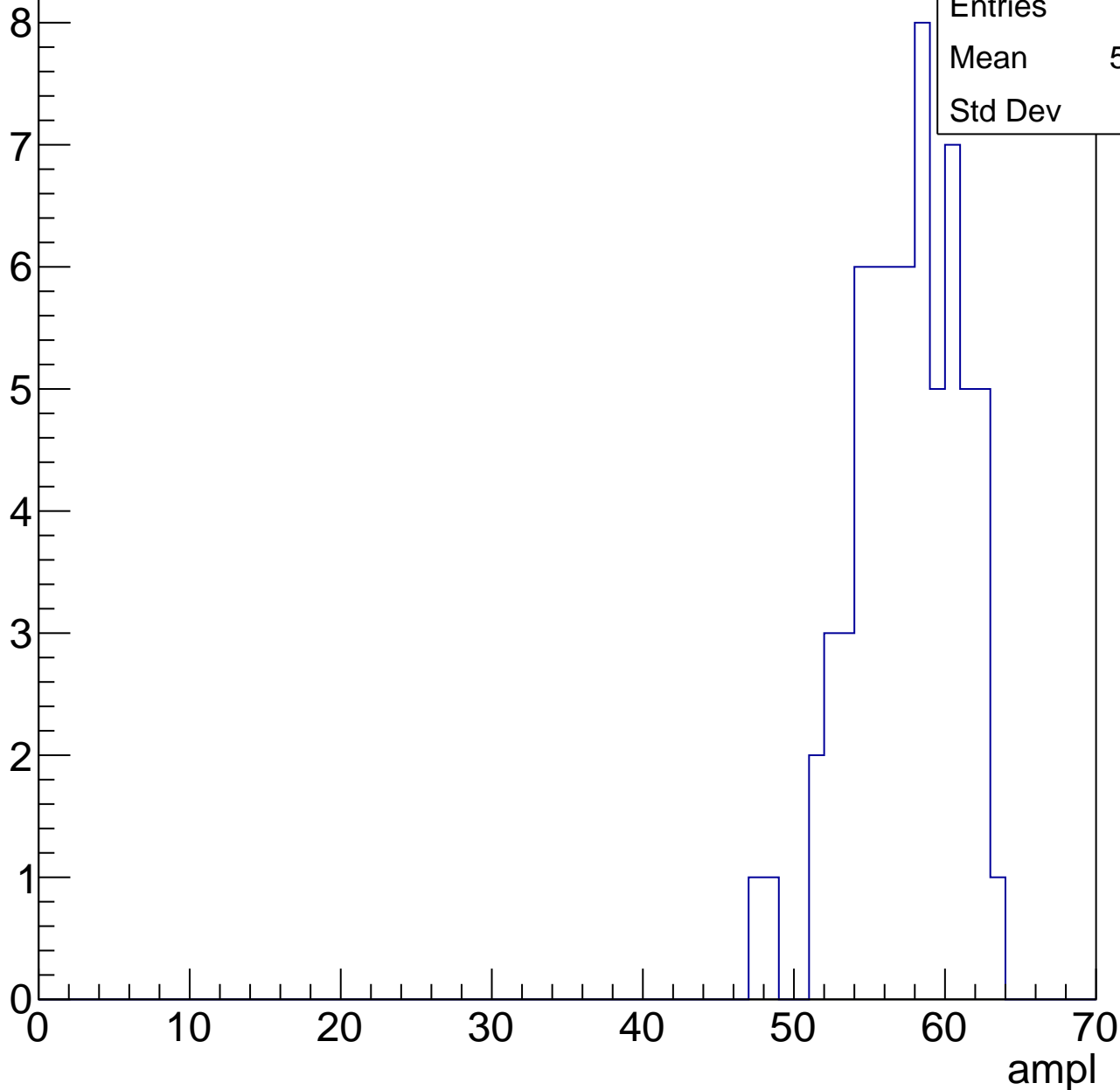
Entries	31
Mean	53
Std Dev	2.463



# B1L002S, U3-ch80, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

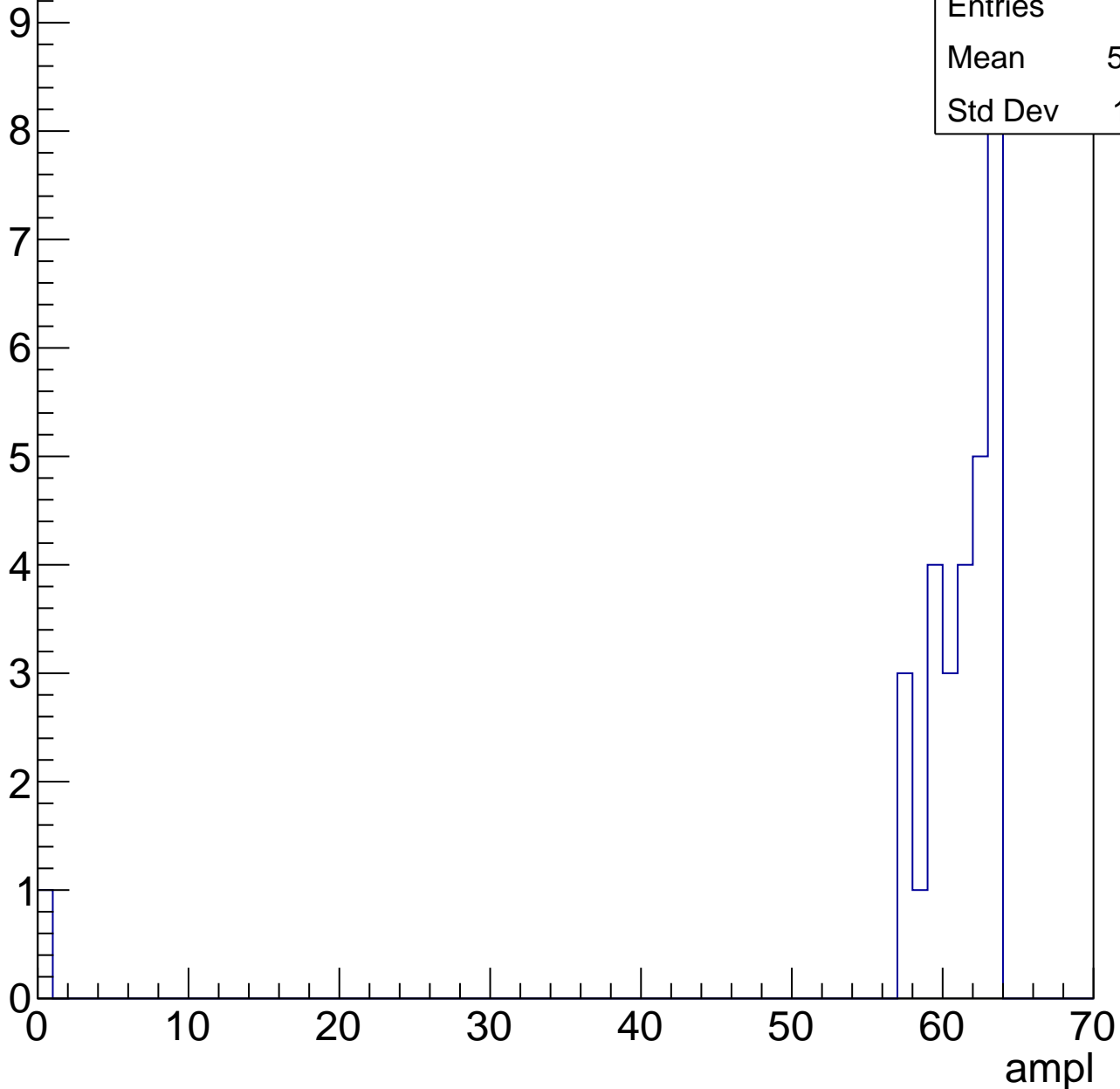


# B1L002S, U3-ch80, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	30
Mean	58.87
Std Dev	11.11

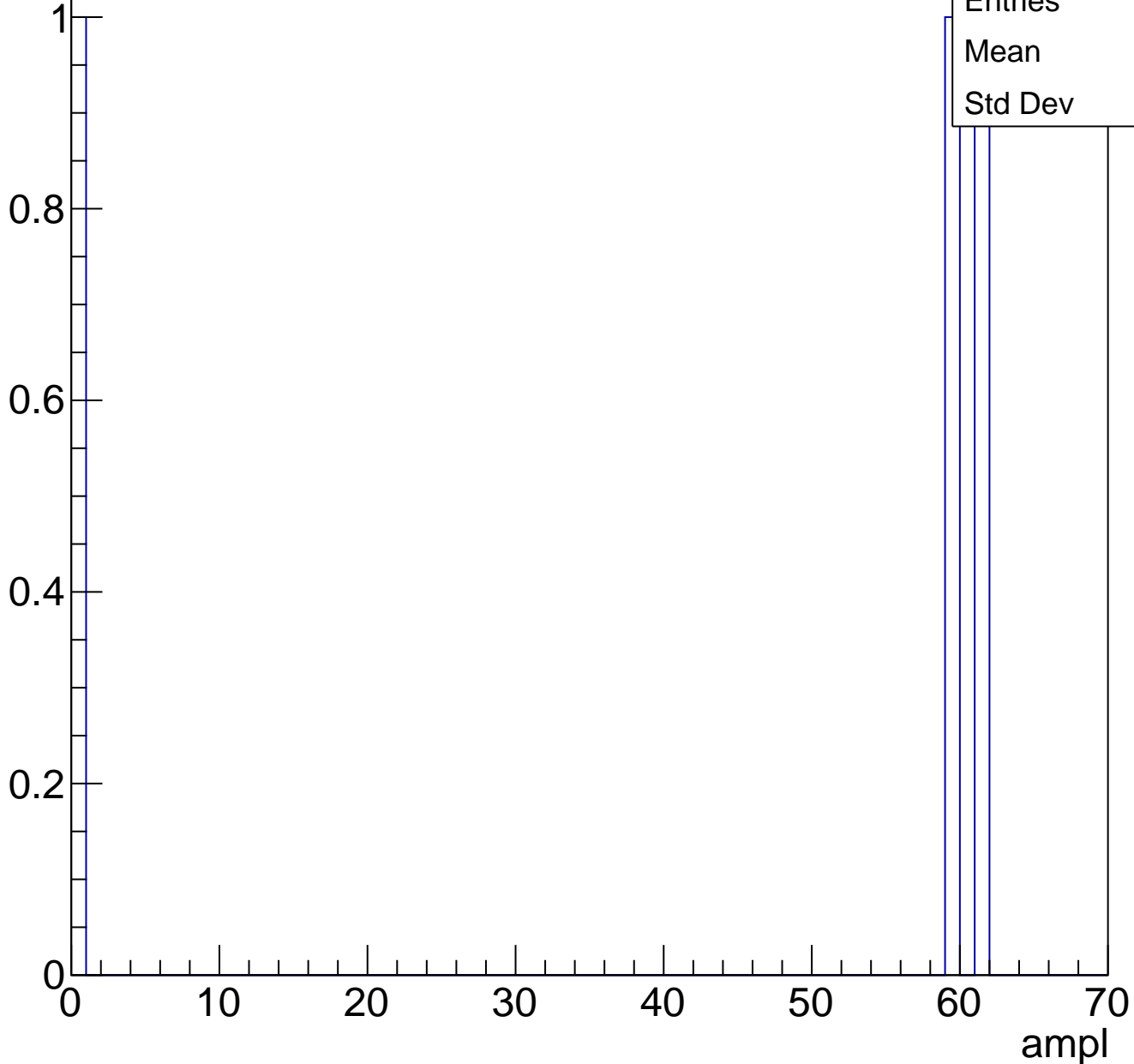




# B1L002S, U3-ch80, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch81, adc0

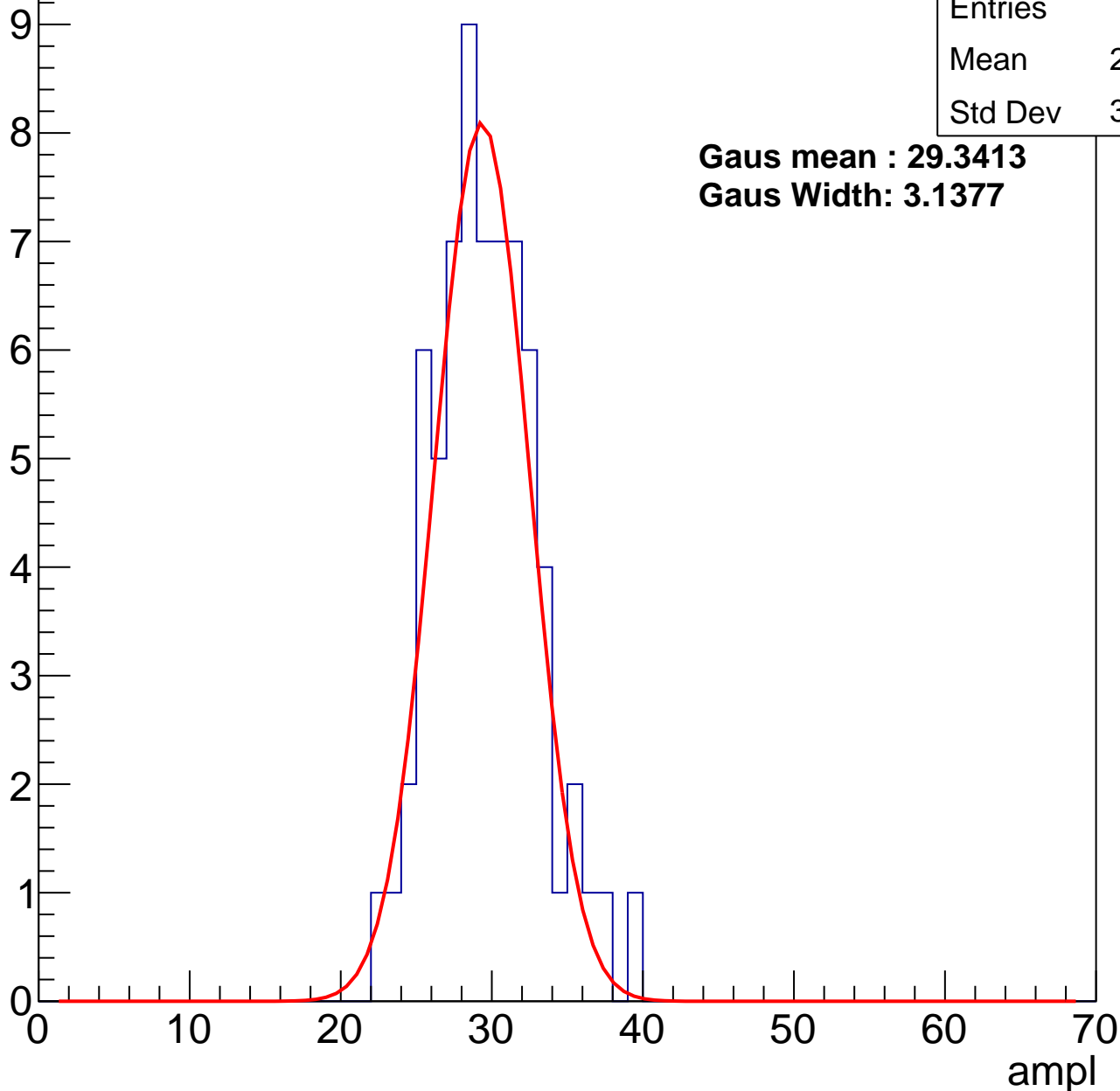
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	29.18
Std Dev	3.374

**Gaus mean : 29.3413**

**Gaus Width: 3.1377**



# B1L002S, U3-ch81, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	71
Mean	35.73
Std Dev	3.39

**Gaus mean : 36.0008**

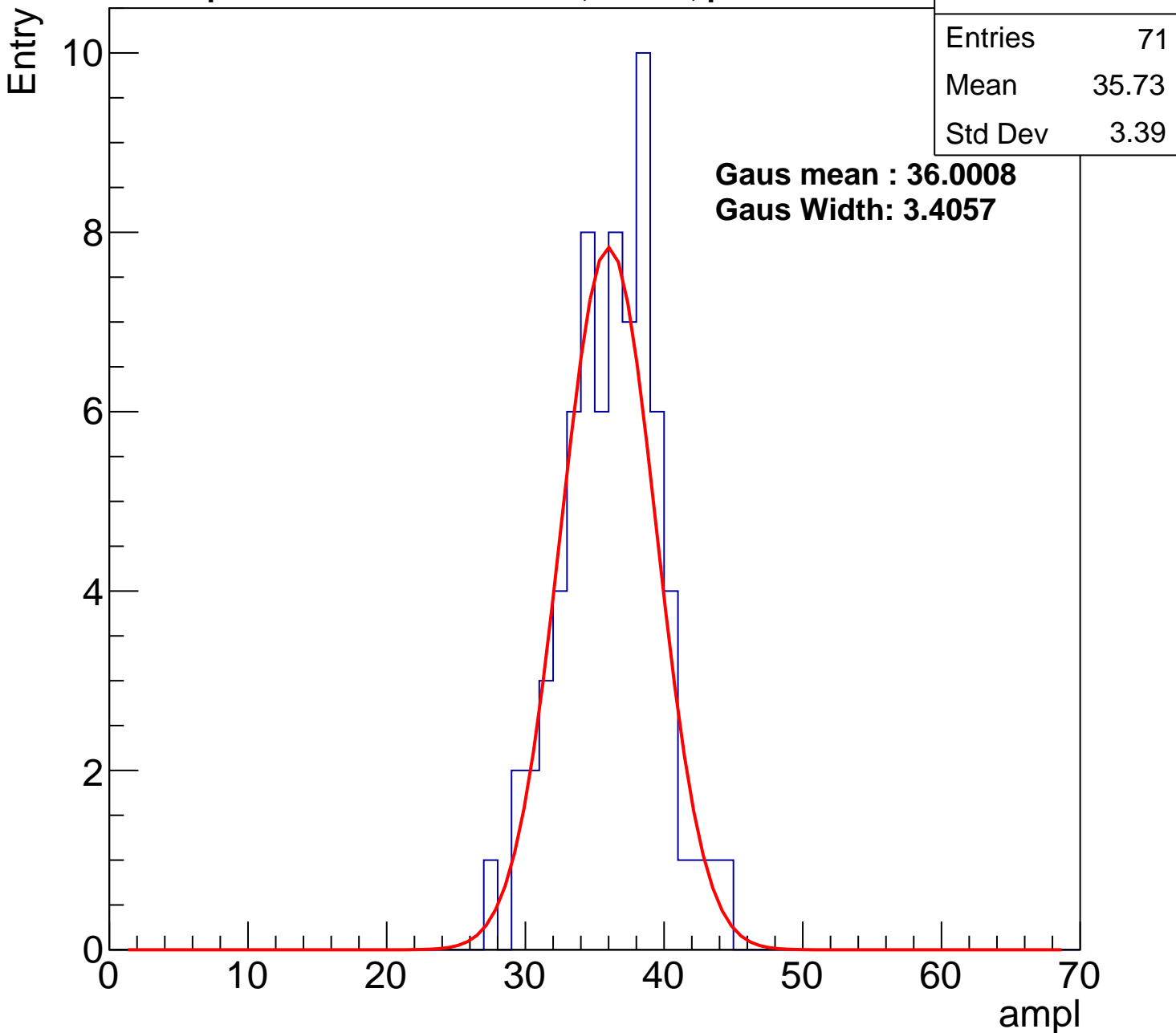
**Gaus Width: 3.4057**

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch81, adc2

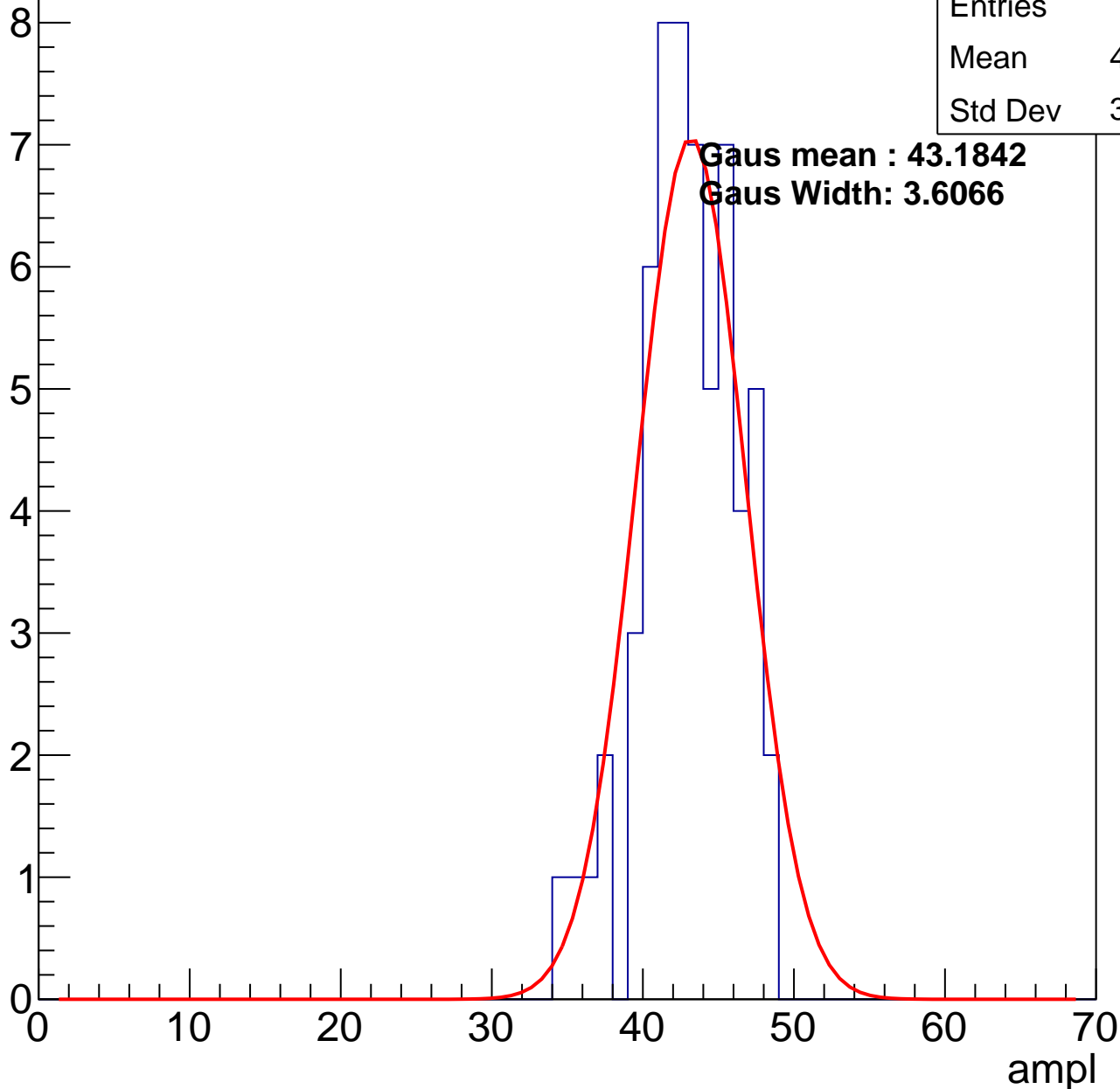
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	42.52
Std Dev	3.144

**Gaus mean : 43.1842**

**Gaus Width: 3.6066**



# B1L002S, U3-ch81, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

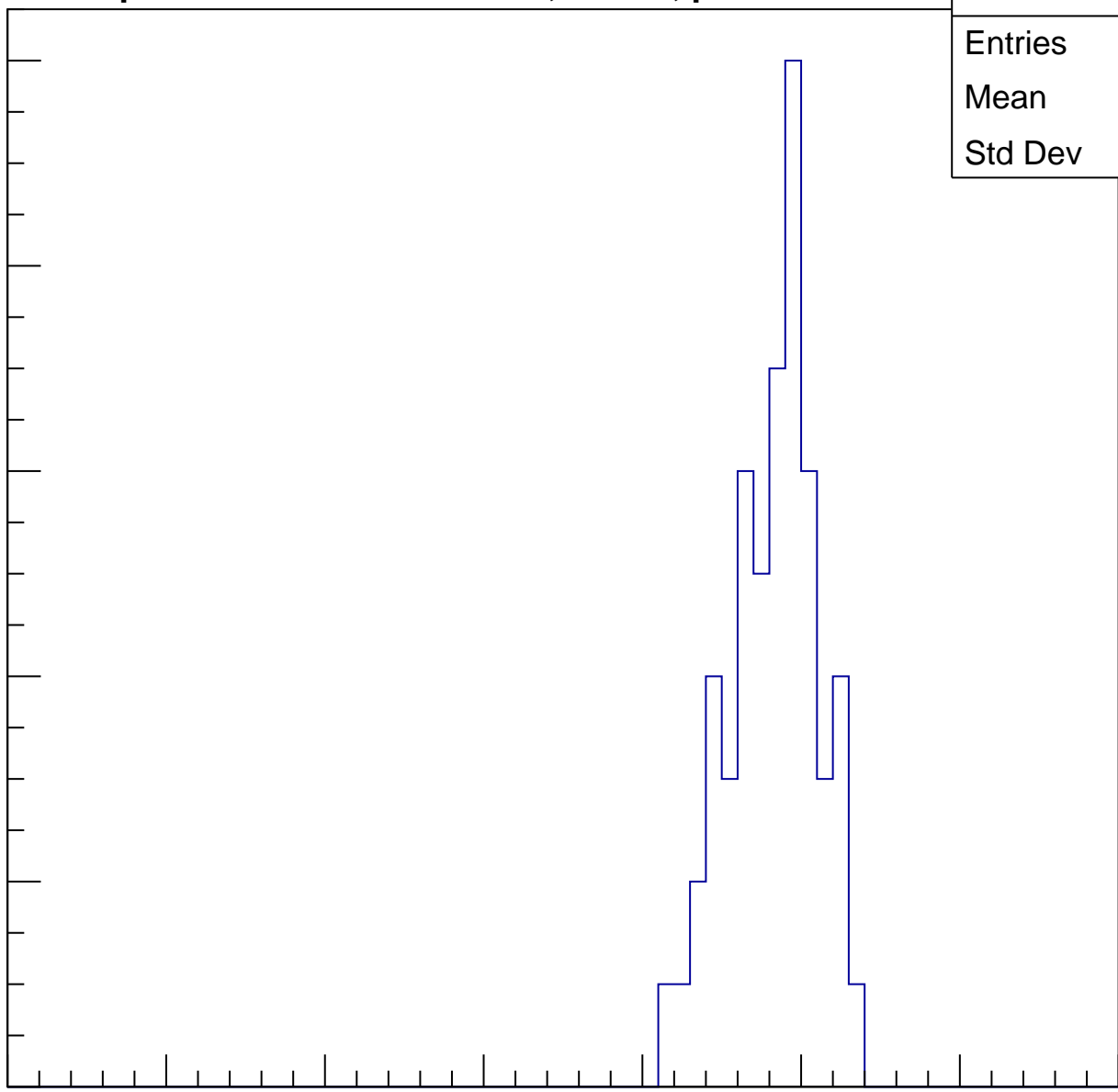
Entries	53
Mean	47.75
Std Dev	2.753

Entry

10  
8  
6  
4  
2  
0

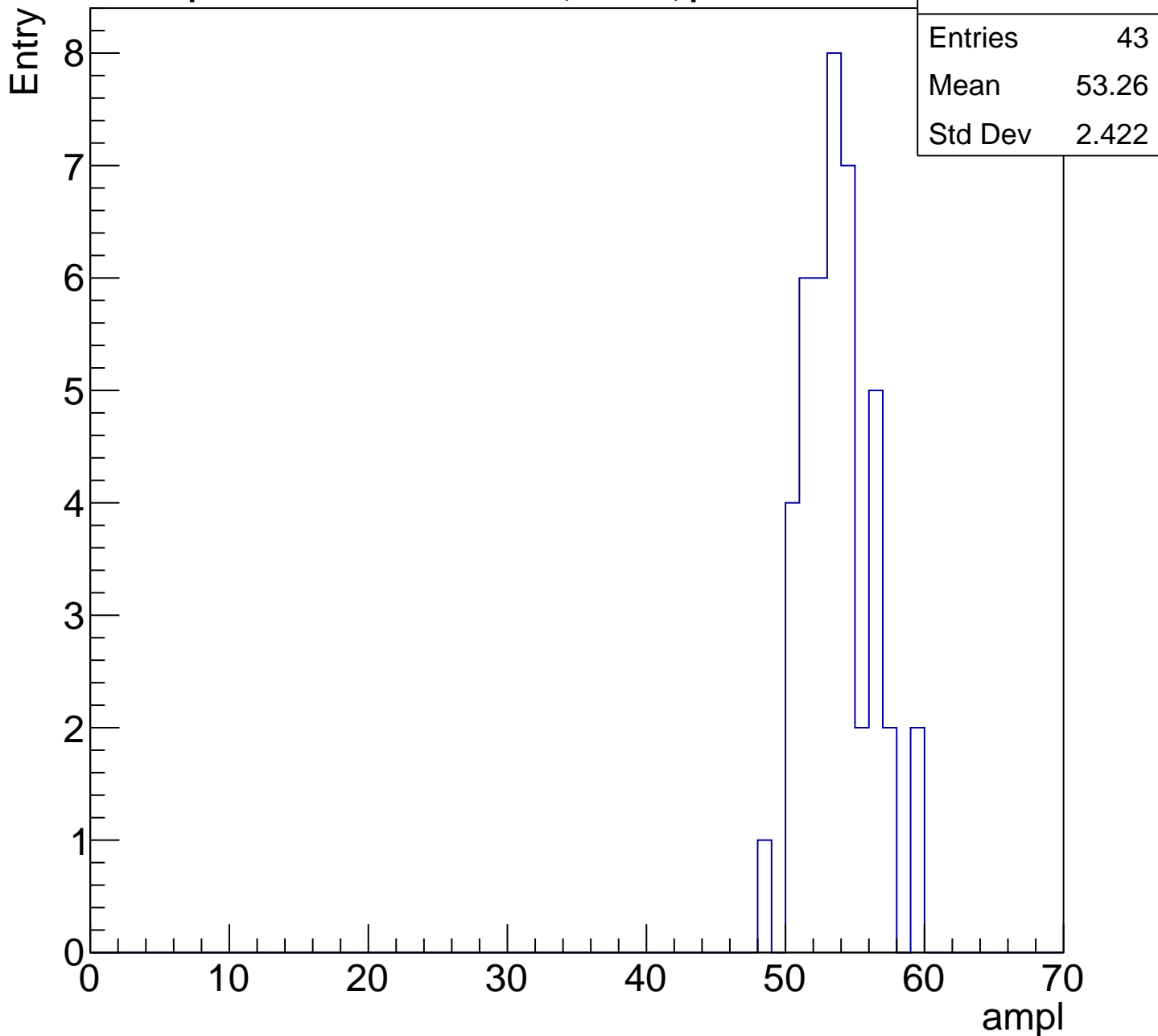
0 10 20 30 40 50 60 70

ampl



# B1L002S, U3-ch81, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

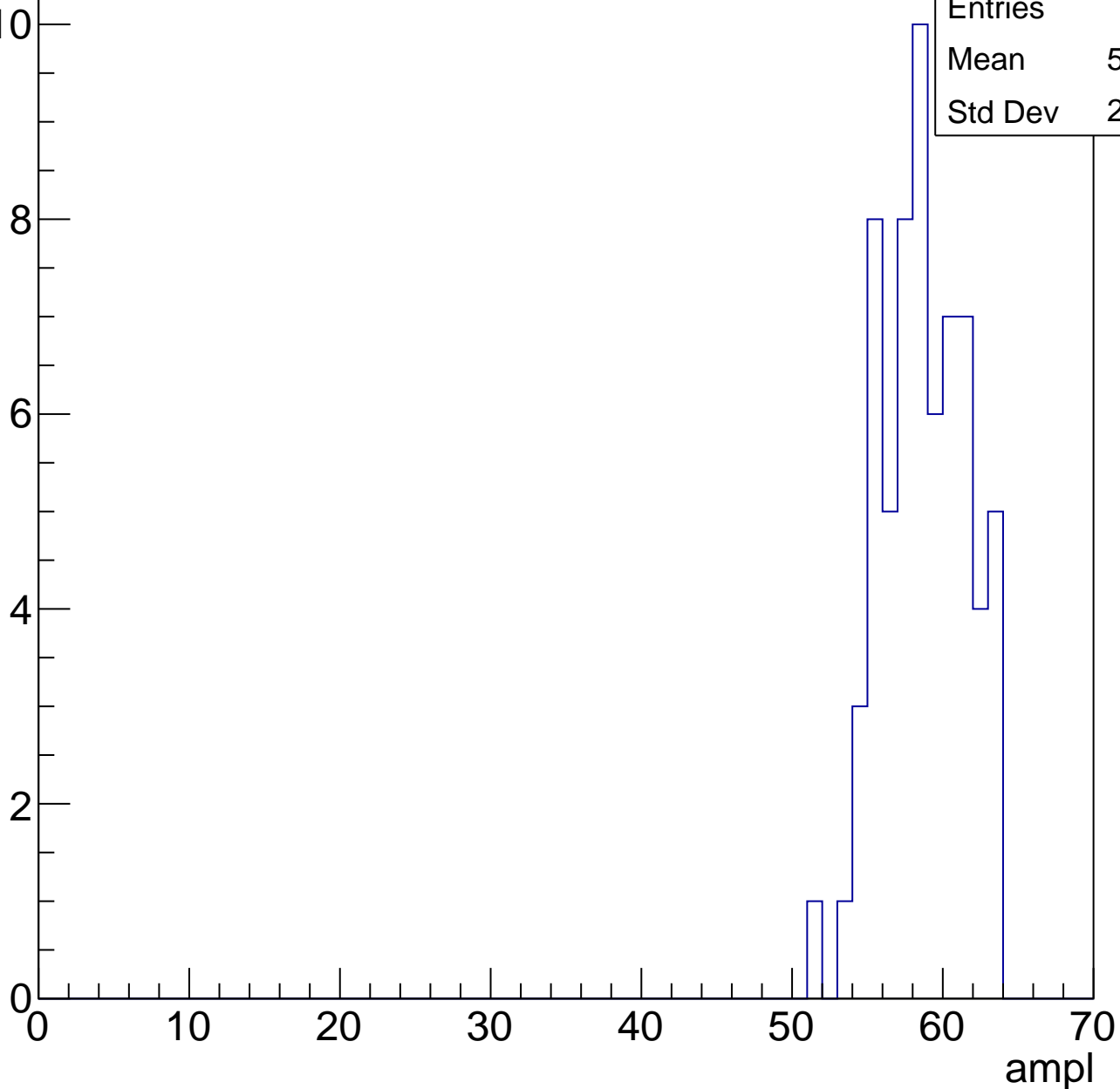


# B1L002S, U3-ch81, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	58.25
Std Dev	2.785

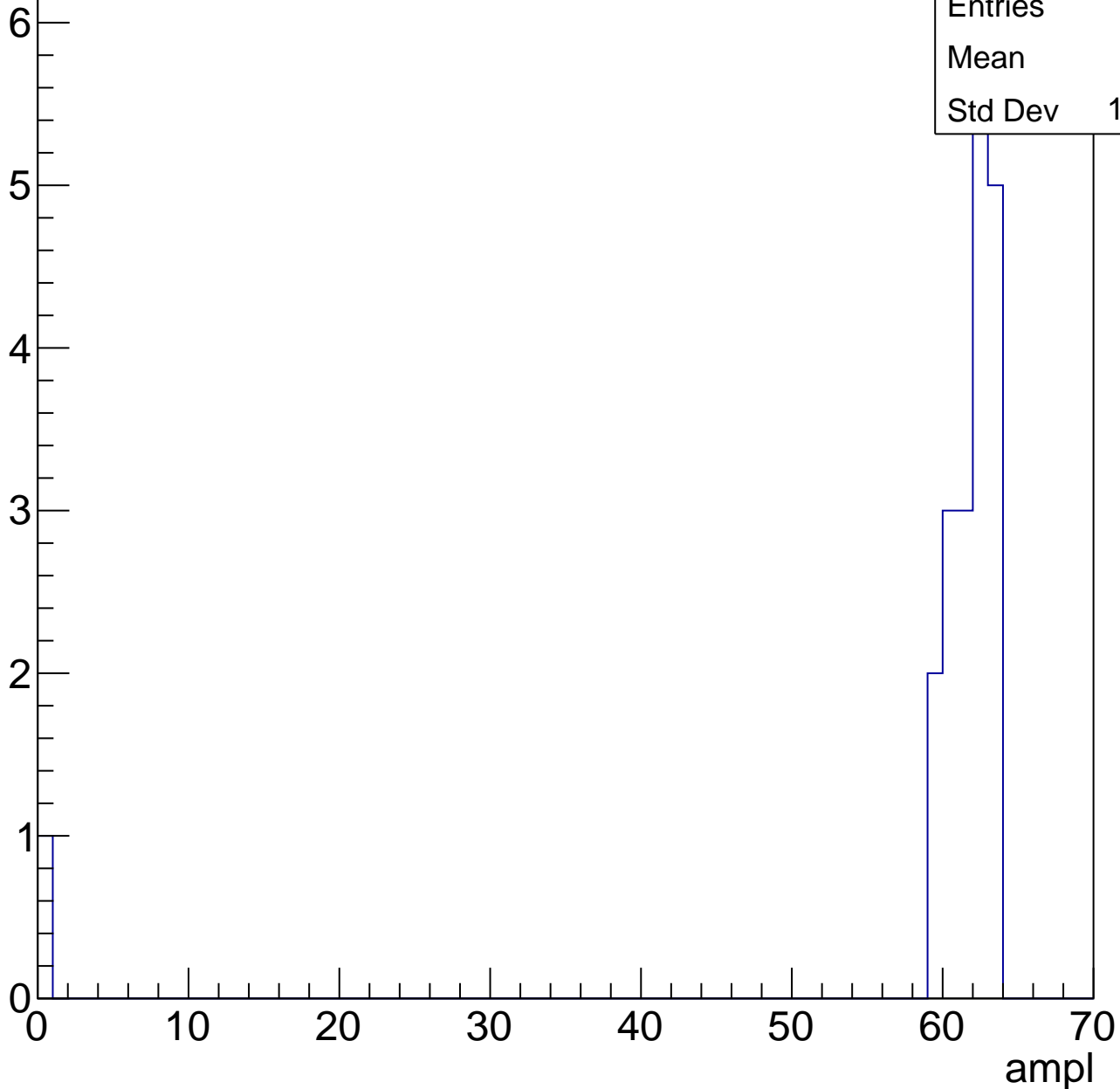


# B1L002S, U3-ch81, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	20
Mean	58.4
Std Dev	13.46





# B1L002S, U3-ch81, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	0
Mean	0
Std Dev	0

# B1L002S, U3-ch82, adc0

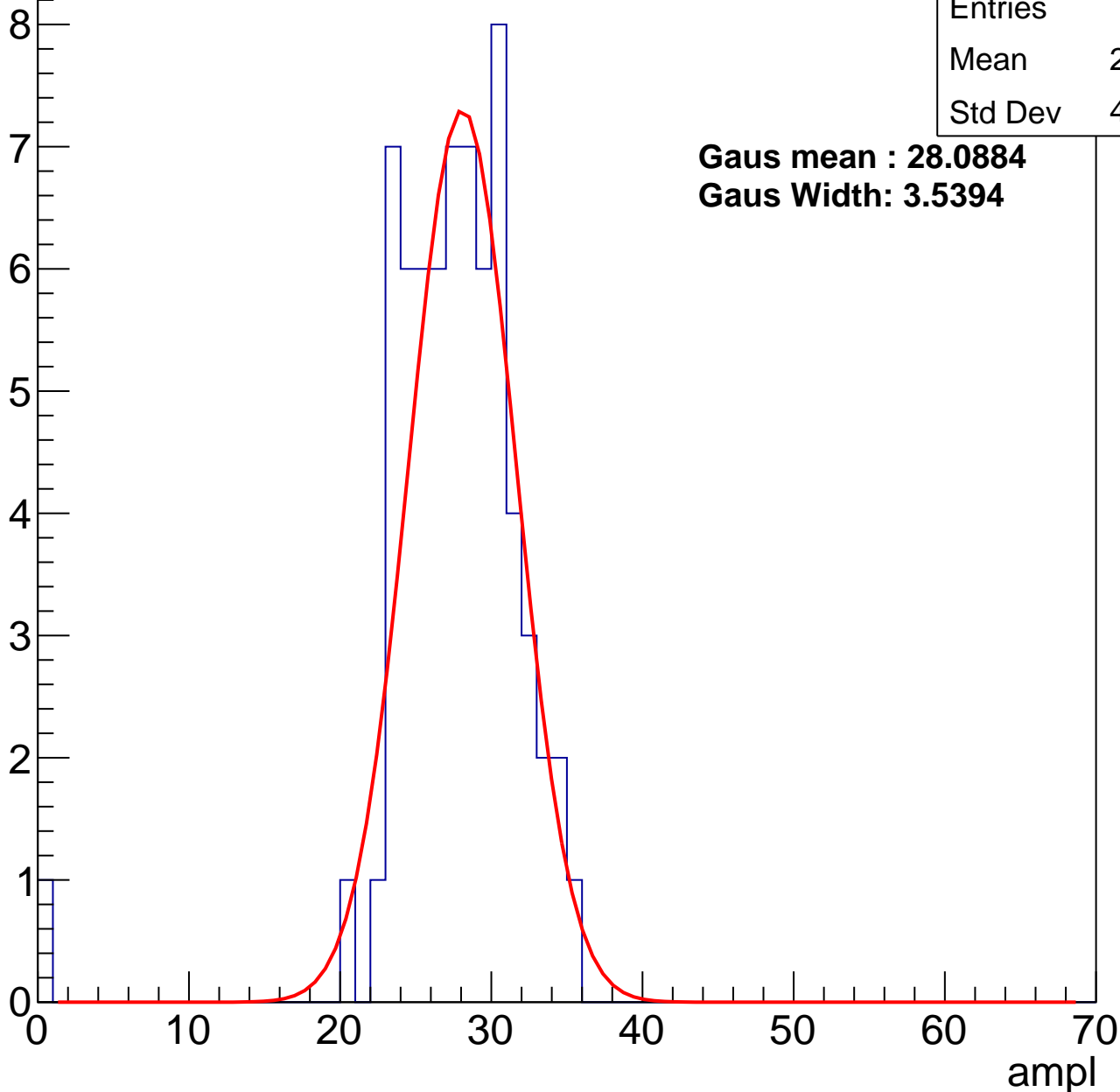
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.07
Std Dev	4.657

**Gaus mean : 28.0884**

**Gaus Width: 3.5394**



# B1L002S, U3-ch82, adc1

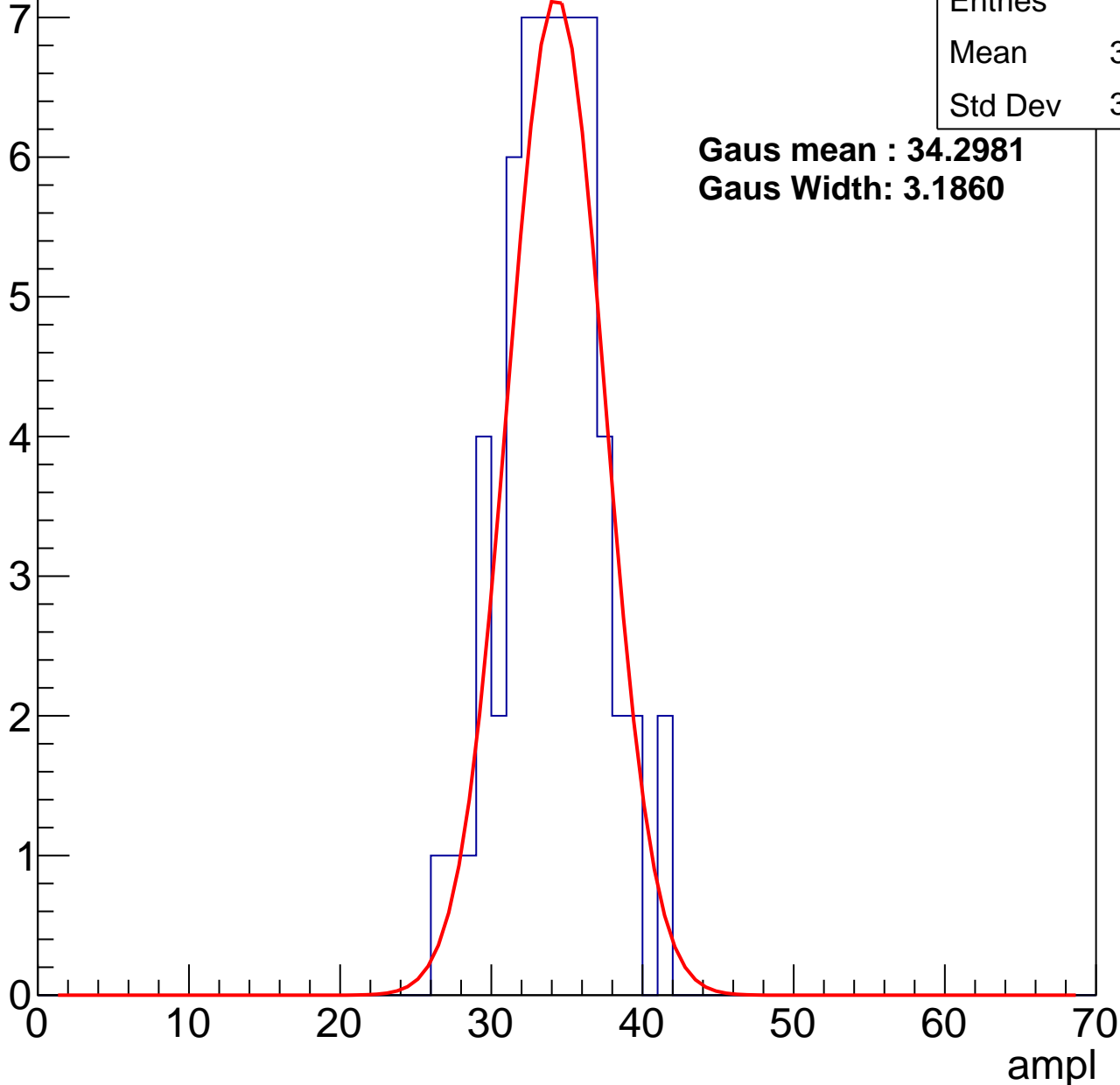
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	33.62
Std Dev	3.194

**Gaus mean : 34.2981**

**Gaus Width: 3.1860**



# B1L002S, U3-ch82, adc2

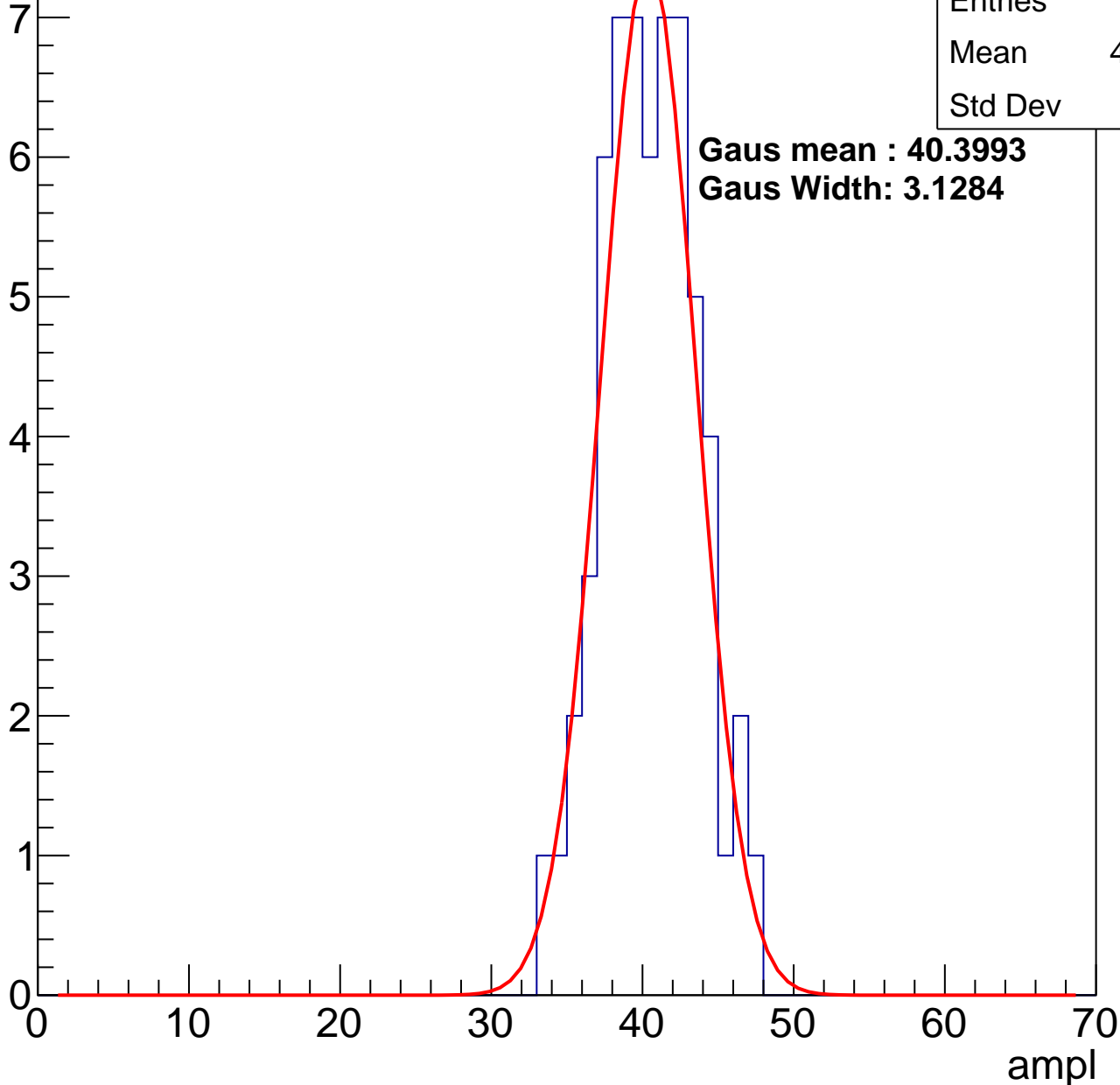
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	40.03
Std Dev	3.06

**Gaus mean : 40.3993**

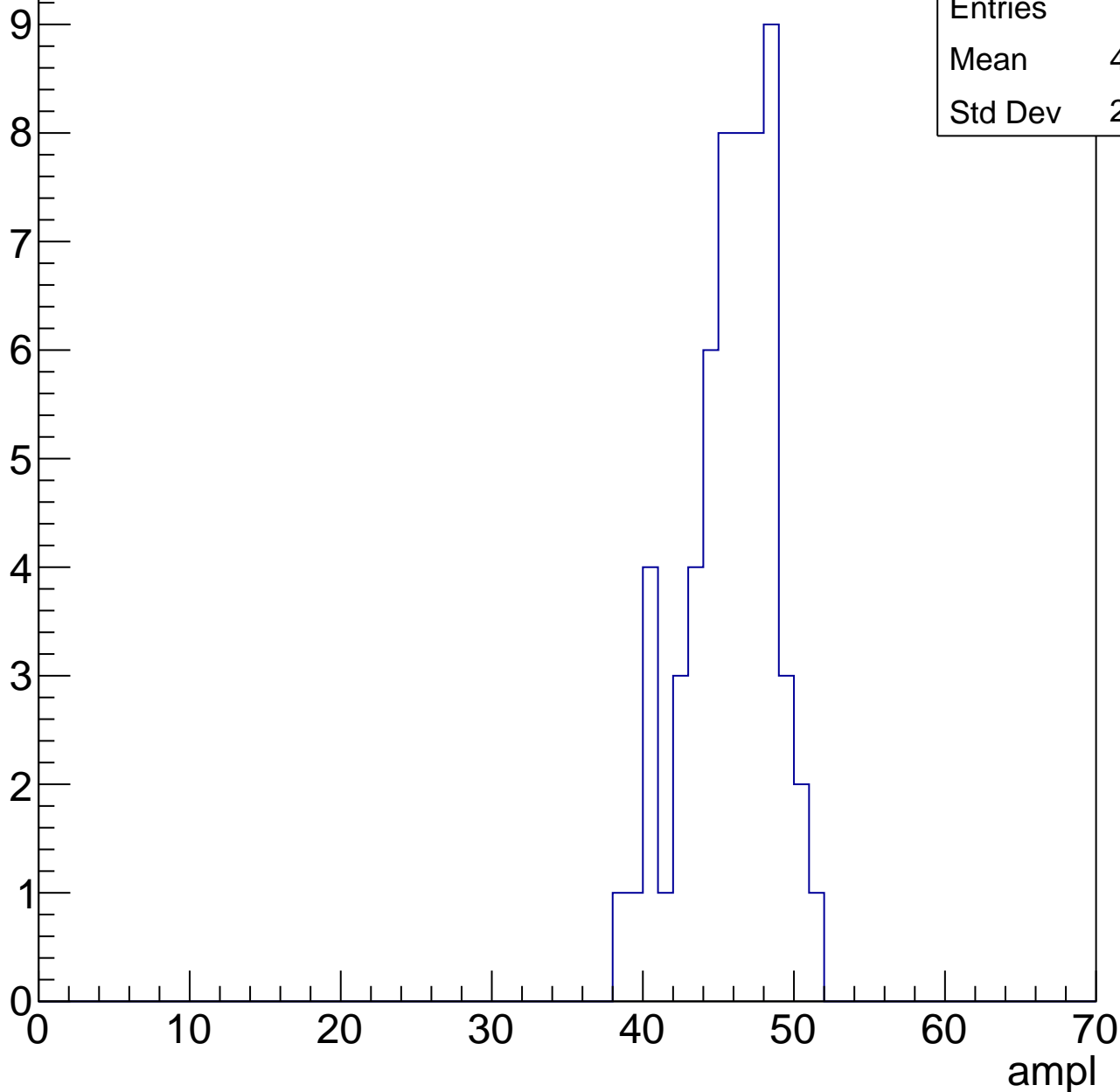
**Gaus Width: 3.1284**



# B1L002S, U3-ch82, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

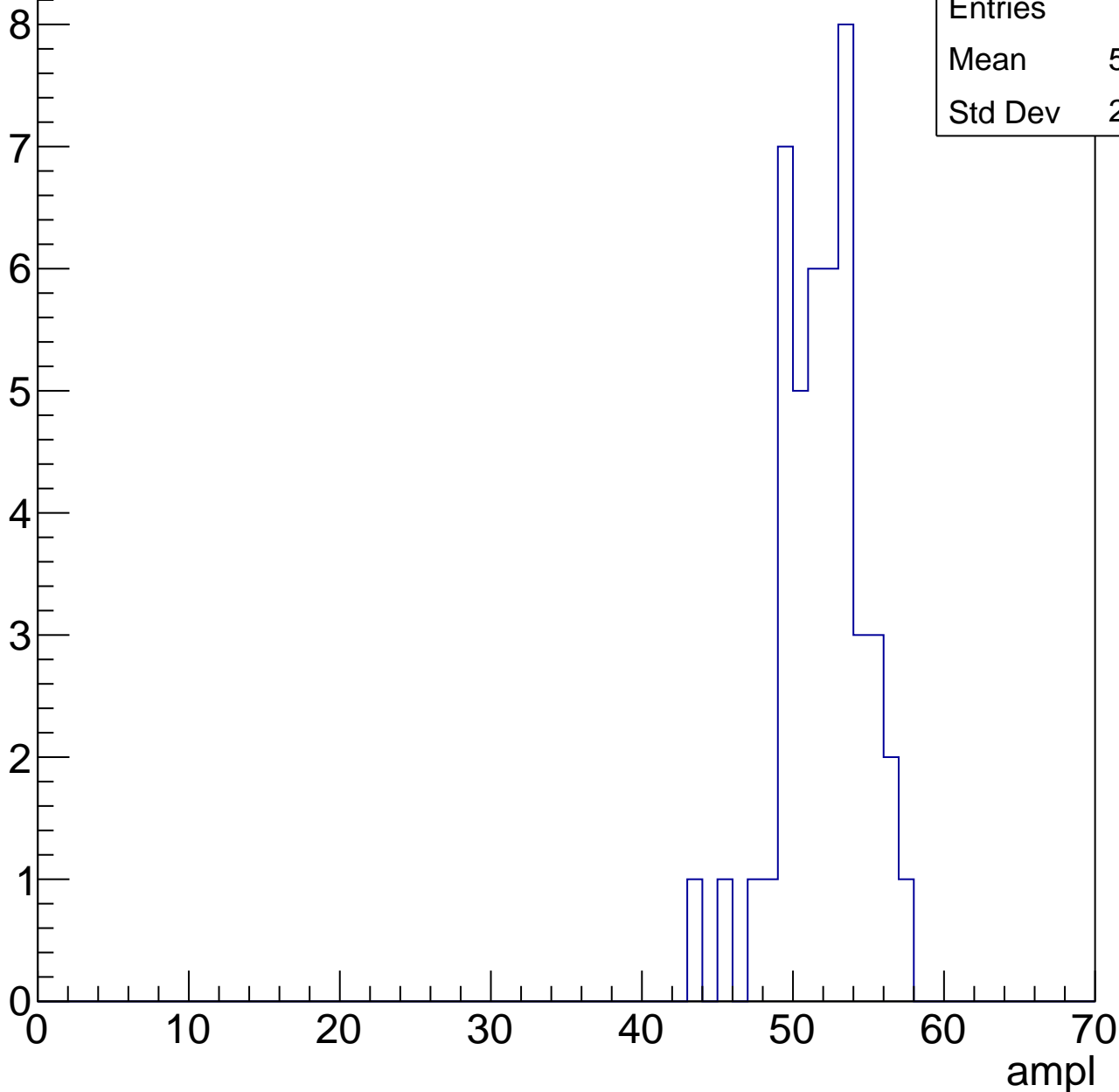


# B1L002S, U3-ch82, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	51.42
Std Dev	2.785

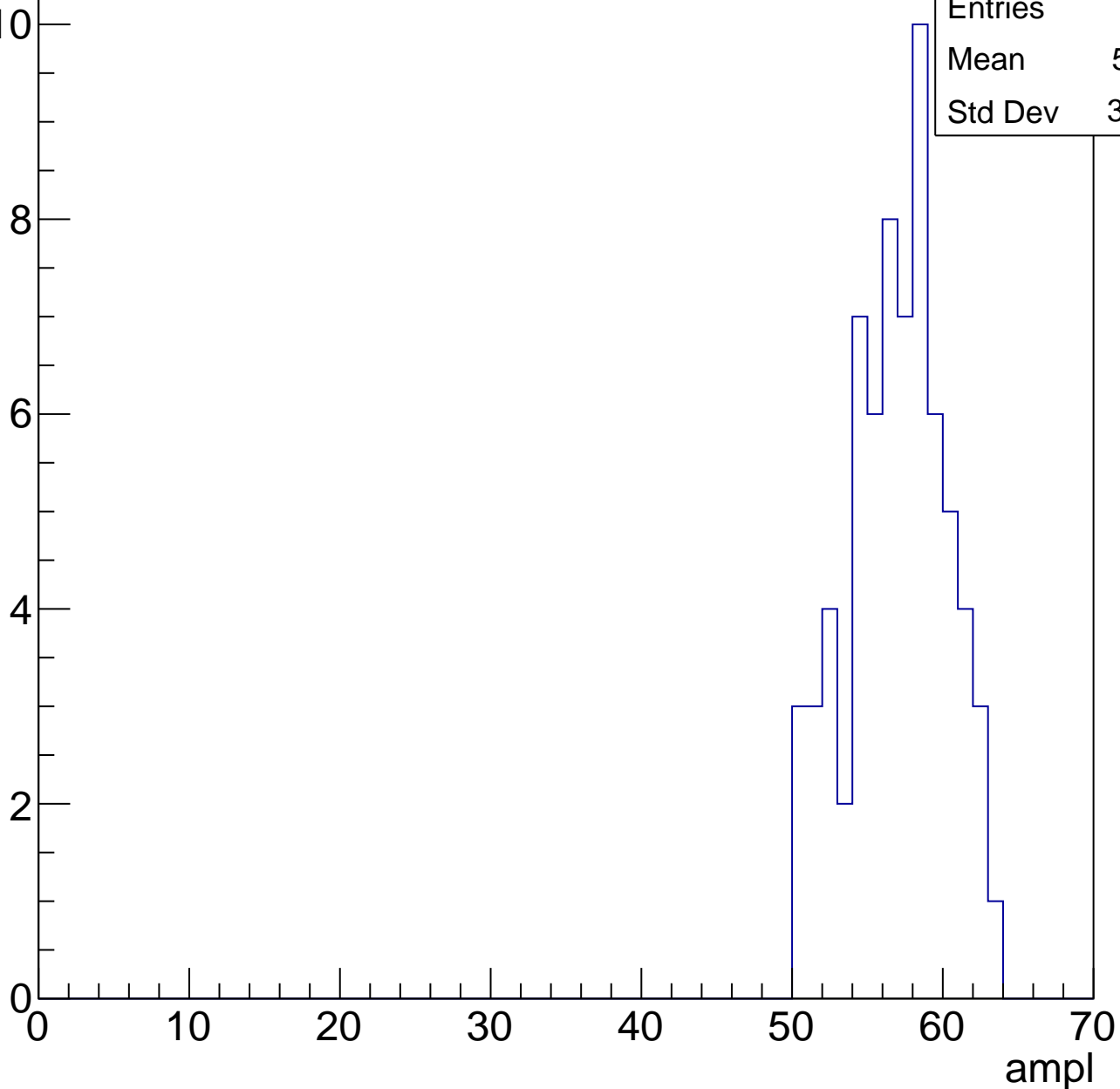


# B1L002S, U3-ch82, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	56.51
Std Dev	3.229

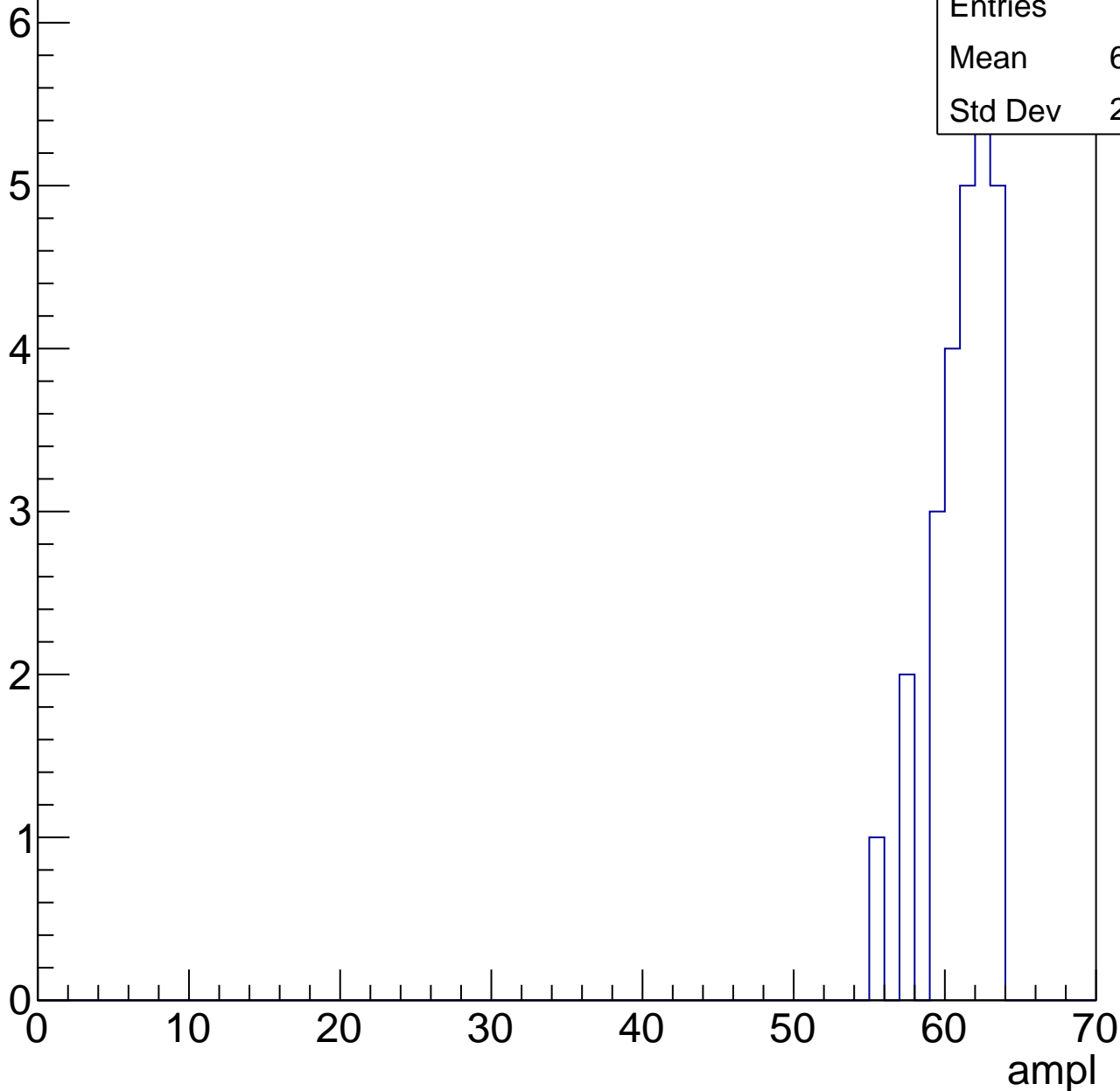


# B1L002S, U3-ch82, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	26
Mean	60.69
Std Dev	2.034

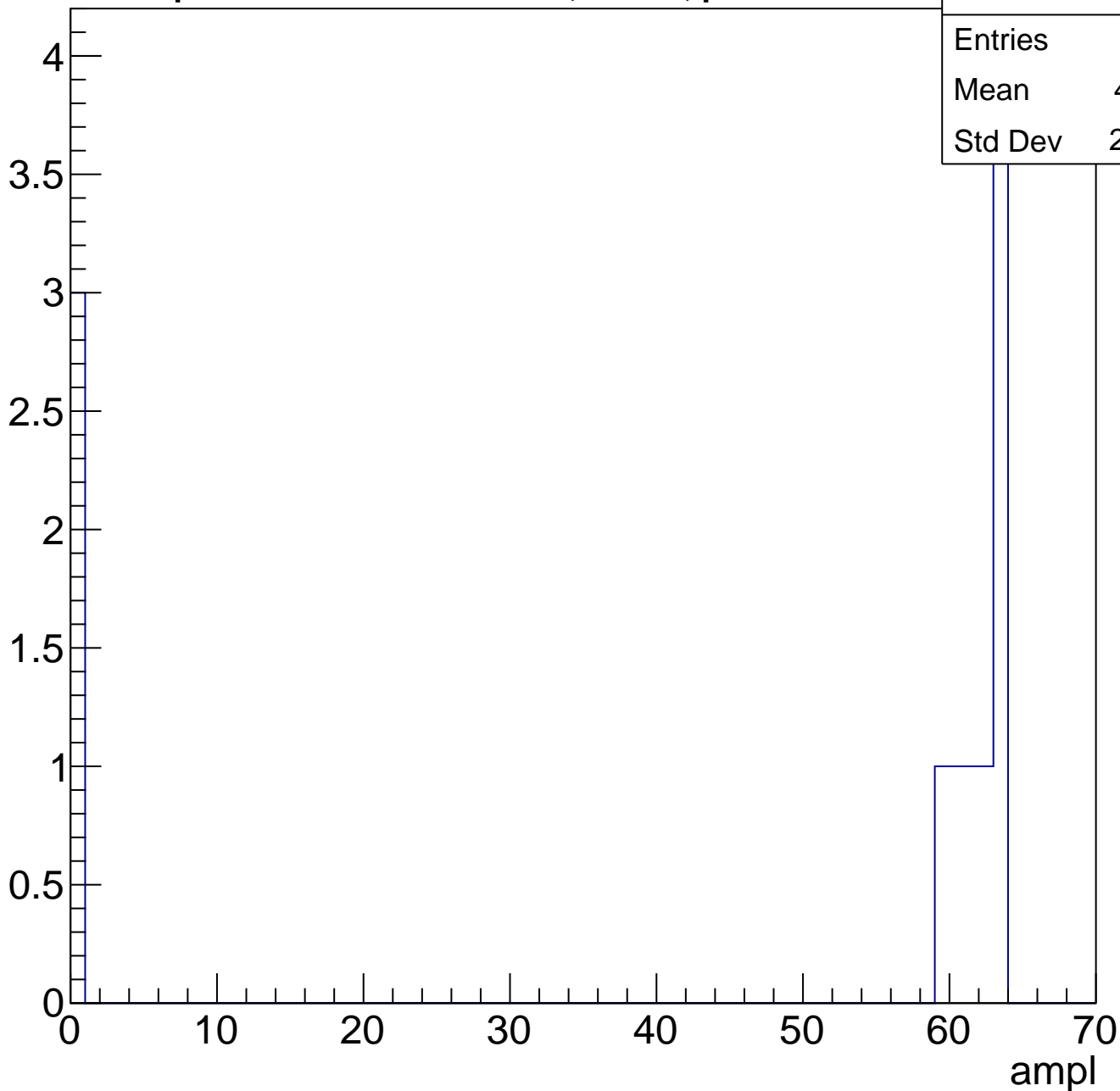




# B1L002S, U3-ch82, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch83, adc0

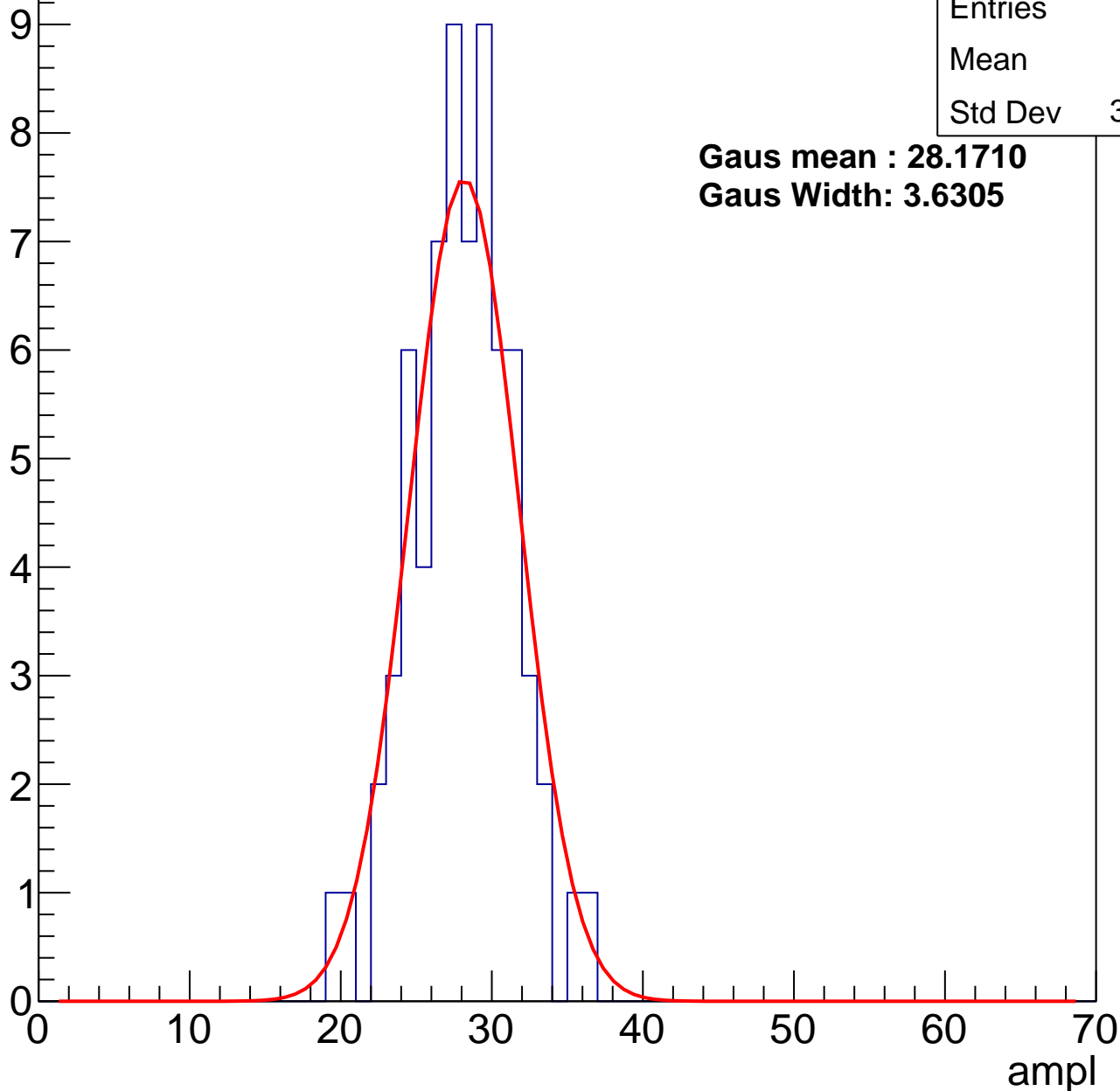
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.6
Std Dev	3.317

**Gaus mean : 28.1710**

**Gaus Width: 3.6305**



# B1L002S, U3-ch83, adc1

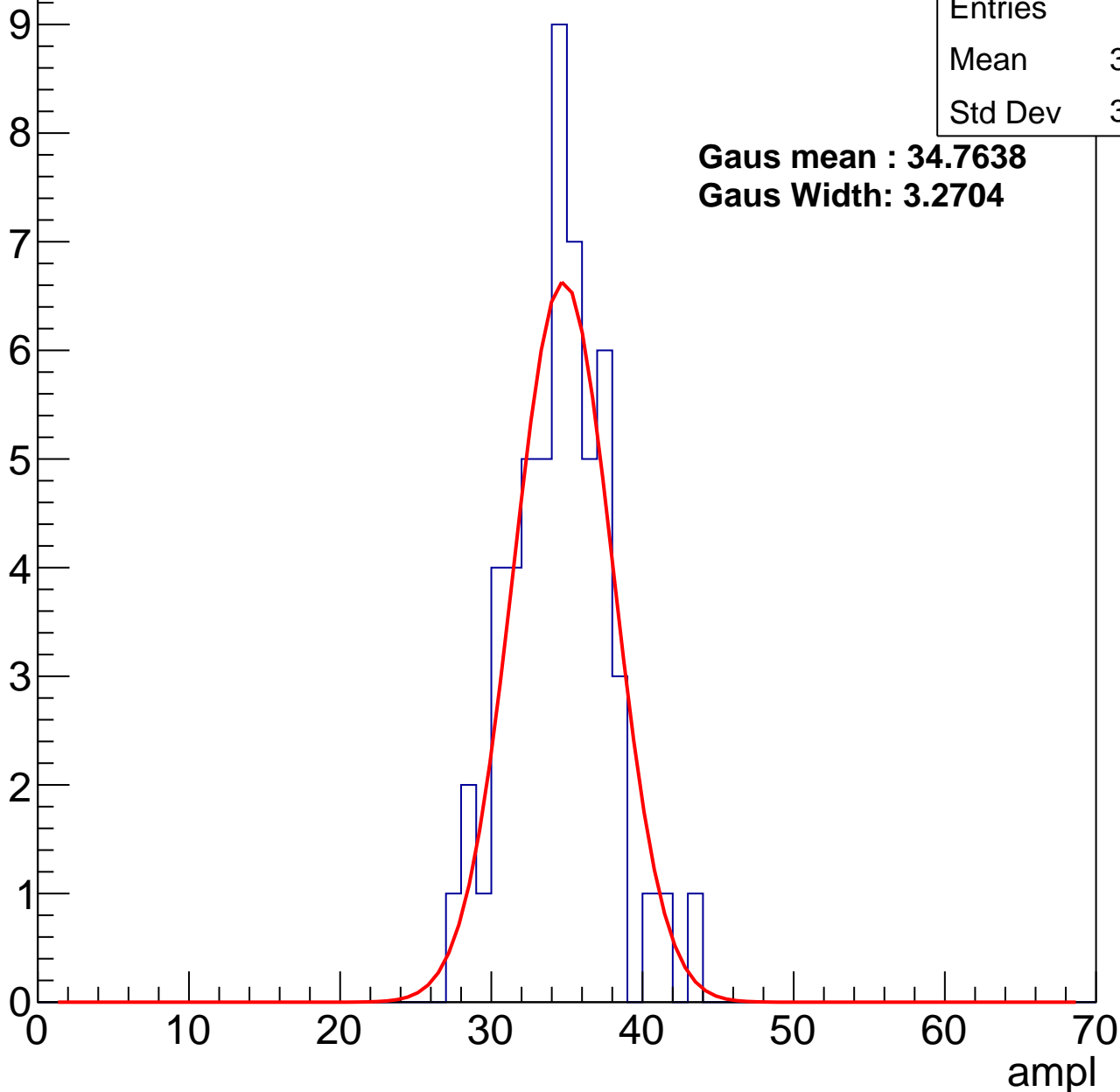
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	34.04
Std Dev	3.208

**Gaus mean : 34.7638**

**Gaus Width: 3.2704**



# B1L002S, U3-ch83, adc2

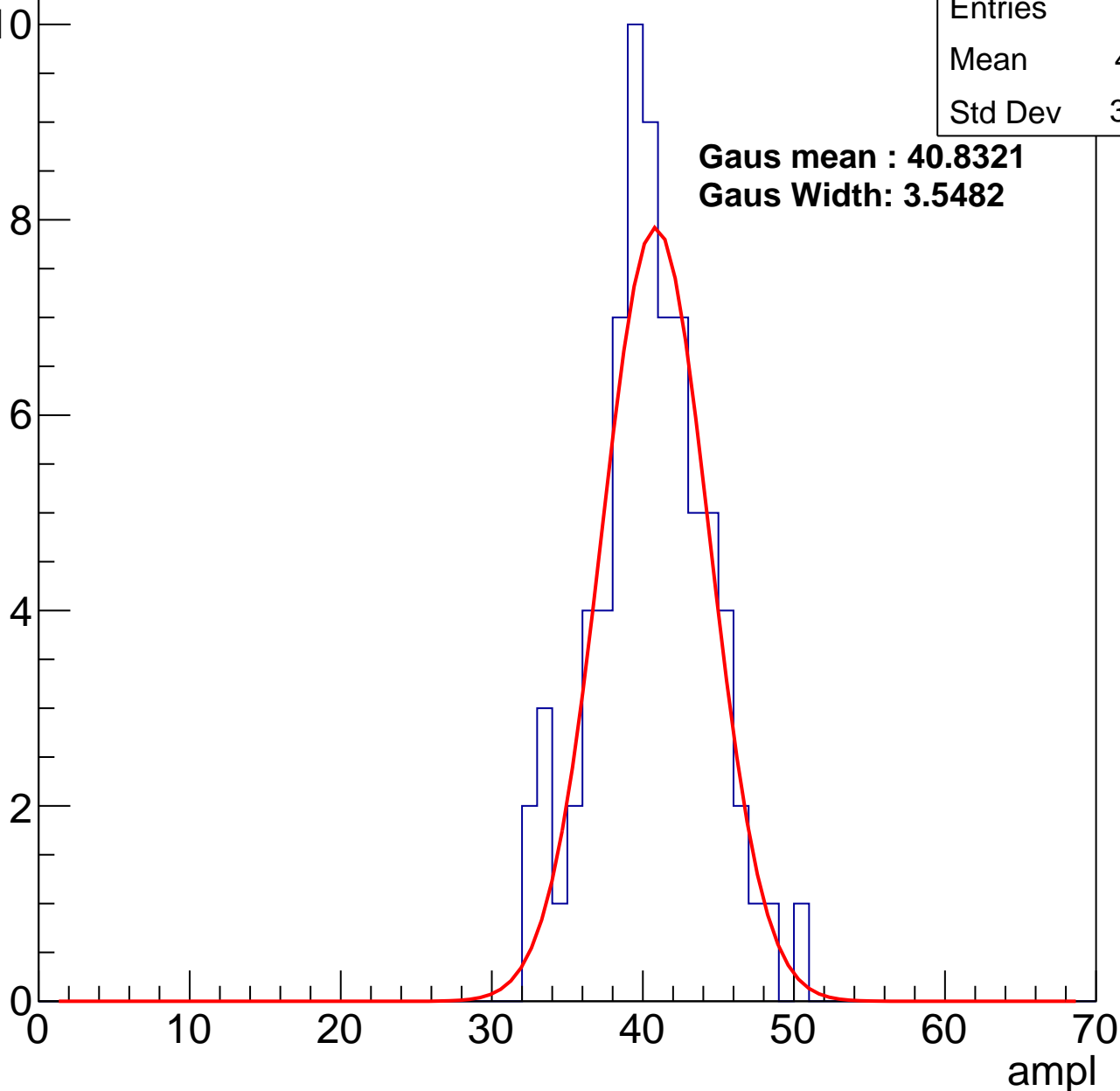
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	40.11
Std Dev	3.729

**Gaus mean : 40.8321**

**Gaus Width: 3.5482**

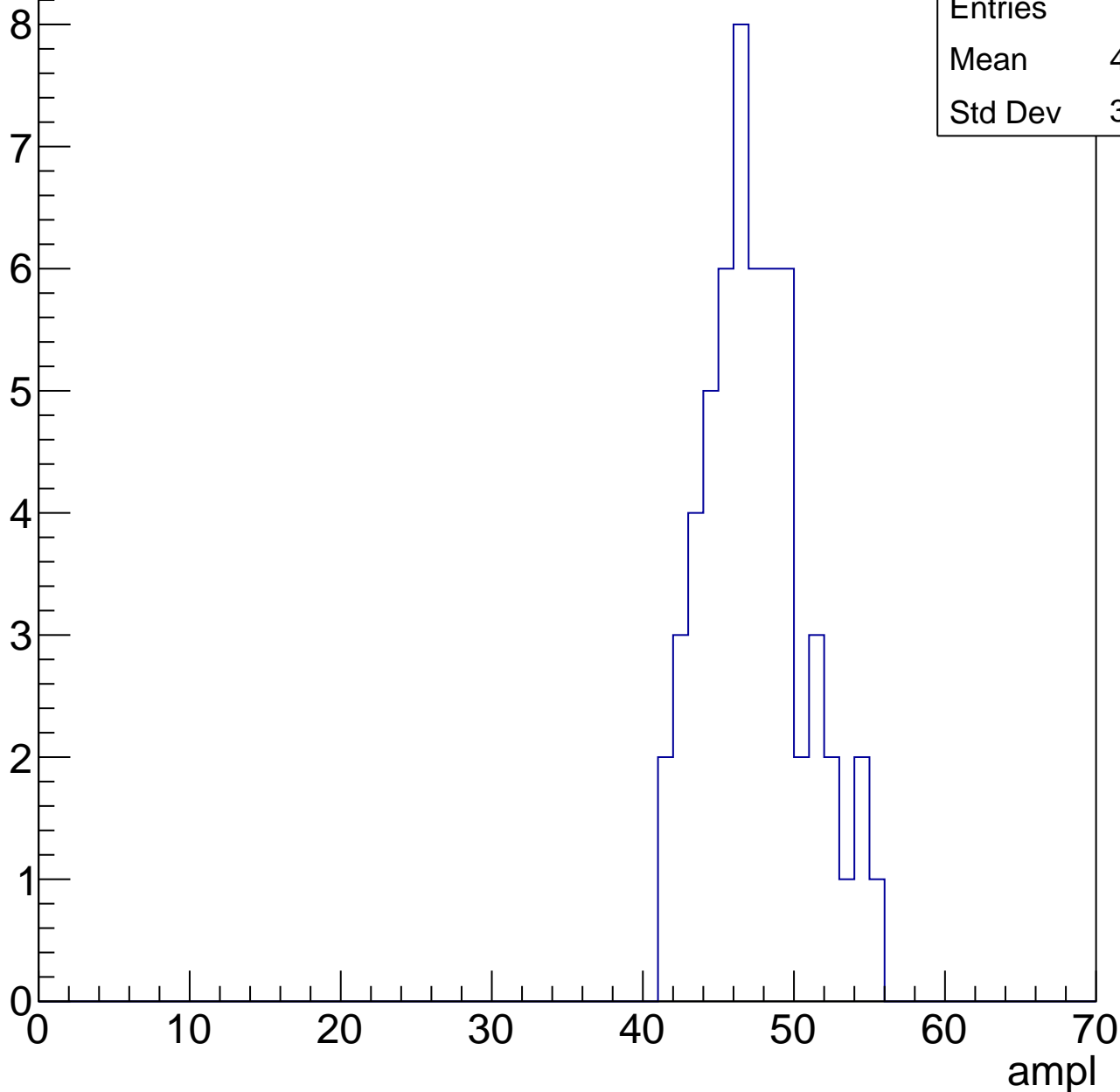


# B1L002S, U3-ch83, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.93
Std Dev	3.329

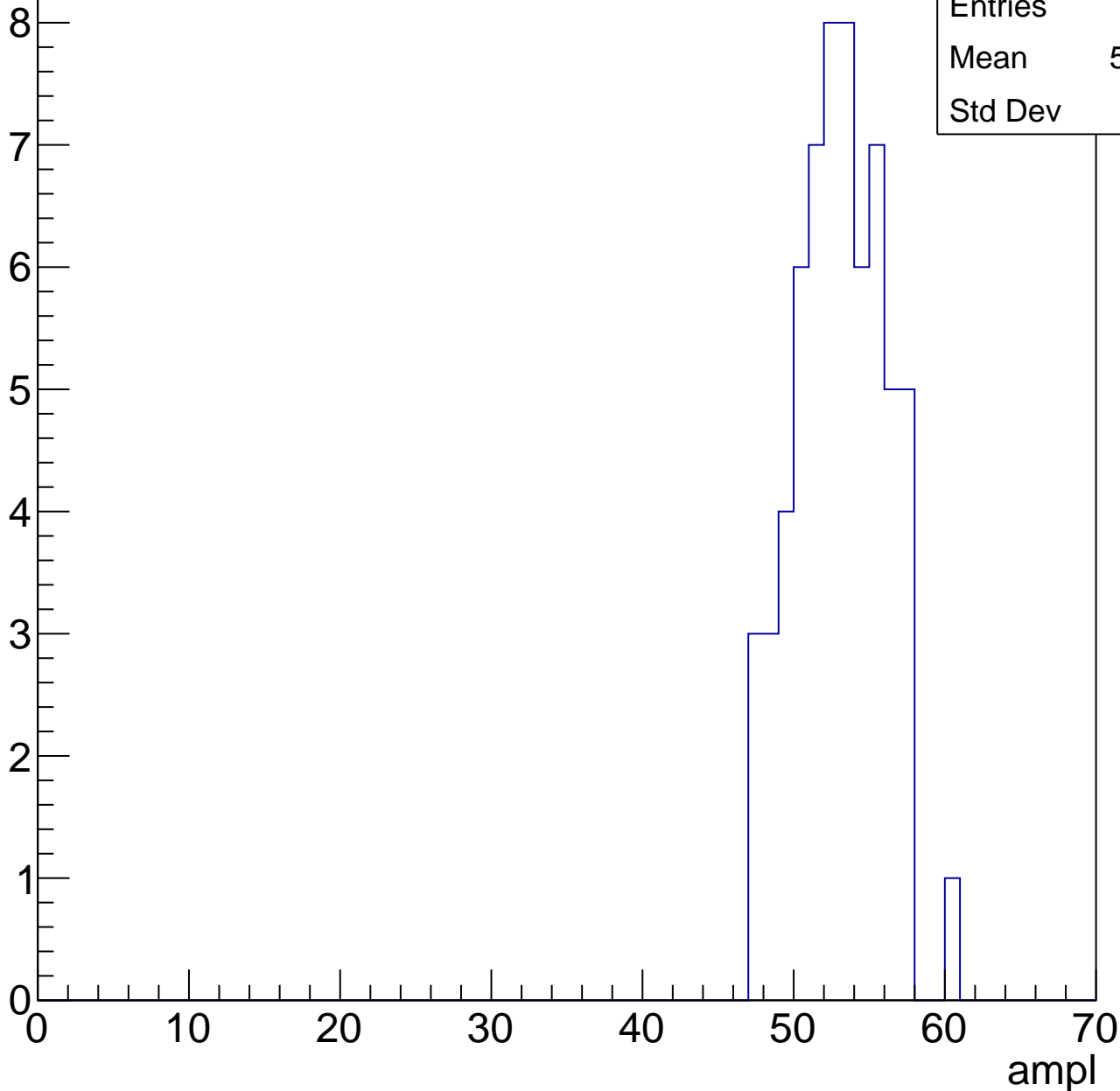


# B1L002S, U3-ch83, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	52.57
Std Dev	2.91



# B1L002S, U3-ch83, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

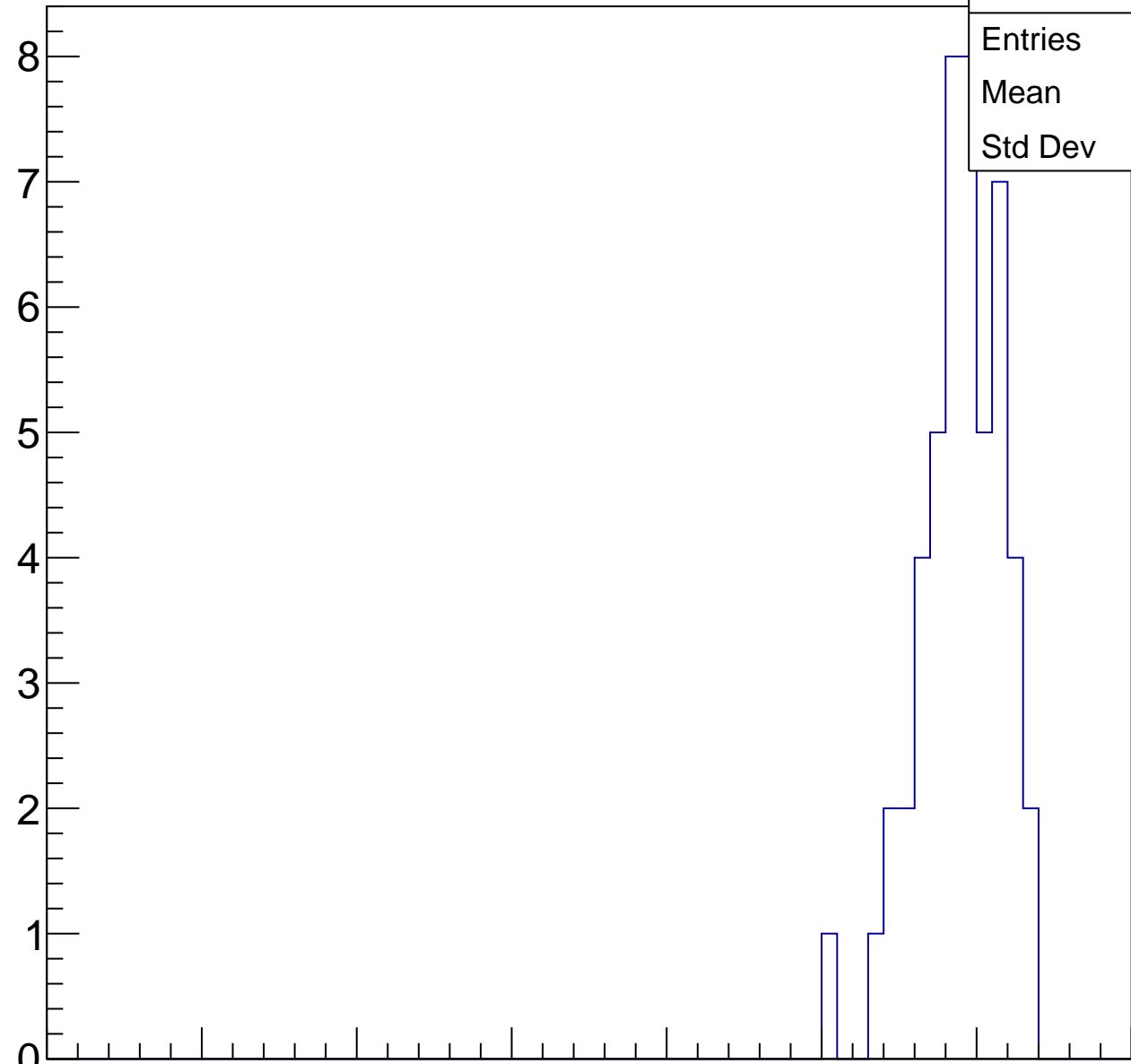
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.51
Std Dev	2.681

ampl

0 10 20 30 40 50 60 70

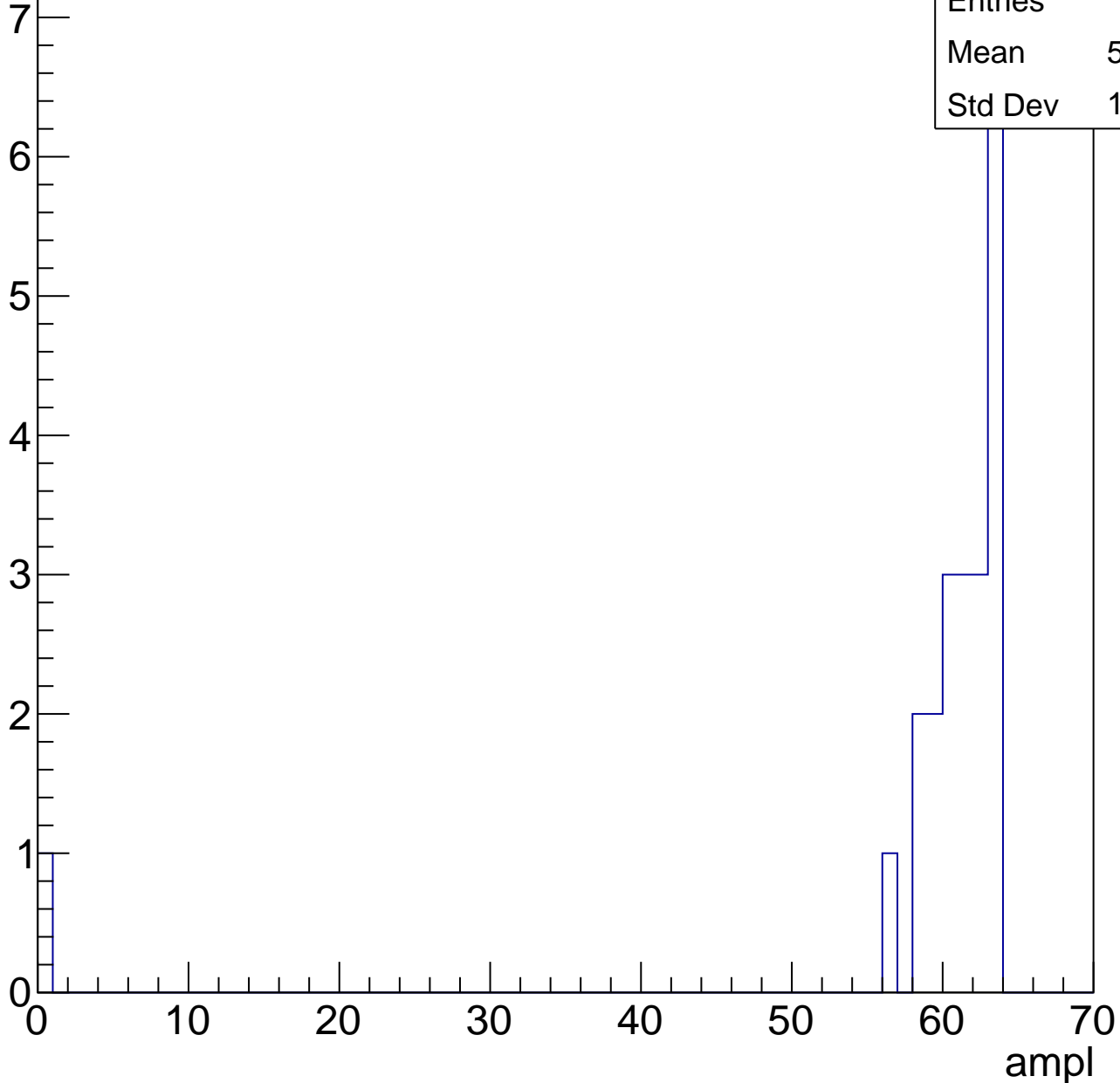


# B1L002S, U3-ch83, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	22
Mean	58.18
Std Dev	12.85

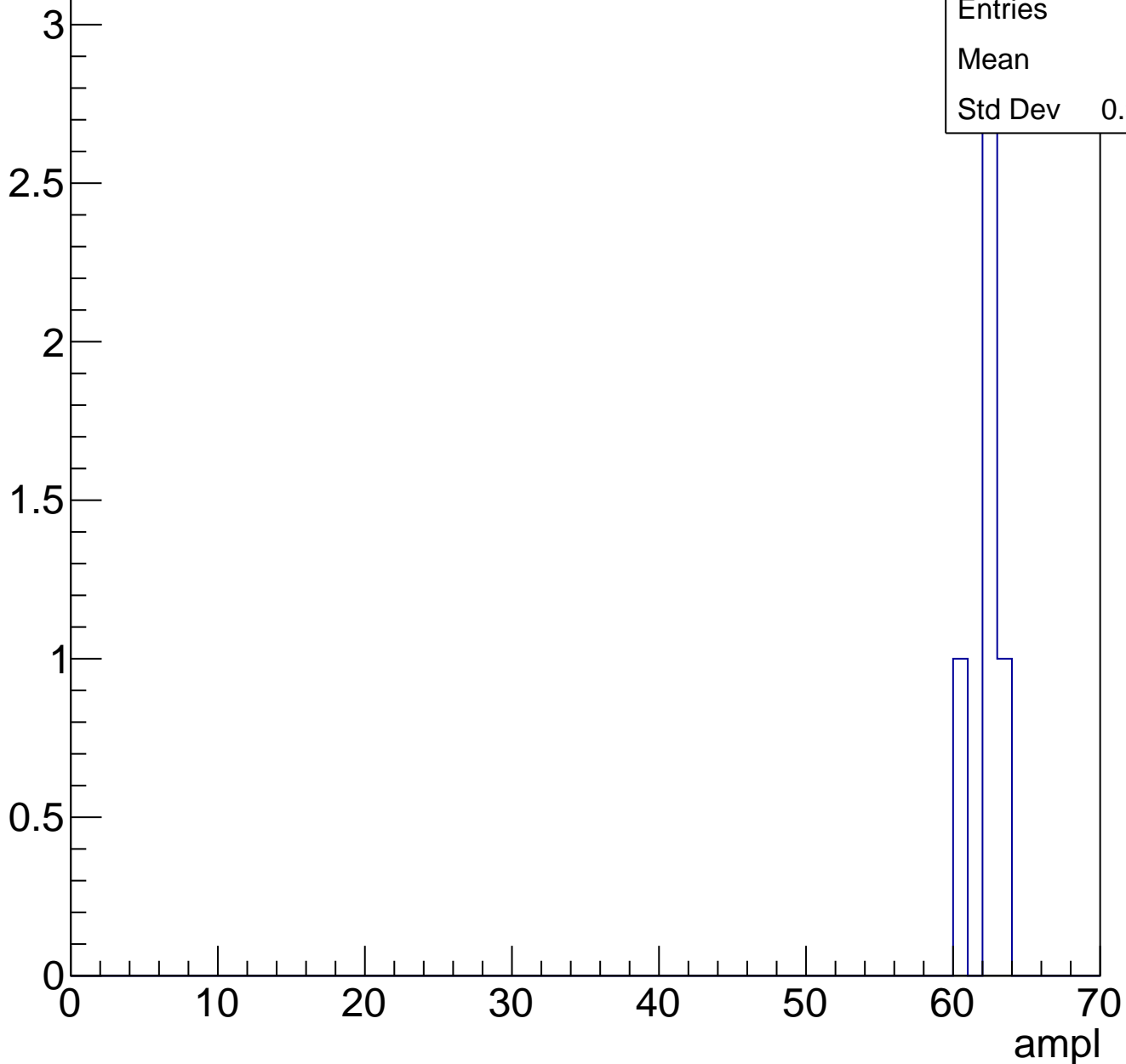




# B1L002S, U3-ch83, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch84, adc0

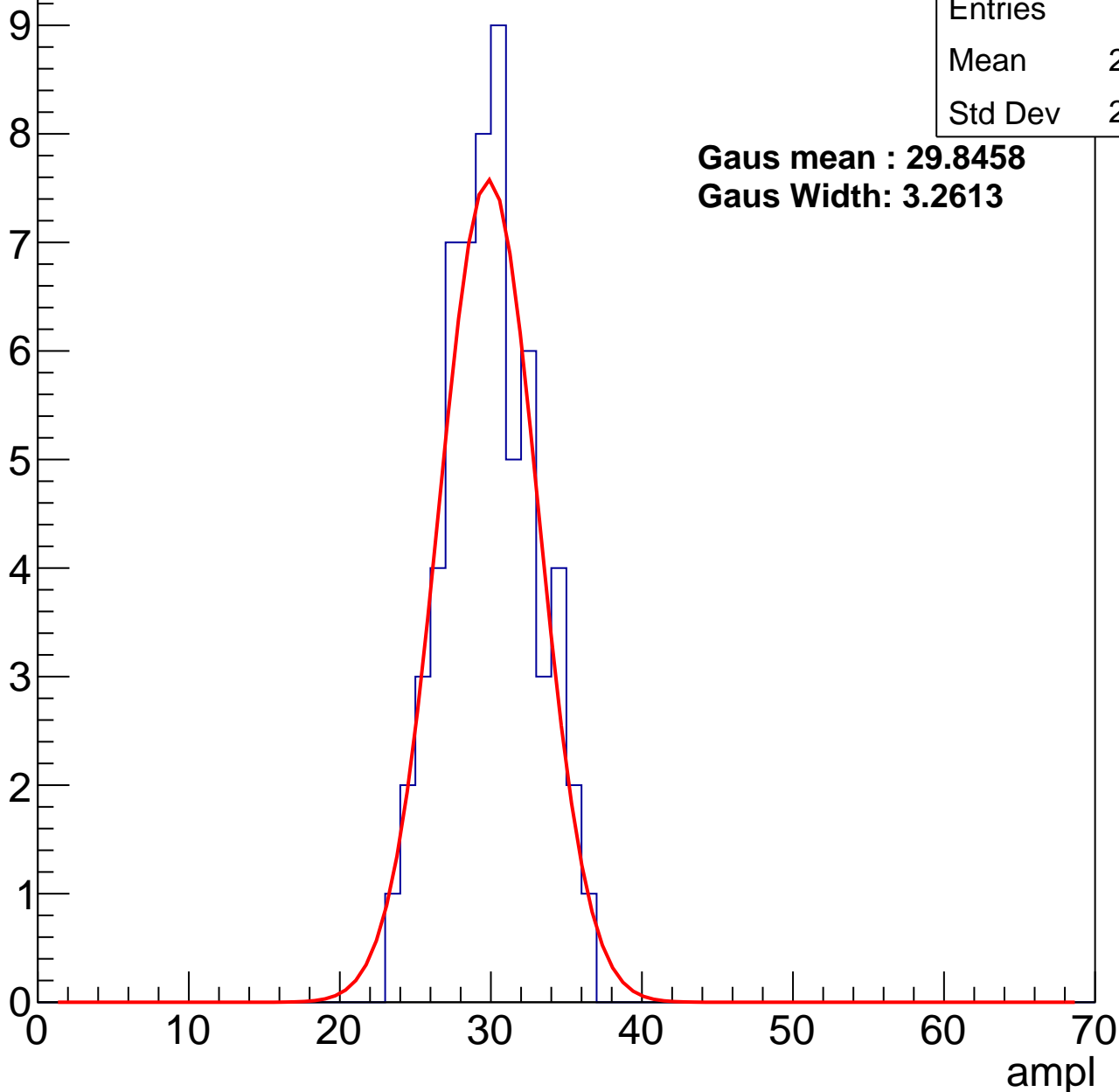
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	29.44
Std Dev	2.966

**Gaus mean : 29.8458**

**Gaus Width: 3.2613**



# B1L002S, U3-ch84, adc1

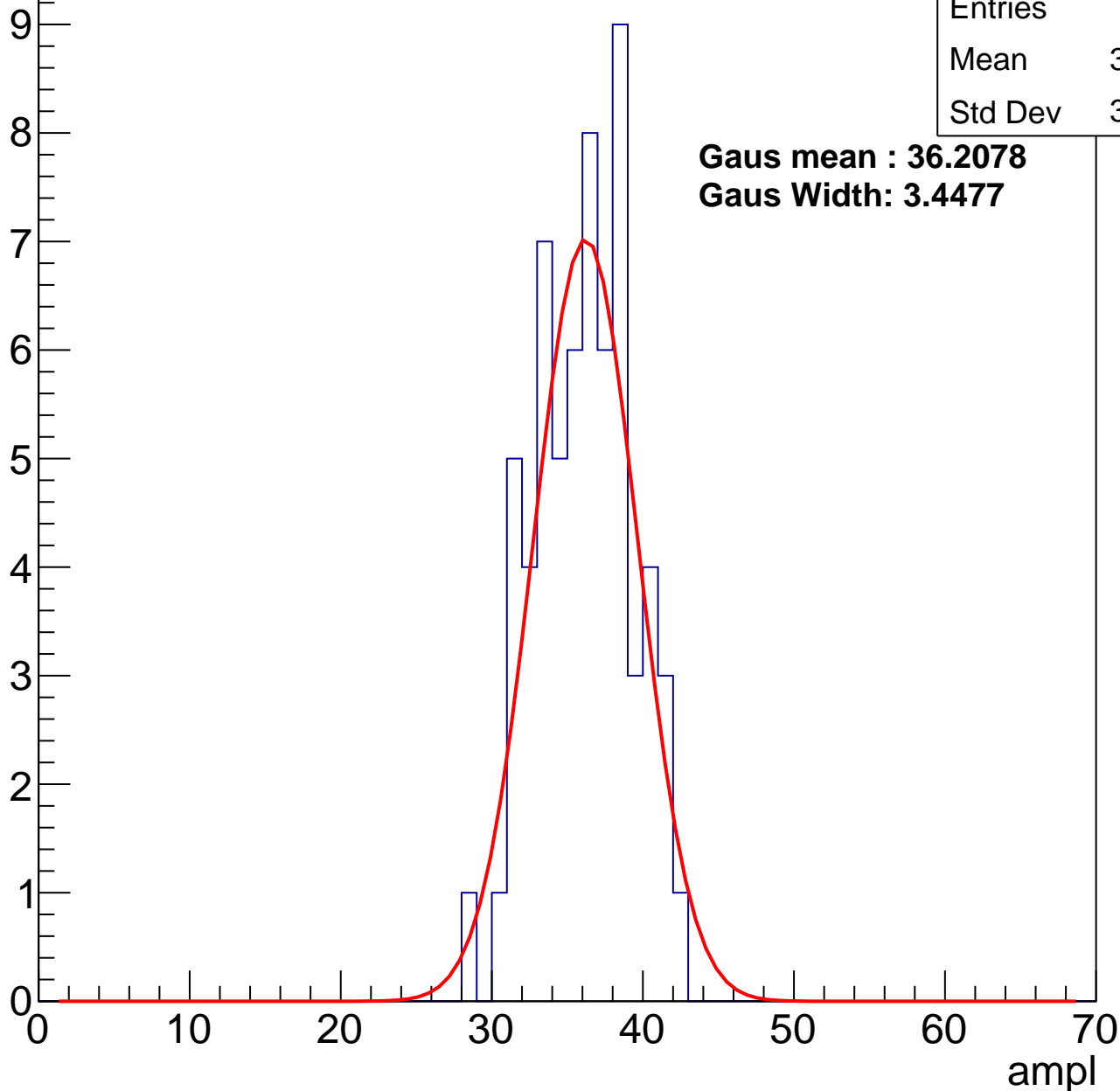
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	35.65
Std Dev	3.123

**Gaus mean : 36.2078**

**Gaus Width: 3.4477**



# B1L002S, U3-ch84, adc2

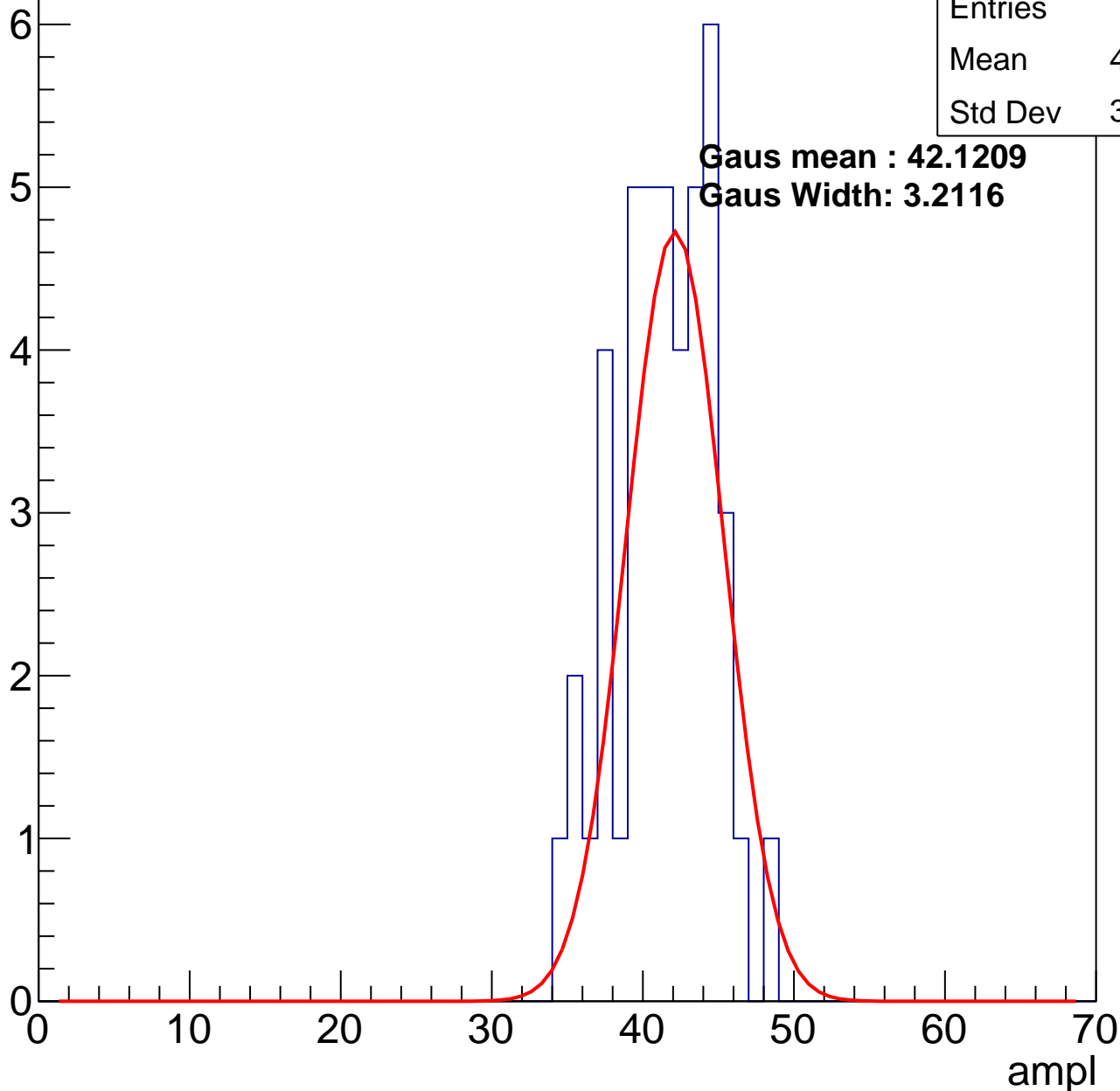
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	40.95
Std Dev	3.176

**Gaus mean : 42.1209**

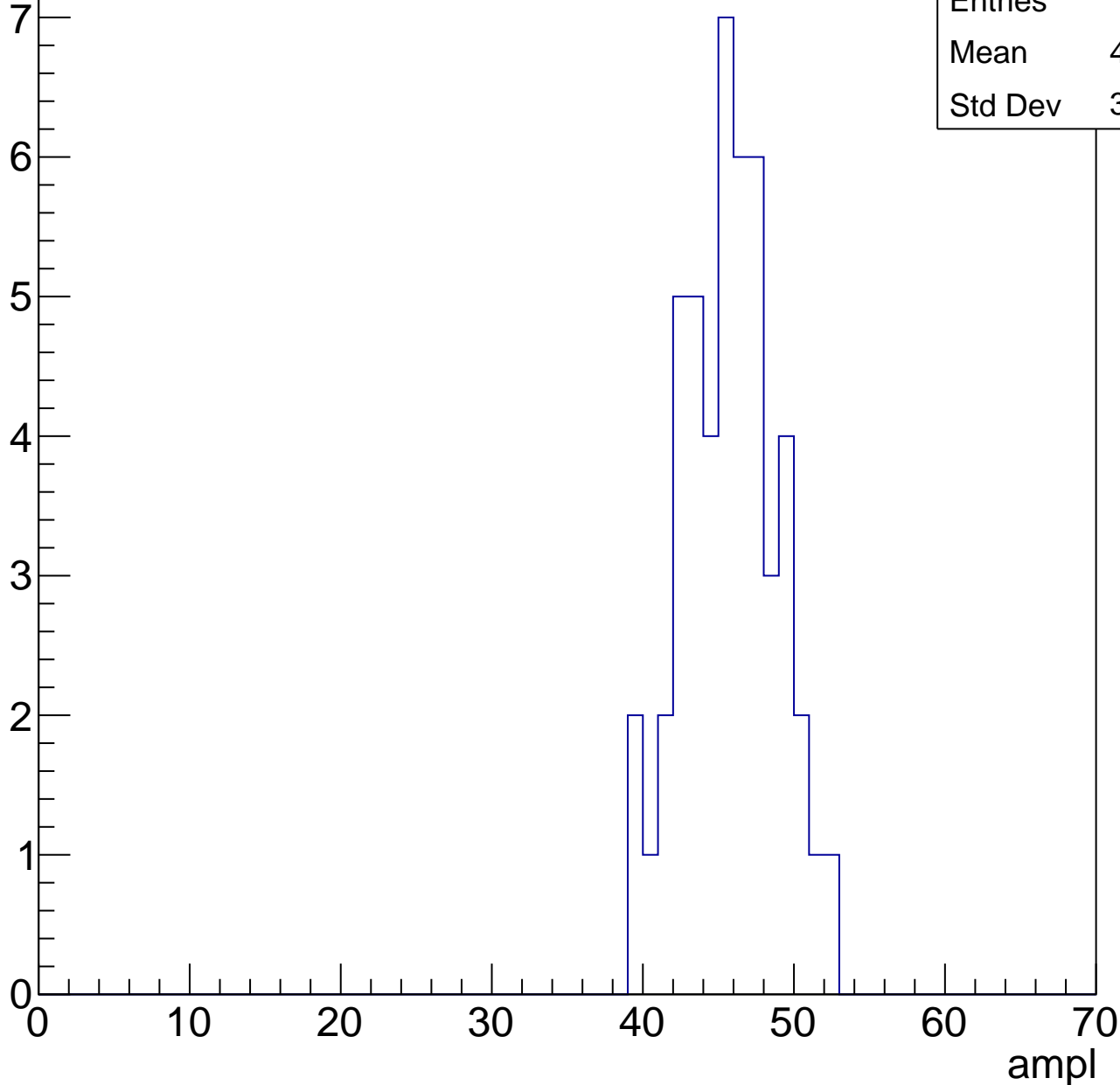
**Gaus Width: 3.2116**



# B1L002S, U3-ch84, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

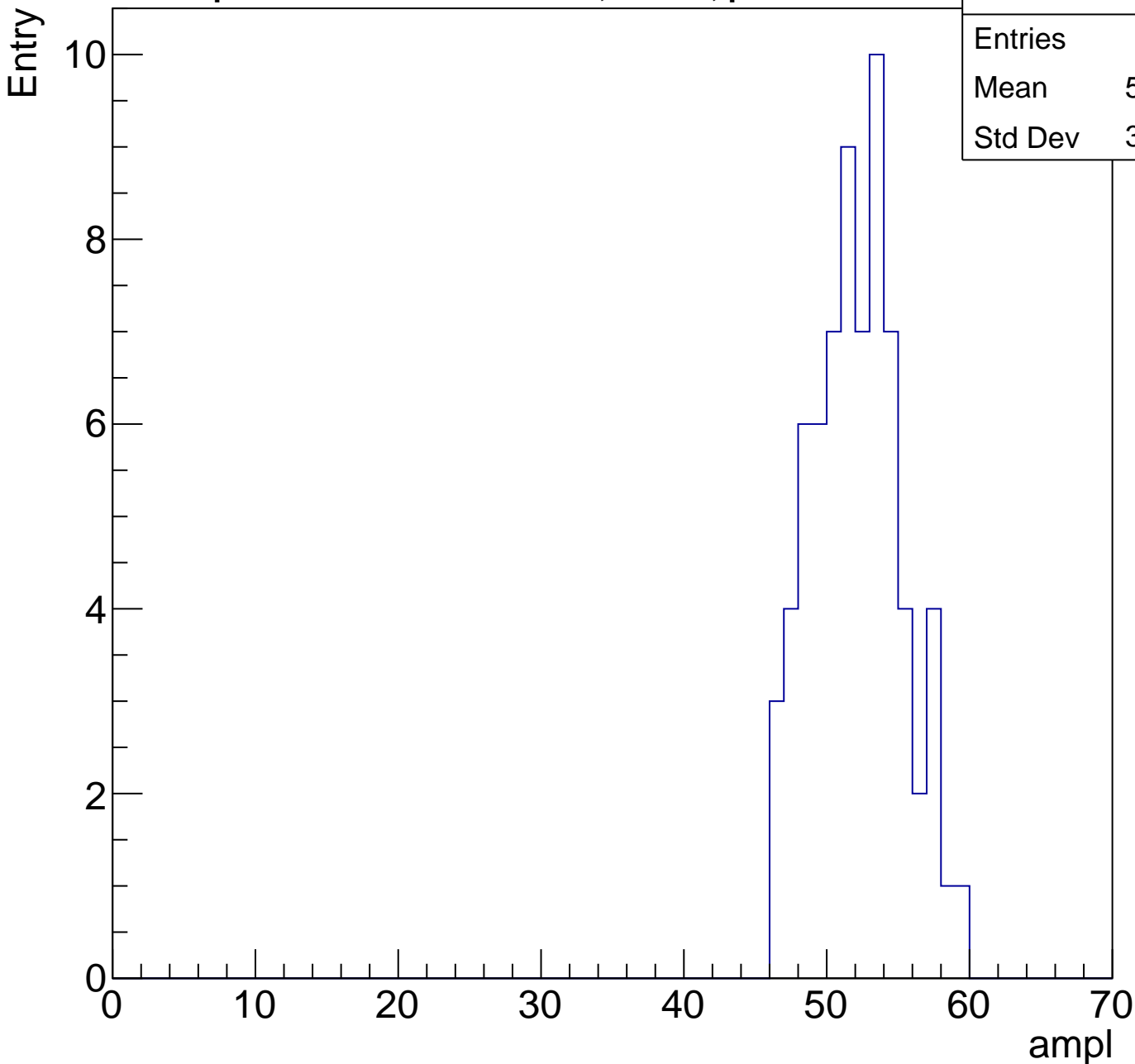


Entries	49
Mean	45.24
Std Dev	3.034

# B1L002S, U3-ch84, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

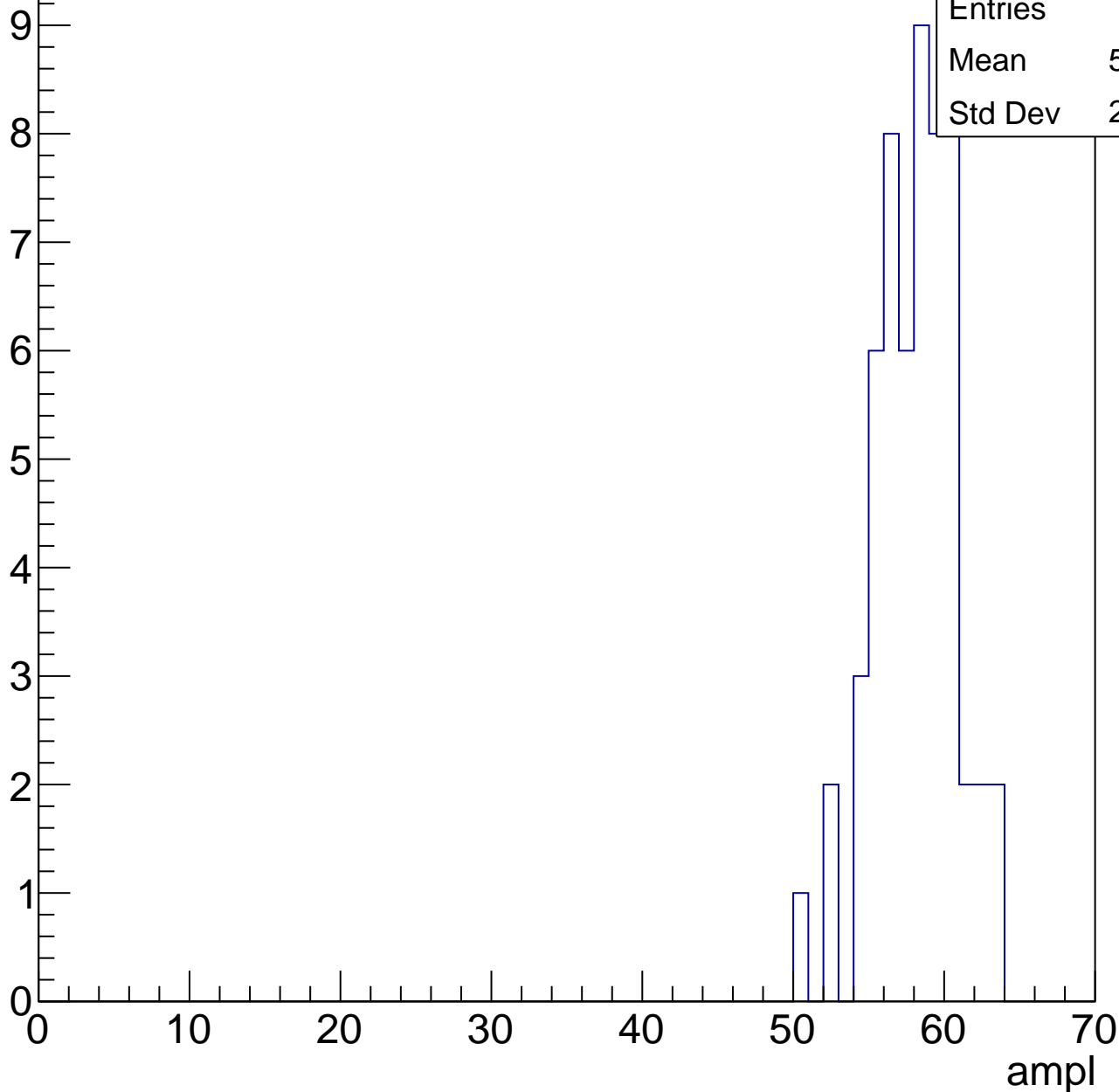
Entries	71
Mean	51.63
Std Dev	3.087



# B1L002S, U3-ch84, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	57
Mean	57.58
Std Dev	2.669

# B1L002S, U3-ch84, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	27
Mean	59.44
Std Dev	11.71

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

8



# B1L002S, U3-ch84, adc7

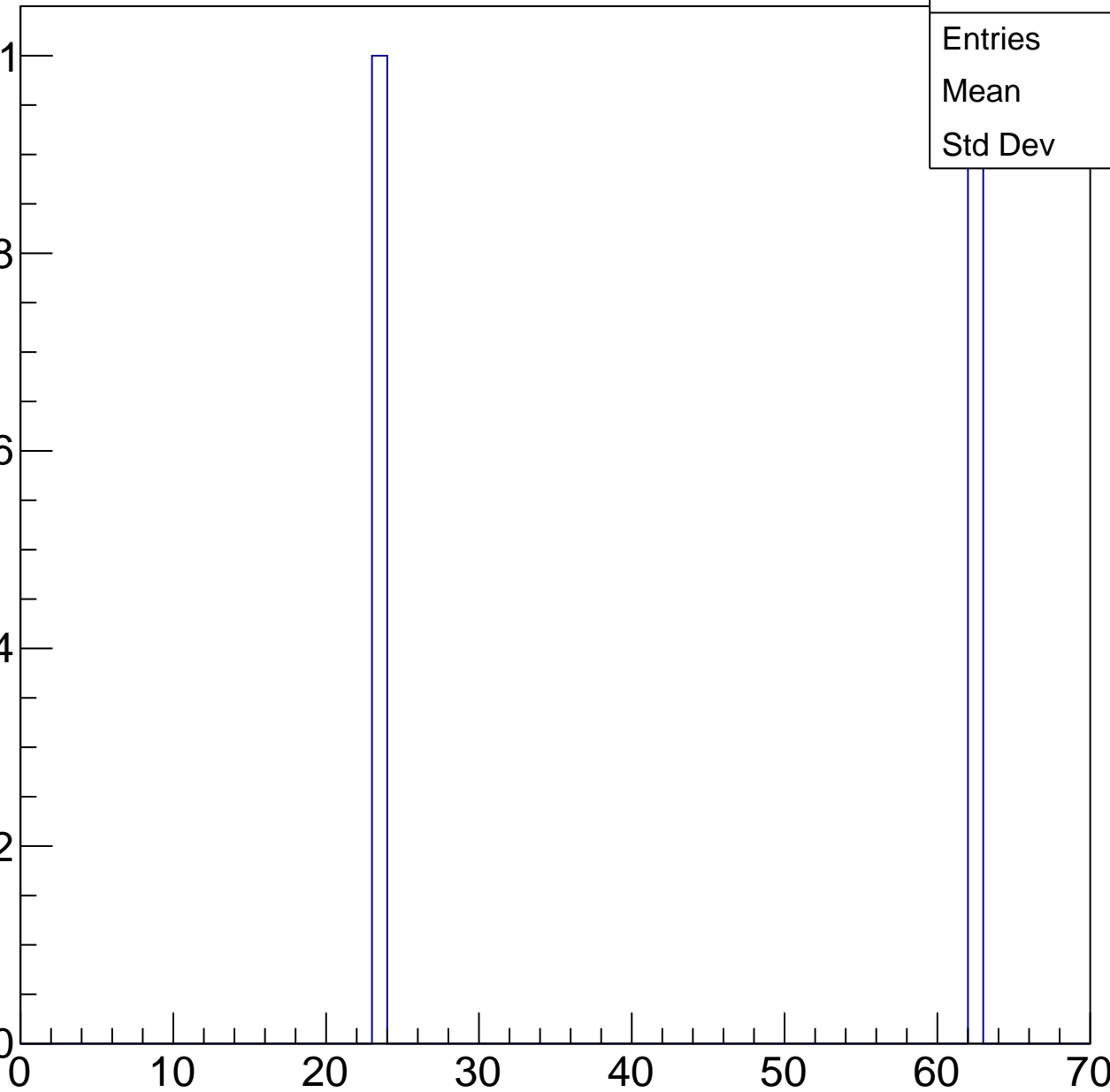
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	2
Mean	42.5
Std Dev	19.5

ampl



# B1L002S, U3-ch85, adc0

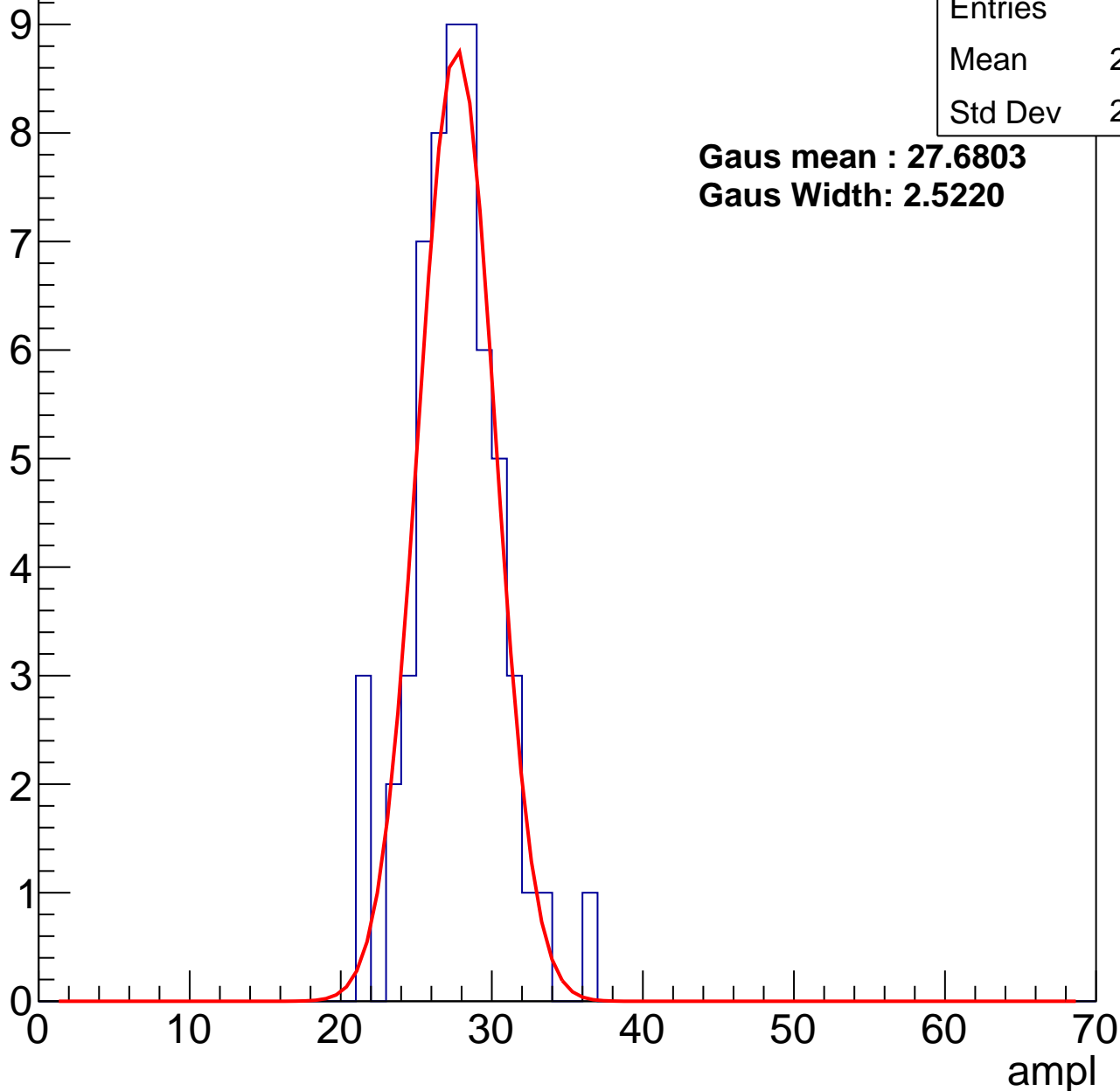
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	27.19
Std Dev	2.843

**Gaus mean : 27.6803**

**Gaus Width: 2.5220**



# B1L002S, U3-ch85, adc1

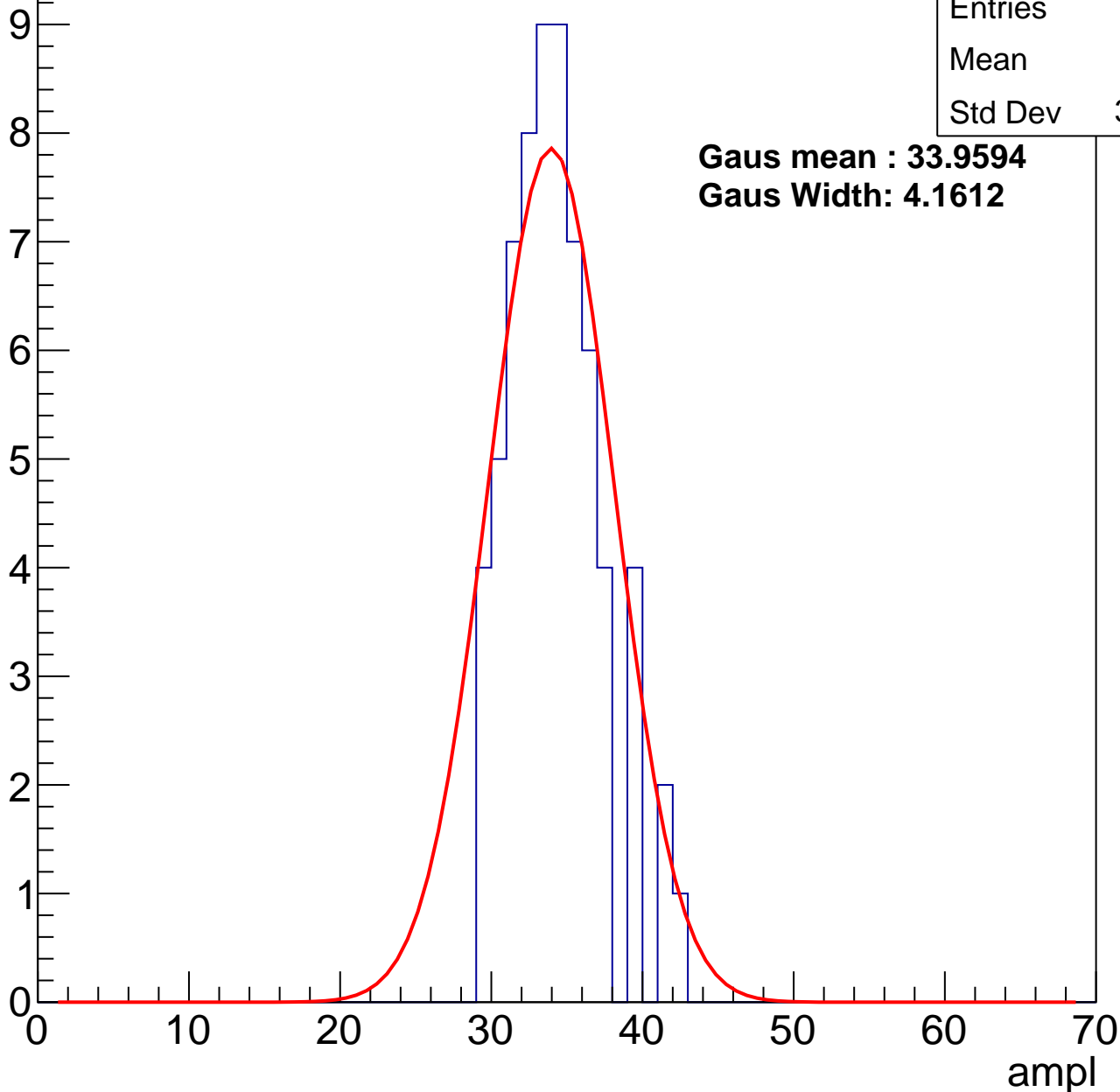
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	33.8
Std Dev	3.041

**Gaus mean : 33.9594**

**Gaus Width: 4.1612**



# B1L002S, U3-ch85, adc2

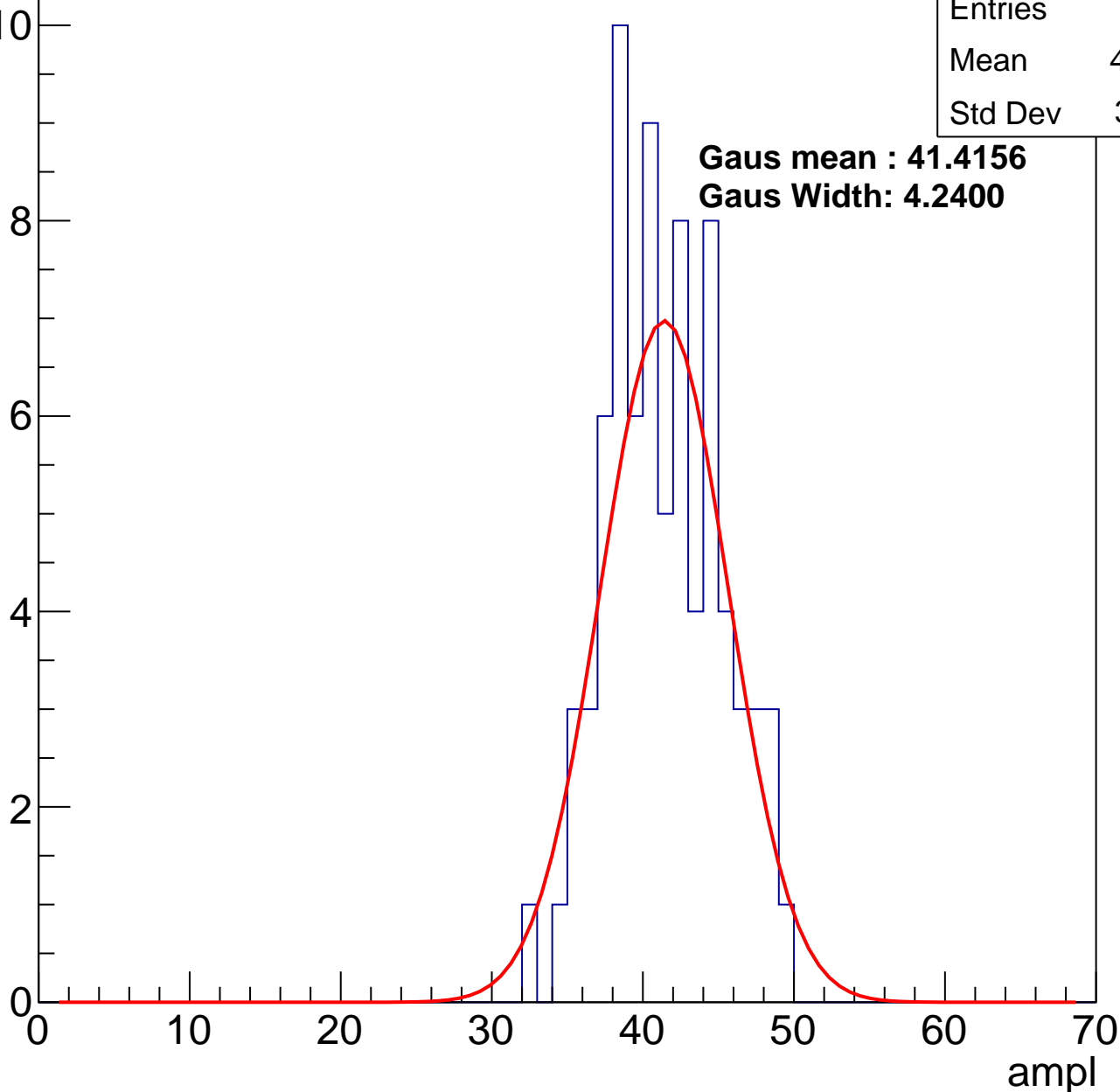
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	40.92
Std Dev	3.731

**Gaus mean : 41.4156**

**Gaus Width: 4.2400**

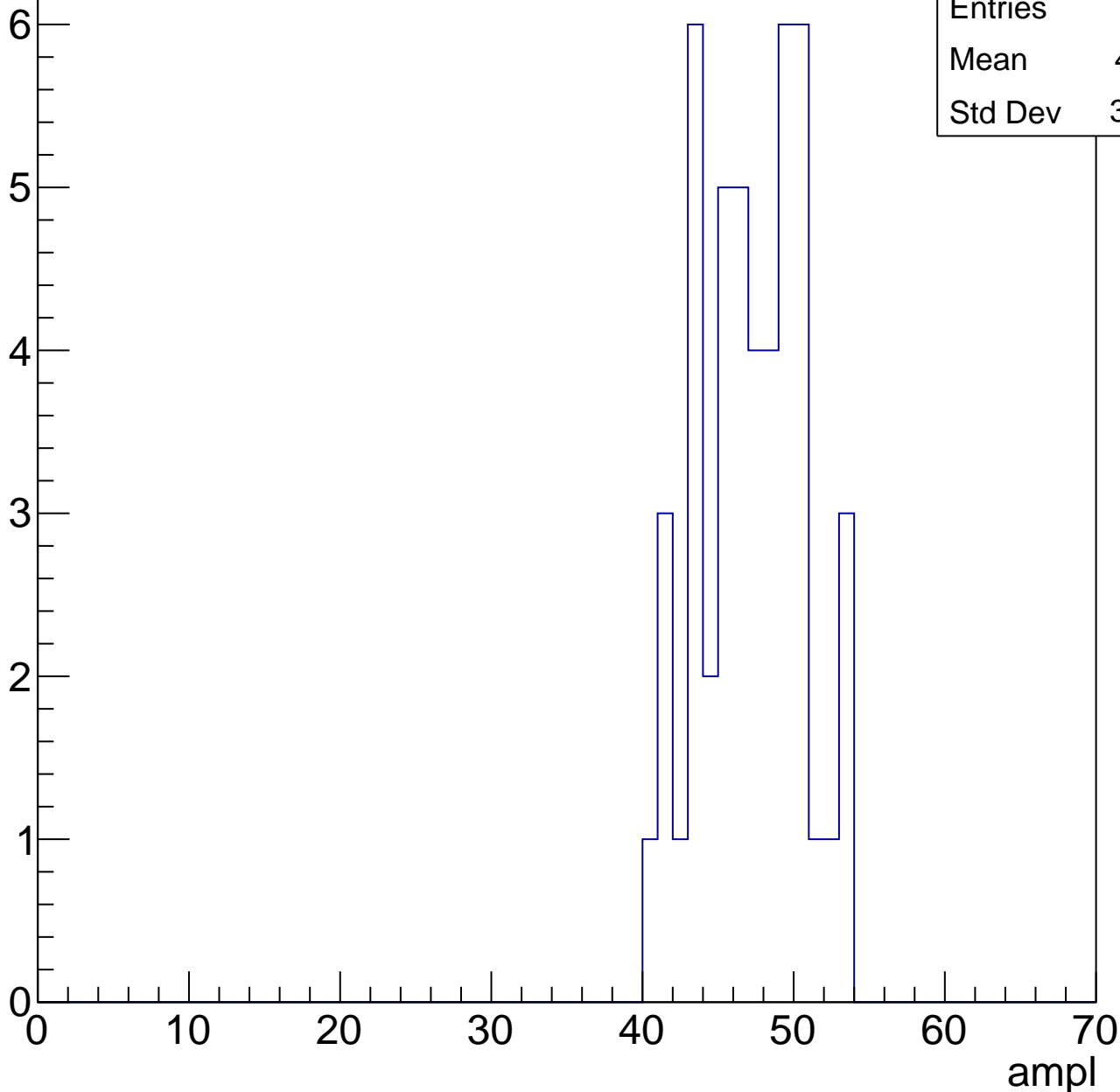


# B1L002S, U3-ch85, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	46.71
Std Dev	3.379

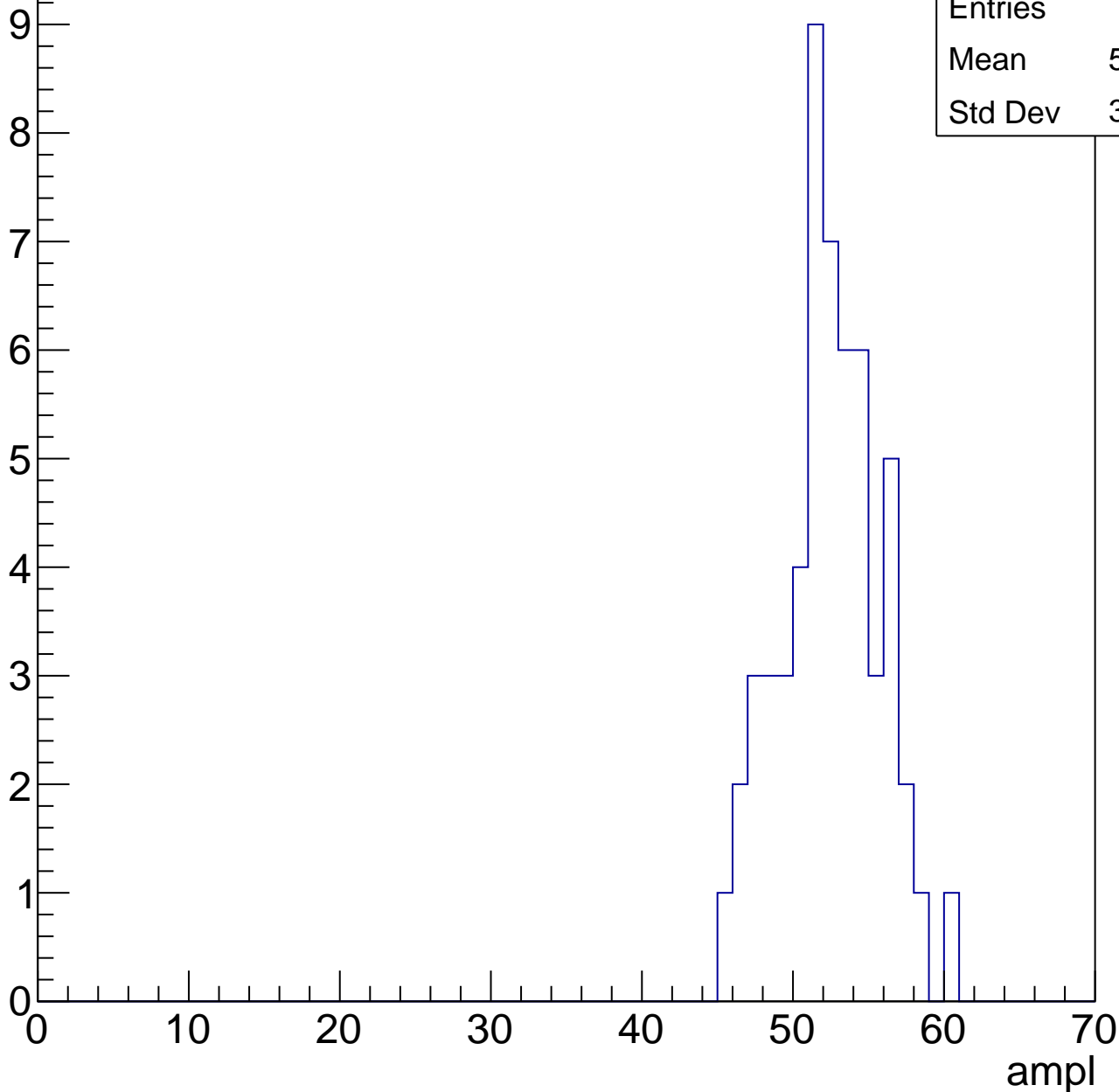


# B1L002S, U3-ch85, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	51.98
Std Dev	3.227

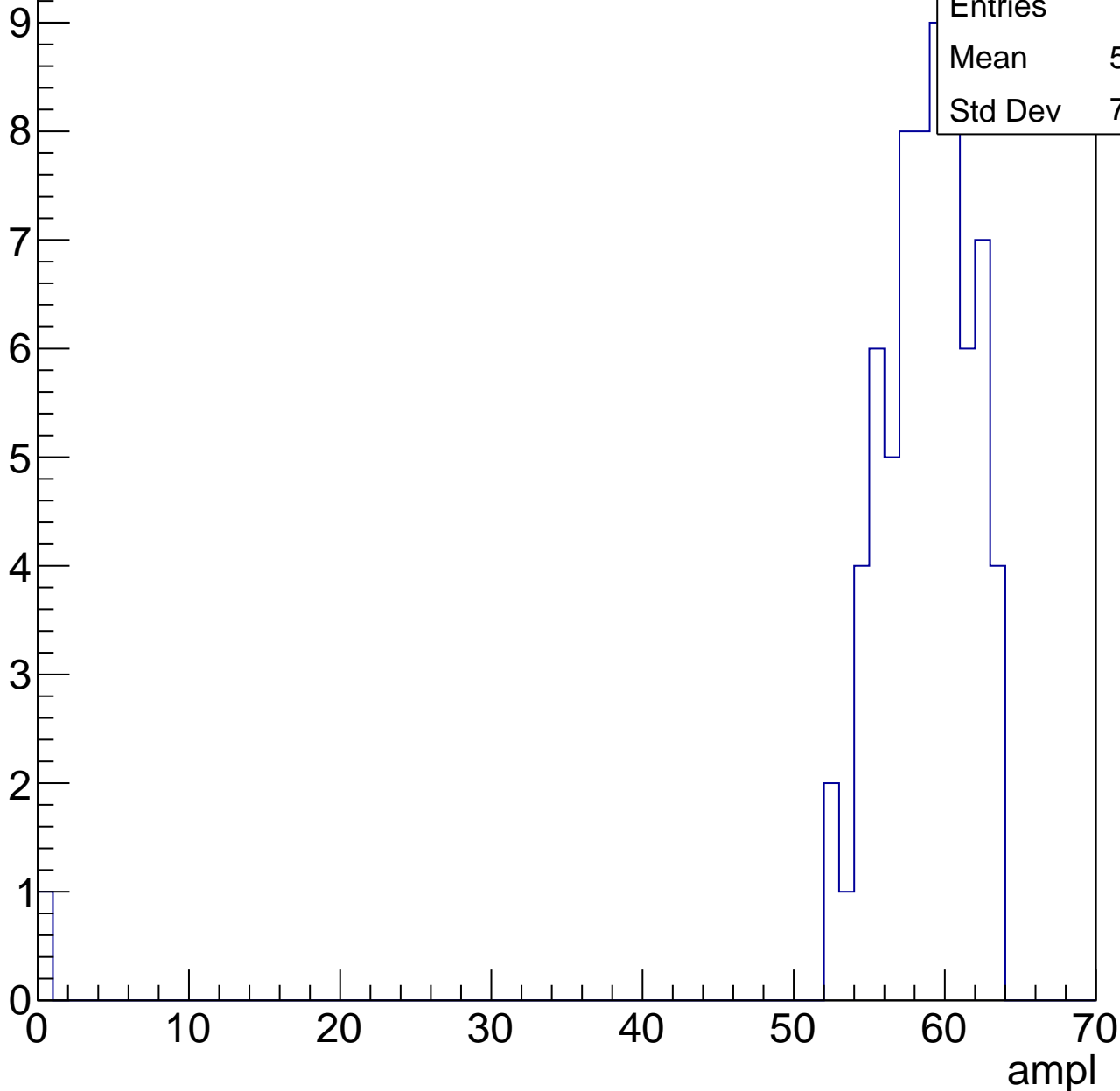


# B1L002S, U3-ch85, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	57.48
Std Dev	7.513

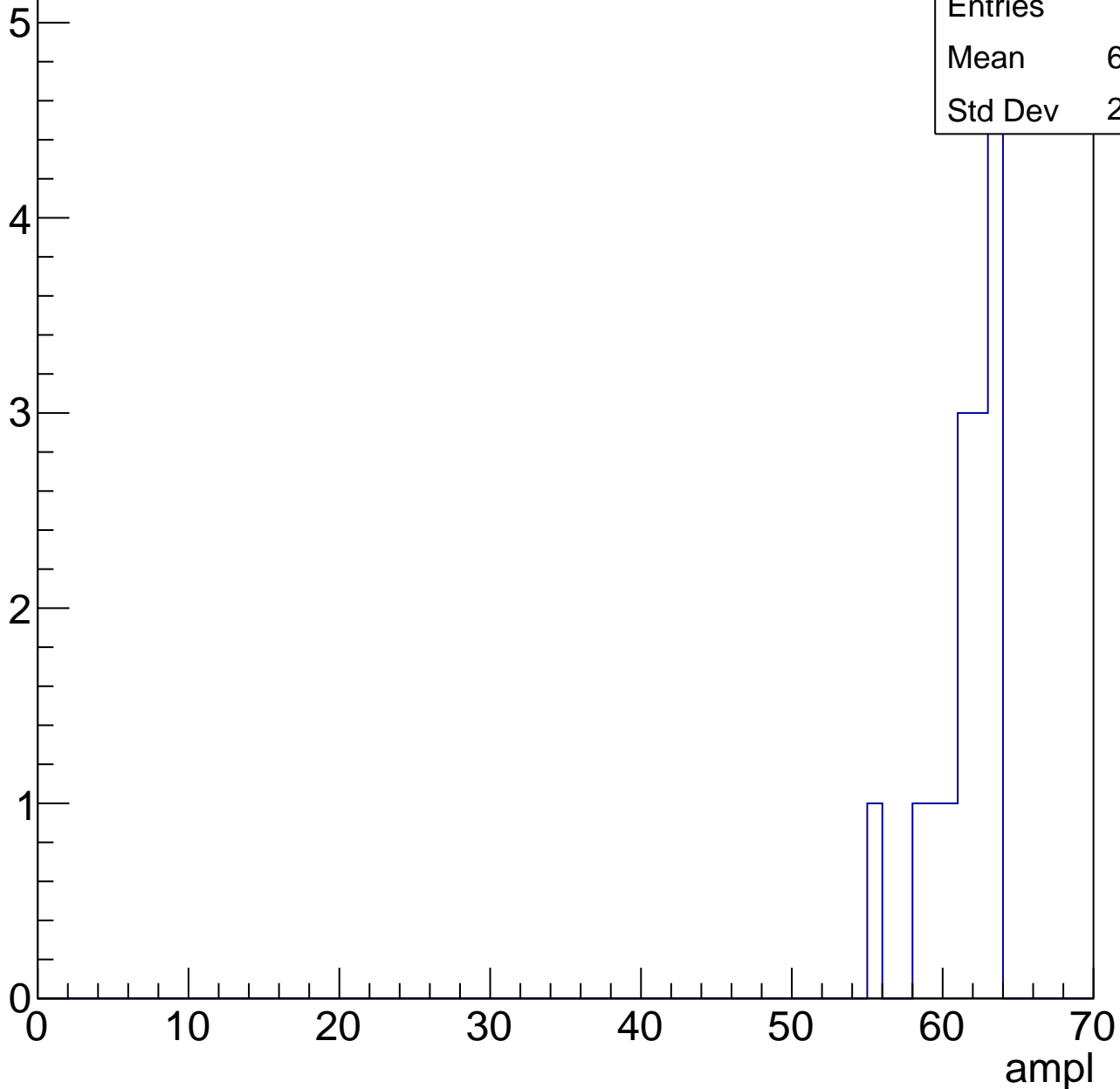


# B1L002S, U3-ch85, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	61.07
Std Dev	2.205

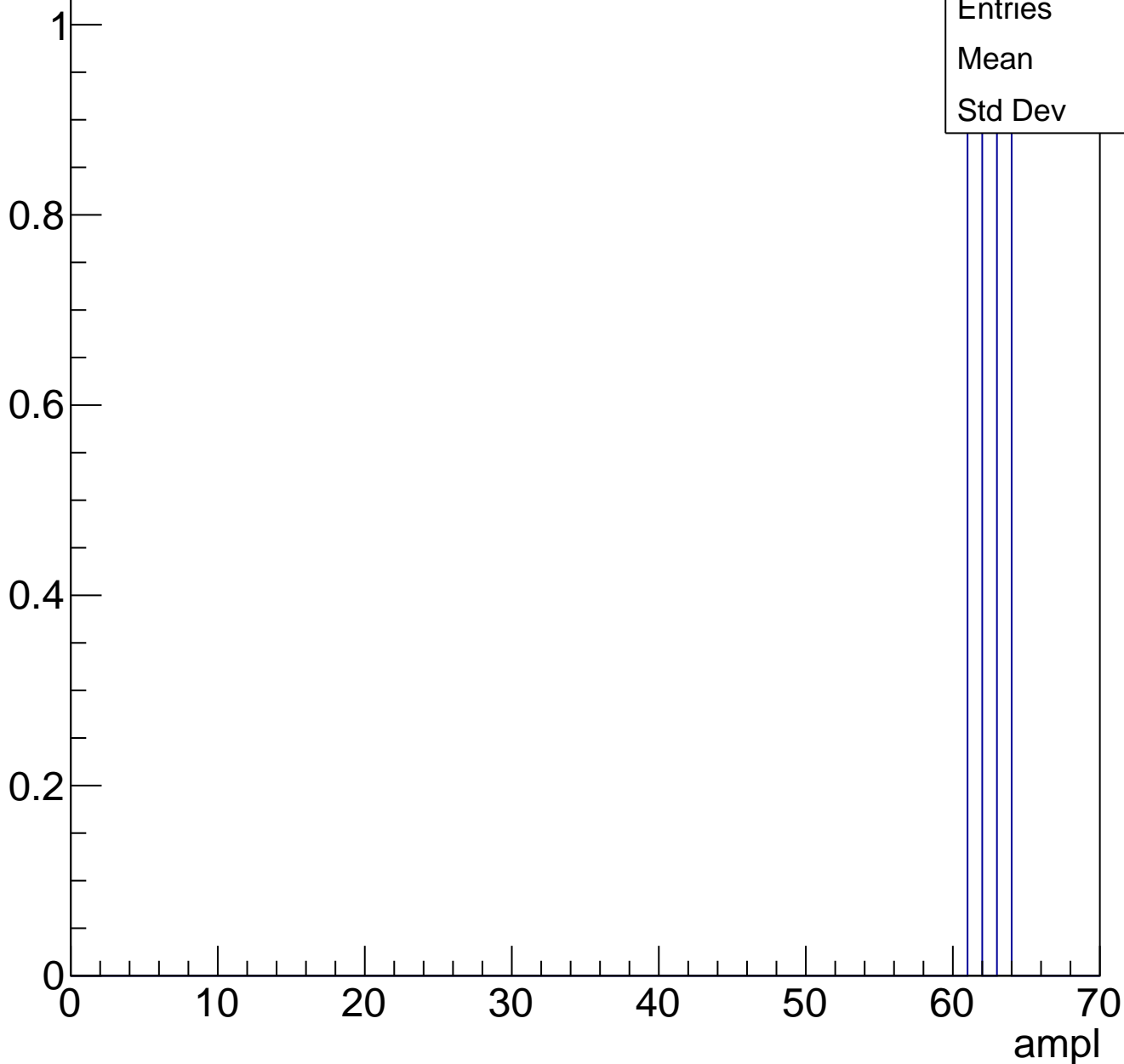




# B1L002S, U3-ch85, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch86, adc0

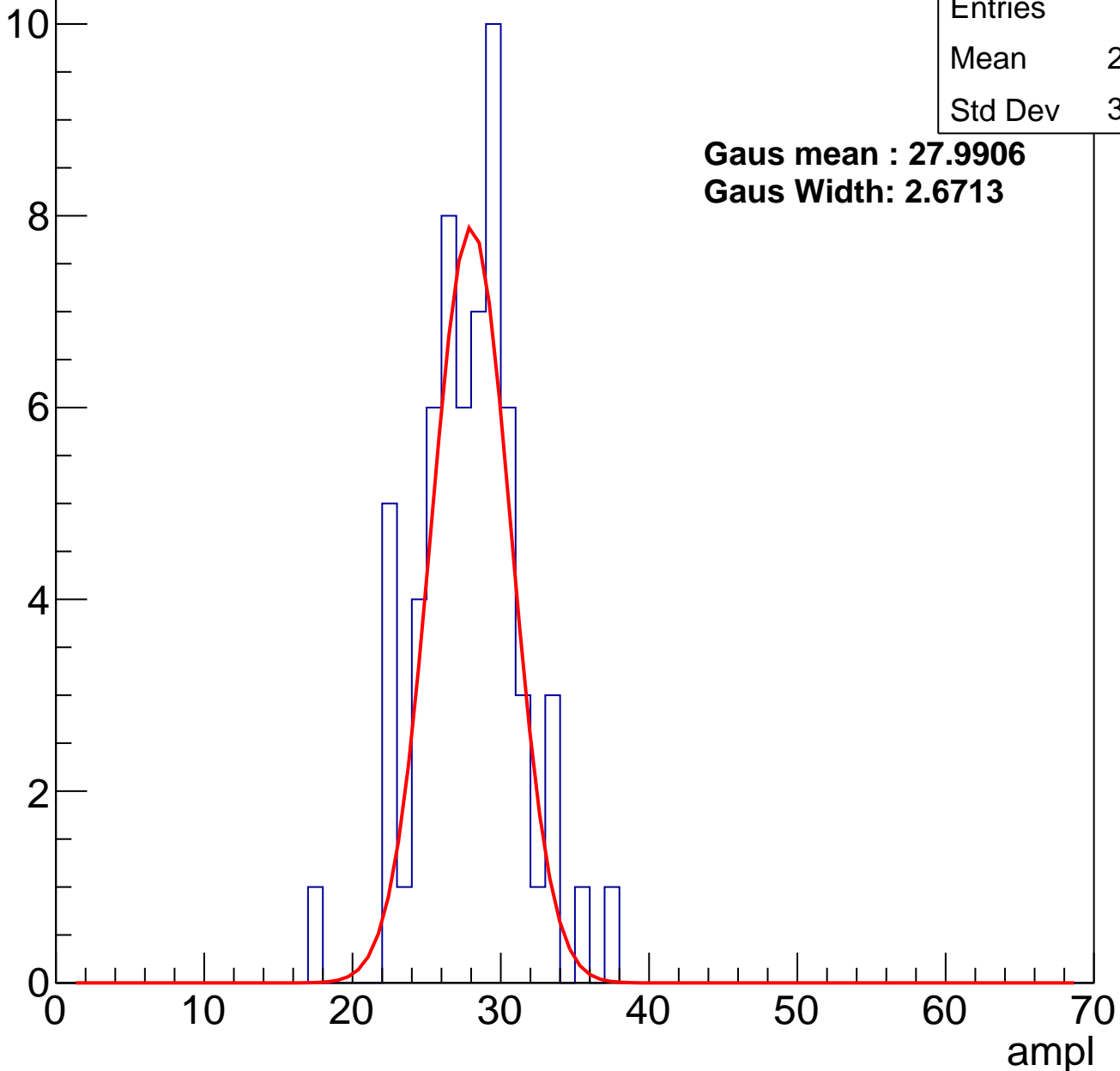
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	63
Mean	27.43
Std Dev	3.435

**Gaus mean : 27.9906**

**Gaus Width: 2.6713**

Entry



# B1L002S, U3-ch86, adc1

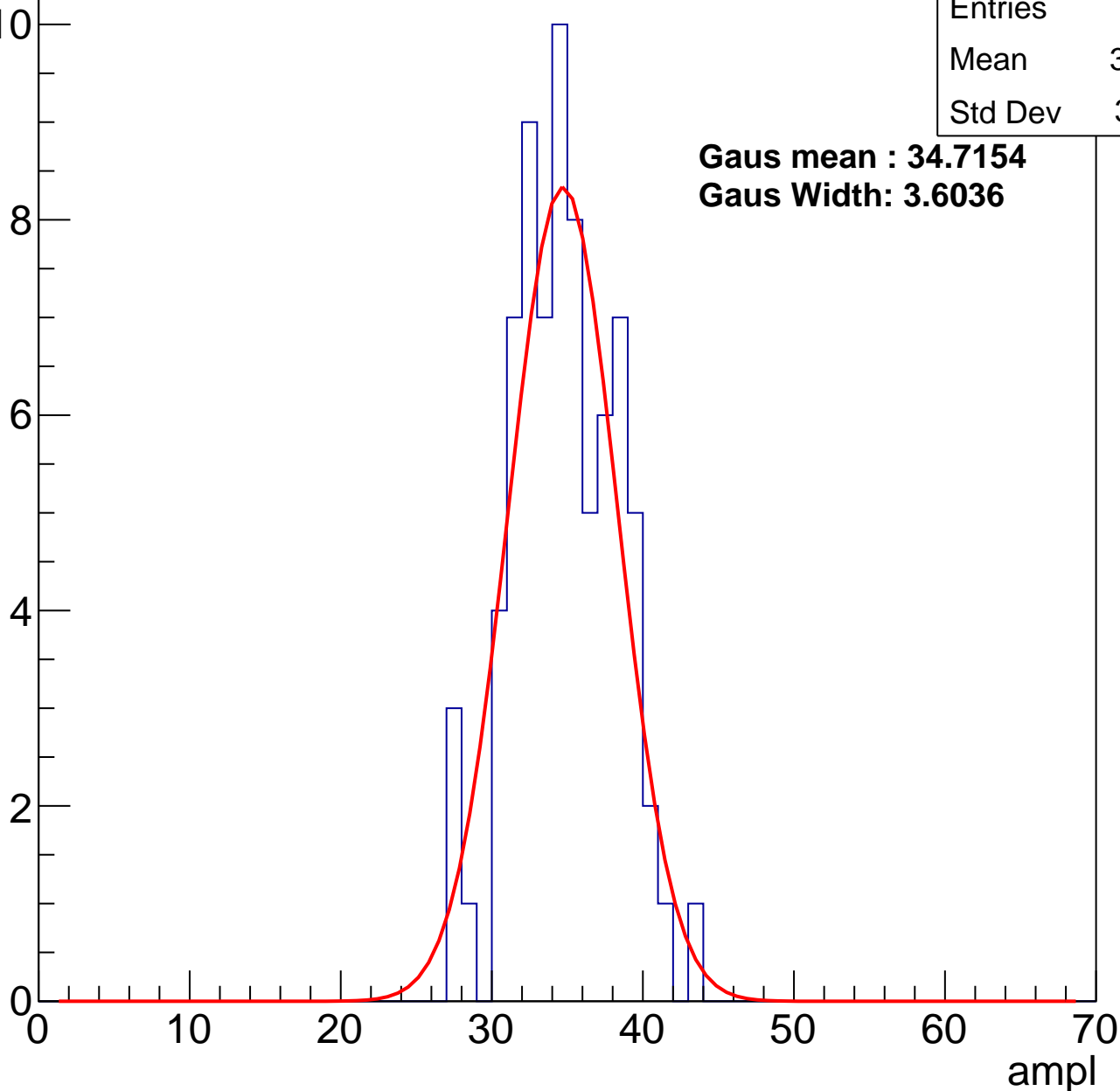
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	34.37
Std Dev	3.371

**Gaus mean : 34.7154**

**Gaus Width: 3.6036**



# B1L002S, U3-ch86, adc2

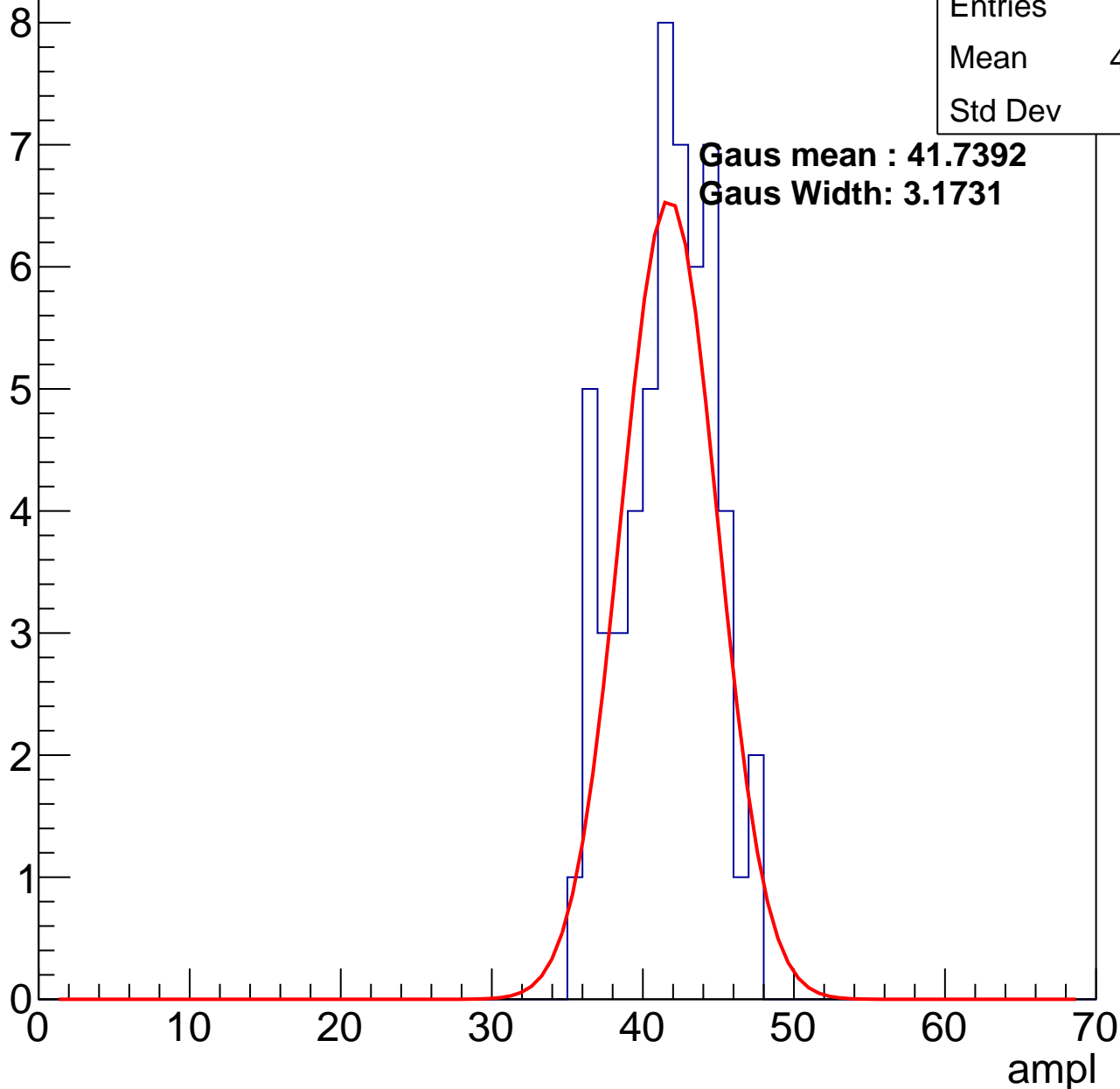
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	41.14
Std Dev	3.02

**Gaus mean : 41.7392**

**Gaus Width: 3.1731**

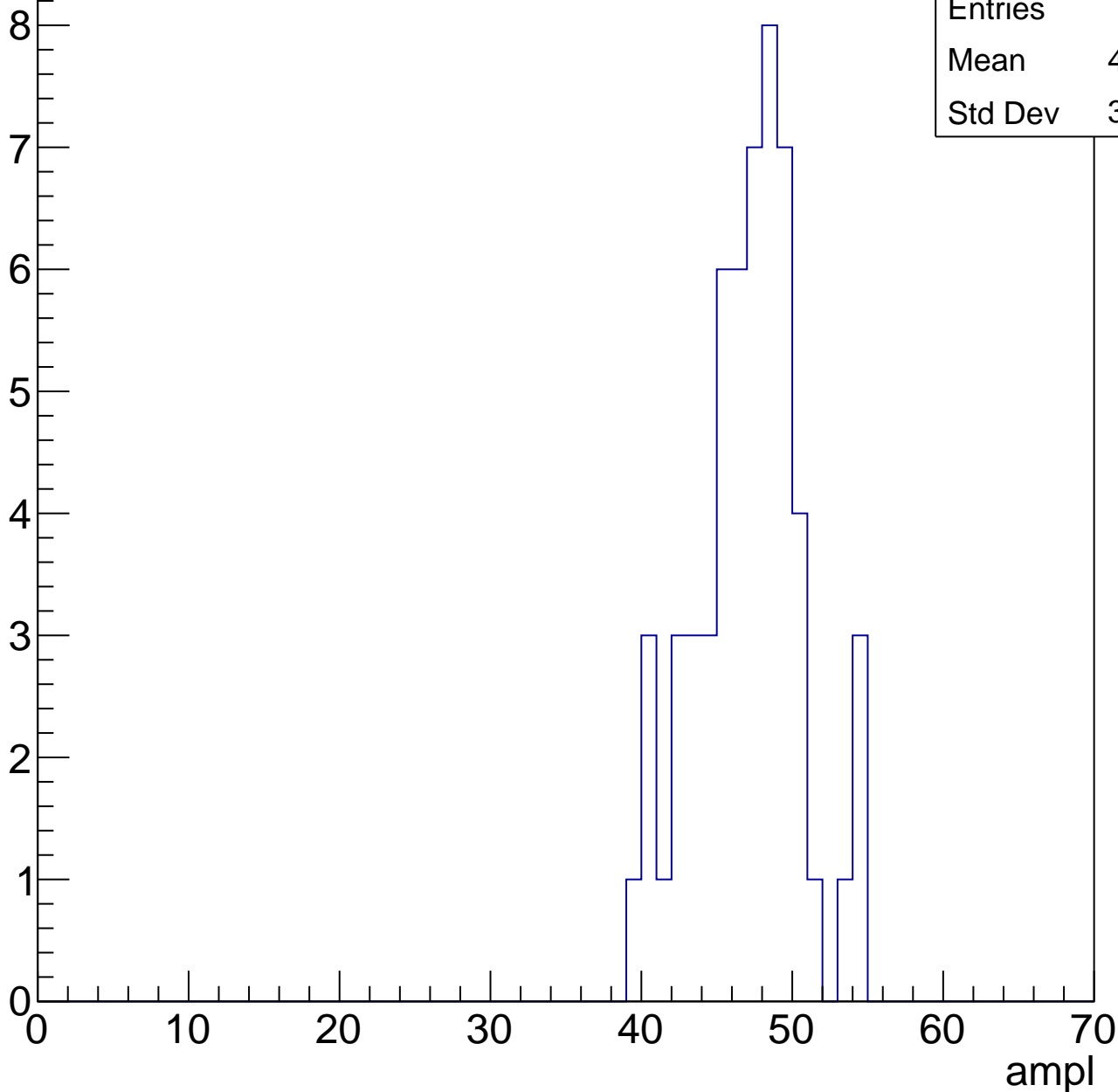


# B1L002S, U3-ch86, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	46.58
Std Dev	3.474

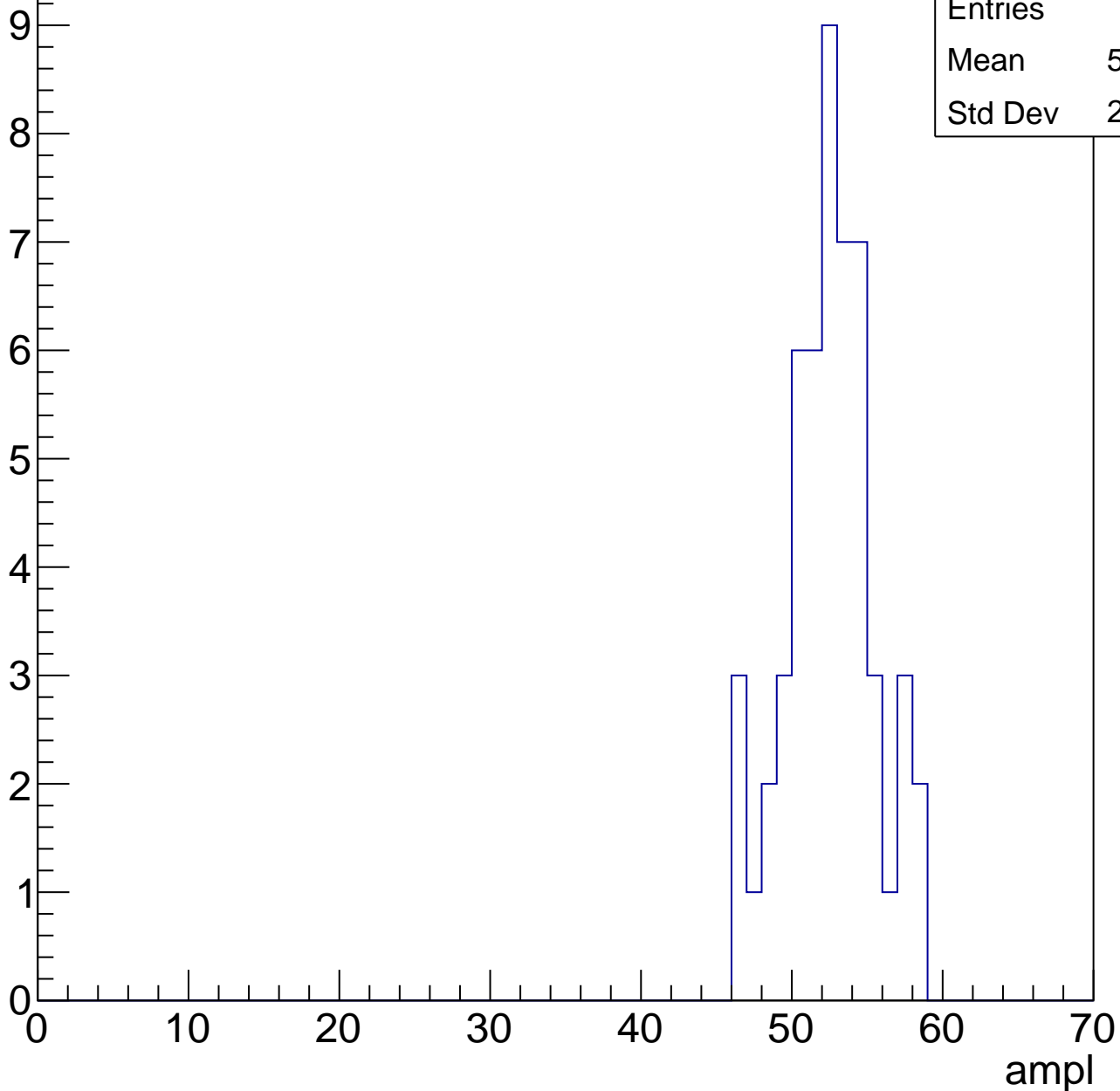


# B1L002S, U3-ch86, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.06
Std Dev	2.904

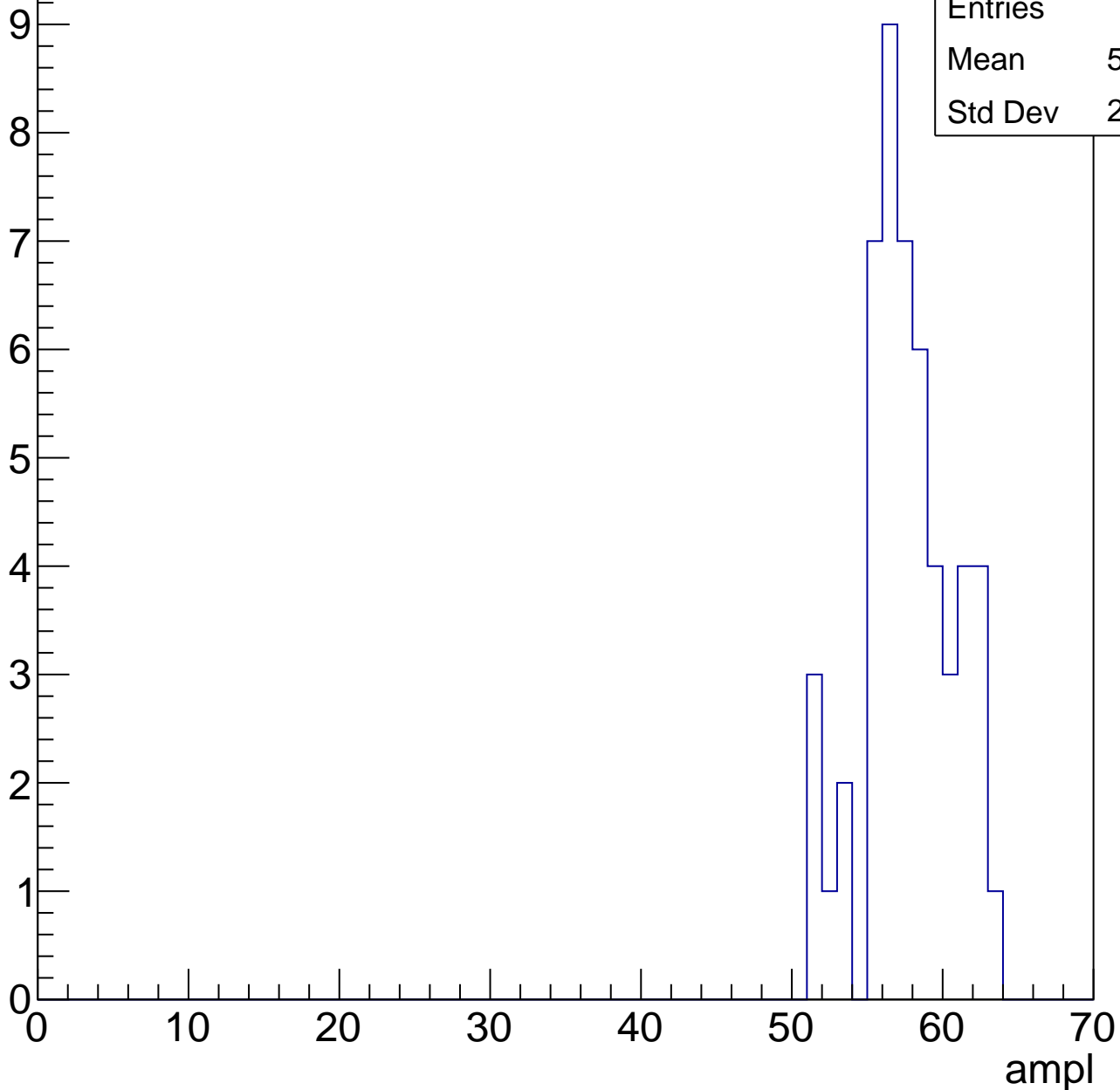


# B1L002S, U3-ch86, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.22
Std Dev	2.966



# B1L002S, U3-ch86, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

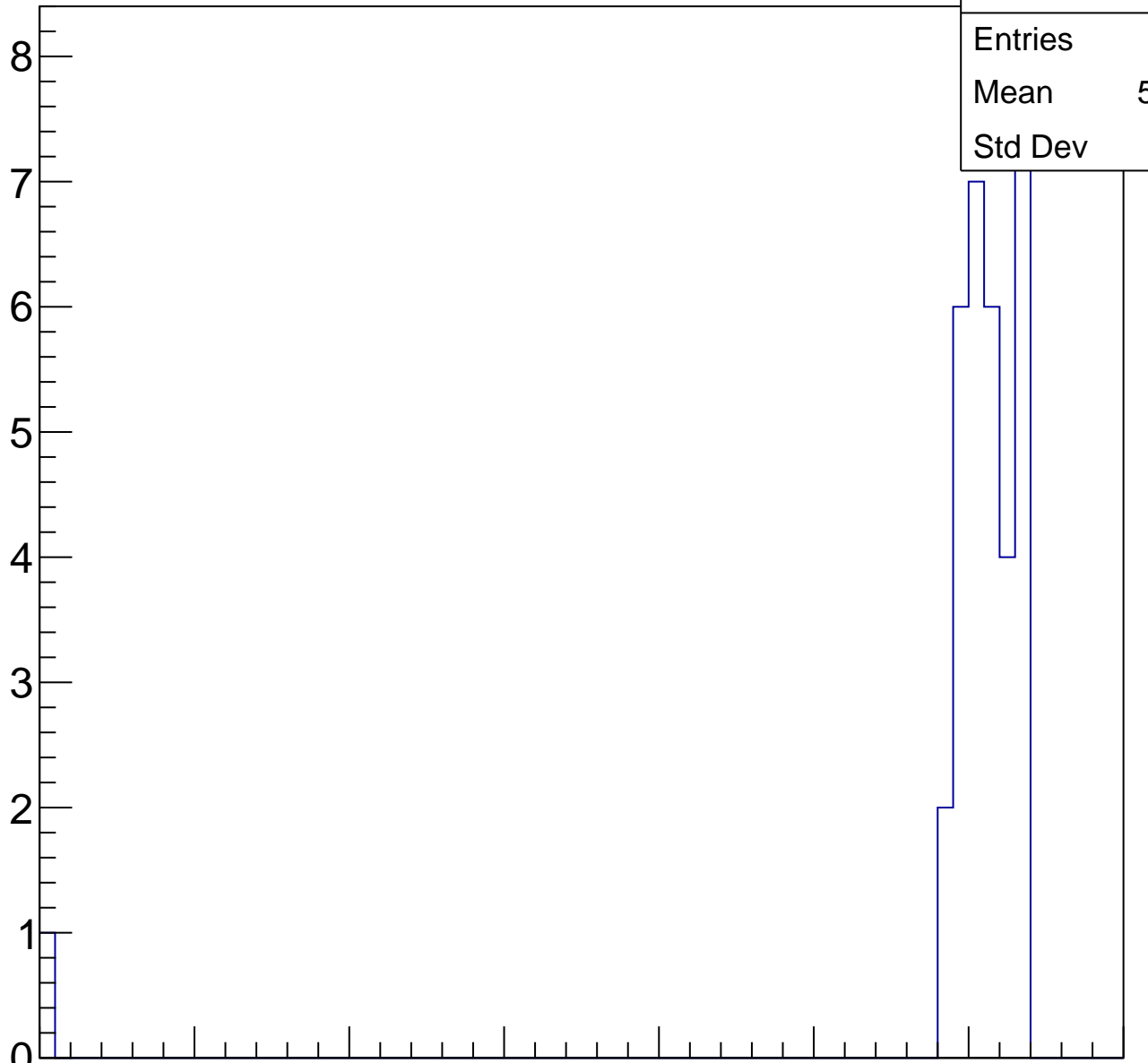
Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	34
Mean	59.06
Std Dev	10.4

ampl

0 10 20 30 40 50 60 70

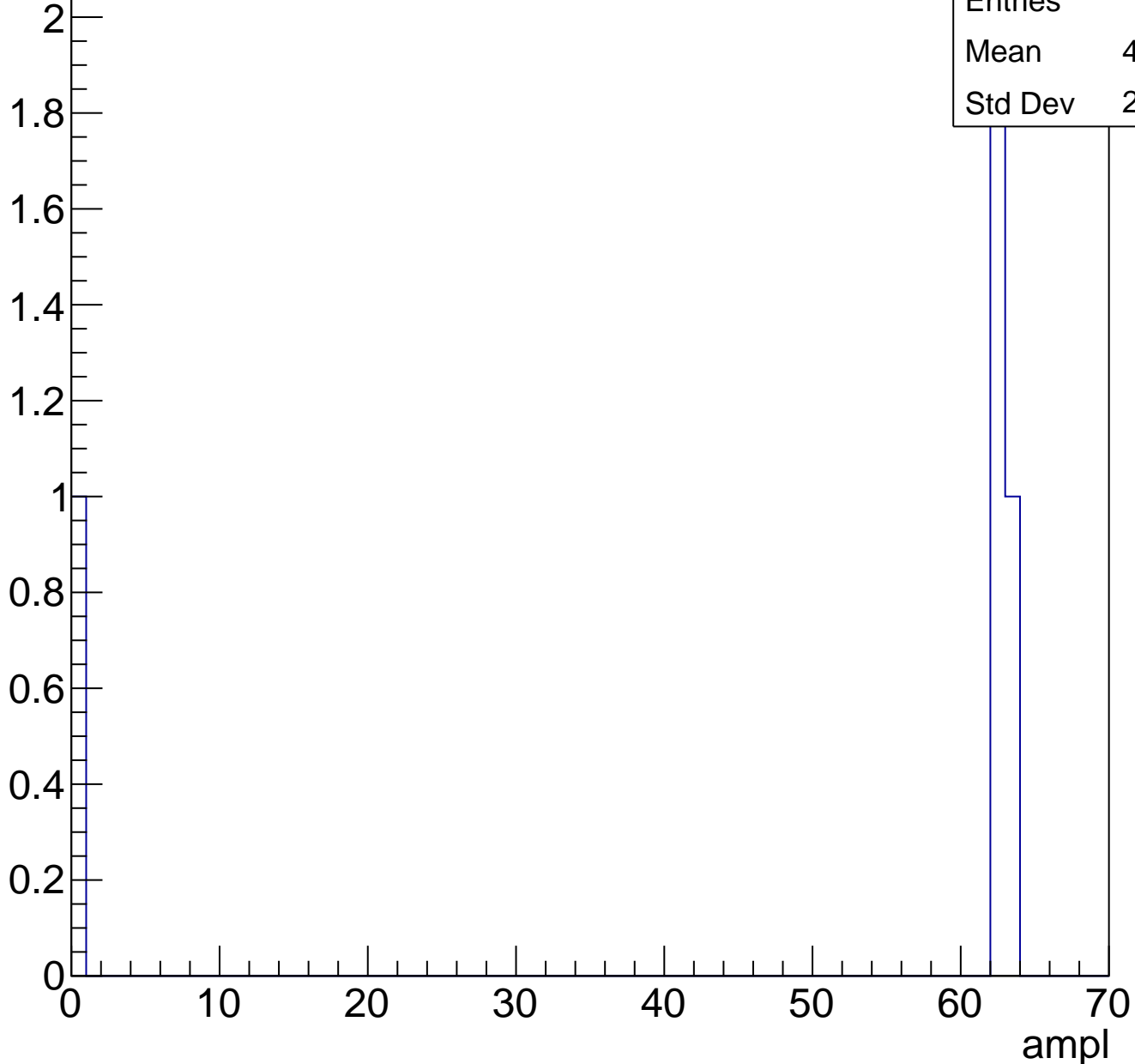




# B1L002S, U3-ch86, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch87, adc0

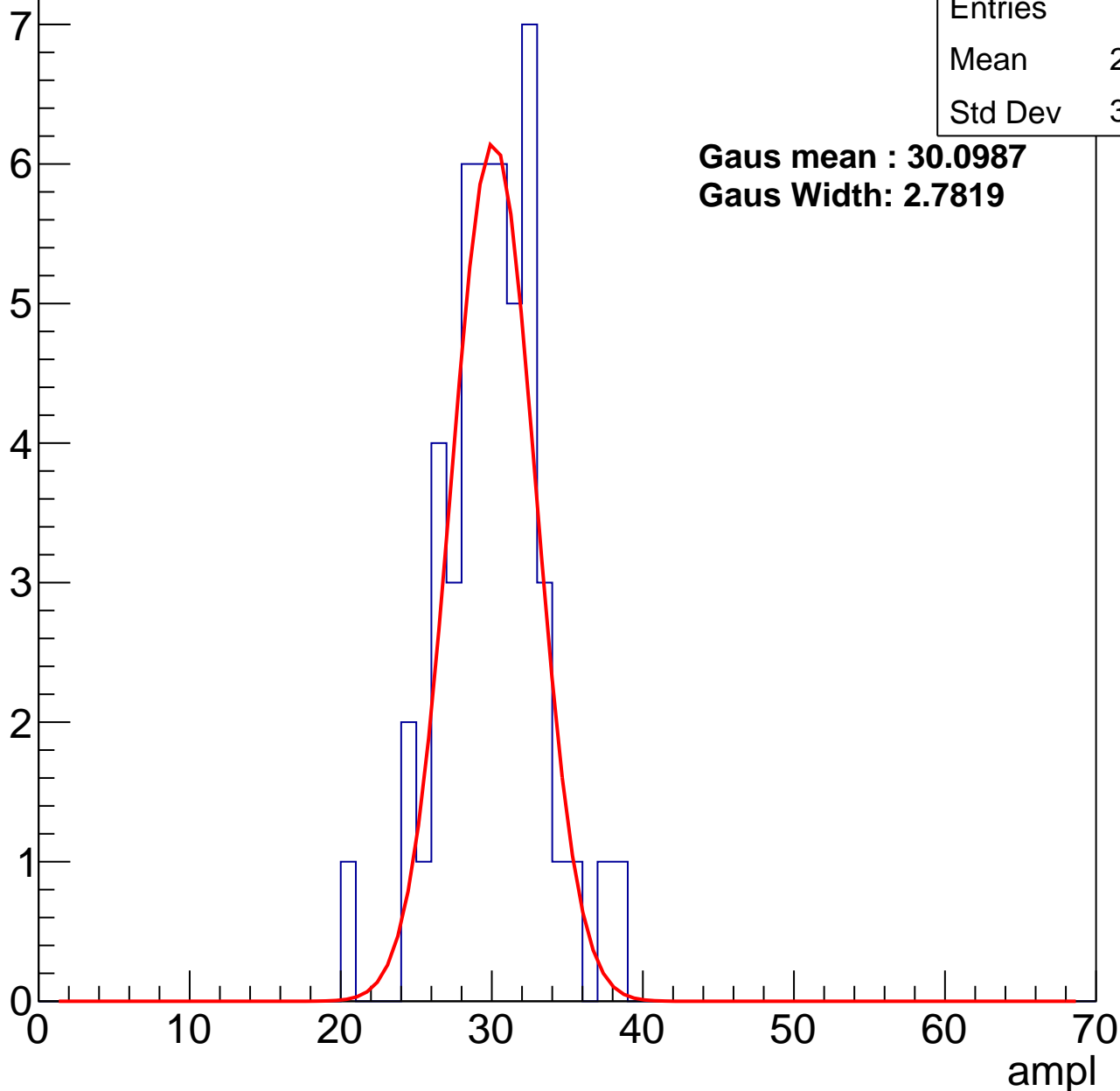
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	29.62
Std Dev	3.308

**Gaus mean : 30.0987**

**Gaus Width: 2.7819**



# B1L002S, U3-ch87, adc1

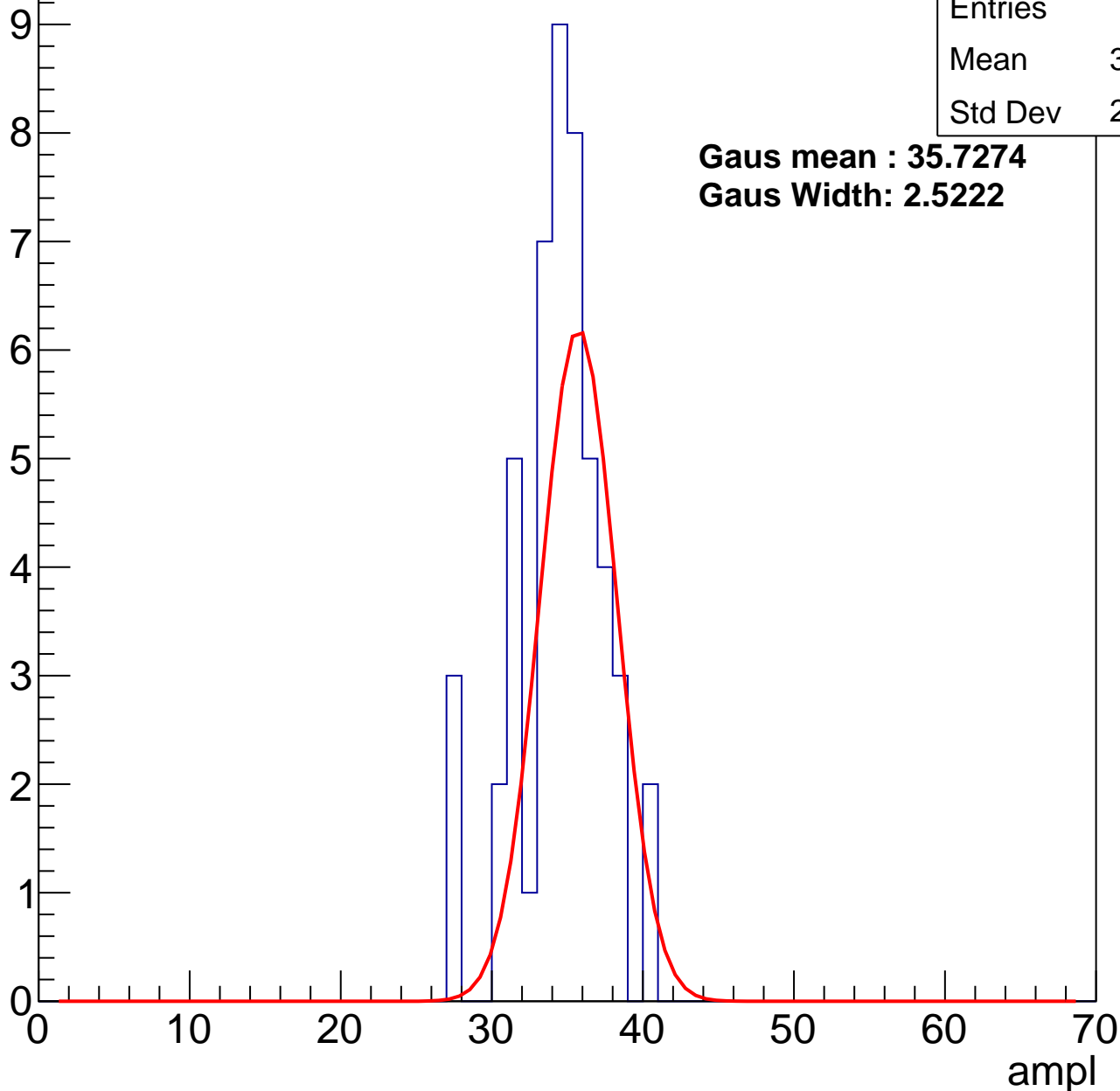
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	34.02
Std Dev	2.924

**Gaus mean : 35.7274**

**Gaus Width: 2.5222**



# B1L002S, U3-ch87, adc2

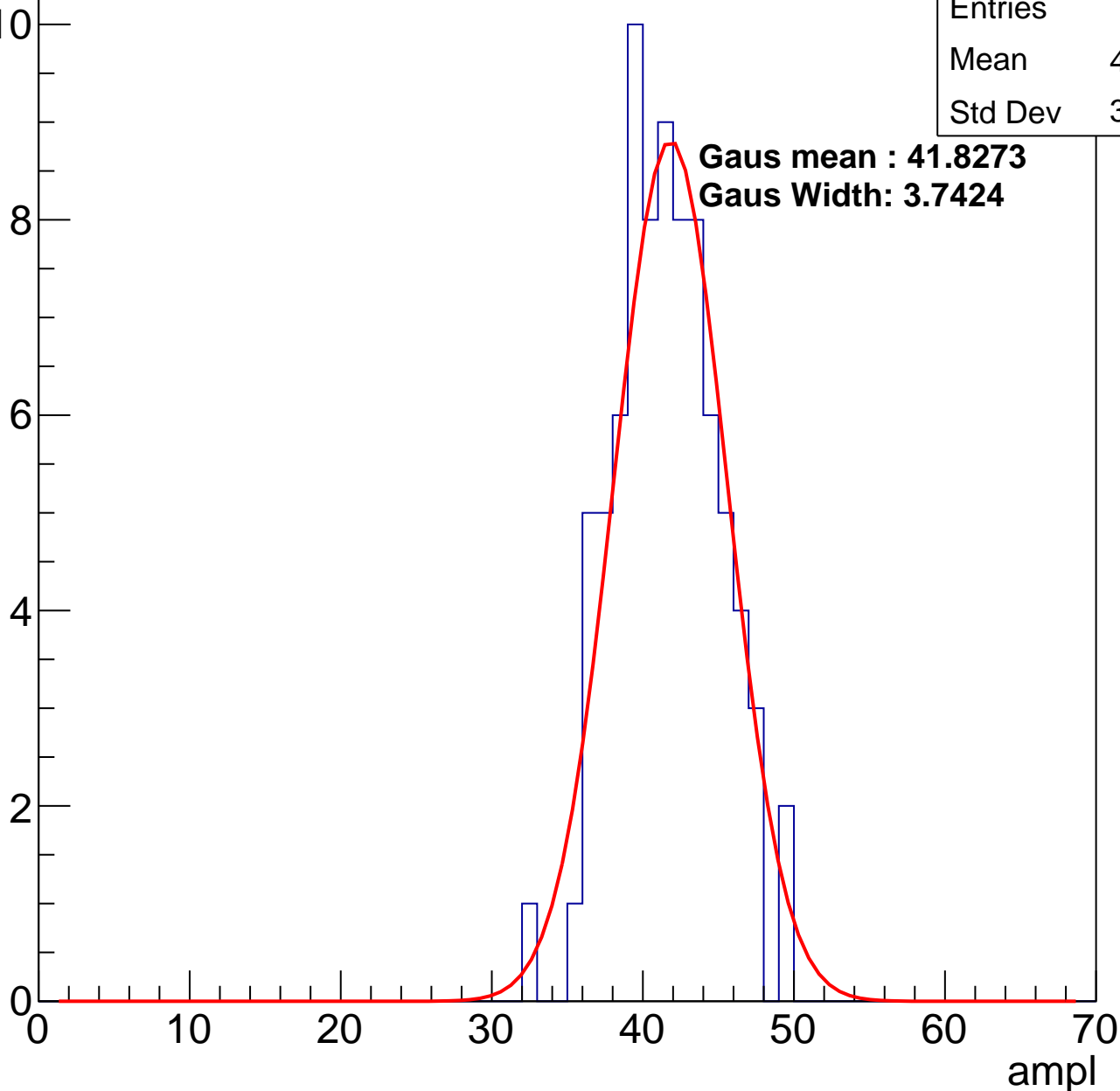
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	81
Mean	41.12
Std Dev	3.393

**Gaus mean : 41.8273**

**Gaus Width: 3.7424**



# B1L002S, U3-ch87, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	51
Mean	47.24
Std Dev	2.798

Entry

10

8

6

4

2

0

0

10

20

30

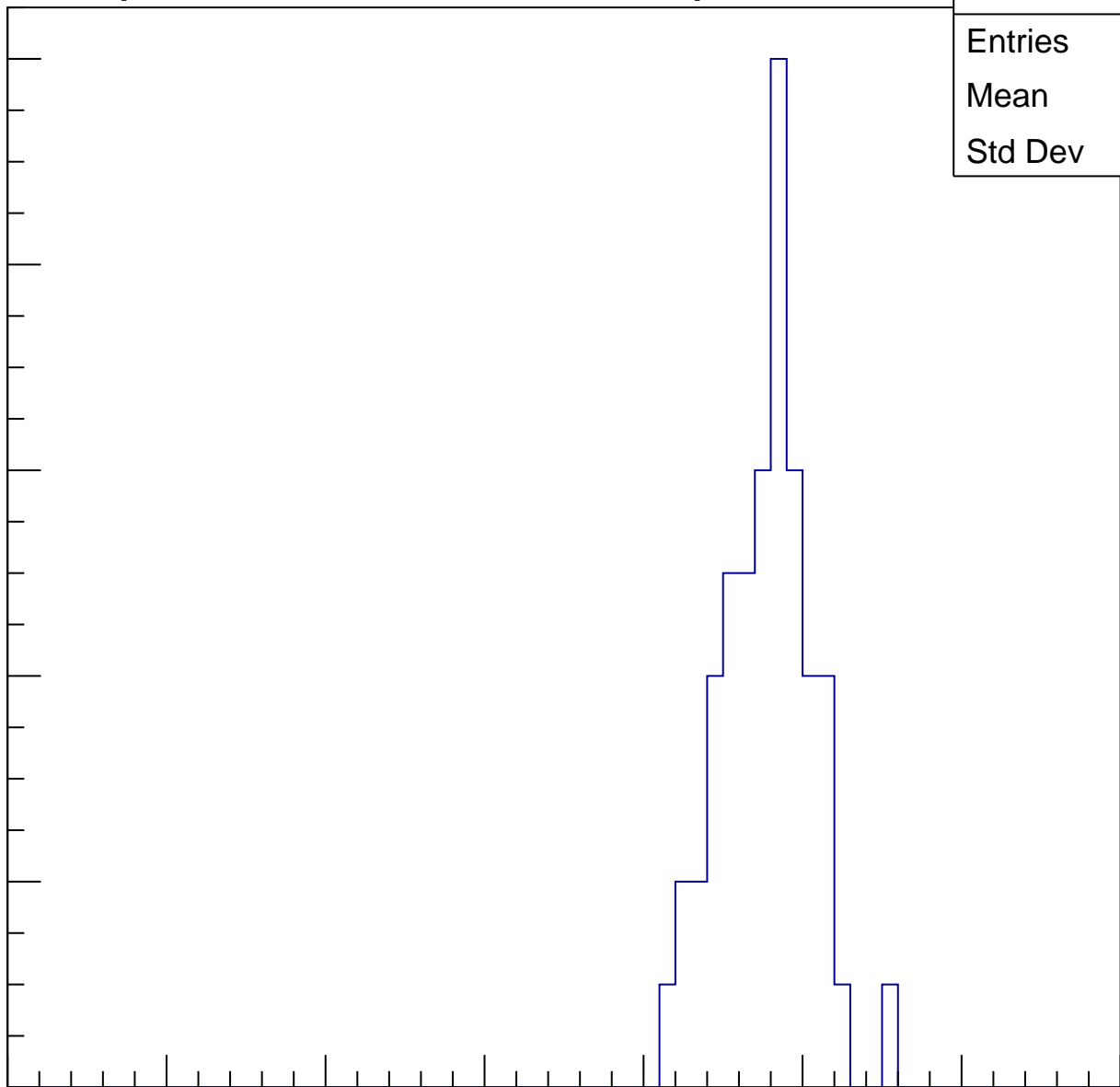
40

50

60

70

ampl

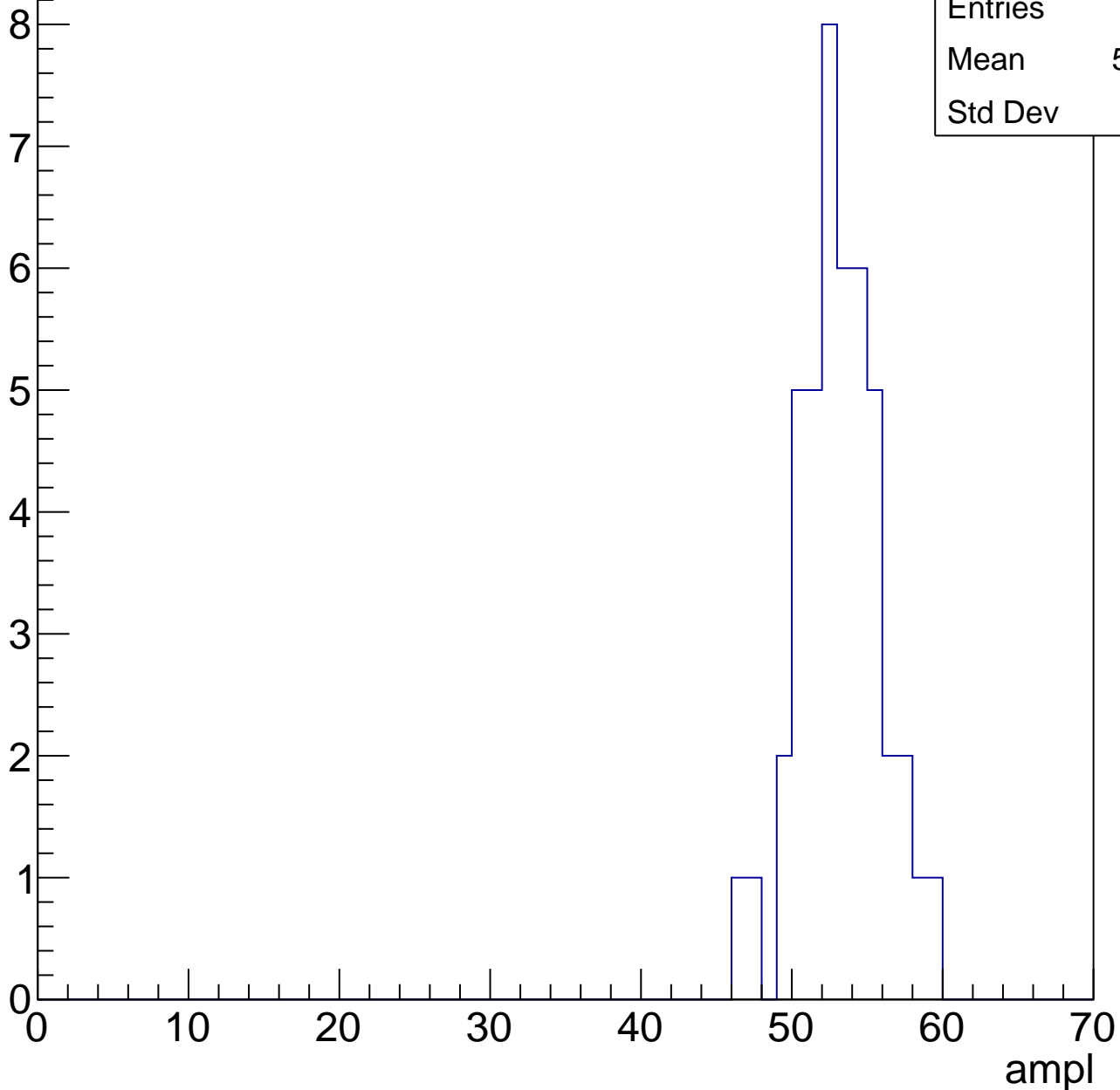


# B1L002S, U3-ch87, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

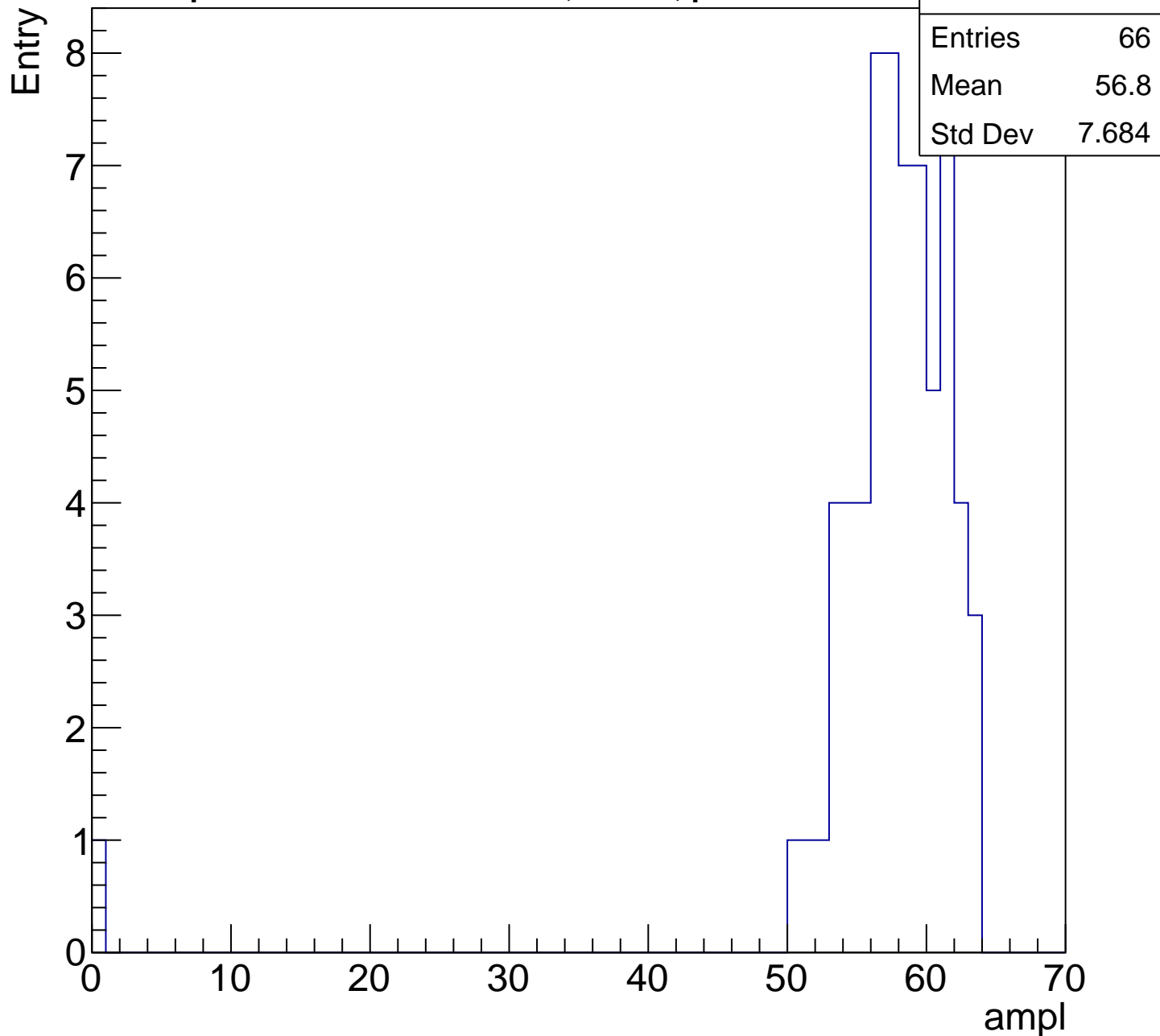
Entry

Entries	45
Mean	52.71
Std Dev	2.68



# B1L002S, U3-ch87, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

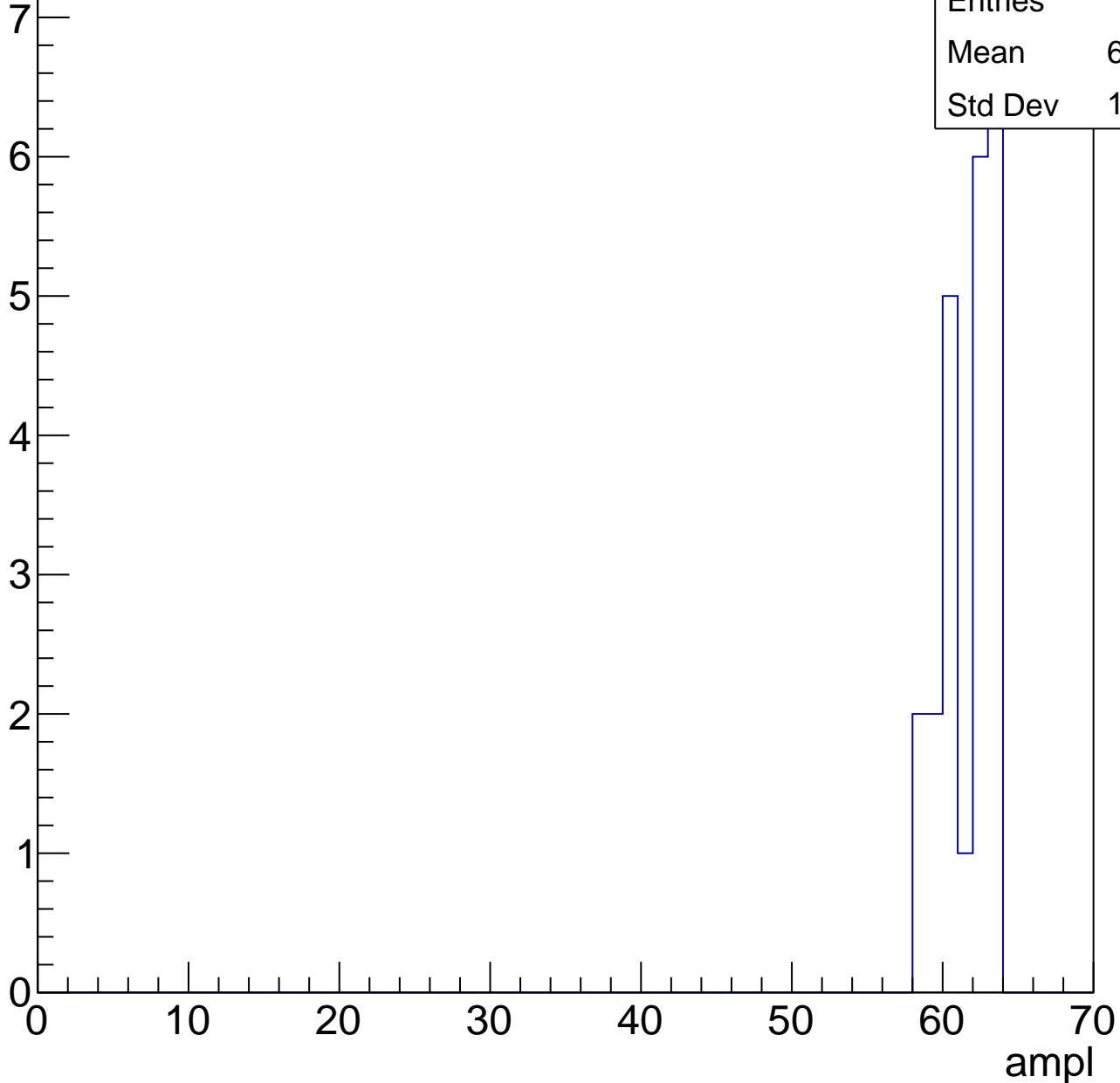


# B1L002S, U3-ch87, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	61.22
Std Dev	1.667

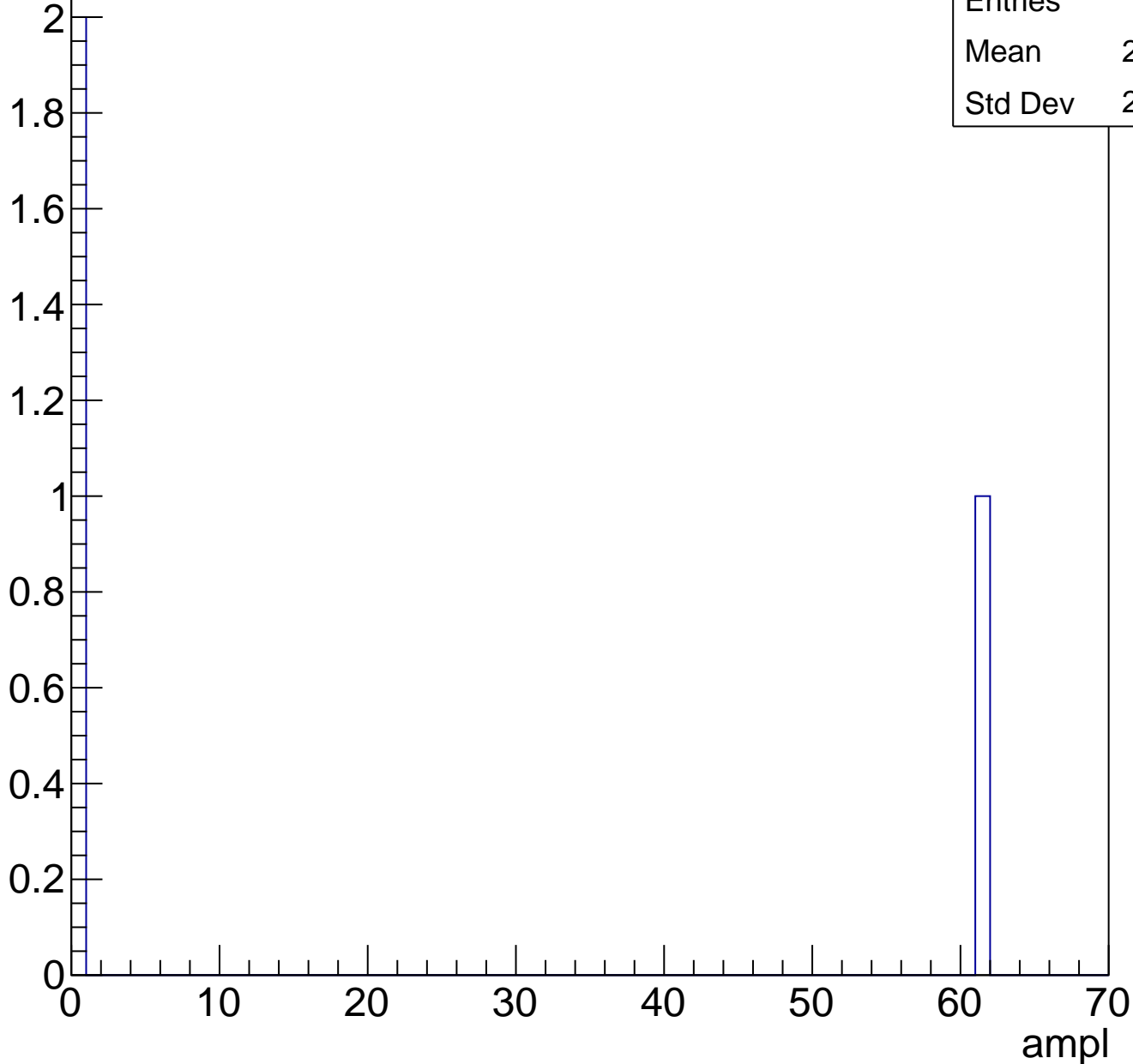




# B1L002S, U3-ch87, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch88, adc0

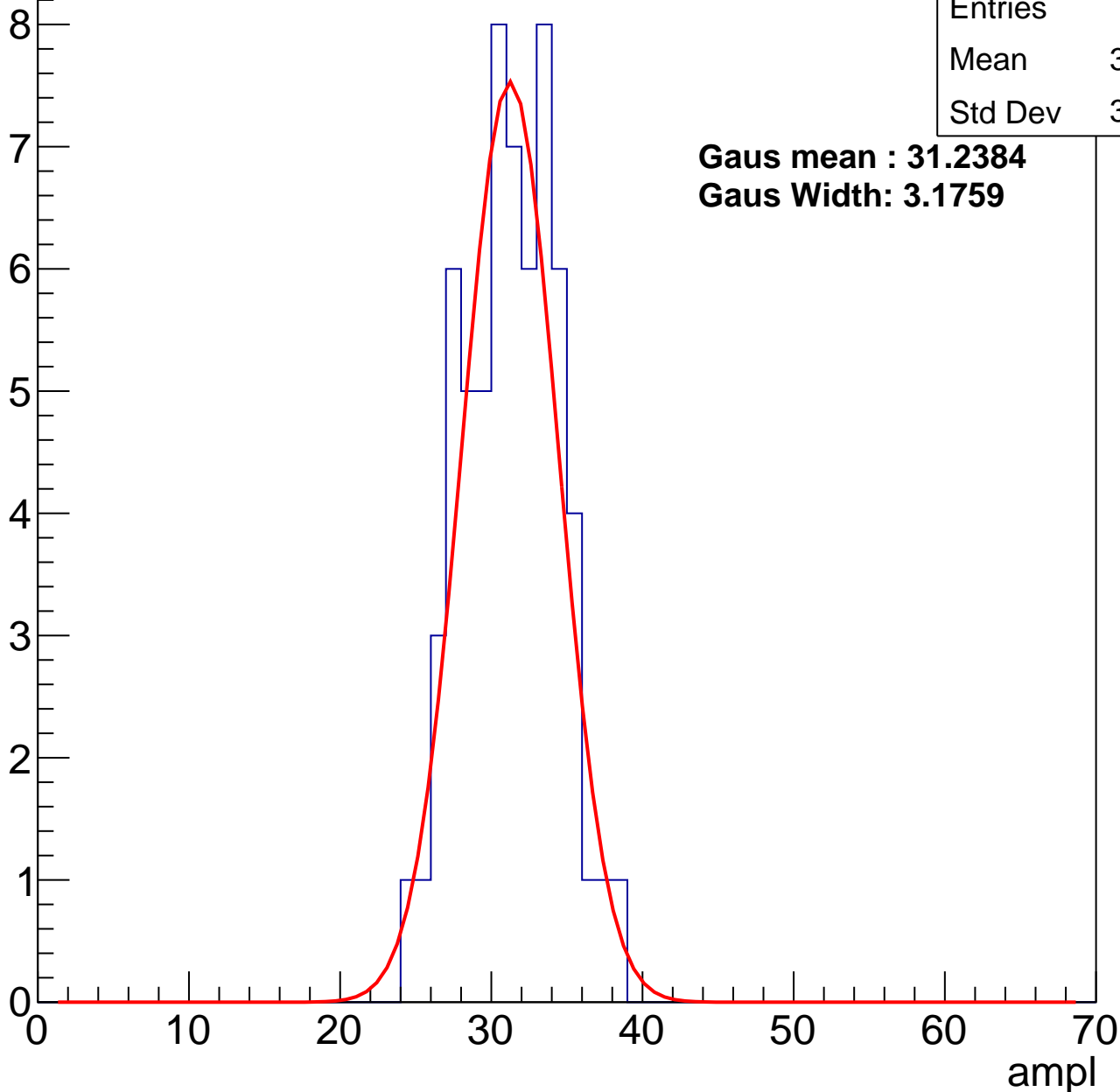
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	30.83
Std Dev	3.068

**Gaus mean : 31.2384**

**Gaus Width: 3.1759**



# B1L002S, U3-ch88, adc1

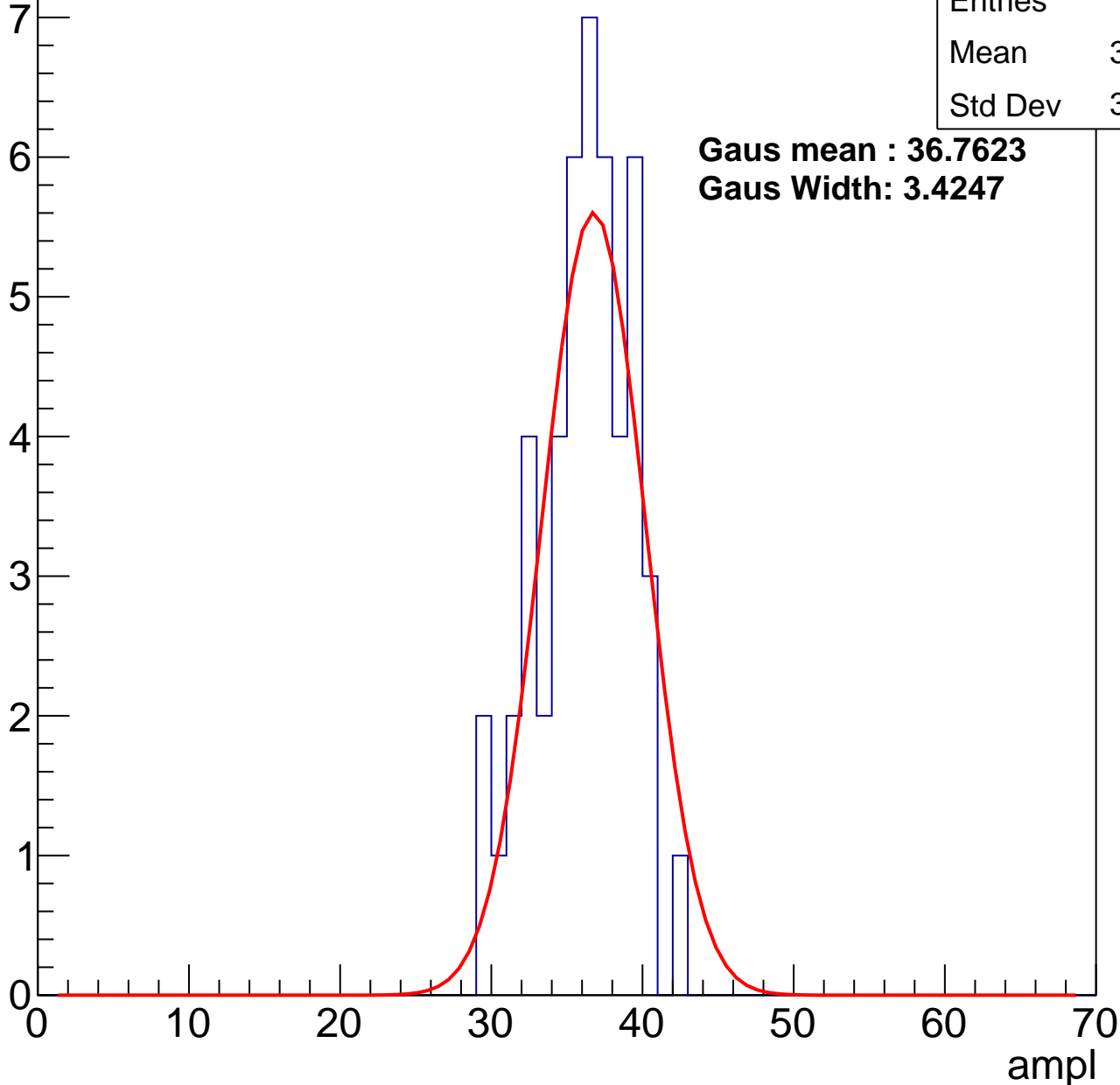
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	35.67
Std Dev	3.037

**Gaus mean : 36.7623**

**Gaus Width: 3.4247**



# B1L002S, U3-ch88, adc2

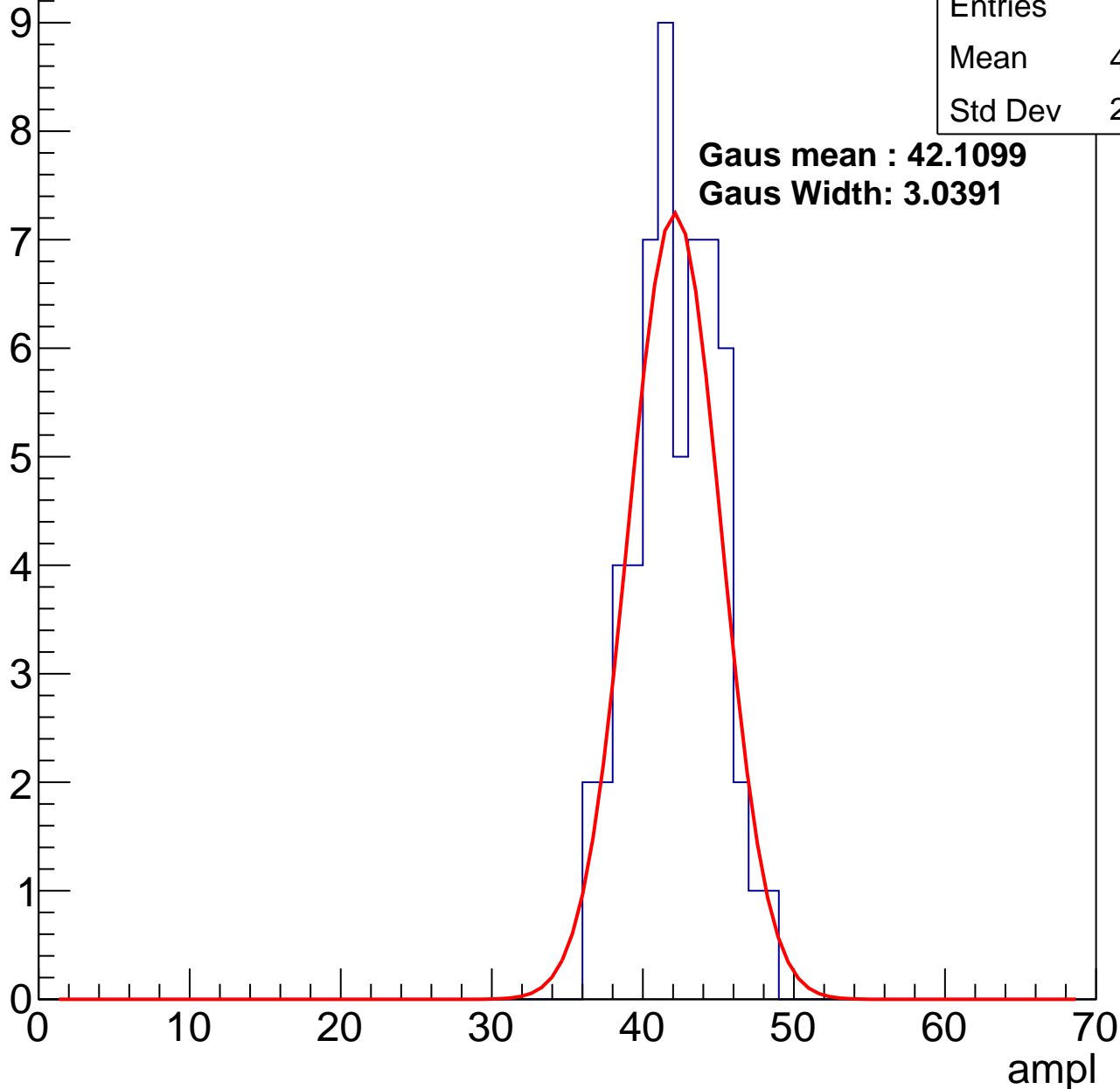
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	41.74
Std Dev	2.769

**Gaus mean : 42.1099**

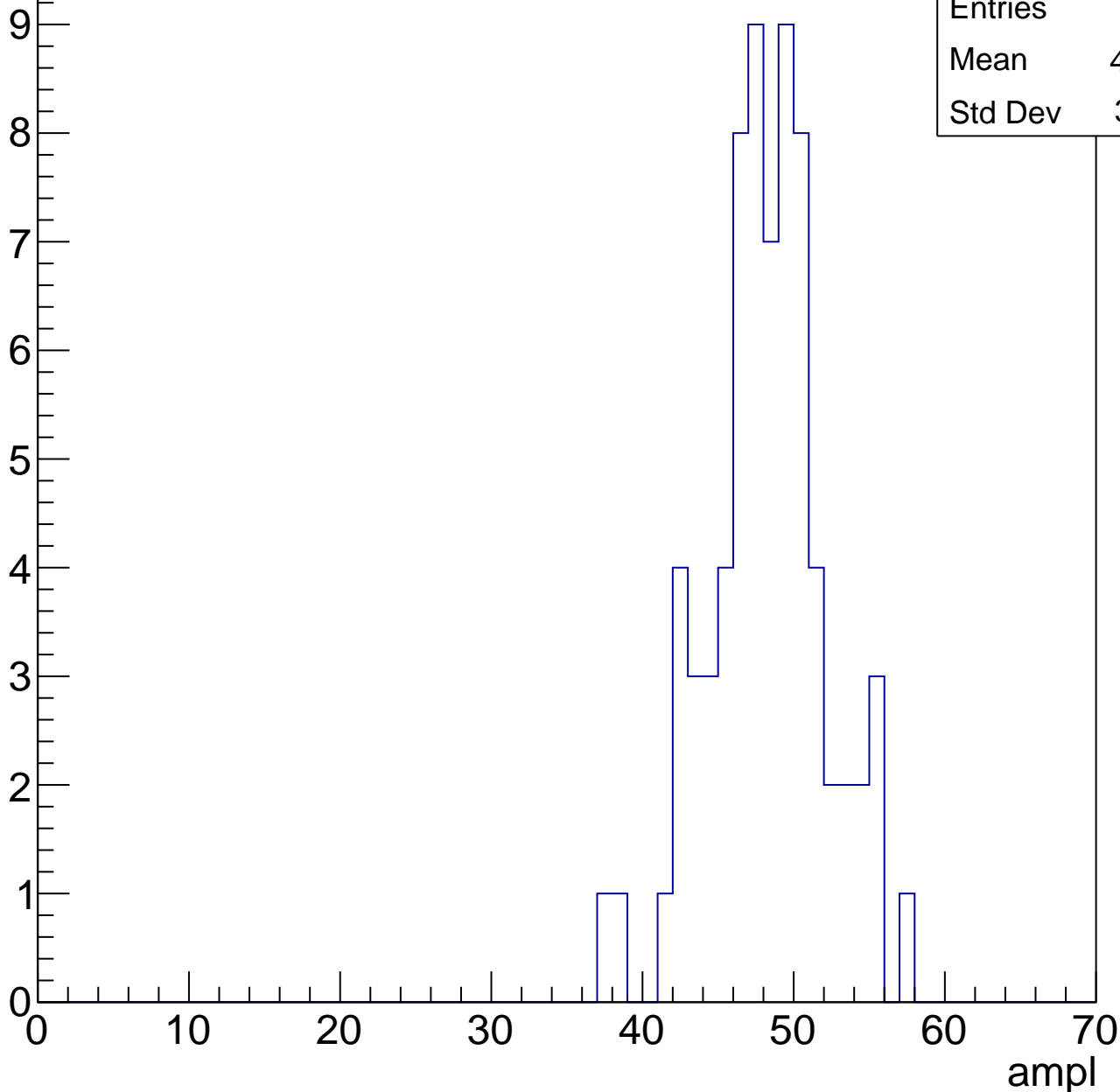
**Gaus Width: 3.0391**



# B1L002S, U3-ch88, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

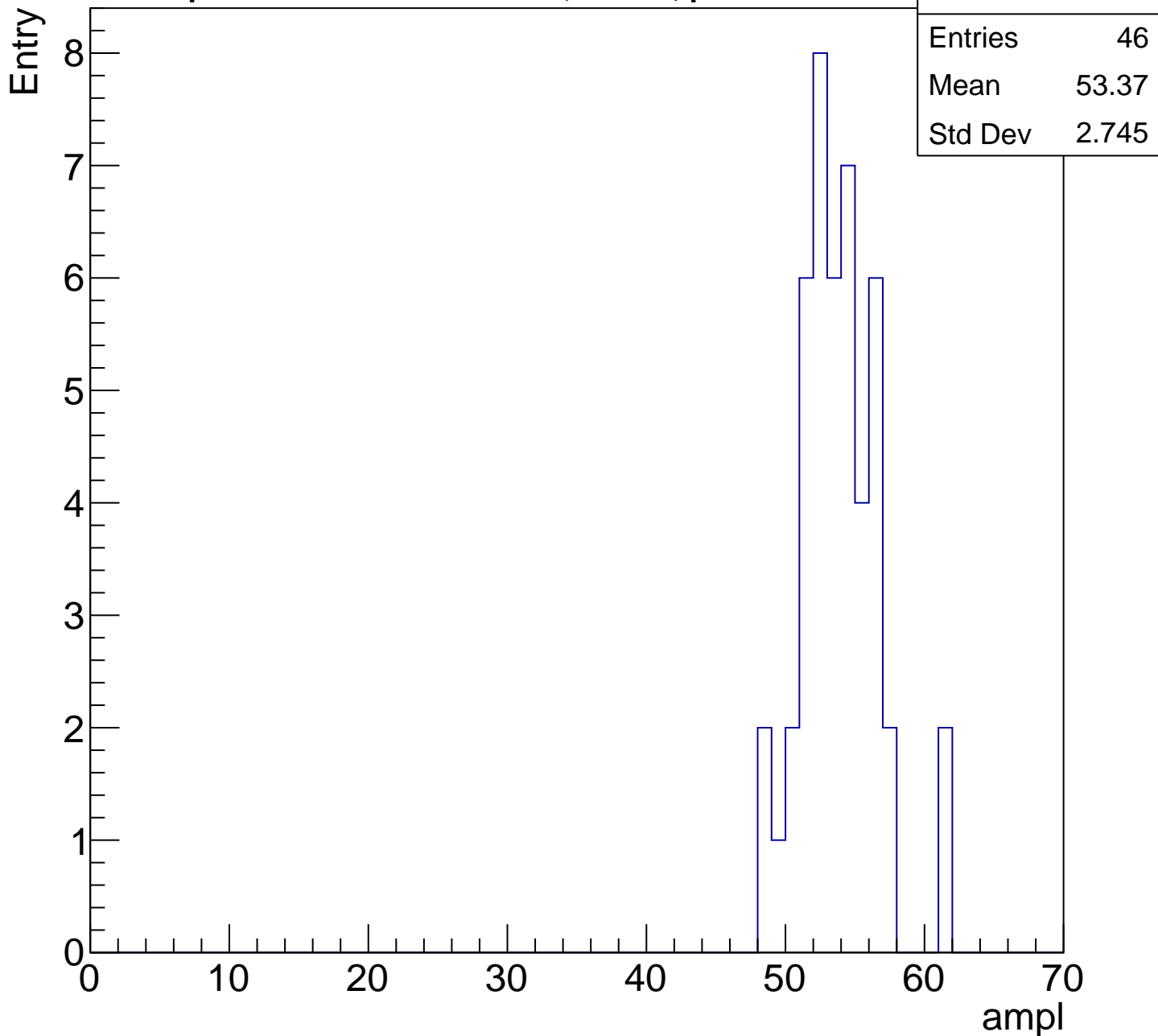
Entry



Entries	72
Mean	47.74
Std Dev	3.851

# B1L002S, U3-ch88, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

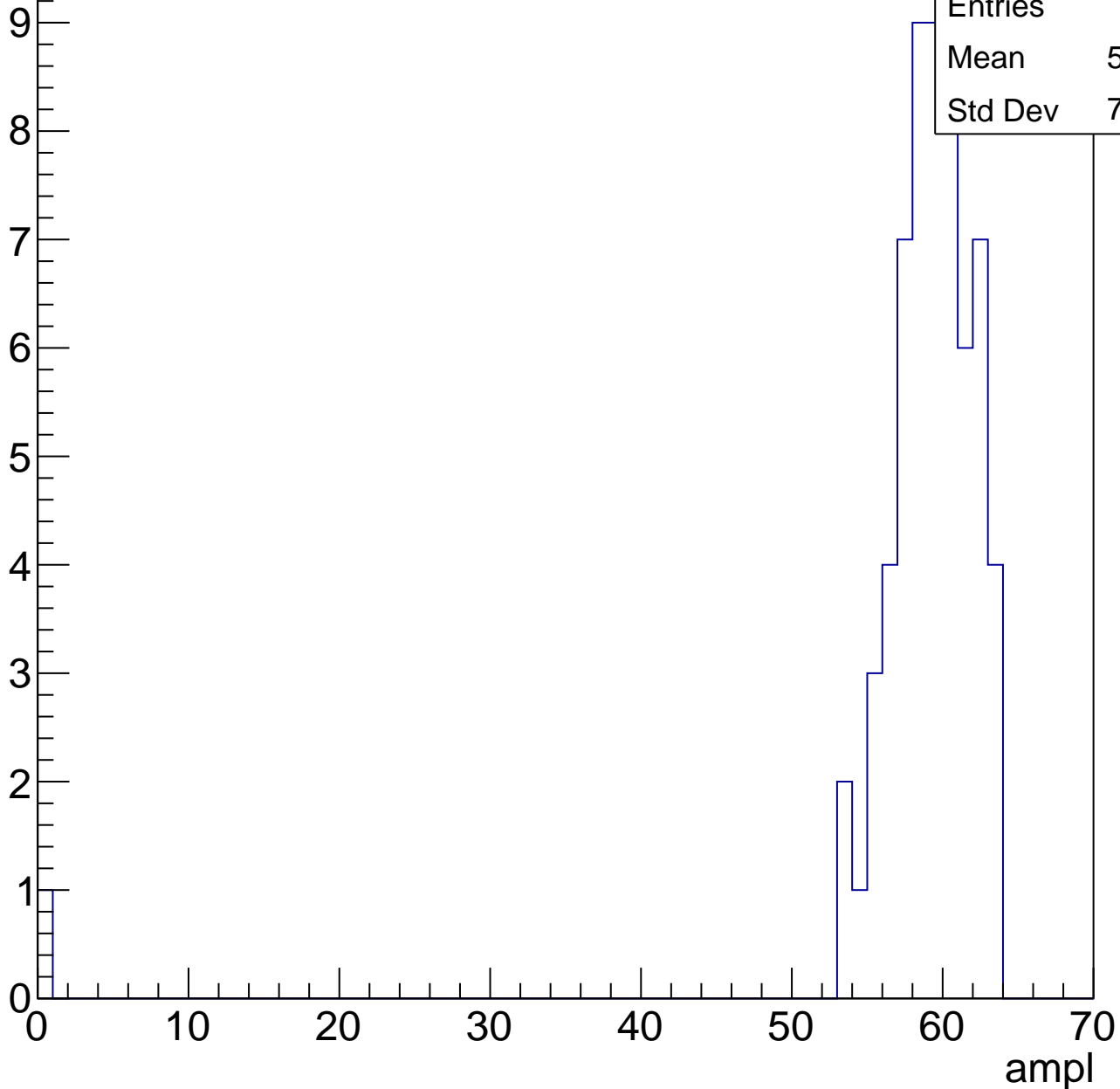


# B1L002S, U3-ch88, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	57.92
Std Dev	7.879

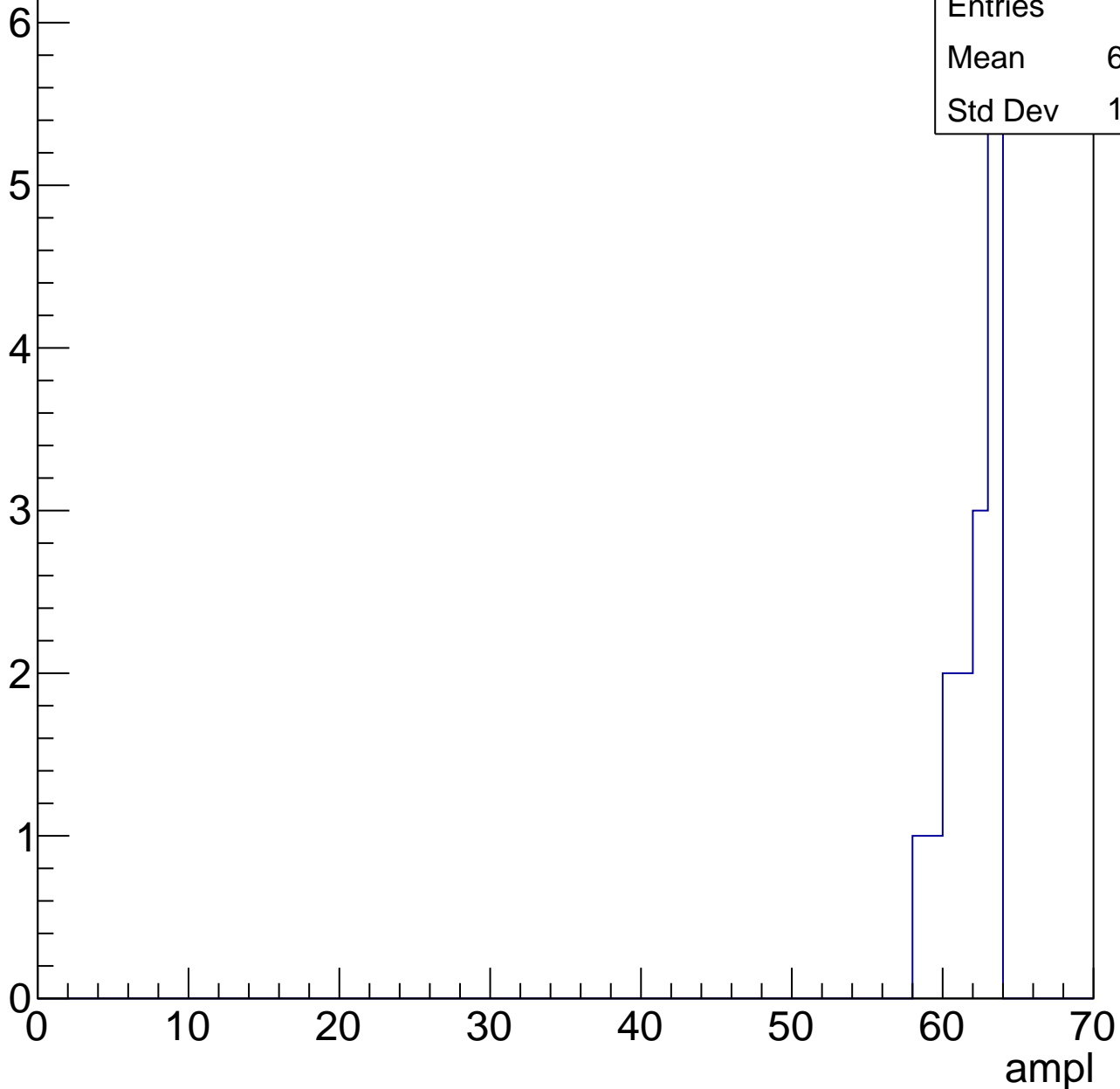


# B1L002S, U3-ch88, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	15
Mean	61.53
Std Dev	1.586





# B1L002S, U3-ch88, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch89, adc0

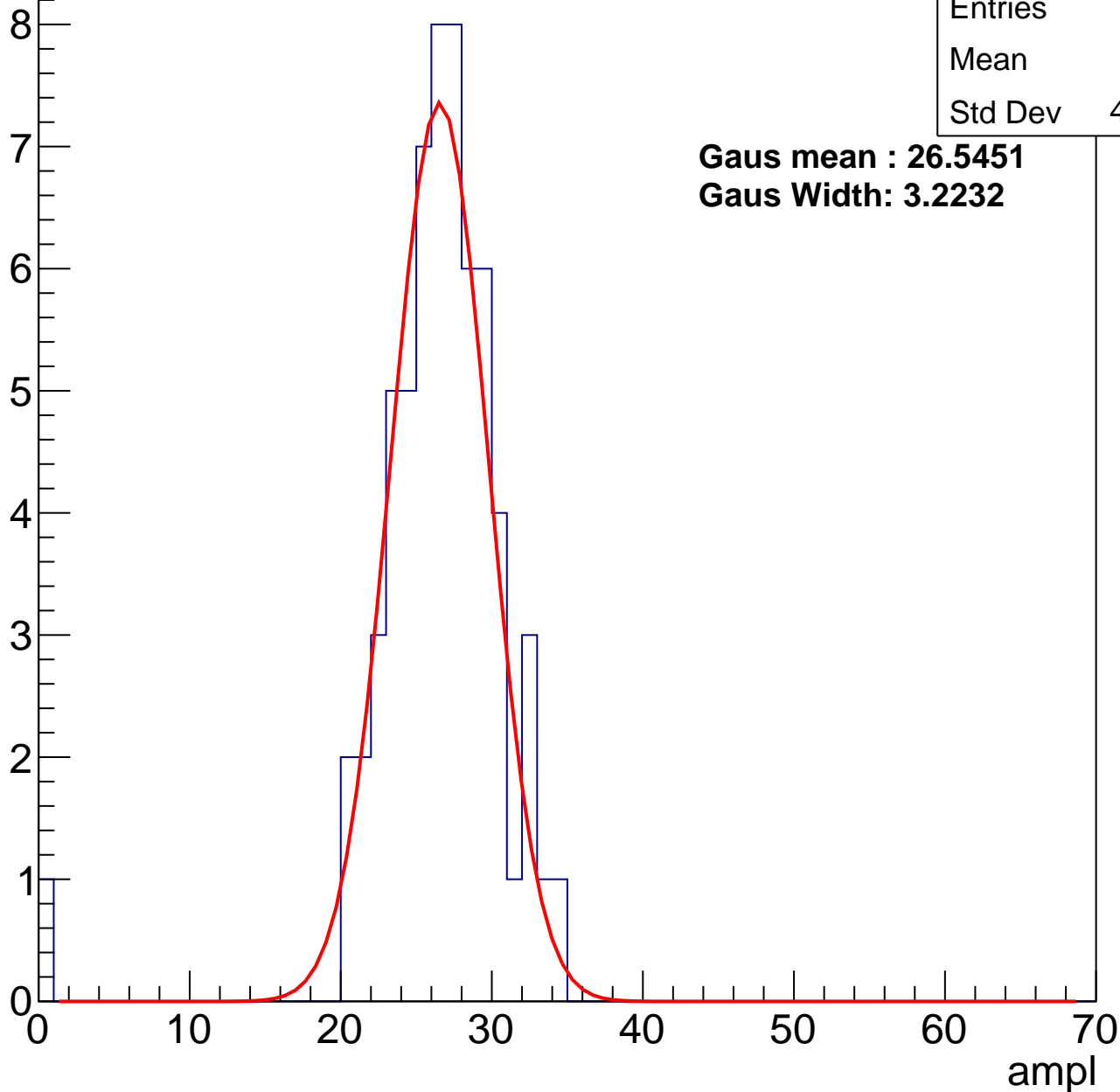
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	26
Std Dev	4.567

**Gaus mean : 26.5451**

**Gaus Width: 3.2232**



# B1L002S, U3-ch89, adc1

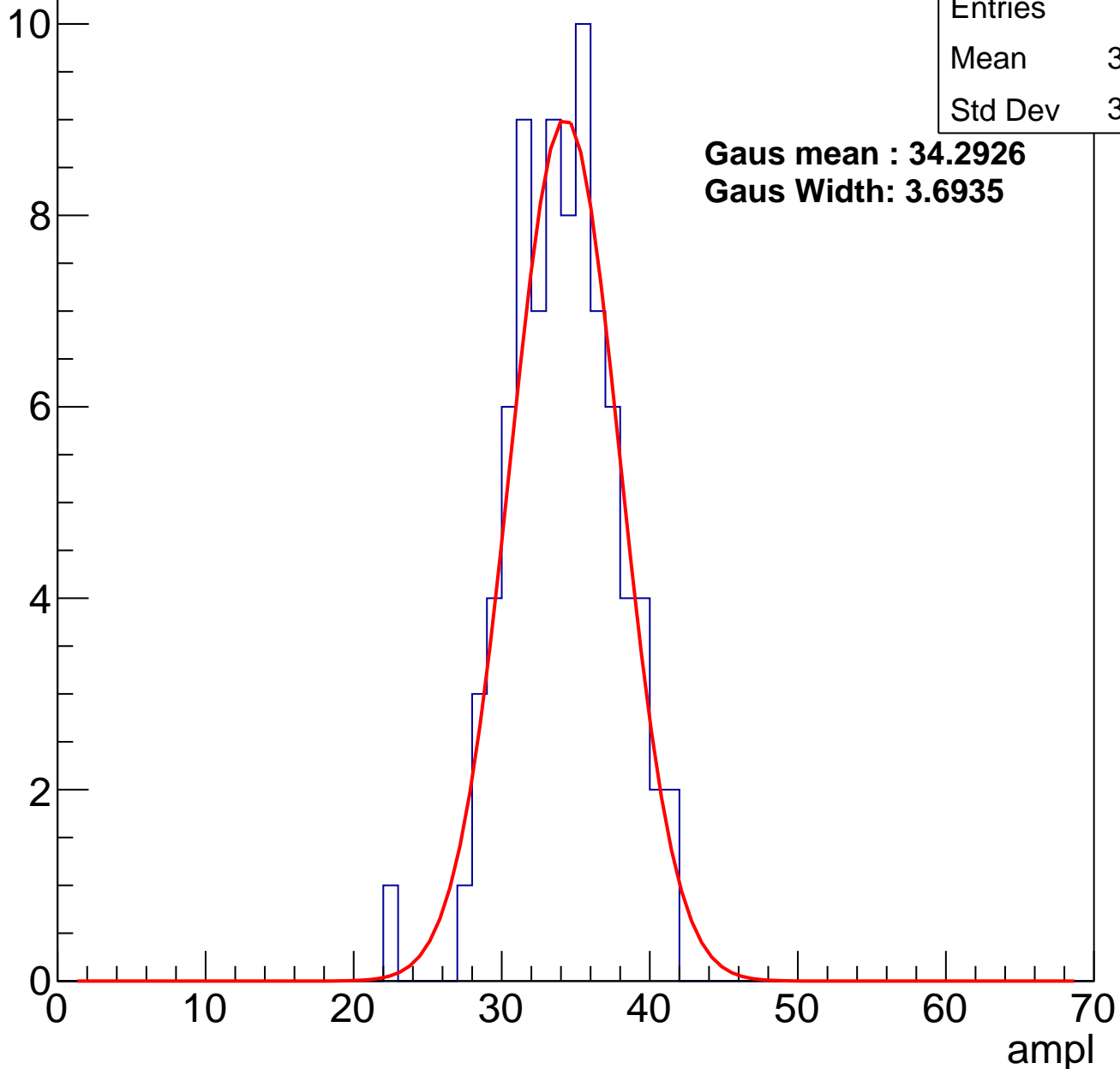
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	83
Mean	33.67
Std Dev	3.527

**Gaus mean : 34.2926**

**Gaus Width: 3.6935**

Entry



# B1L002S, U3-ch89, adc2

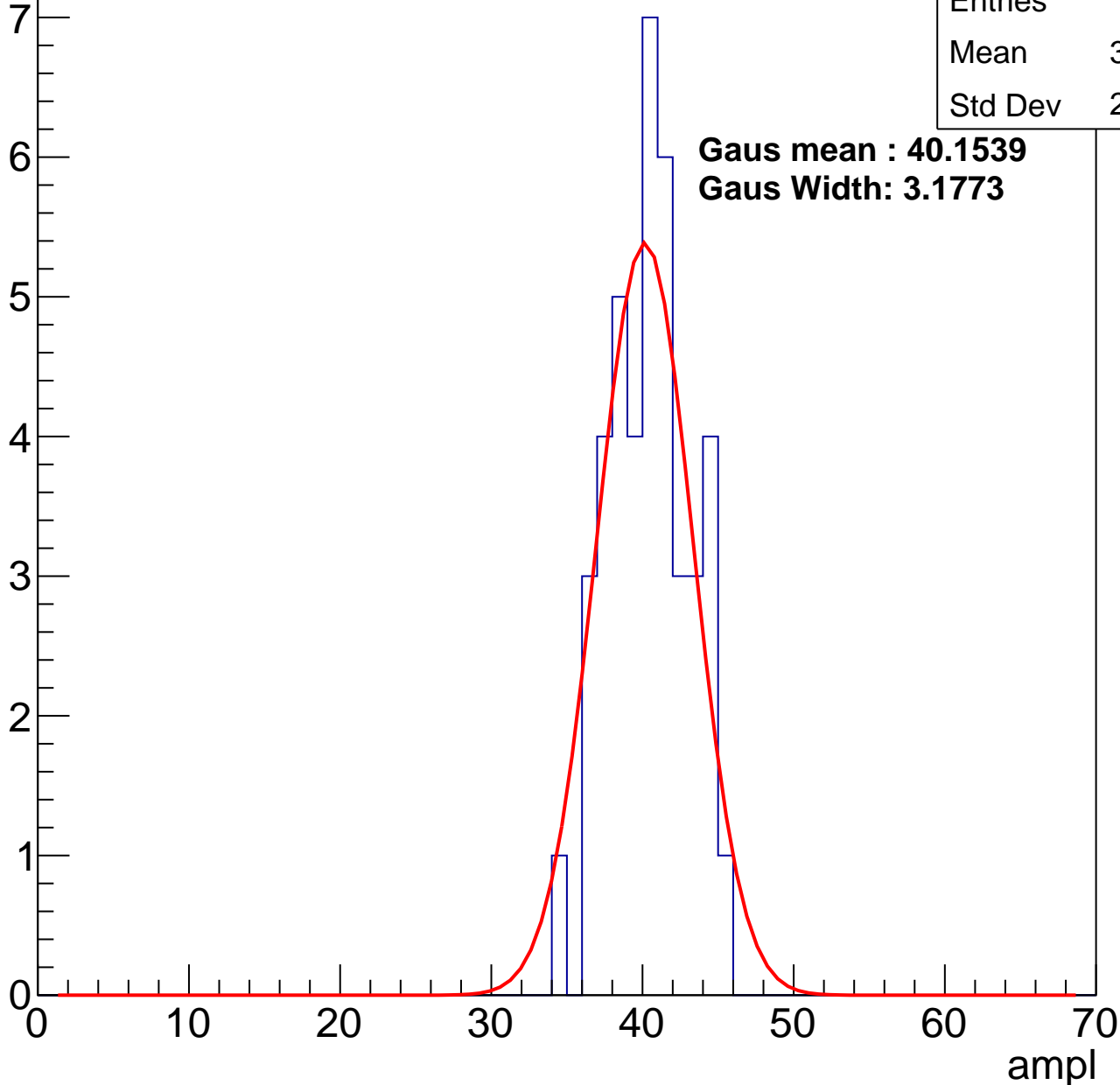
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	39.95
Std Dev	2.603

**Gaus mean : 40.1539**

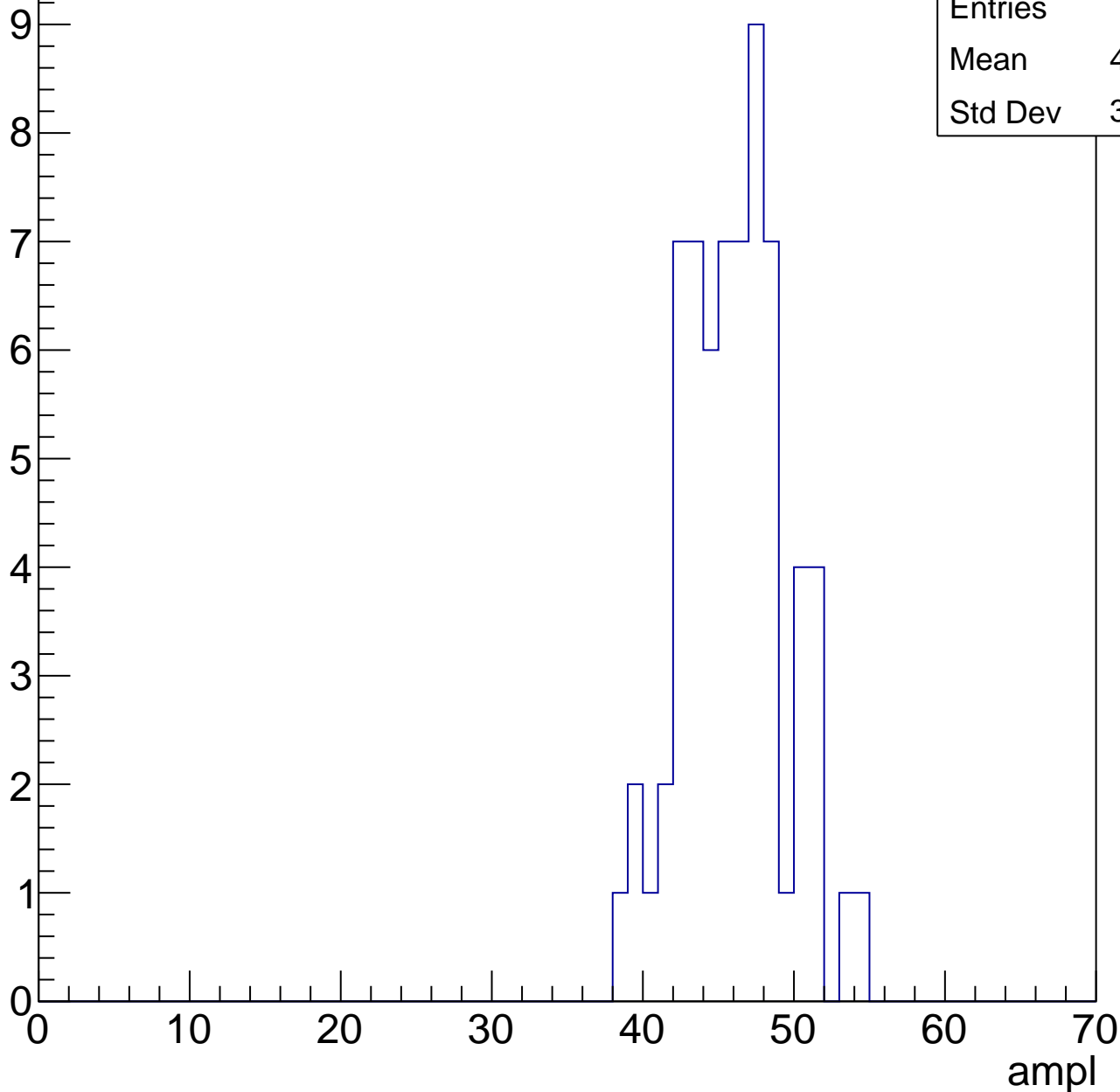
**Gaus Width: 3.1773**



# B1L002S, U3-ch89, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

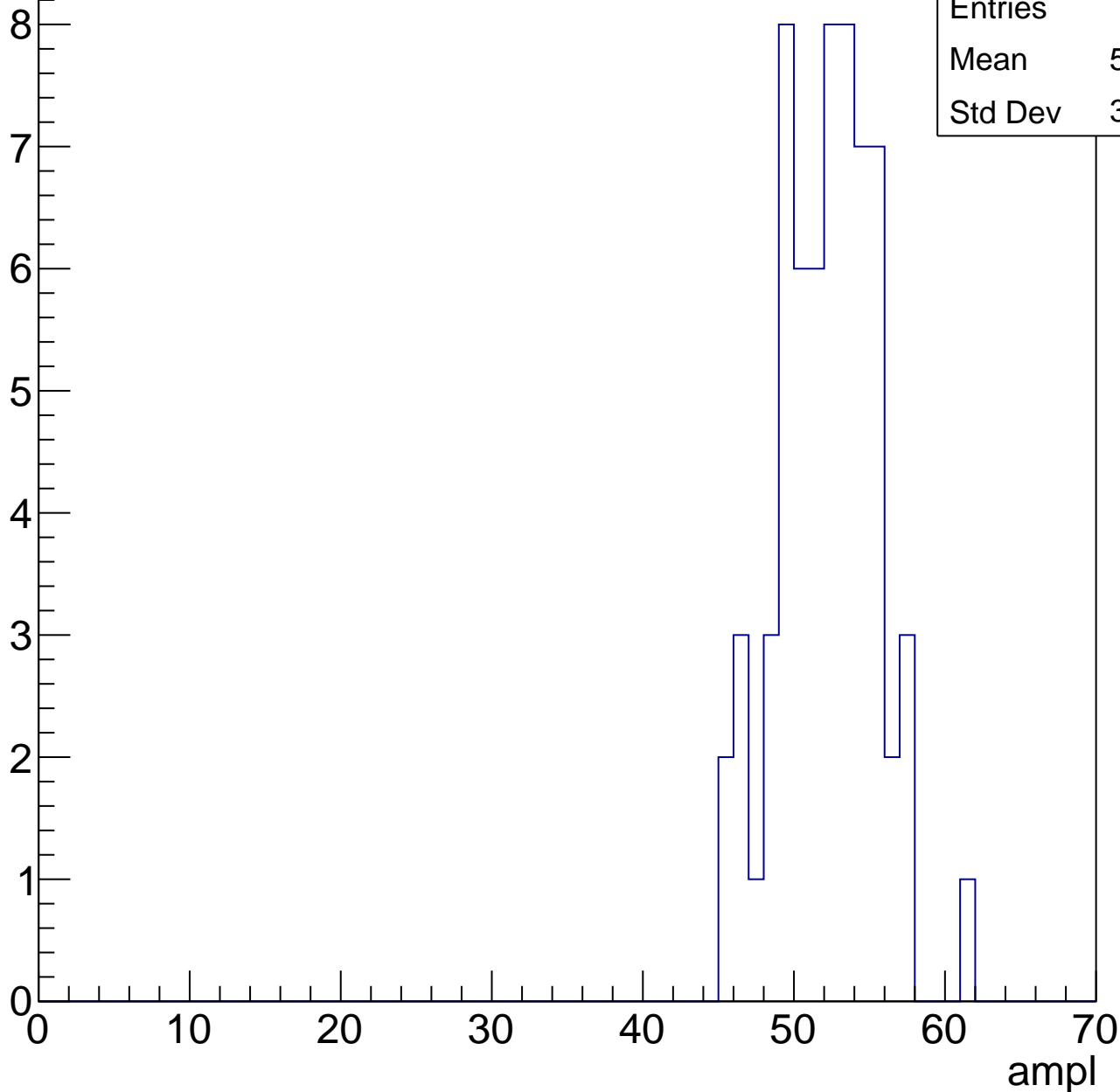


# B1L002S, U3-ch89, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	51.75
Std Dev	3.196

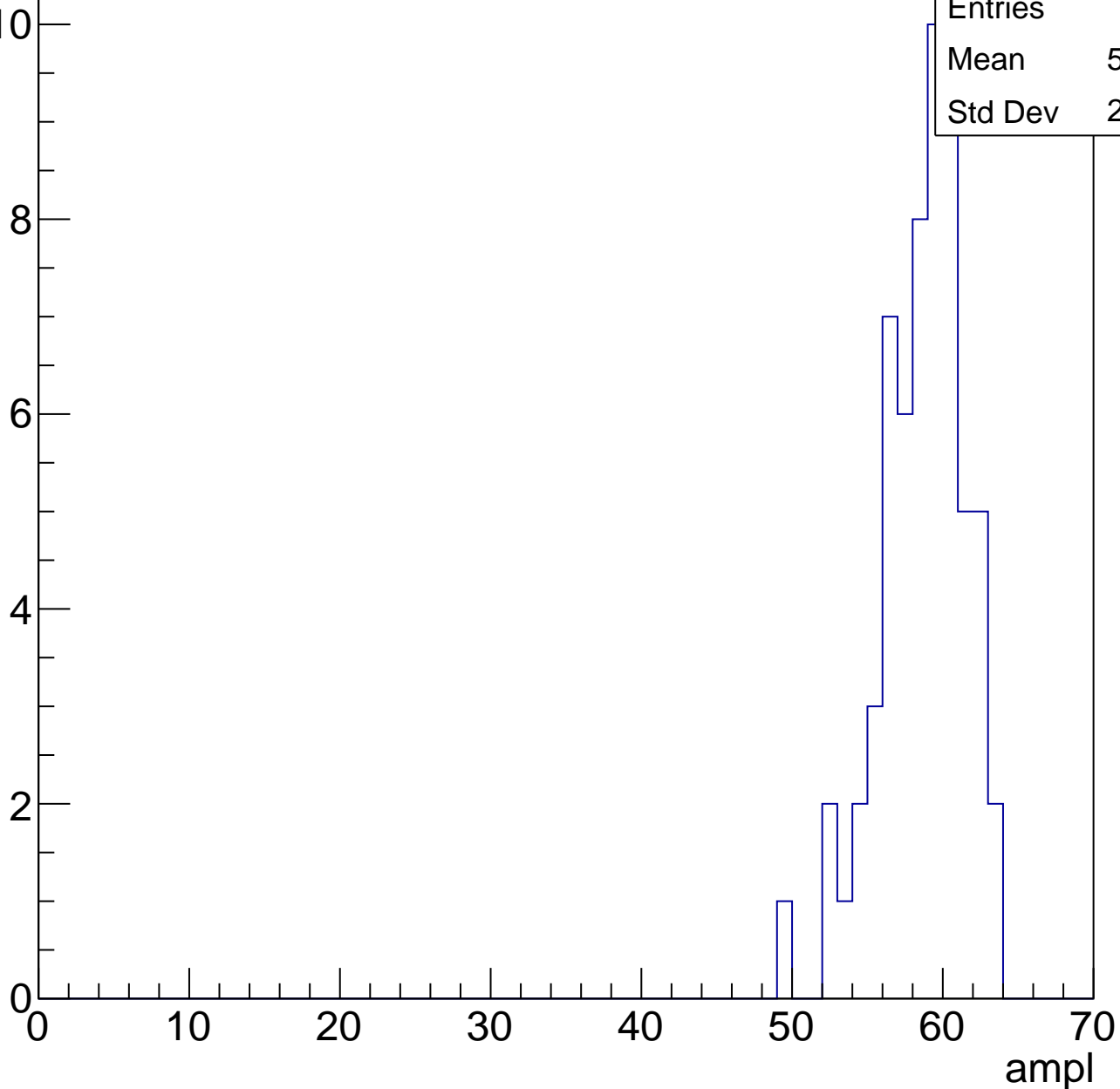


# B1L002S, U3-ch89, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	58.19
Std Dev	2.822

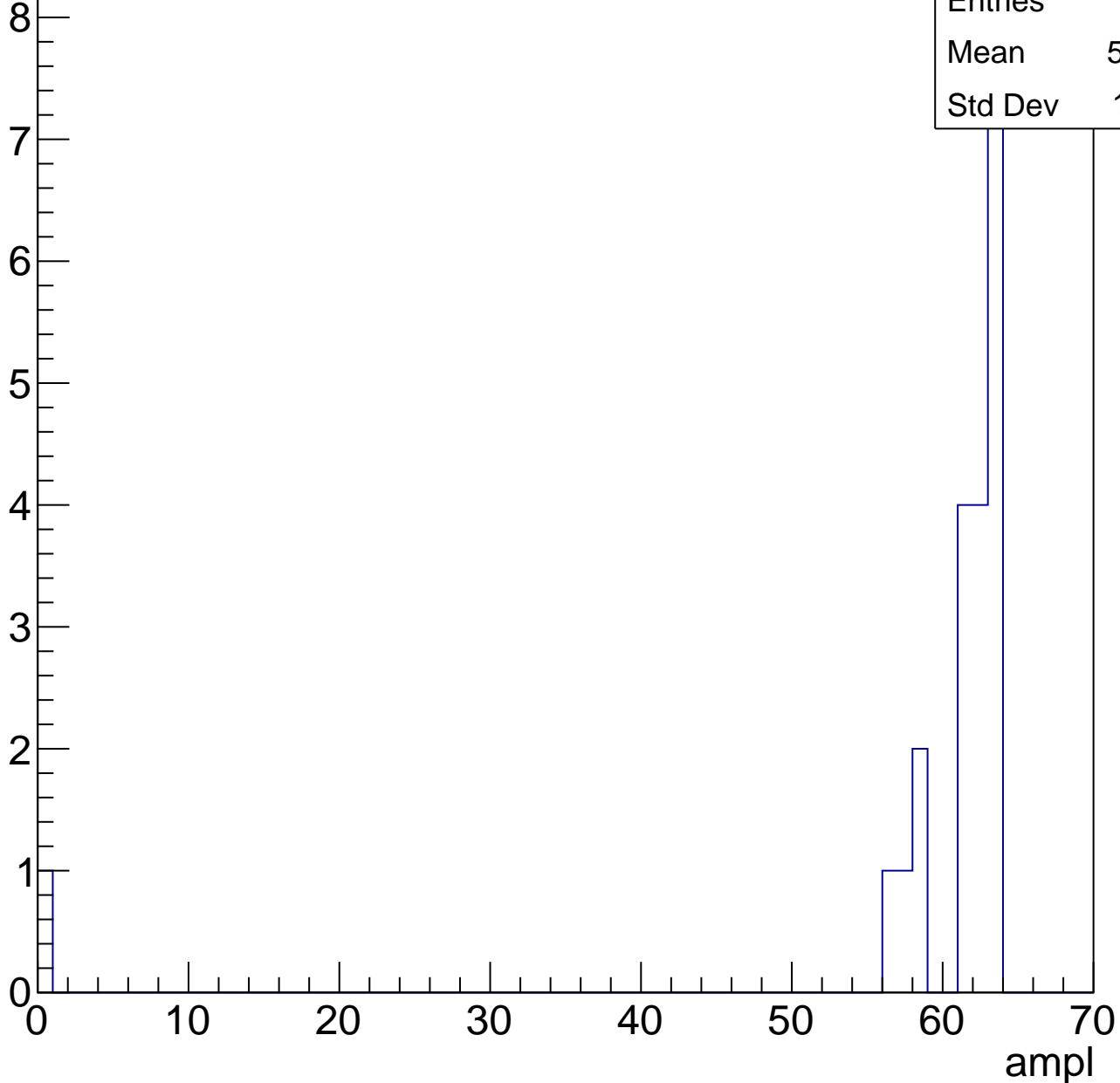


# B1L002S, U3-ch89, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.33
Std Dev	13.21





# B1L002S, U3-ch89, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	20.67
Std Dev	29.23

# B1L002S, U3-ch90, adc0

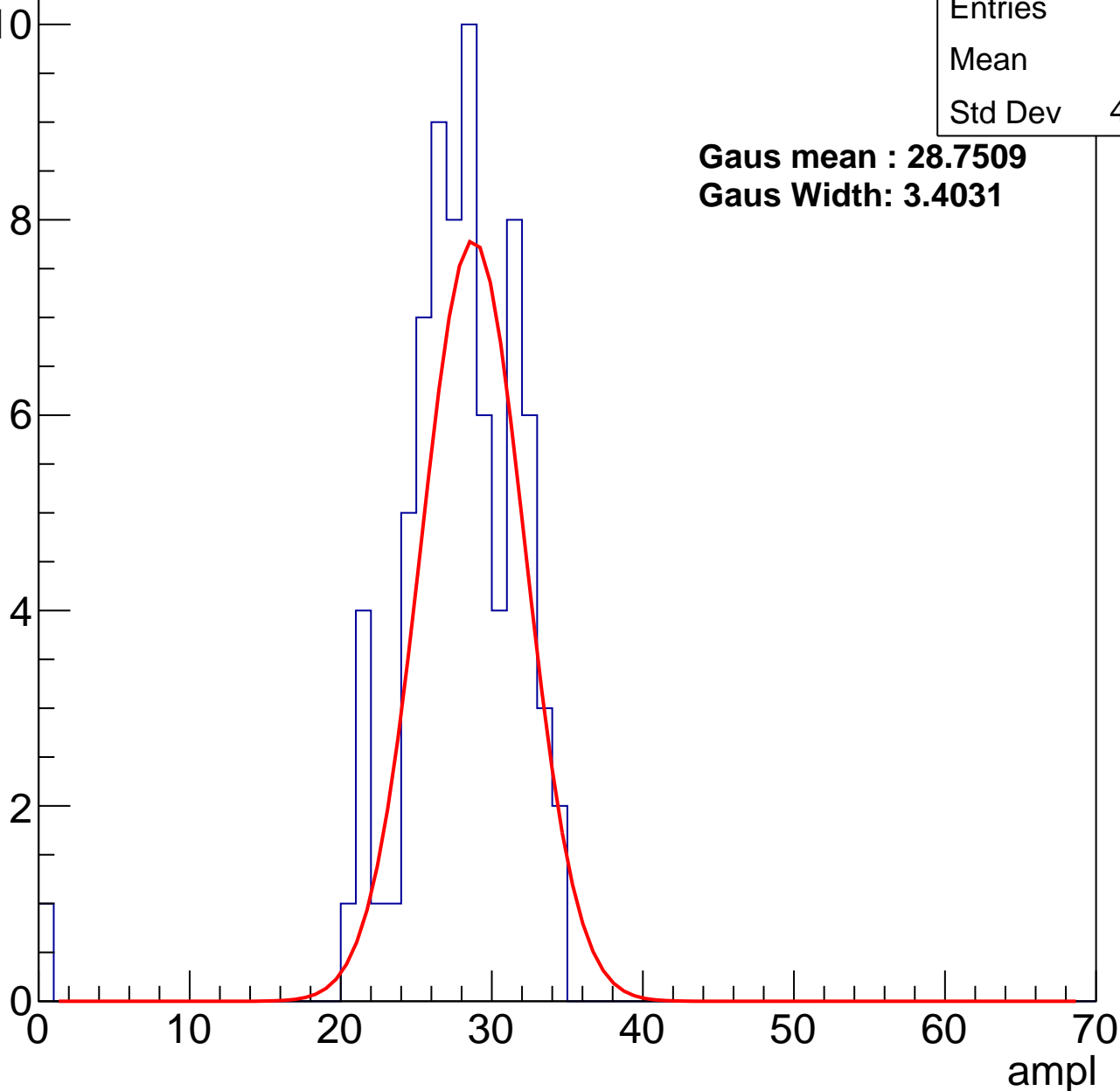
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	76
Mean	27.3
Std Dev	4.577

**Gaus mean : 28.7509**

**Gaus Width: 3.4031**



# B1L002S, U3-ch90, adc1

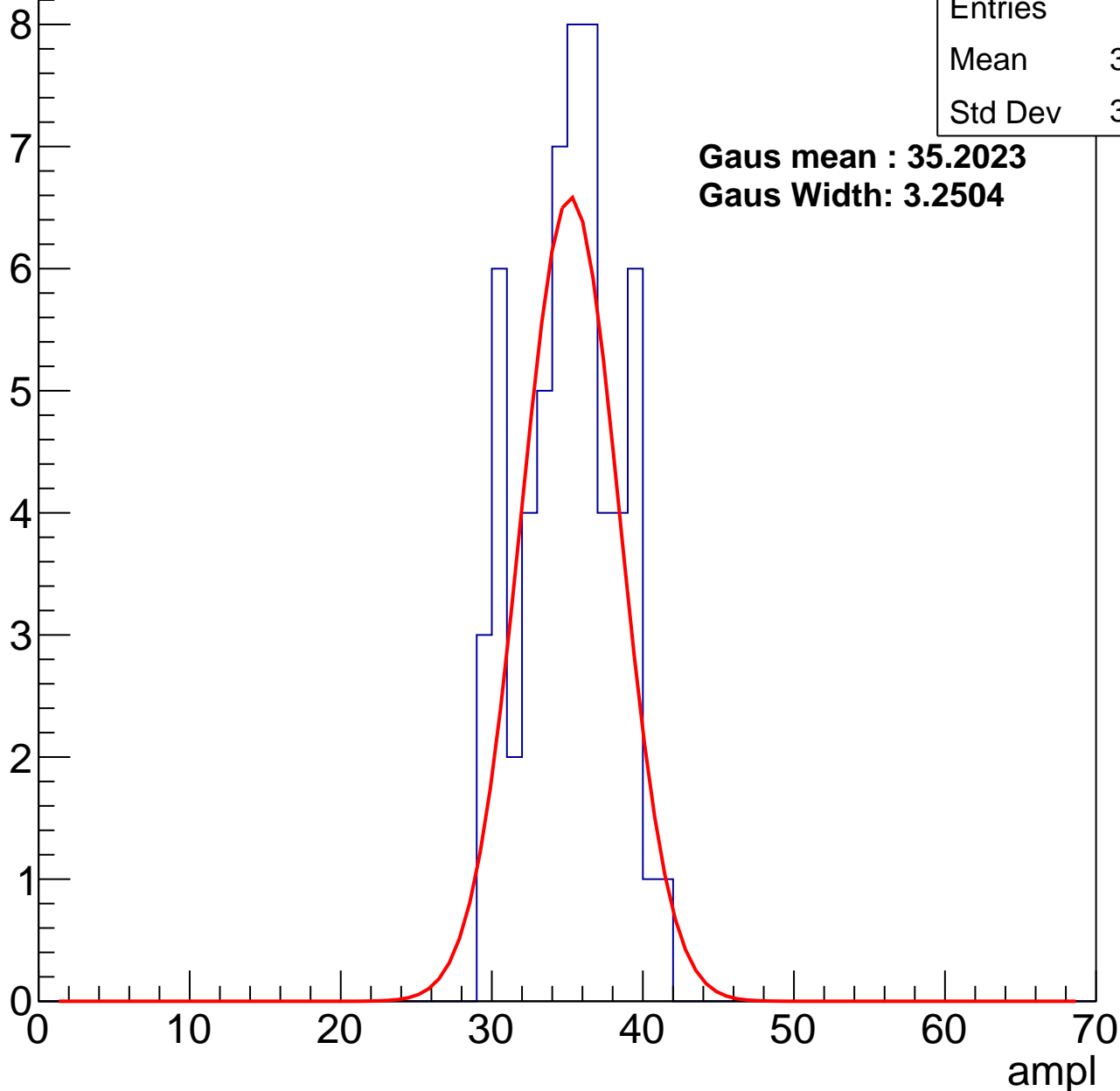
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	34.63
Std Dev	3.086

**Gaus mean : 35.2023**

**Gaus Width: 3.2504**



# B1L002S, U3-ch90, adc2

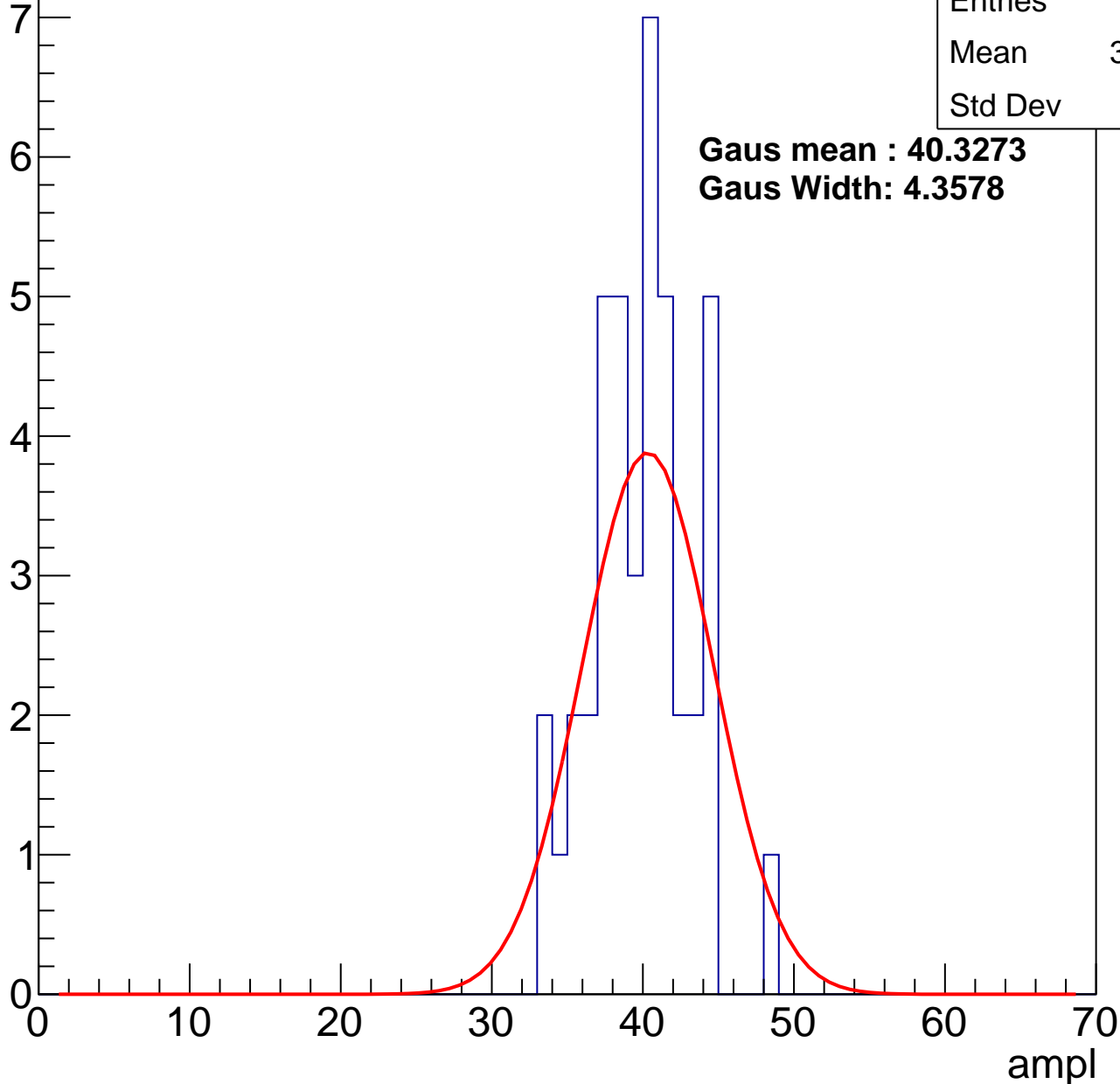
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	39.45
Std Dev	3.26

**Gaus mean : 40.3273**

**Gaus Width: 4.3578**

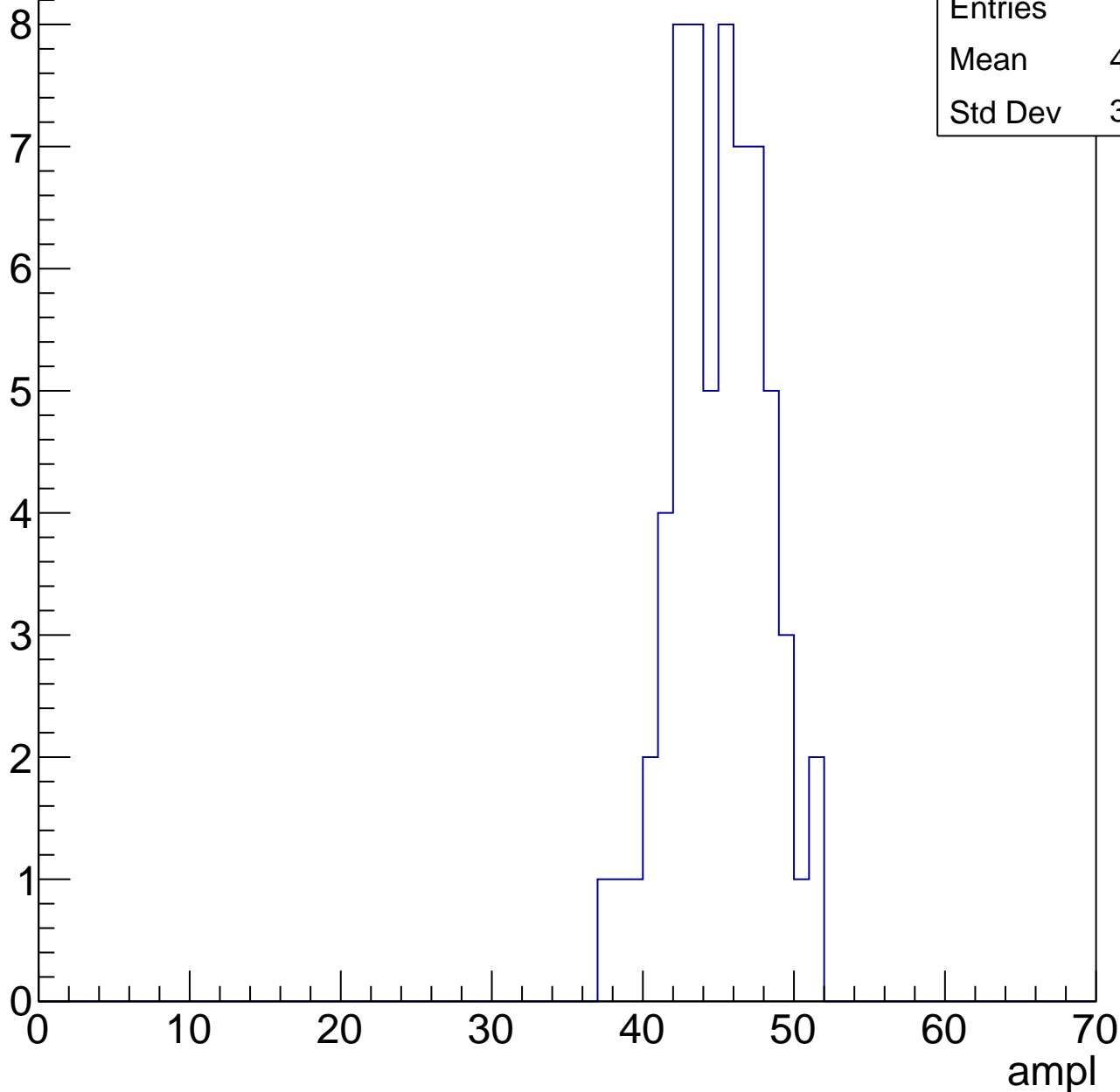


# B1L002S, U3-ch90, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	44.57
Std Dev	3.048



# B1L002S, U3-ch90, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

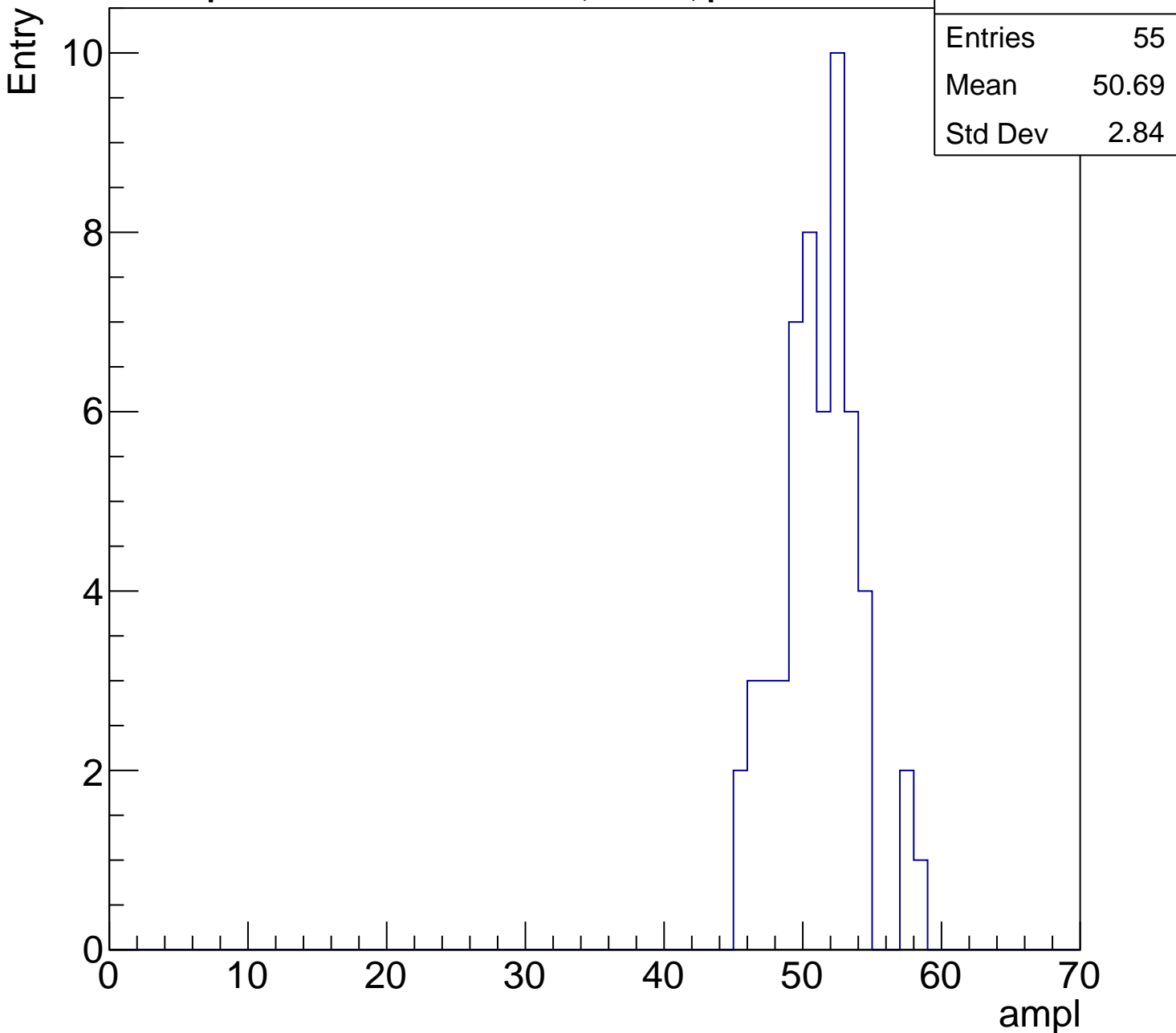
Entries	55
Mean	50.69
Std Dev	2.84

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

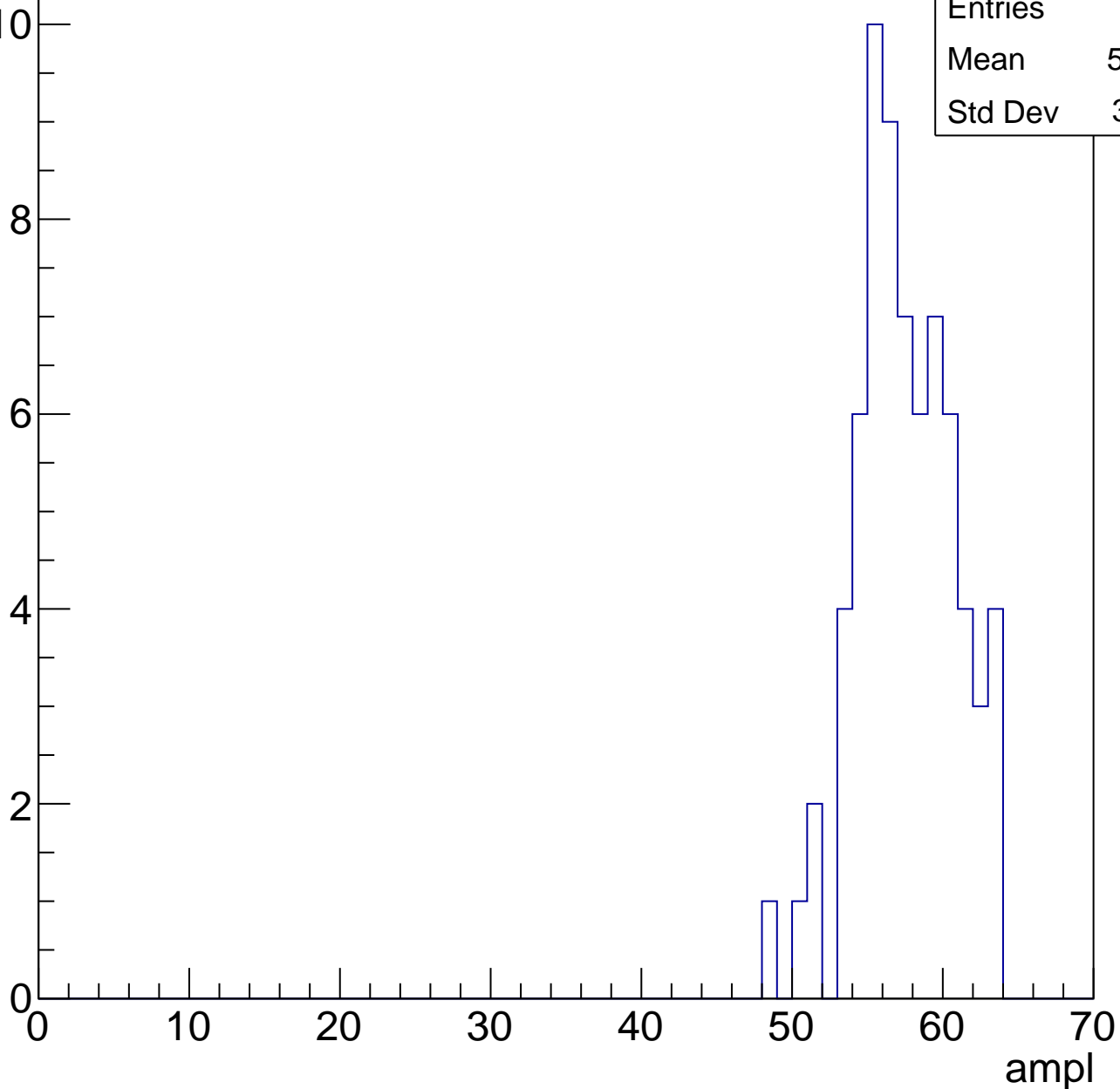


# B1L002S, U3-ch90, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	57.03
Std Dev	3.251

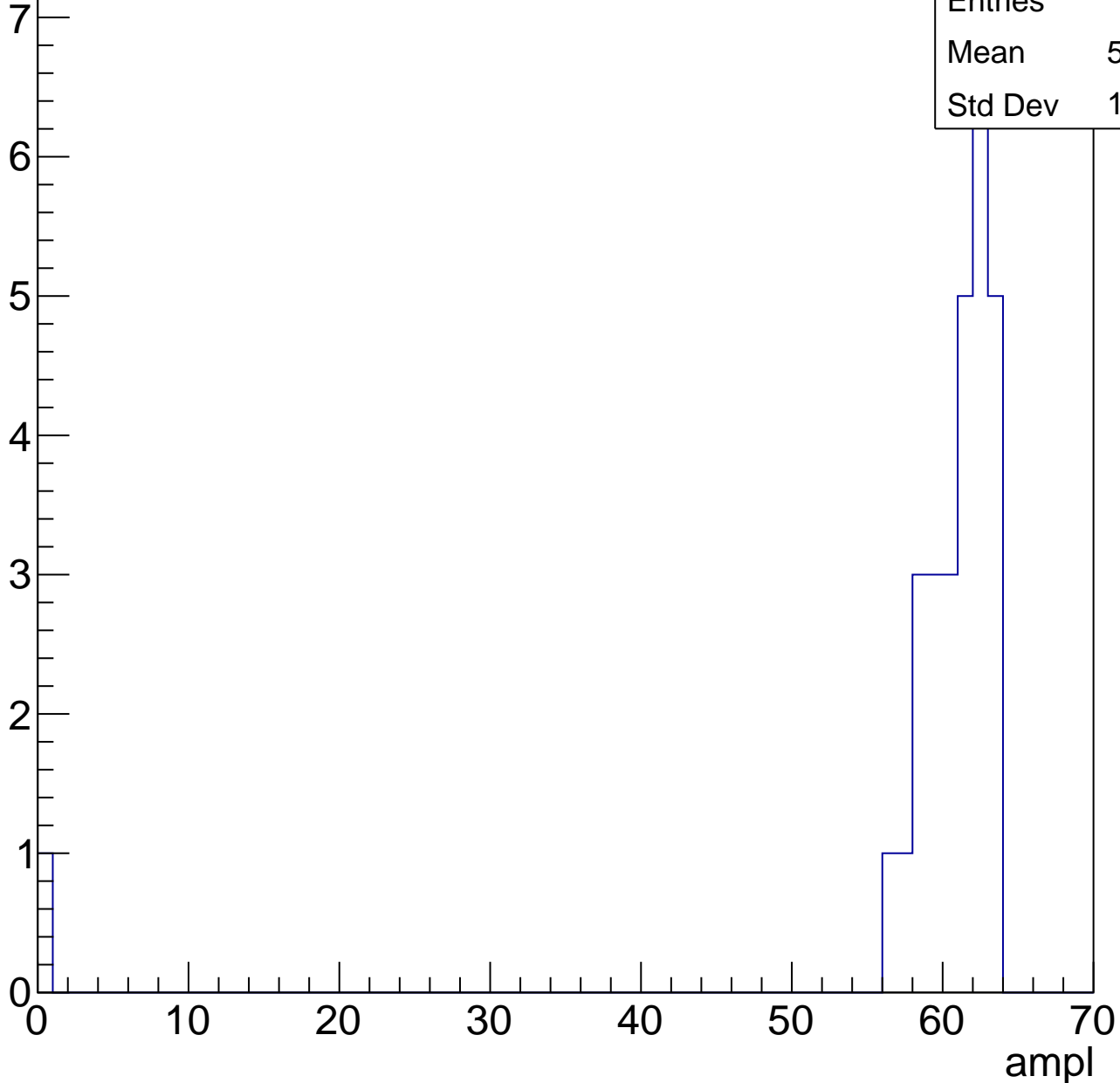


# B1L002S, U3-ch90, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	29
Mean	58.55
Std Dev	11.23

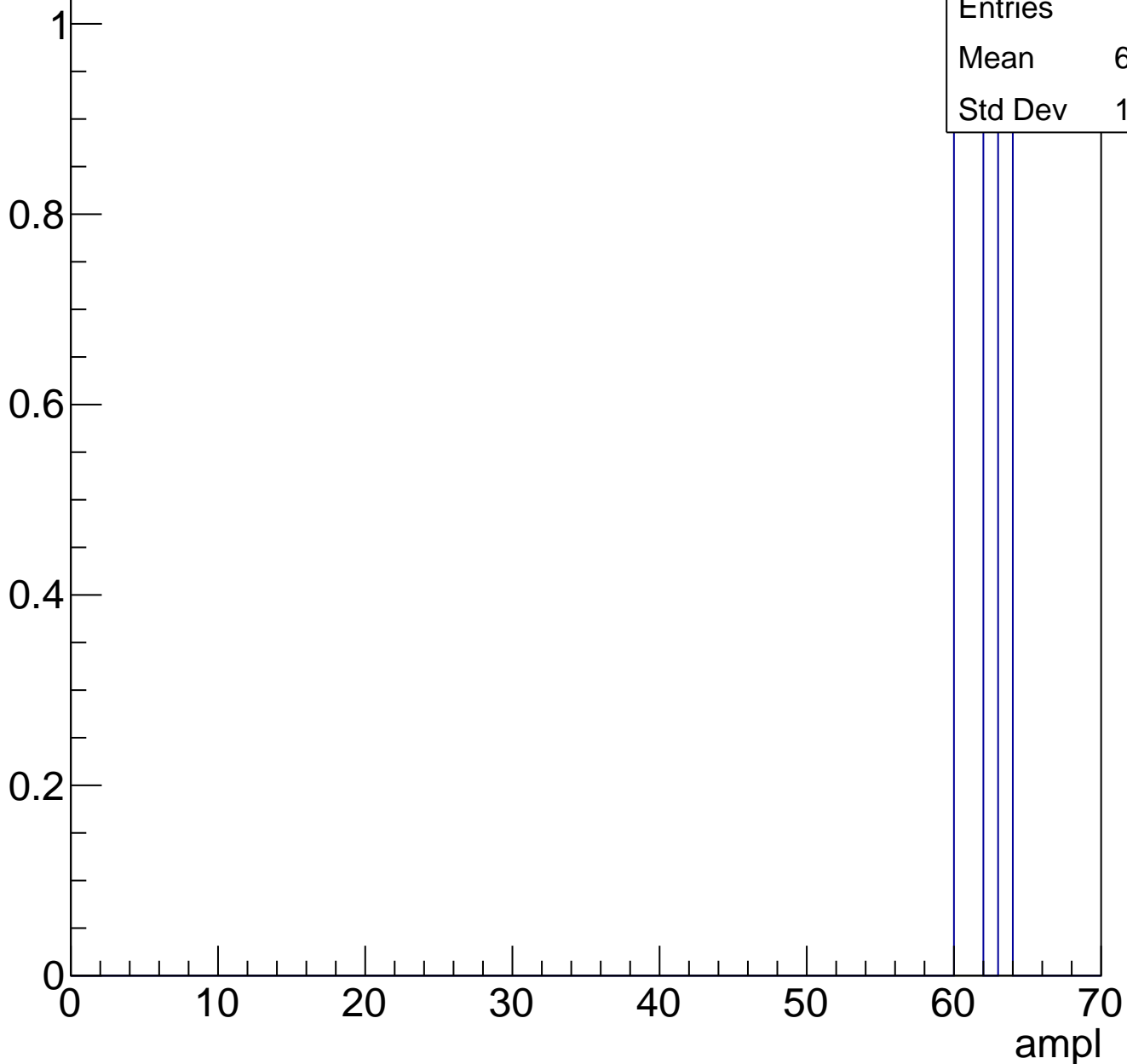




# B1L002S, U3-ch90, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch91, adc0

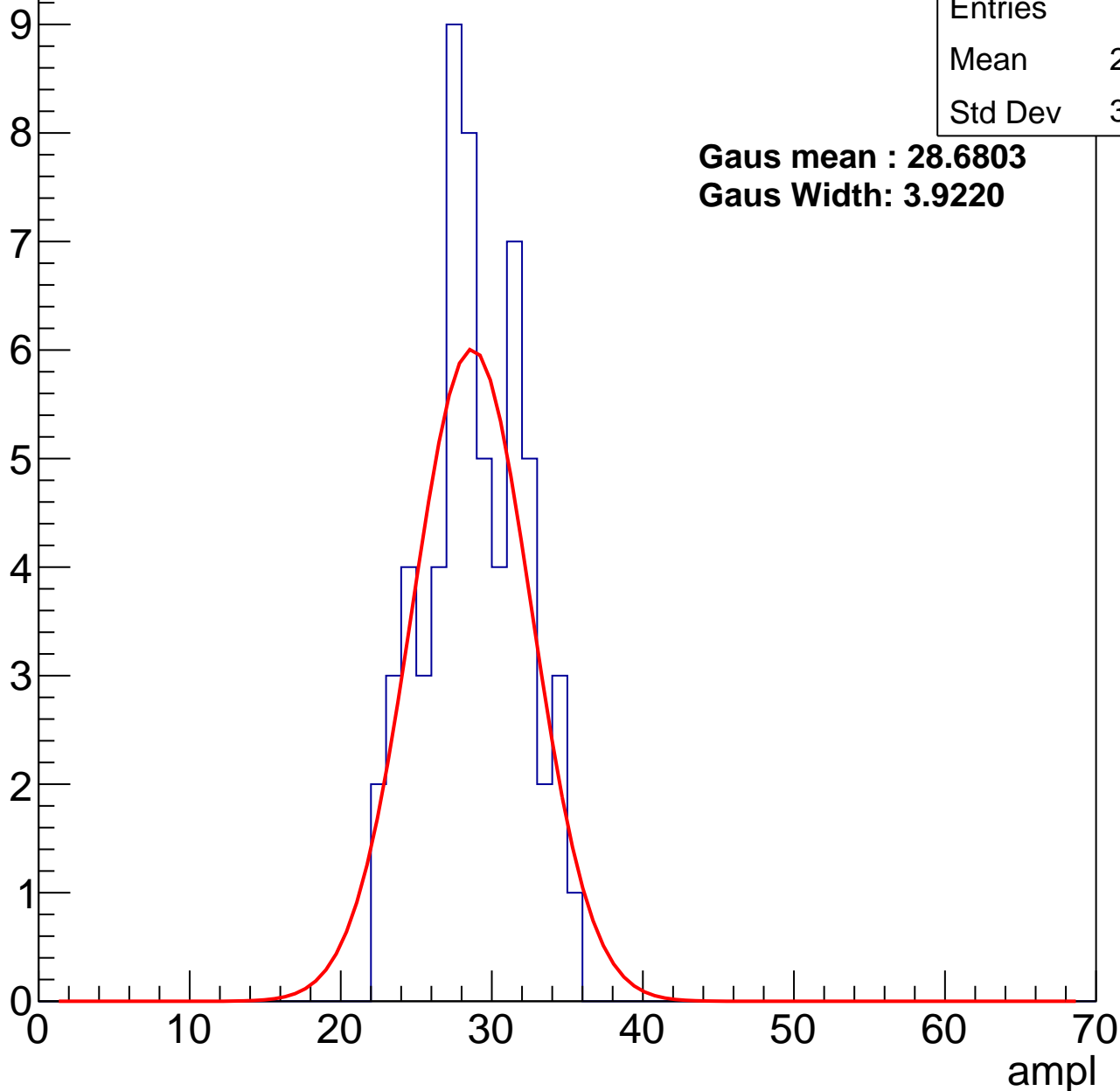
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	28.33
Std Dev	3.234

**Gaus mean : 28.6803**

**Gaus Width: 3.9220**



# B1L002S, U3-ch91, adc1

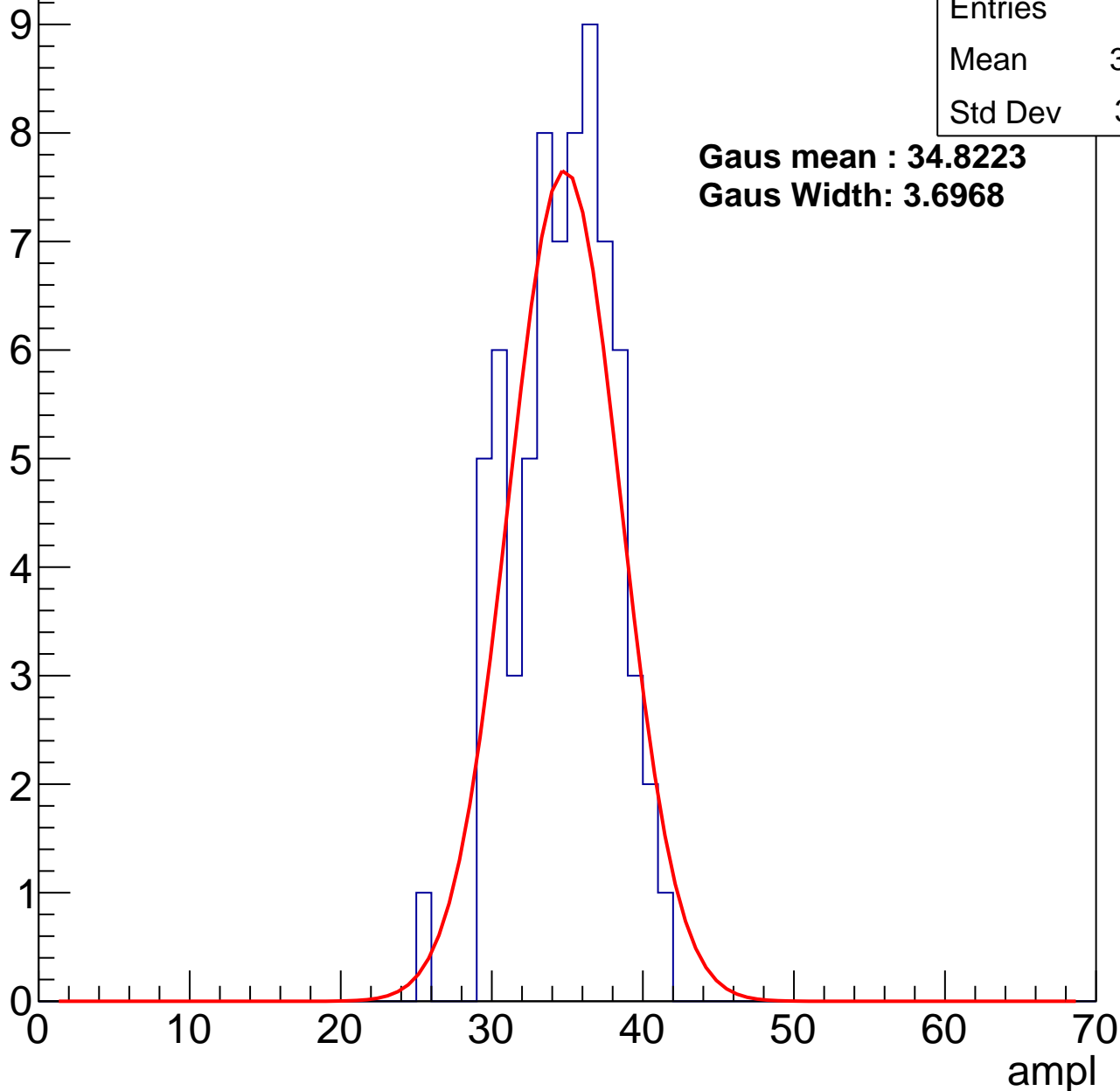
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	34.28
Std Dev	3.251

**Gaus mean : 34.8223**

**Gaus Width: 3.6968**



# B1L002S, U3-ch91, adc2

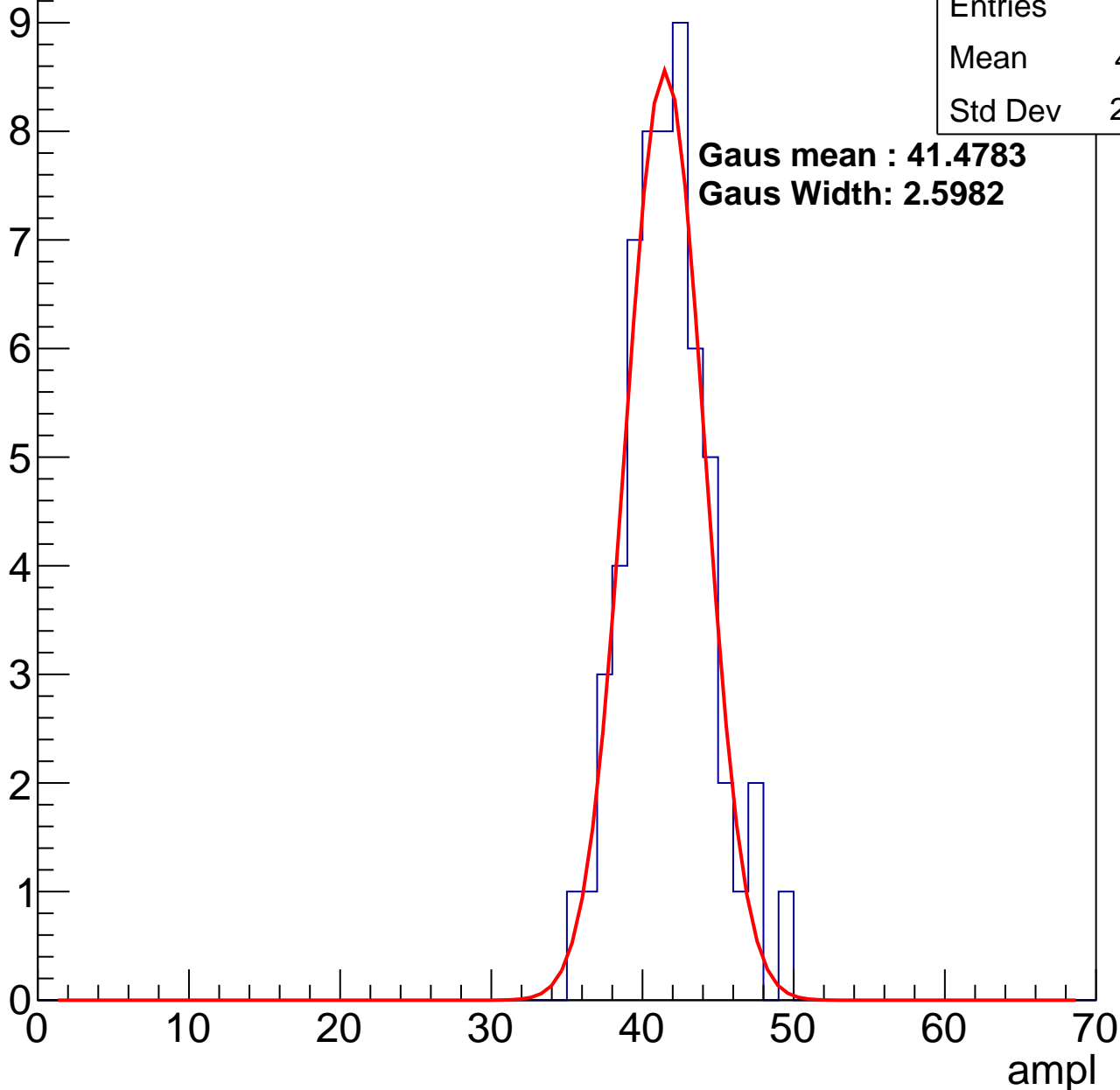
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.21
Std Dev	2.784

**Gaus mean : 41.4783**

**Gaus Width: 2.5982**

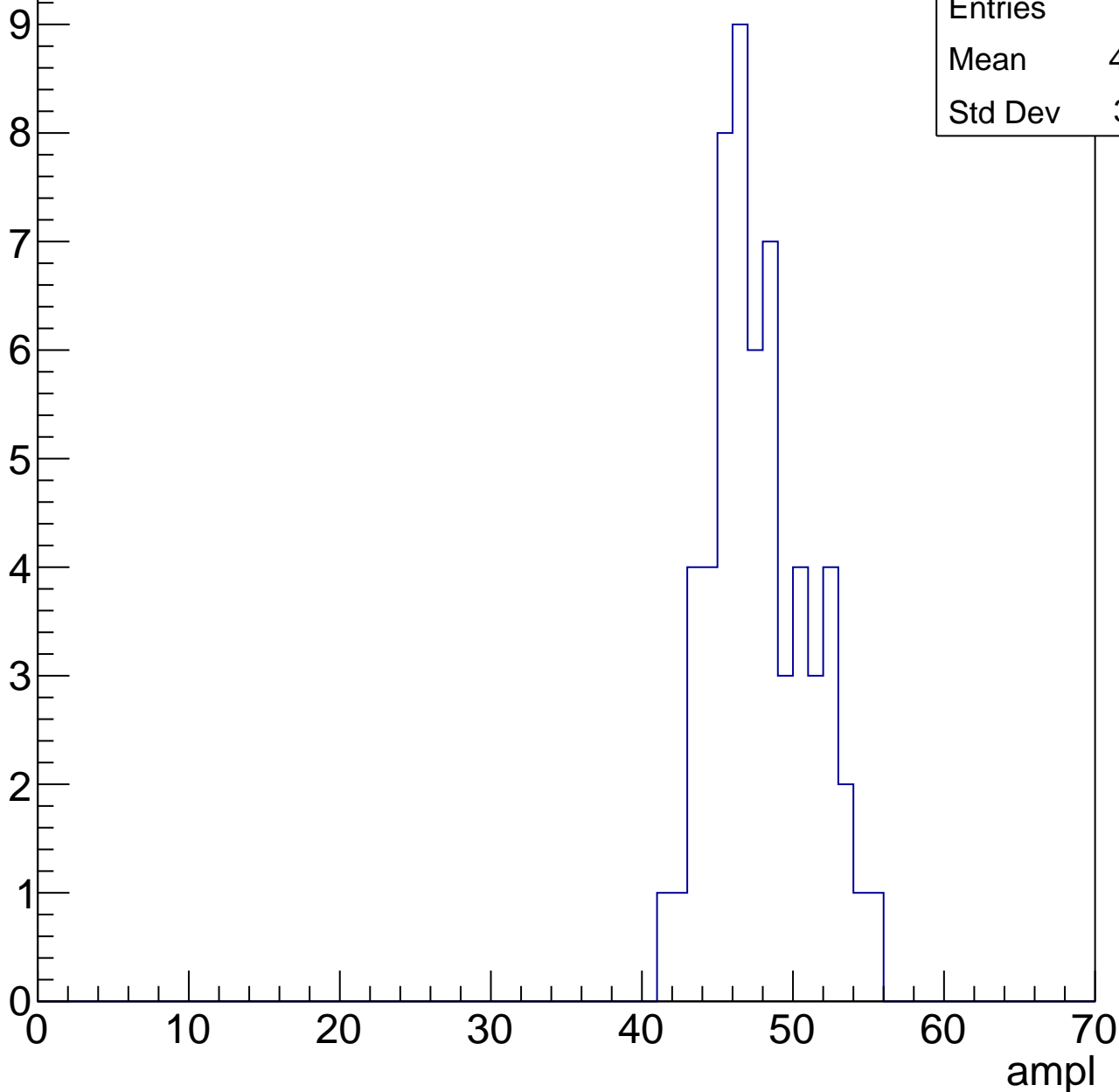


# B1L002S, U3-ch91, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

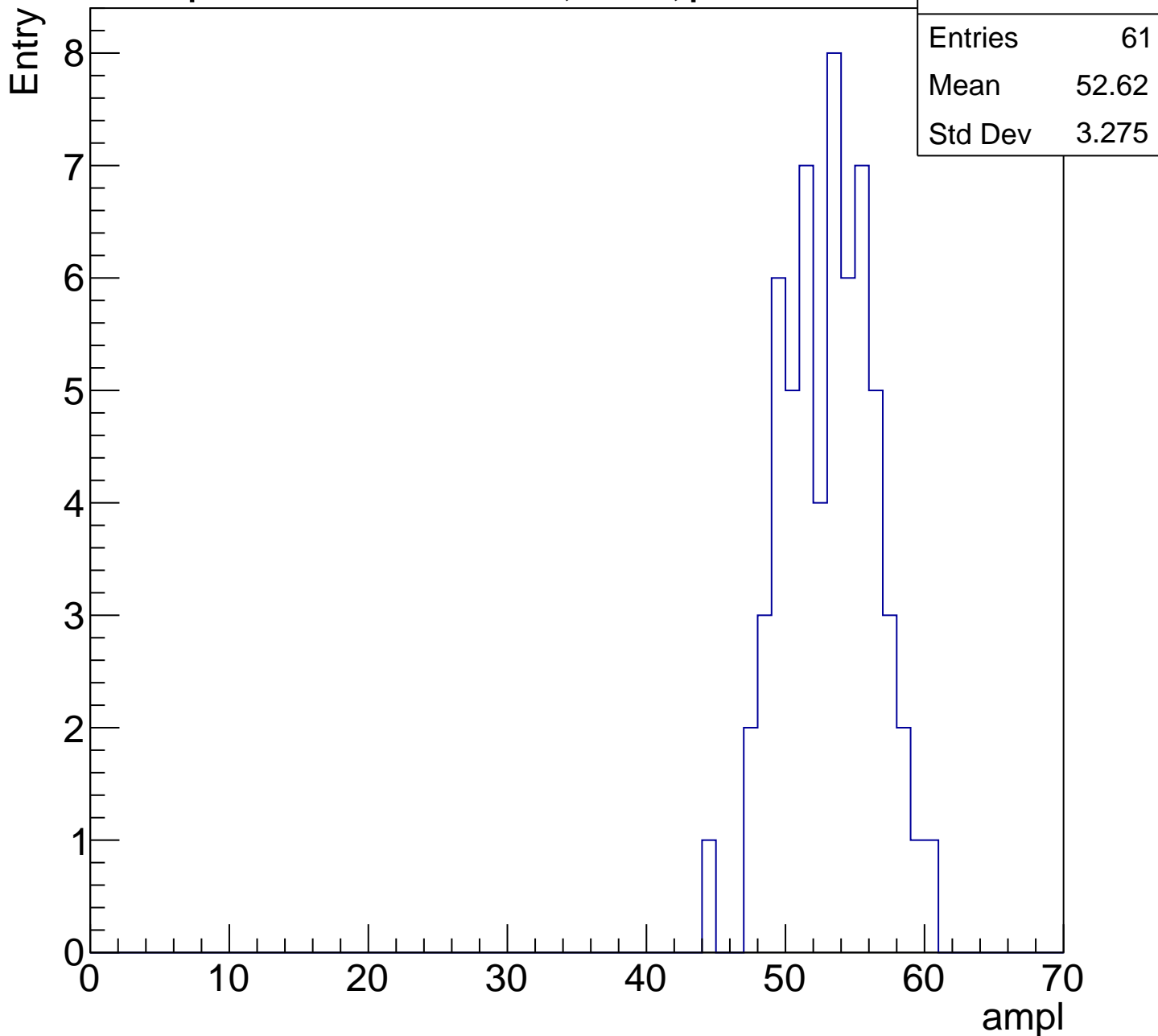
Entry

Entries	58
Mean	47.34
Std Dev	3.171



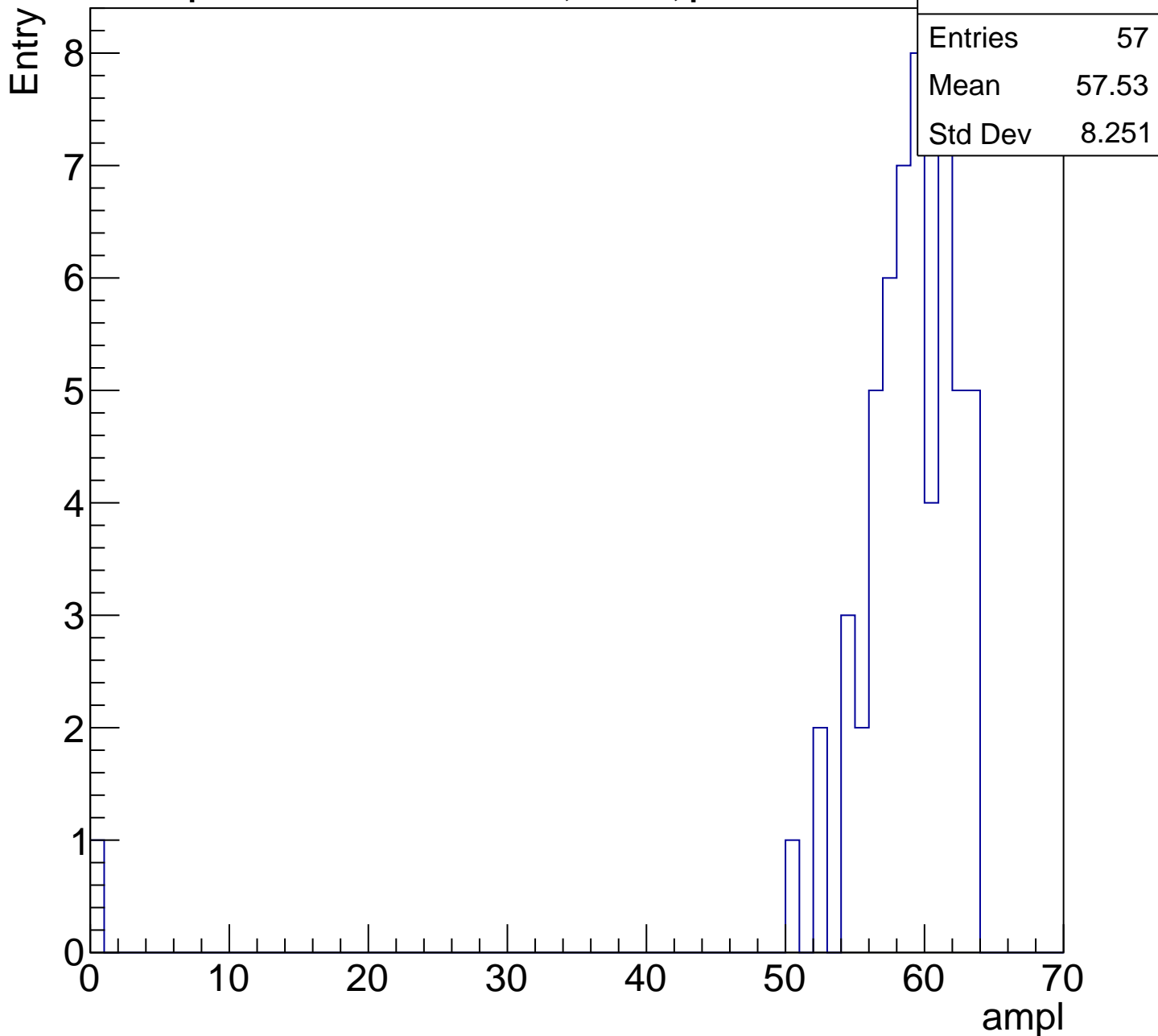
# B1L002S, U3-ch91, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch91, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch91, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries	20
Mean	61.05
Std Dev	1.717



# B1L002S, U3-ch91, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch92, adc0

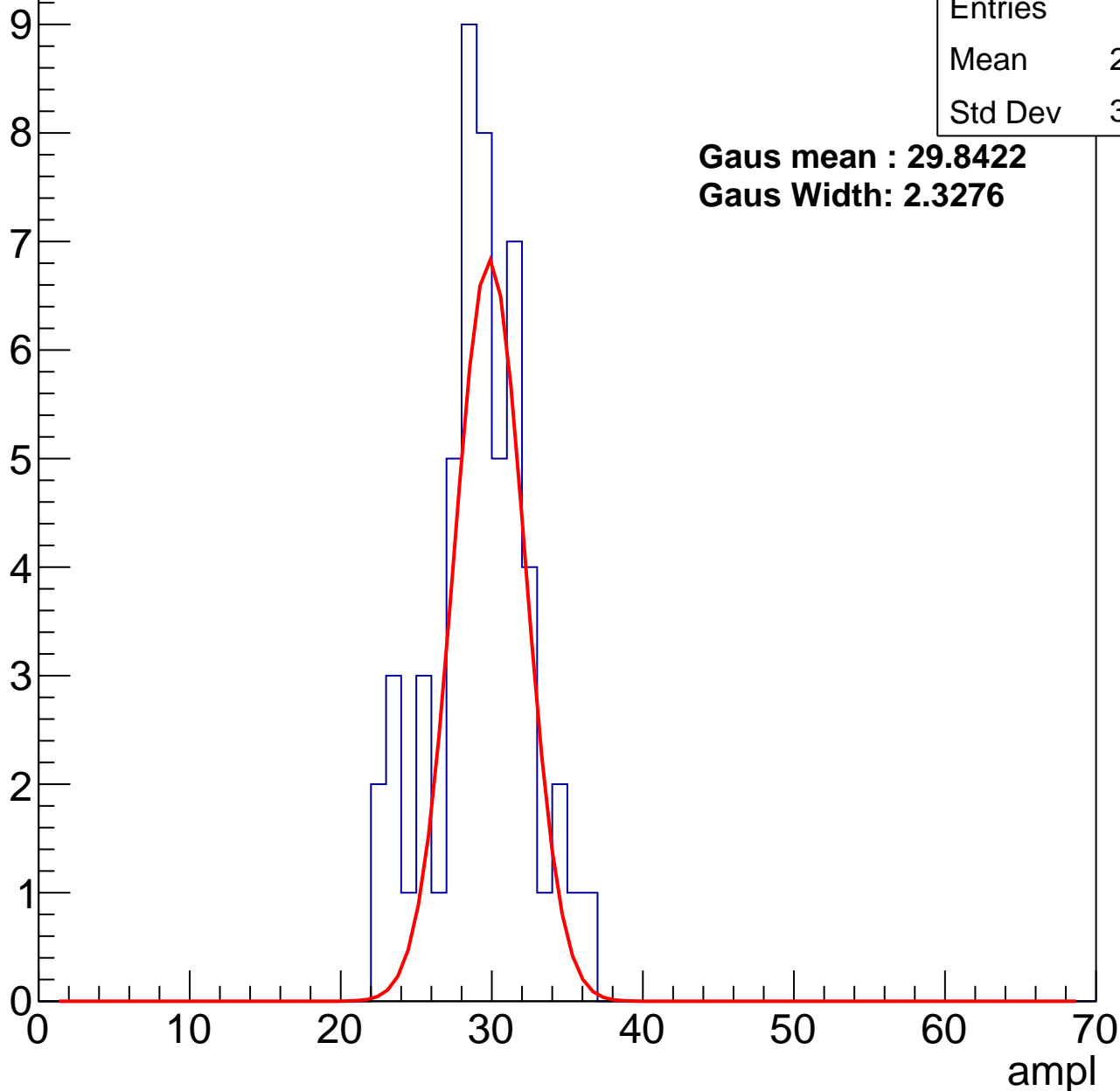
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	28.75
Std Dev	3.174

**Gaus mean : 29.8422**

**Gaus Width: 2.3276**



# B1L002S, U3-ch92, adc1

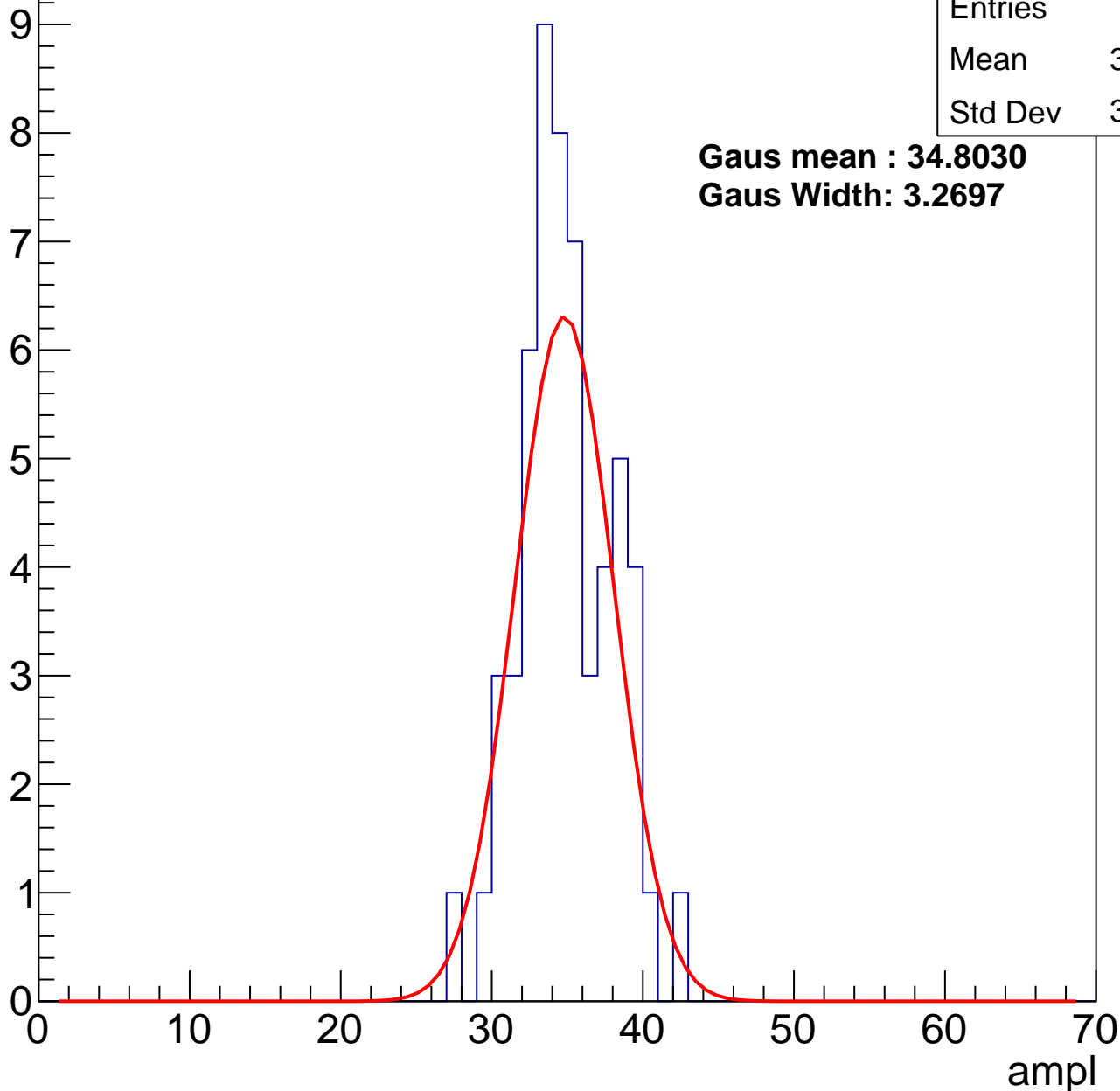
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	34.45
Std Dev	3.005

**Gaus mean : 34.8030**

**Gaus Width: 3.2697**



# B1L002S, U3-ch92, adc2

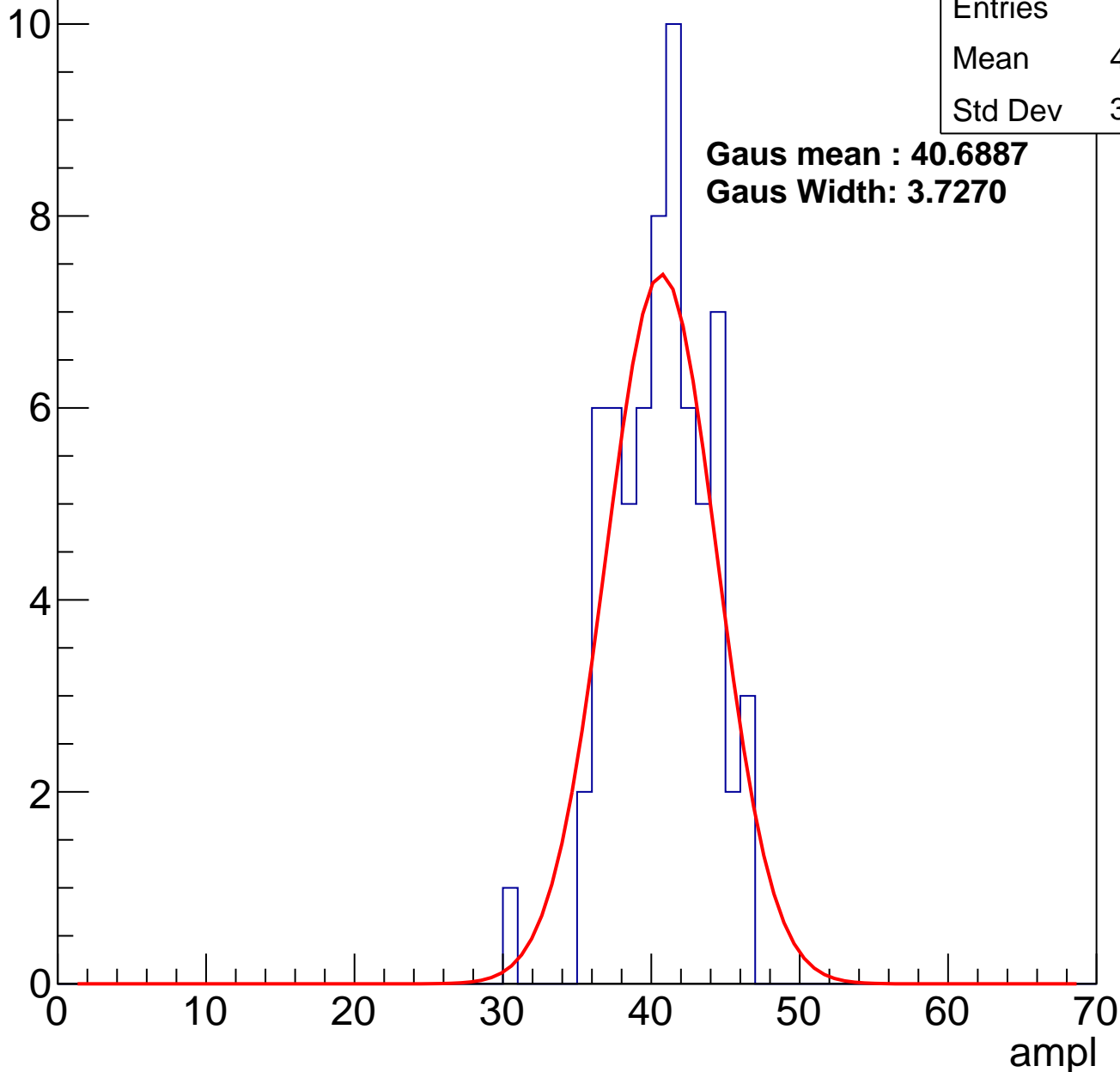
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	40.22
Std Dev	3.166

**Gaus mean : 40.6887**

**Gaus Width: 3.7270**

Entry

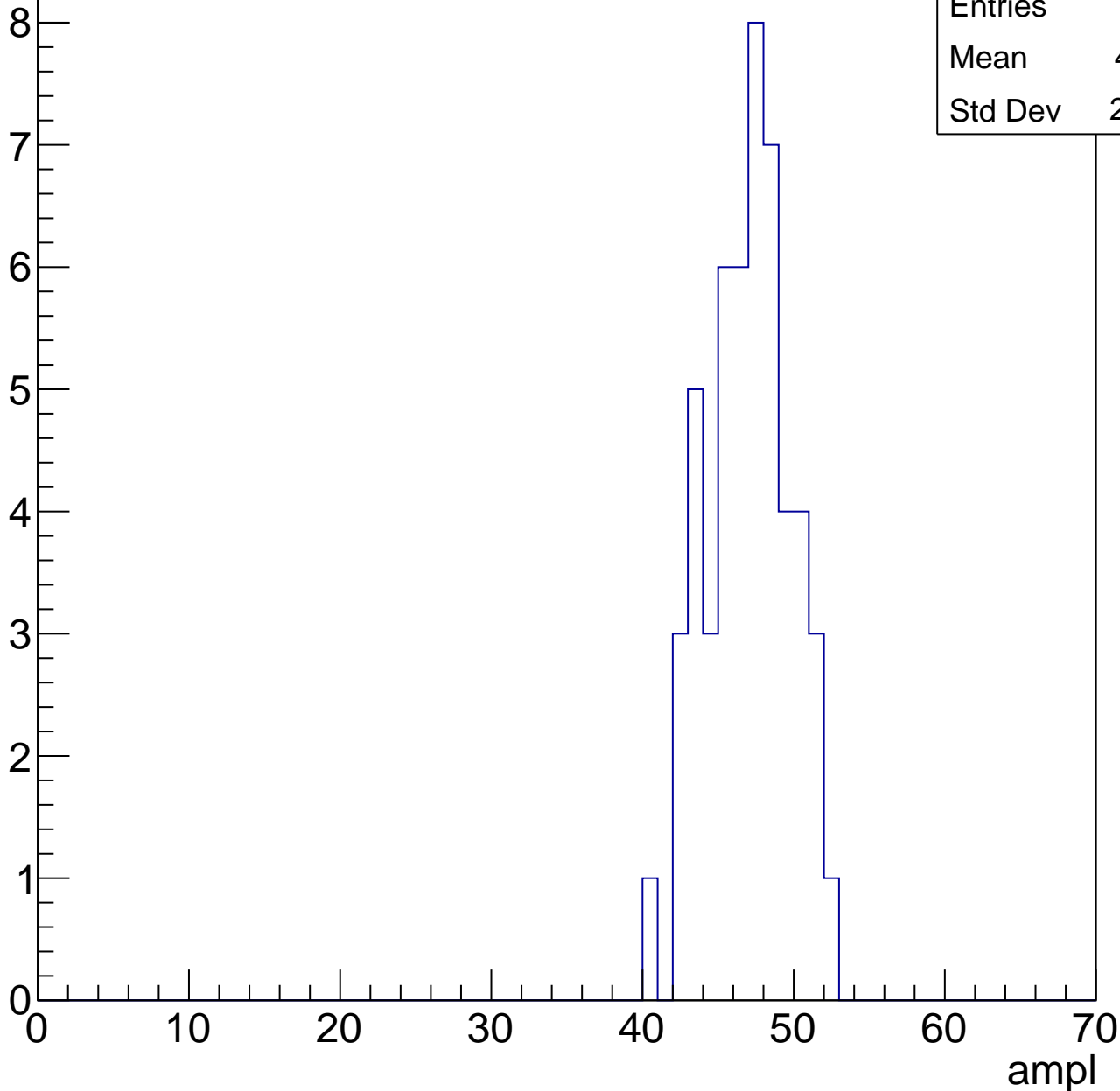


# B1L002S, U3-ch92, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

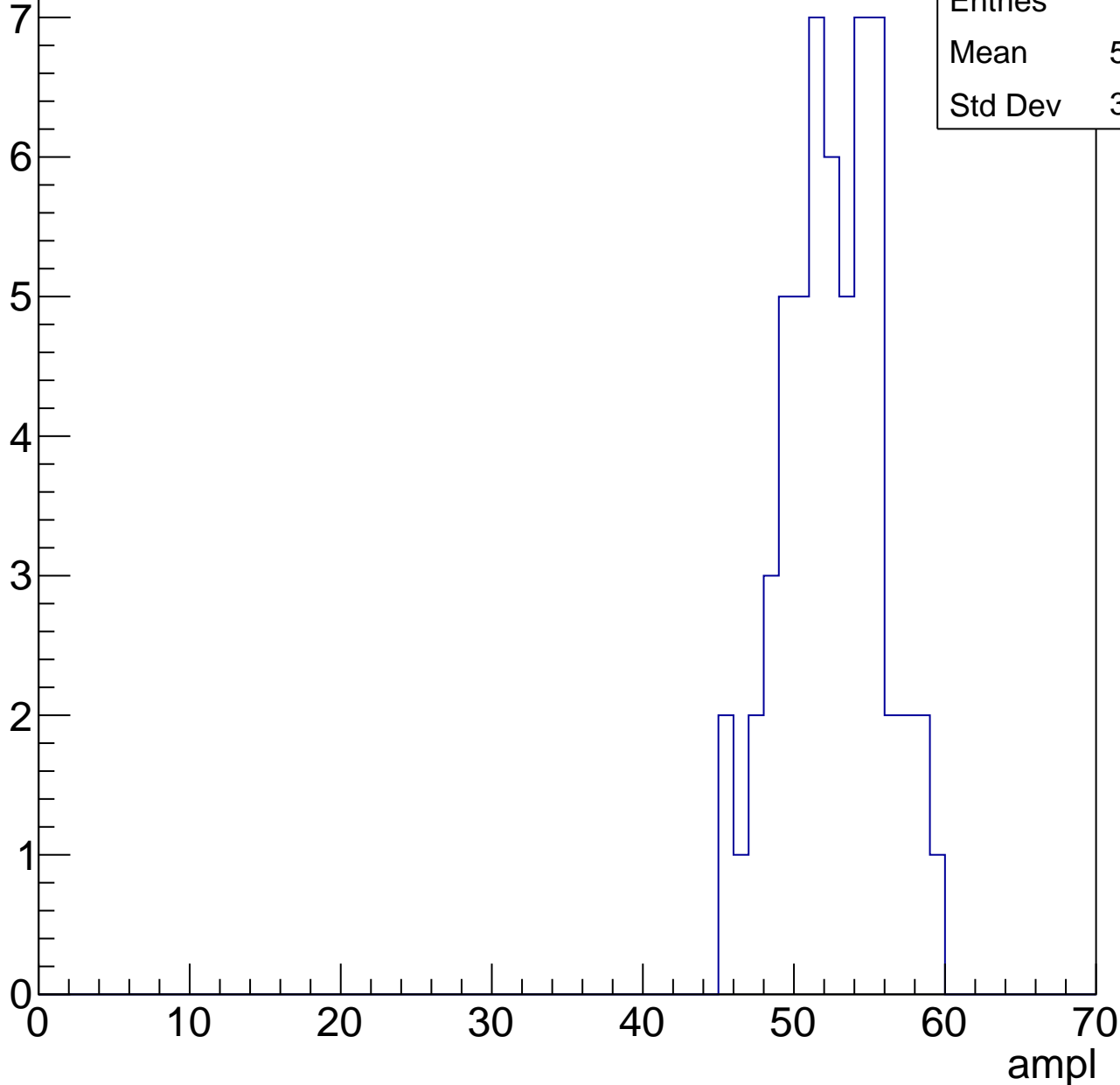
Entries	51
Mean	46.51
Std Dev	2.732



# B1L002S, U3-ch92, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

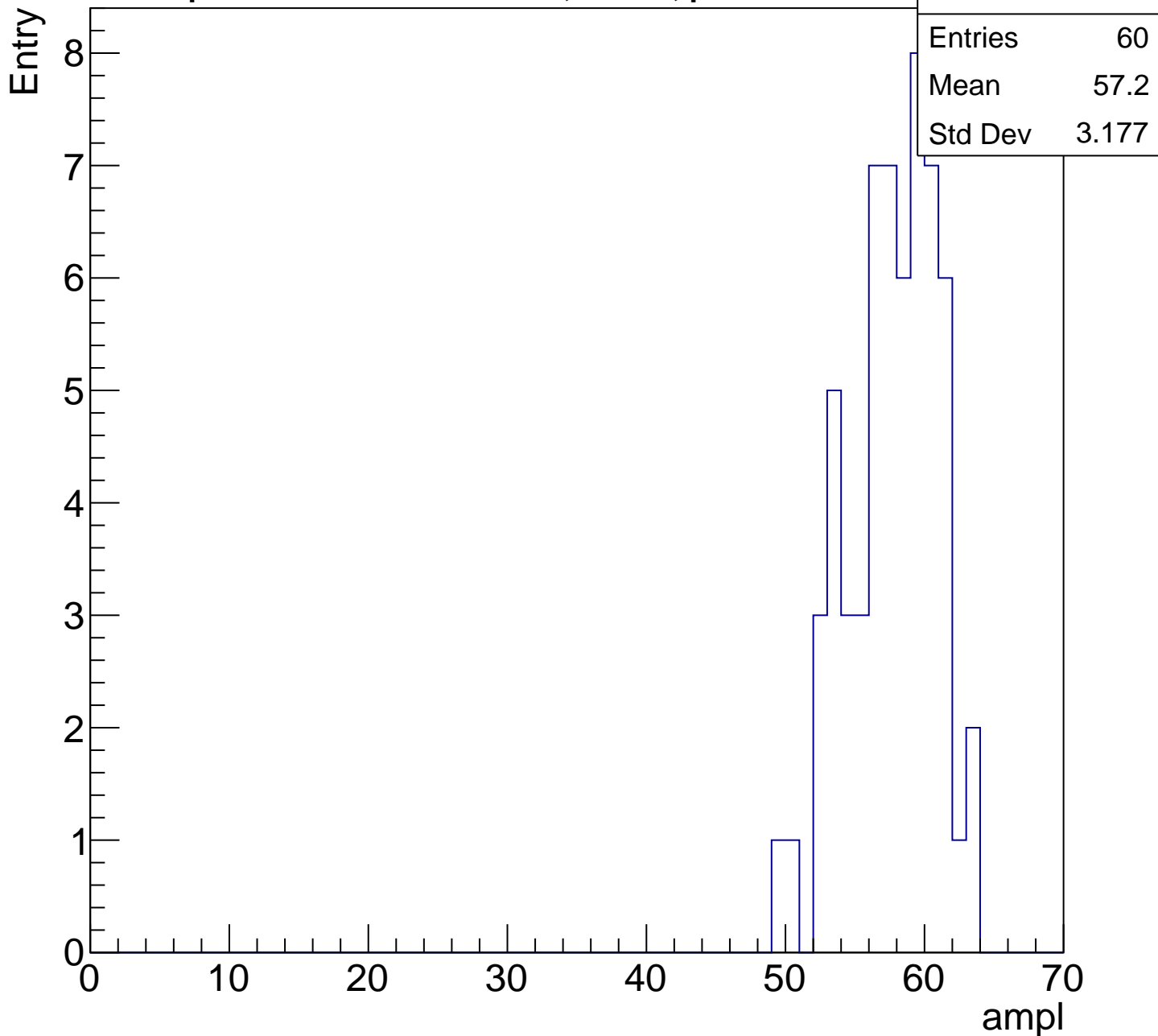
Entry



Entries	57
Mean	52.05
Std Dev	3.252

# B1L002S, U3-ch92, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

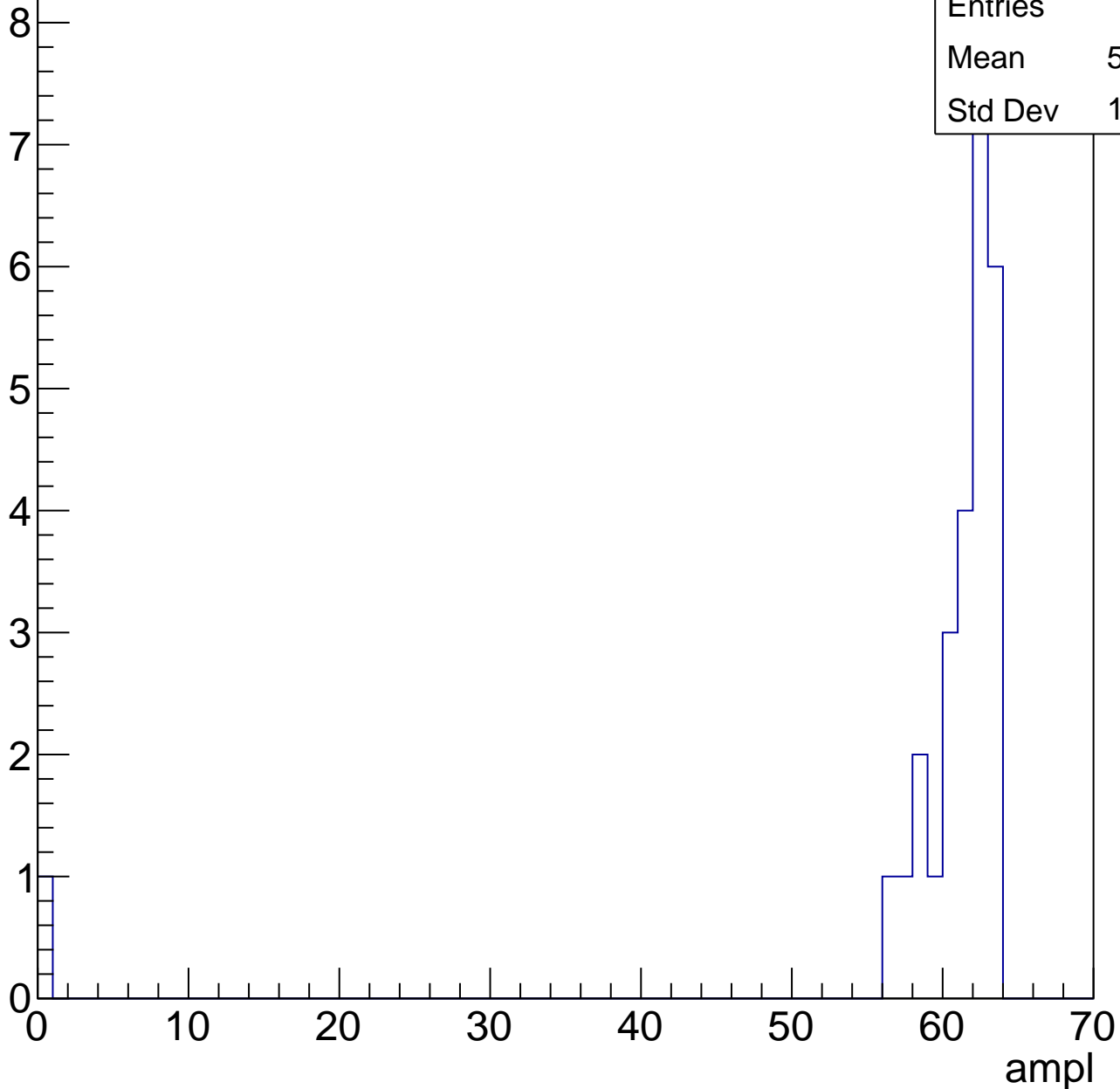


# B1L002S, U3-ch92, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.74
Std Dev	11.68





# B1L002S, U3-ch92, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch93, adc0

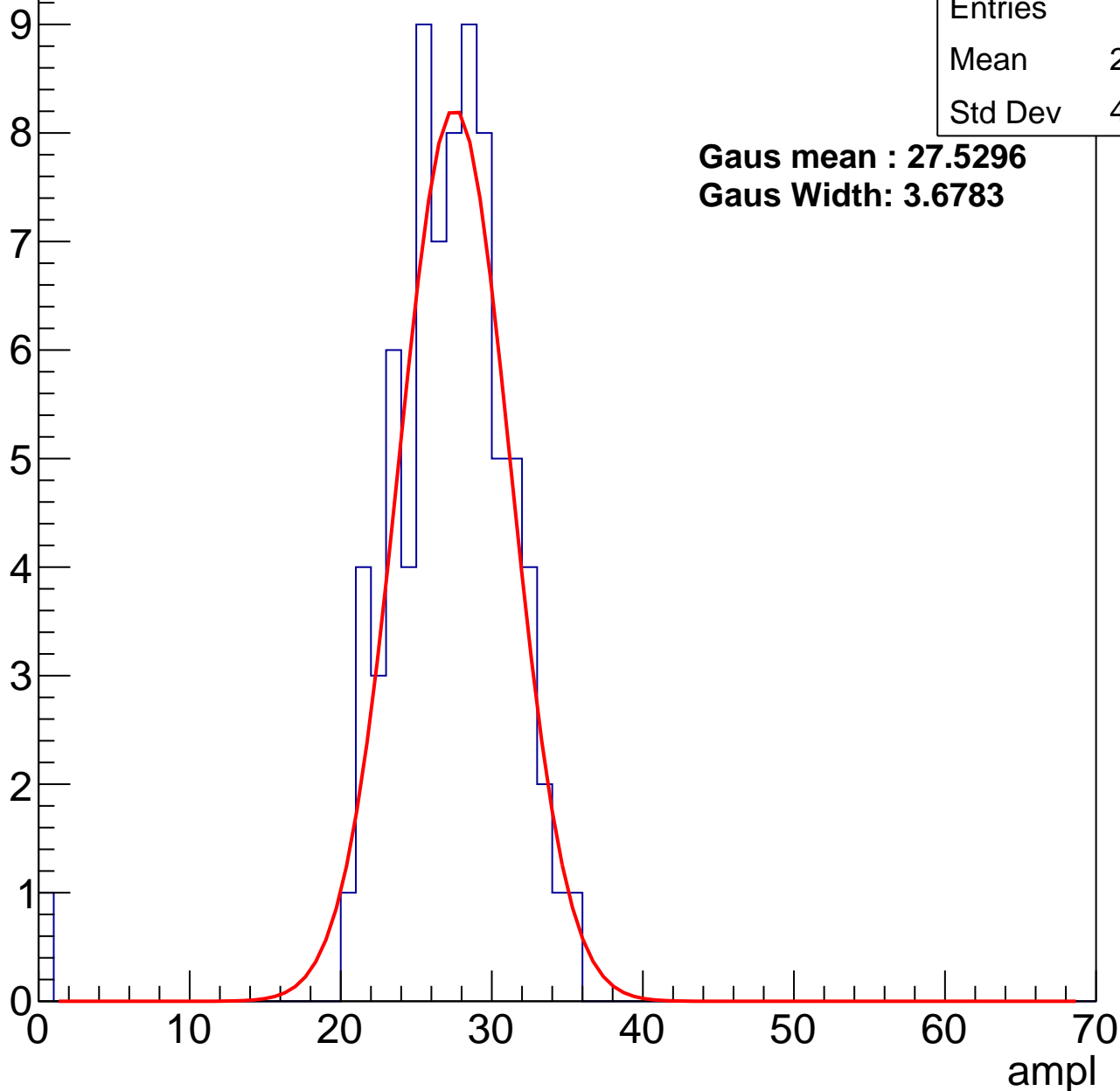
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	78
Mean	26.65
Std Dev	4.548

**Gaus mean : 27.5296**

**Gaus Width: 3.6783**



# B1L002S, U3-ch93, adc1

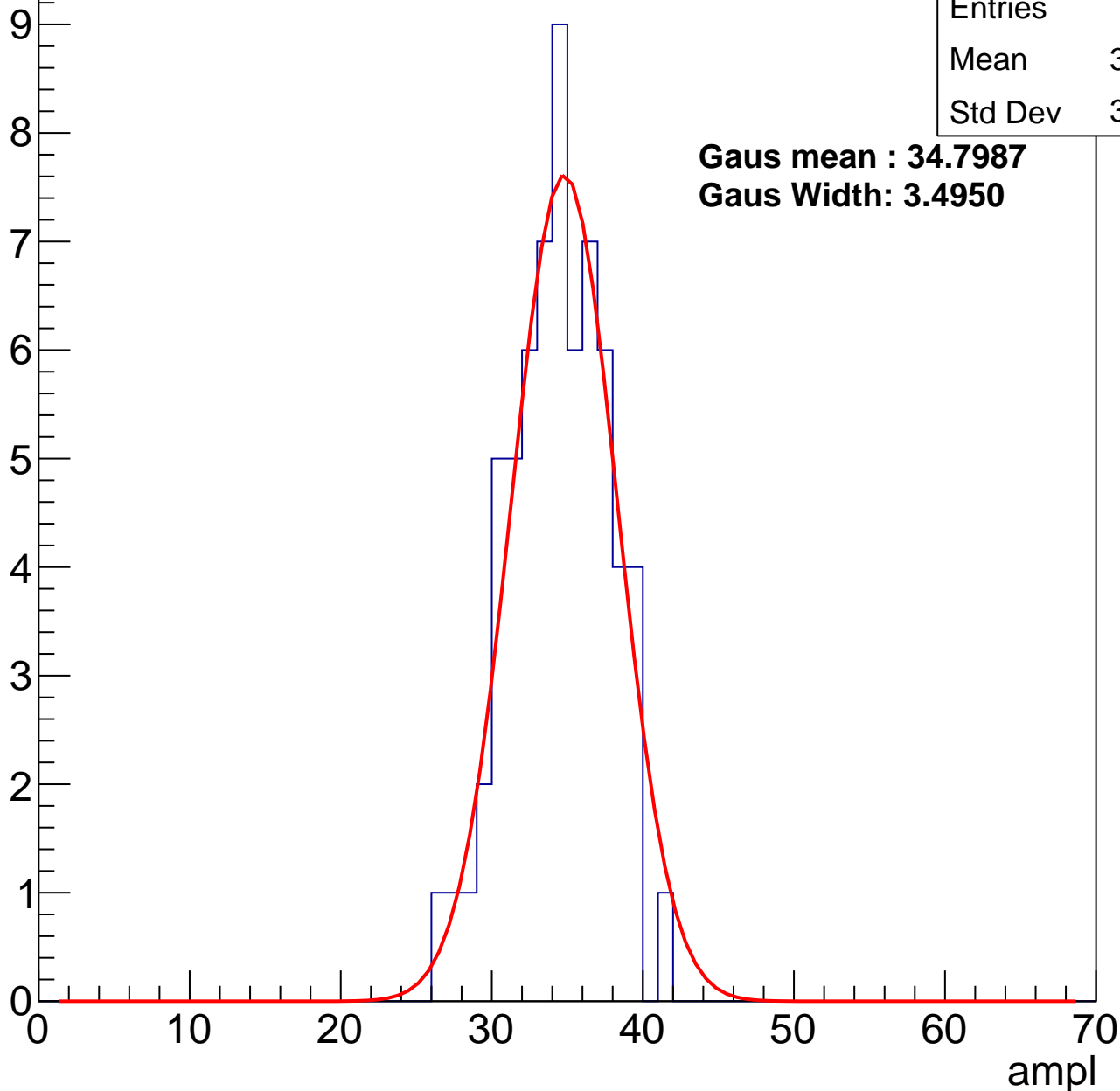
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	33.94
Std Dev	3.176

**Gaus mean : 34.7987**

**Gaus Width: 3.4950**



# B1L002S, U3-ch93, adc2

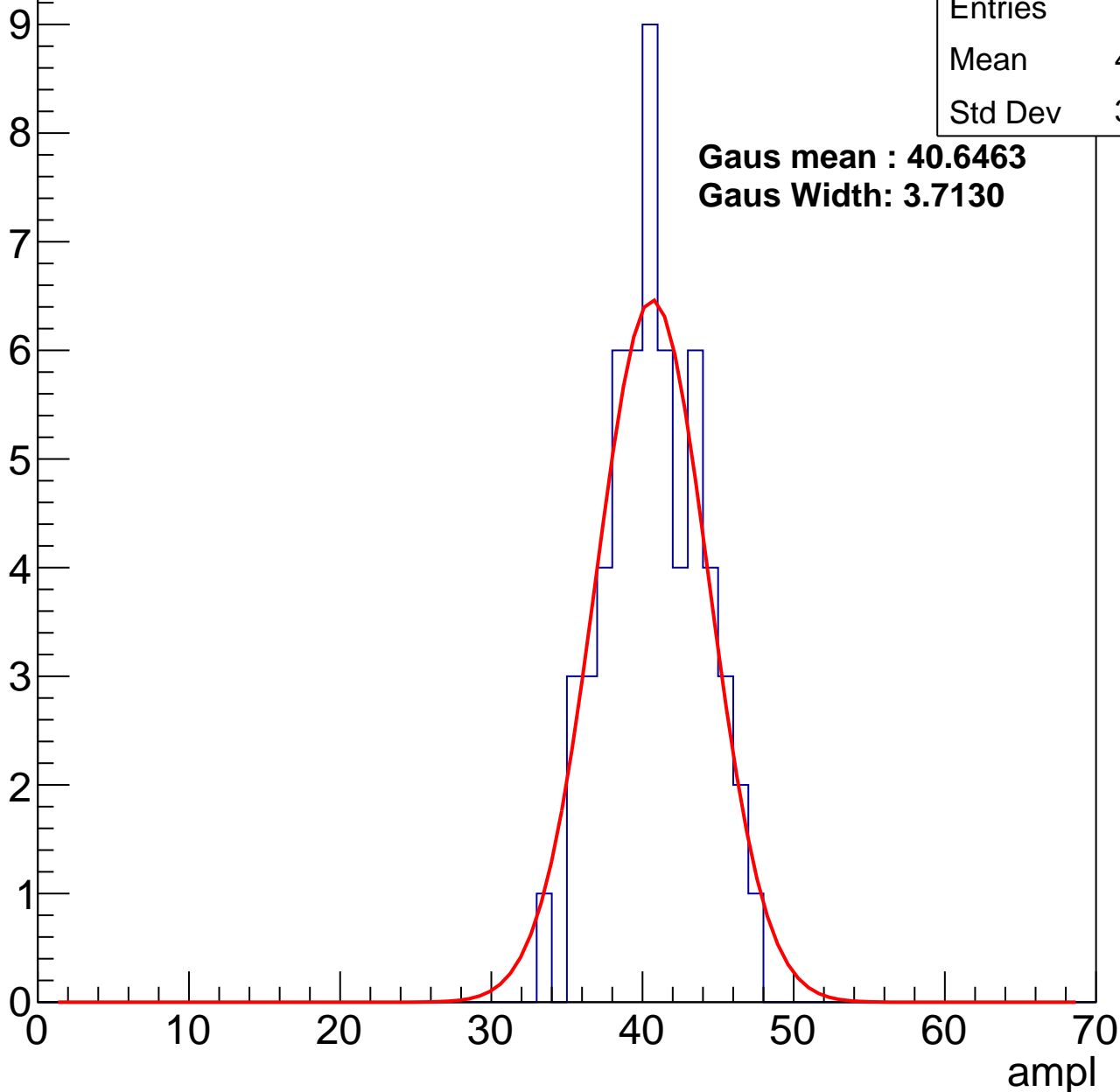
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.31
Std Dev	3.131

**Gaus mean : 40.6463**

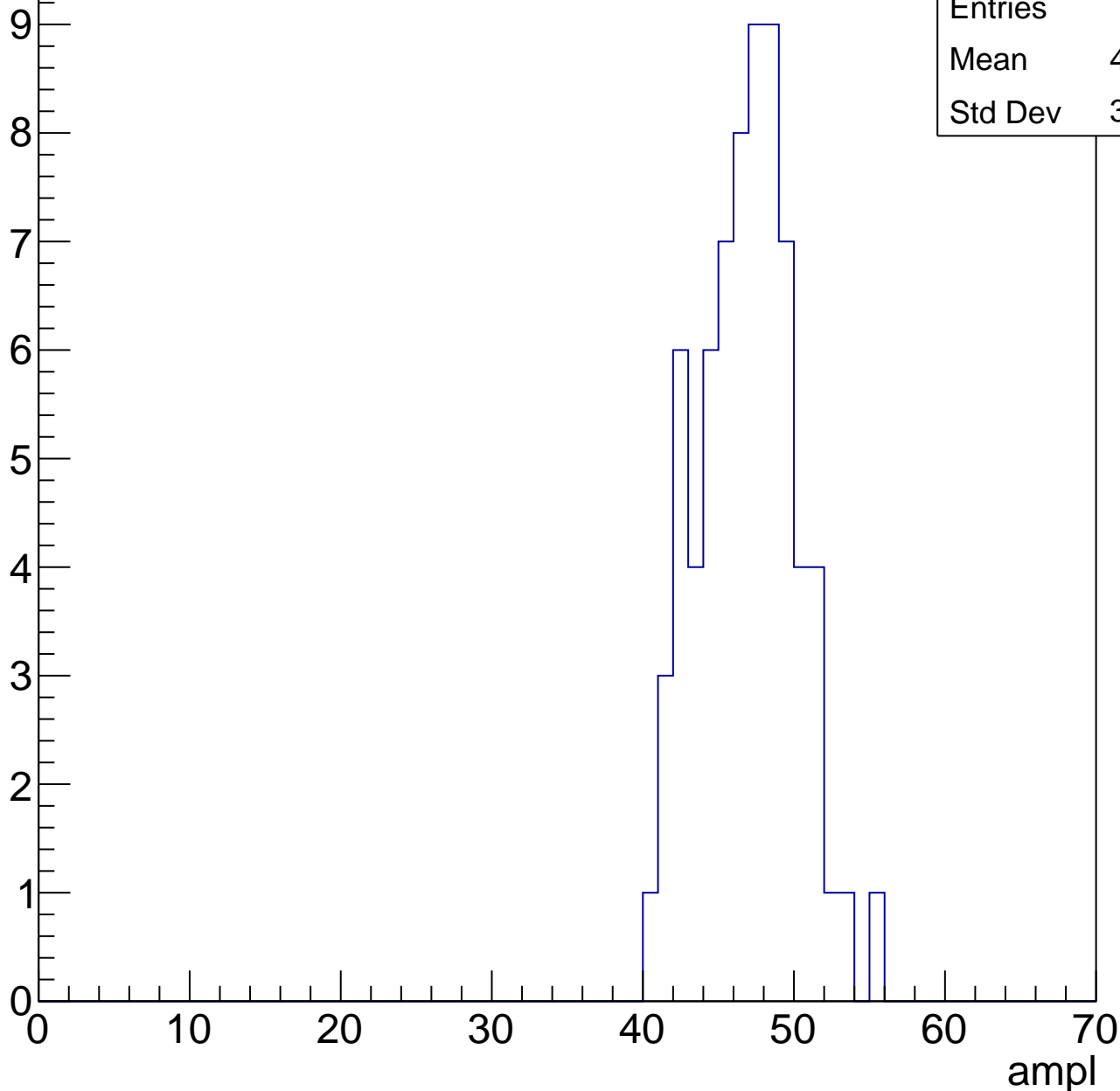
**Gaus Width: 3.7130**



# B1L002S, U3-ch93, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



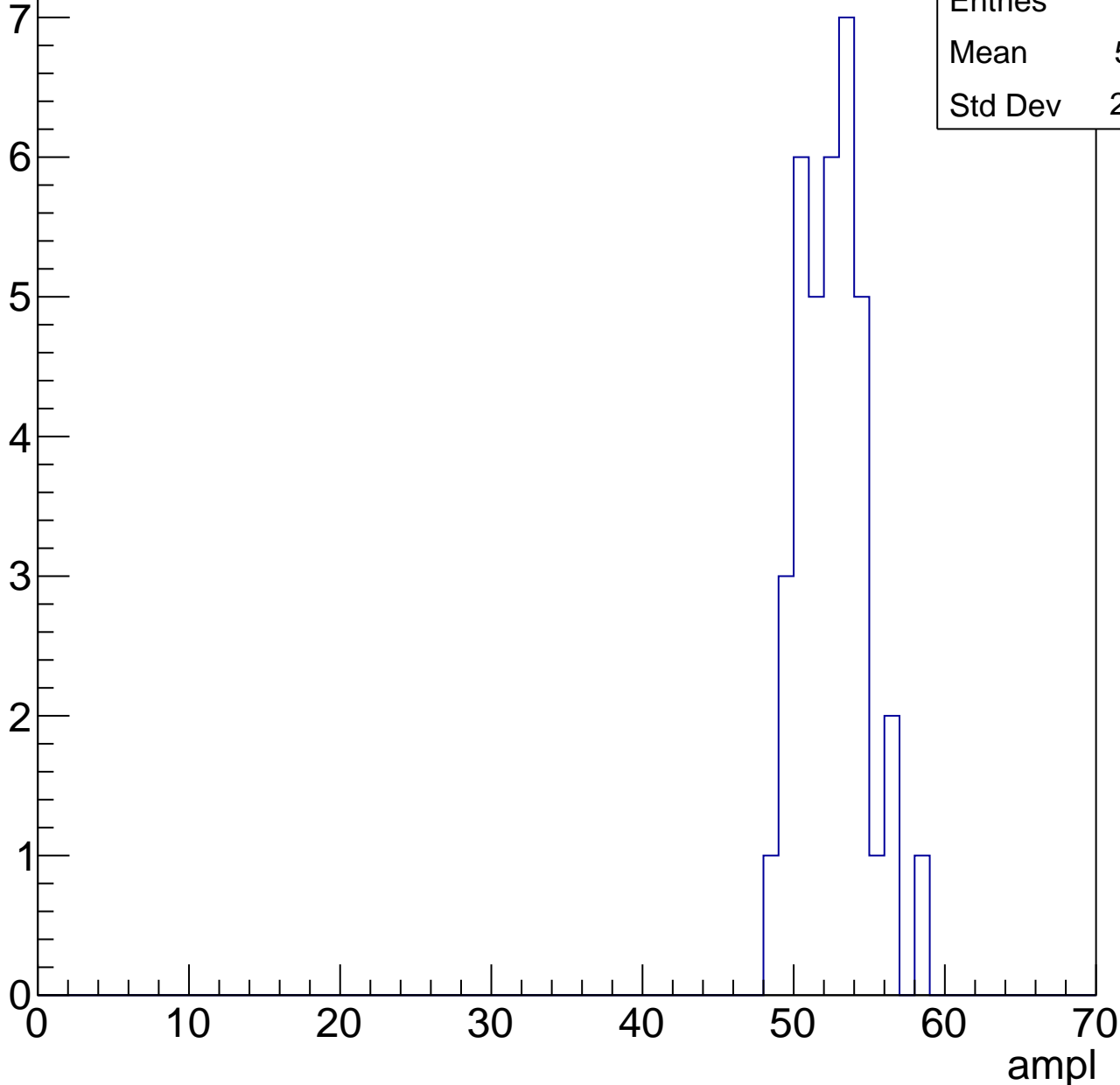
Entries	71
Mean	46.42
Std Dev	3.138

# B1L002S, U3-ch93, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

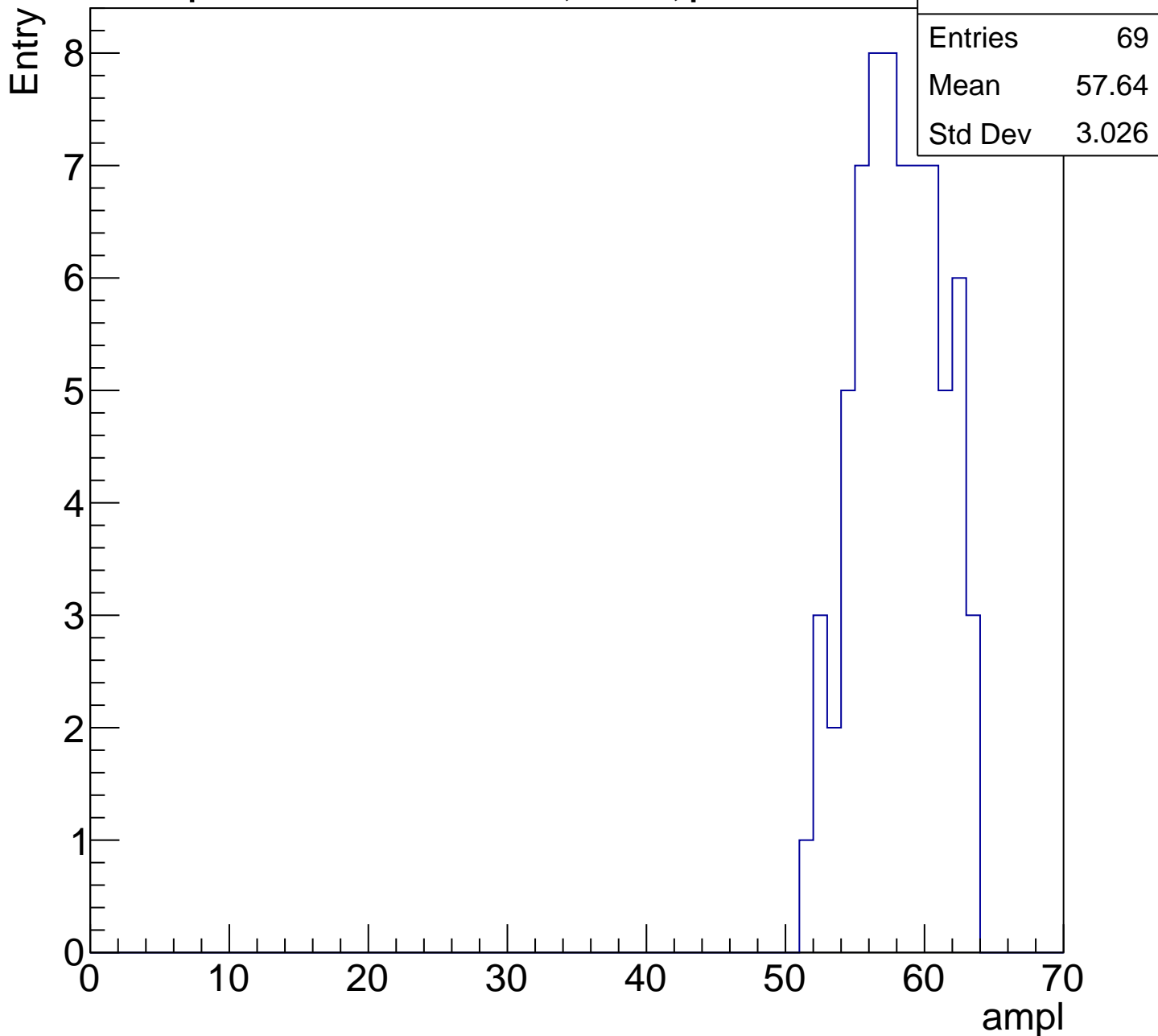
Entry

Entries	37
Mean	52.11
Std Dev	2.178



# B1L002S, U3-ch93, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

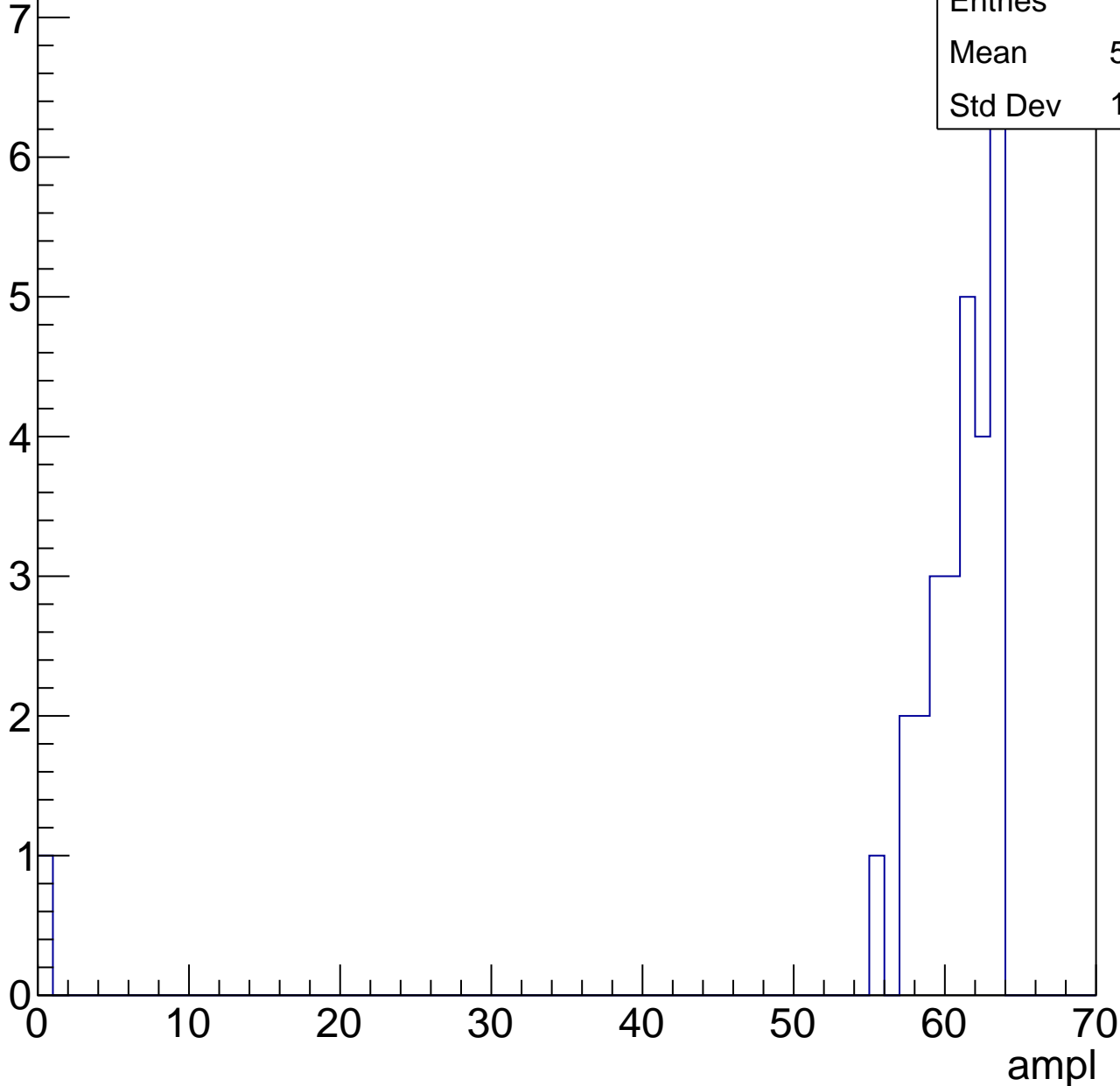


# B1L002S, U3-ch93, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.43
Std Dev	11.45





# B1L002S, U3-ch93, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch94, adc0

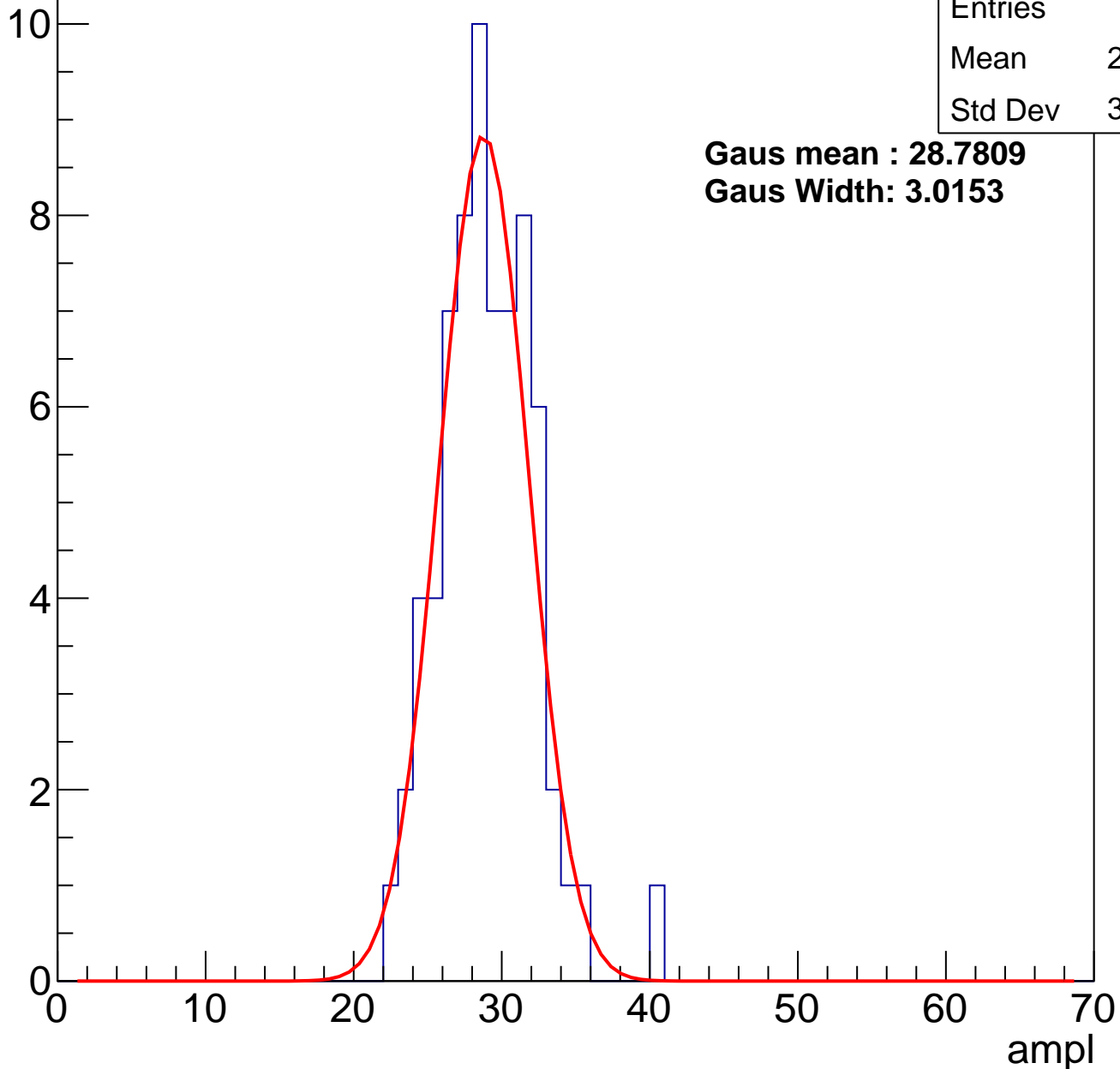
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	28.55
Std Dev	3.146

**Gaus mean : 28.7809**

**Gaus Width: 3.0153**

Entry



# B1L002S, U3-ch94, adc1

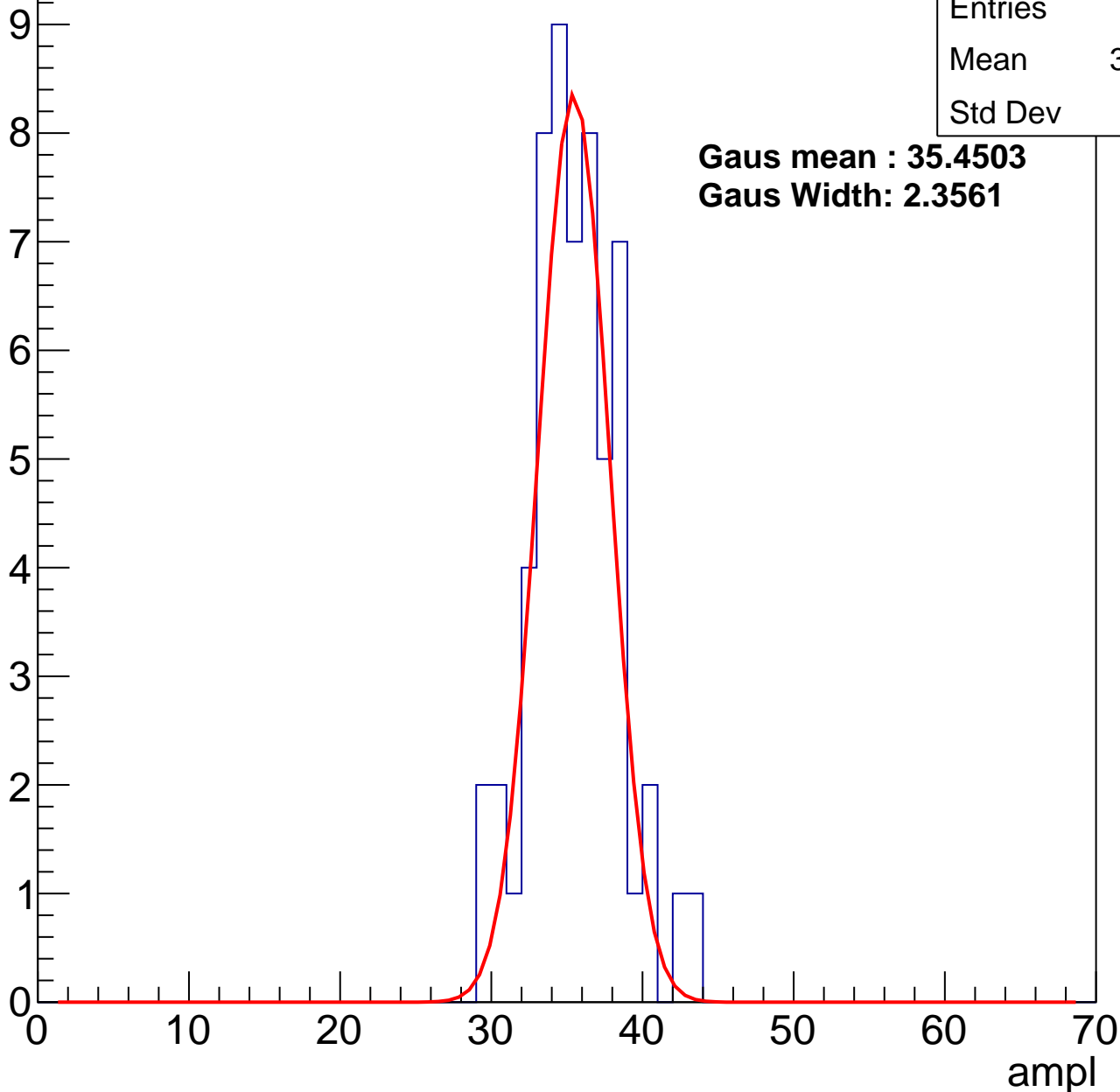
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	35.09
Std Dev	2.89

**Gaus mean : 35.4503**

**Gaus Width: 2.3561**



# B1L002S, U3-ch94, adc2

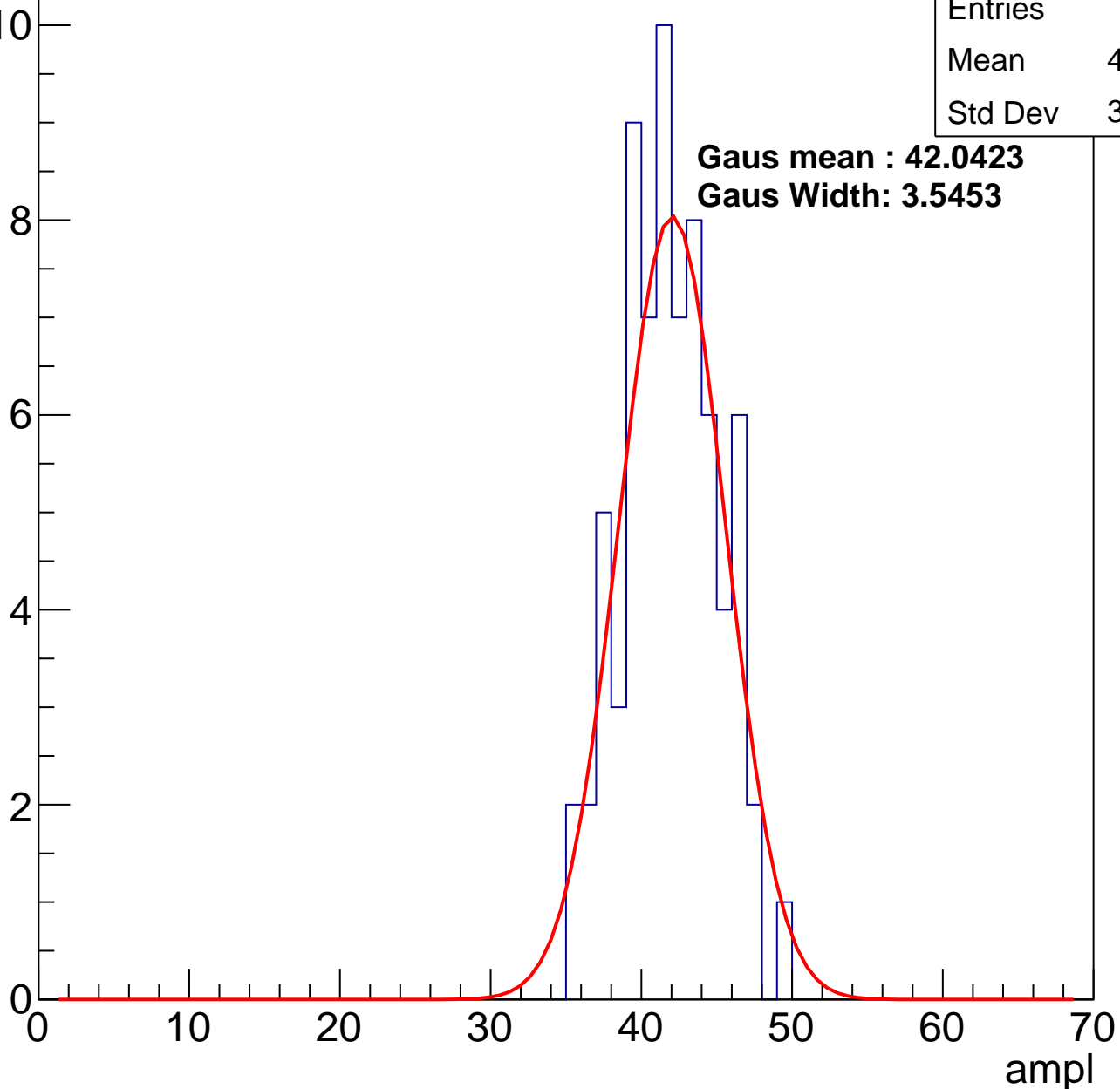
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	41.43
Std Dev	3.122

**Gaus mean : 42.0423**

**Gaus Width: 3.5453**

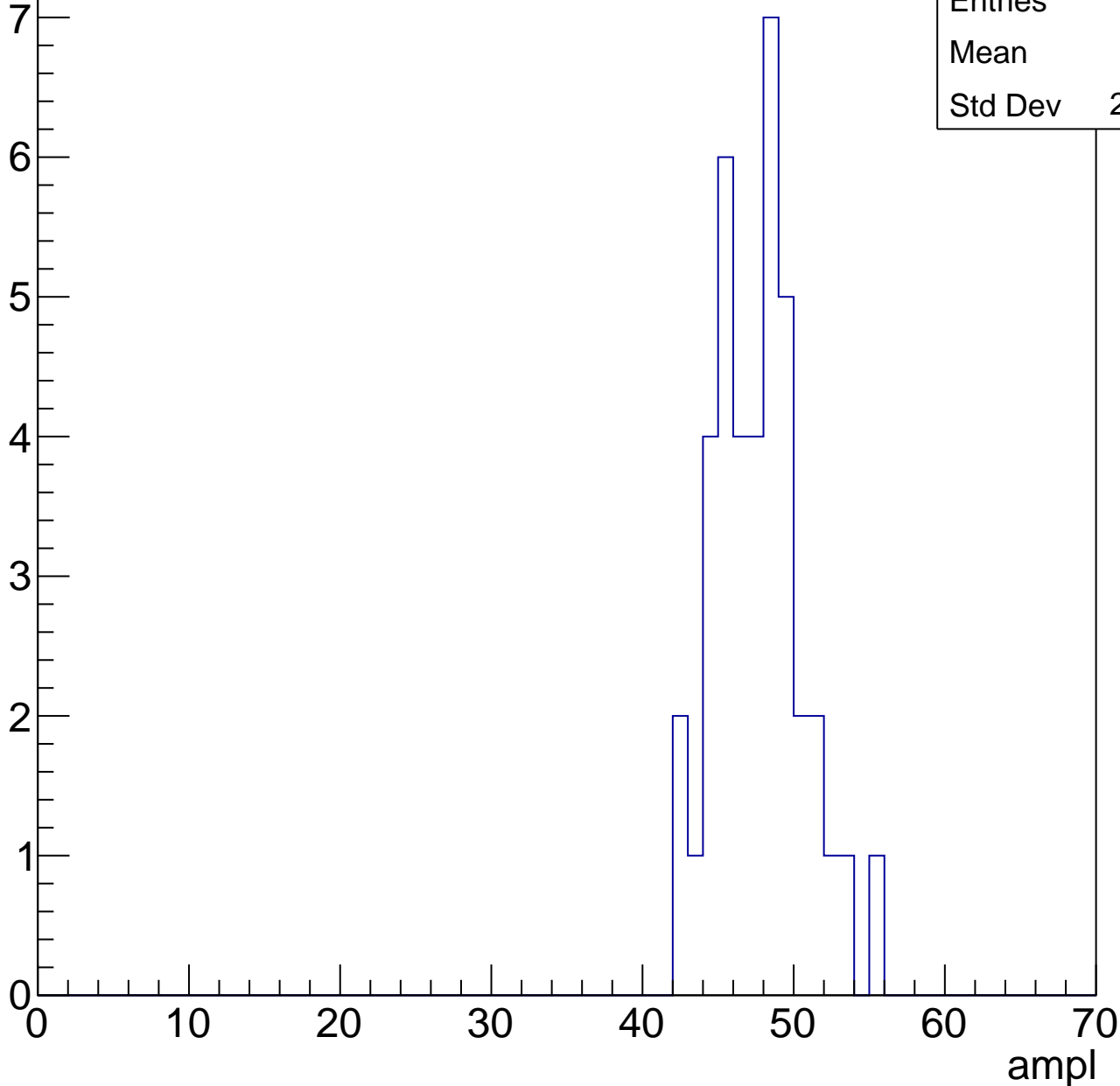


# B1L002S, U3-ch94, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	40
Mean	47.2
Std Dev	2.874

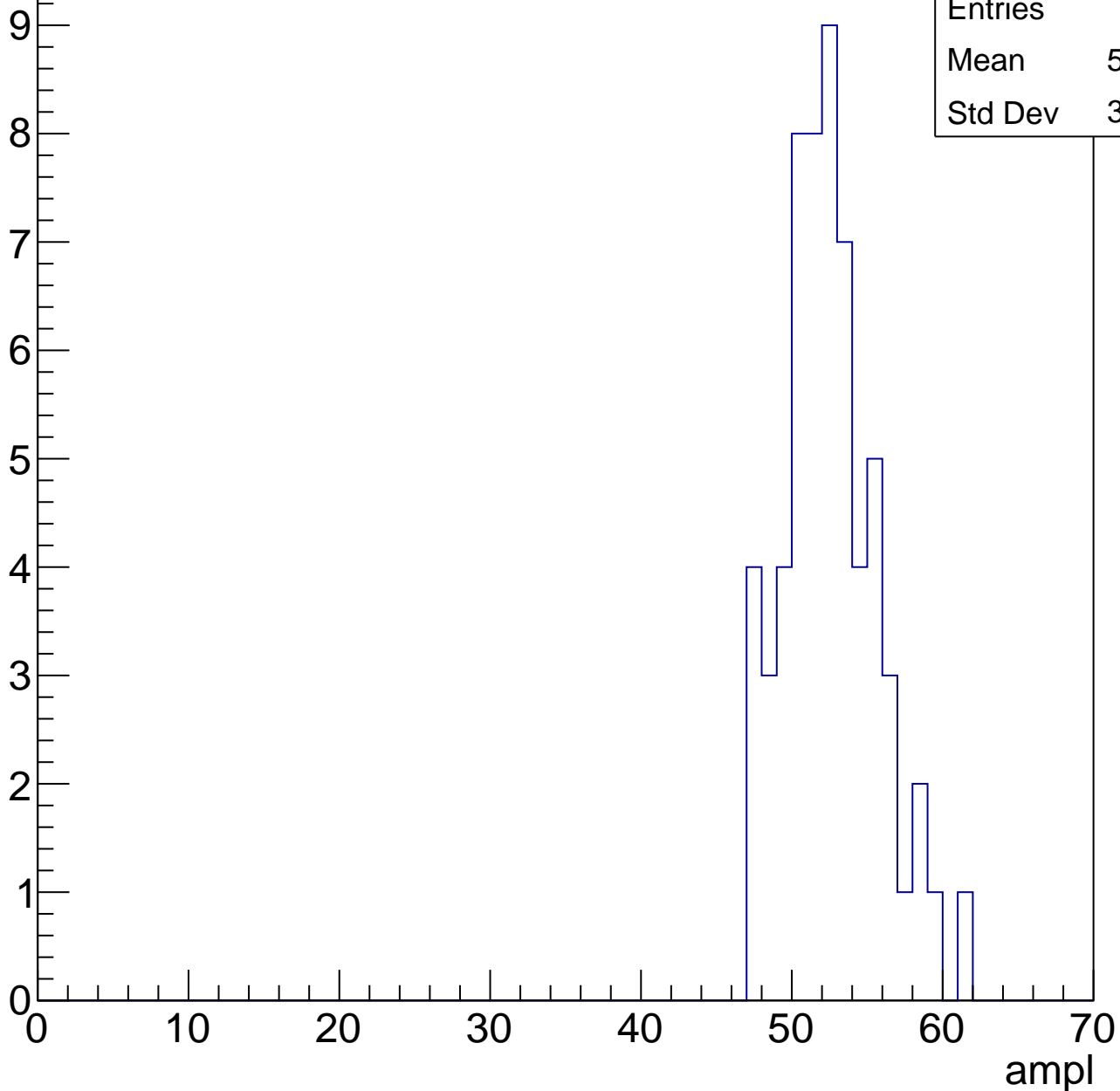


# B1L002S, U3-ch94, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	52.12
Std Dev	3.072



# B1L002S, U3-ch94, adc5

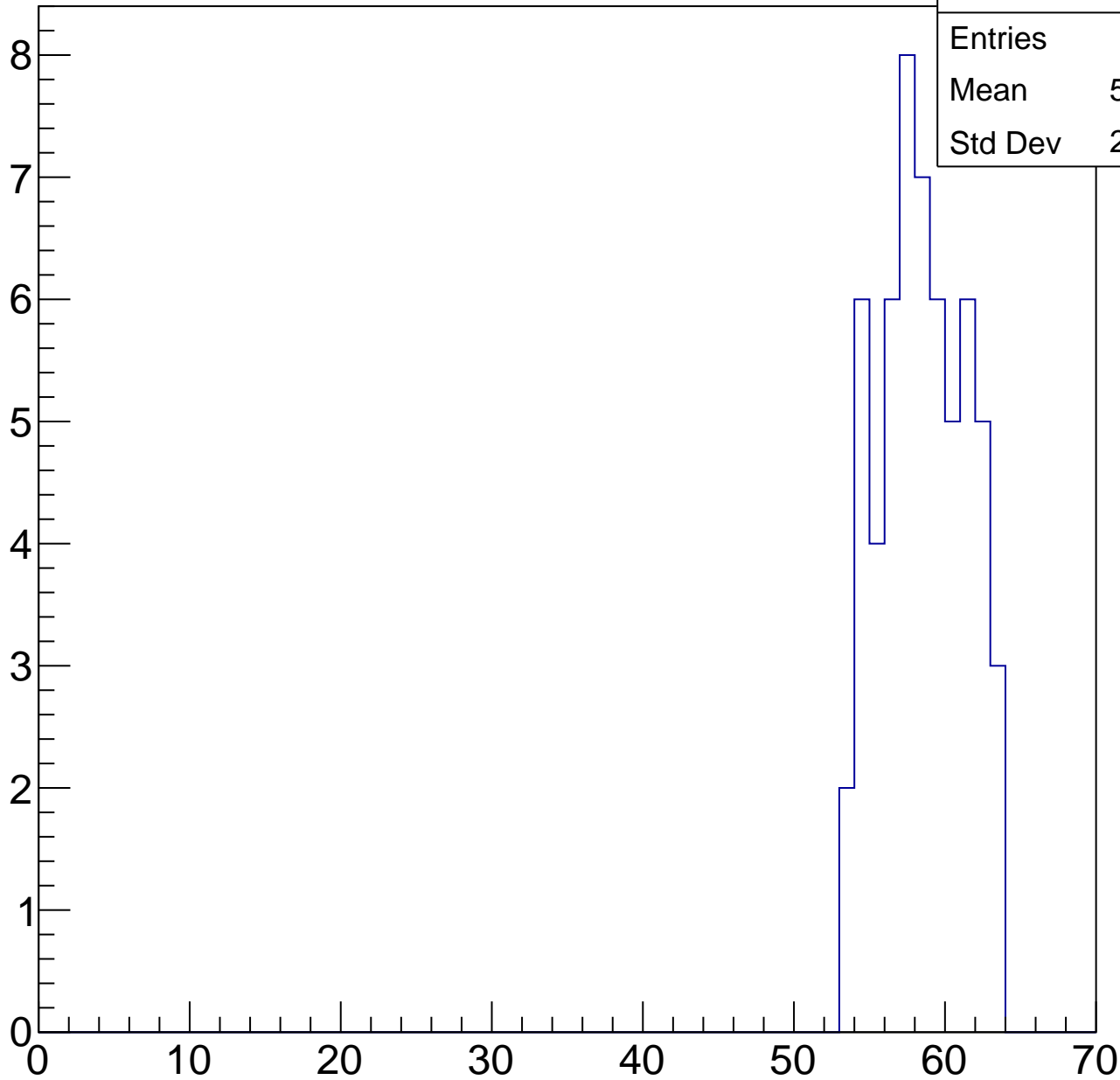
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	58
Mean	58.05
Std Dev	2.782

ampl

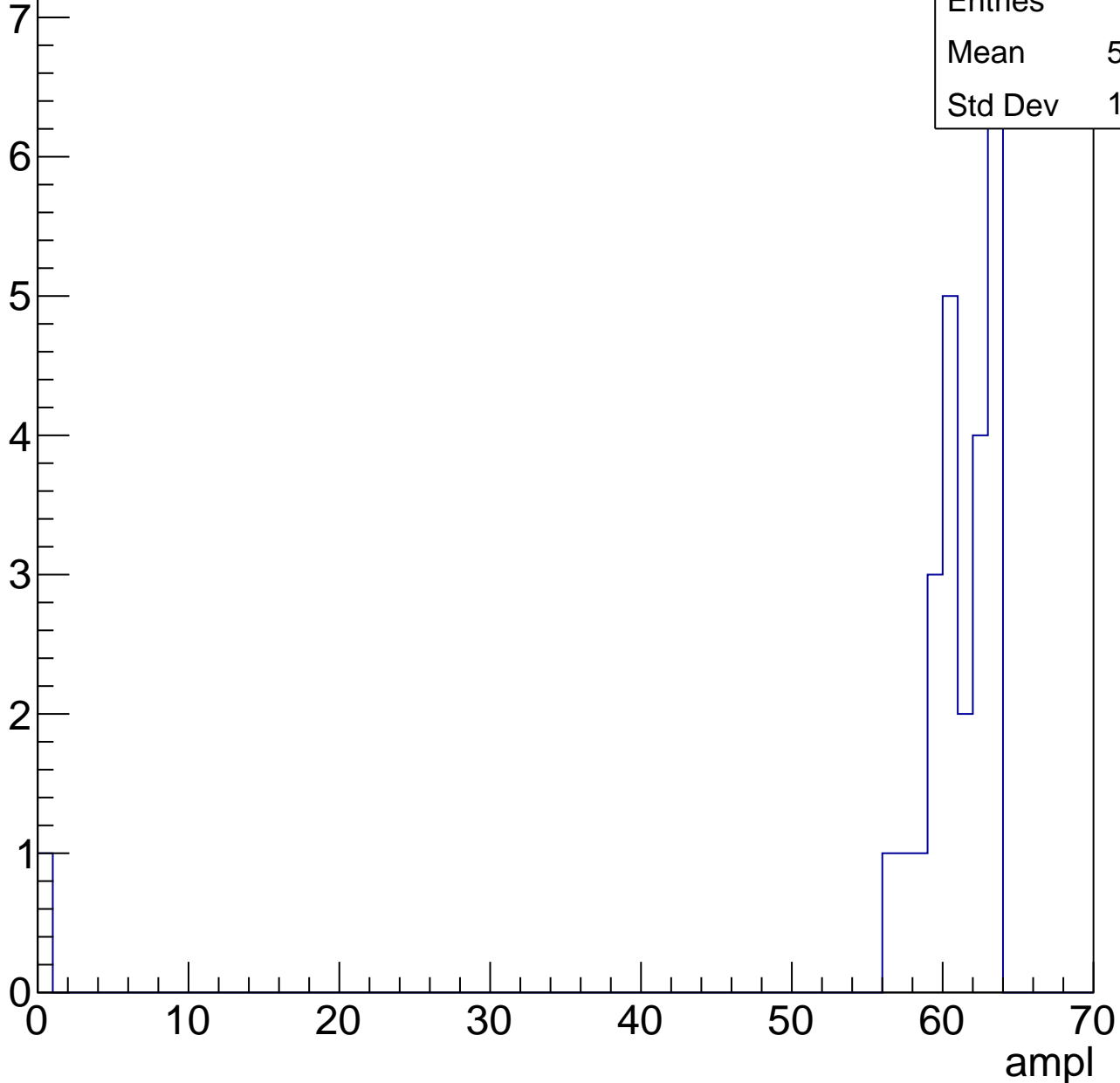


# B1L002S, U3-ch94, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.36
Std Dev	12.08

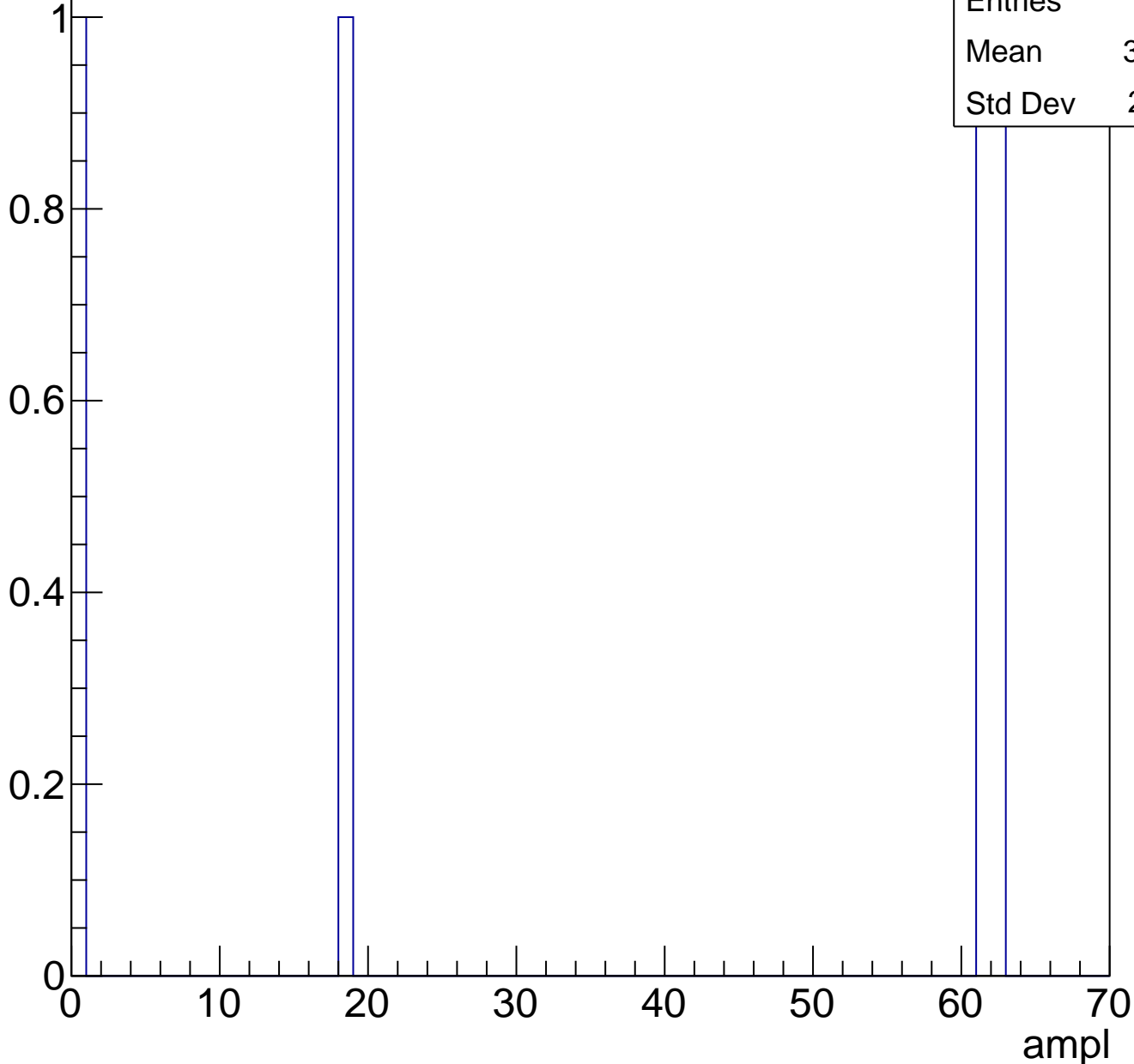




# B1L002S, U3-ch94, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch95, adc0

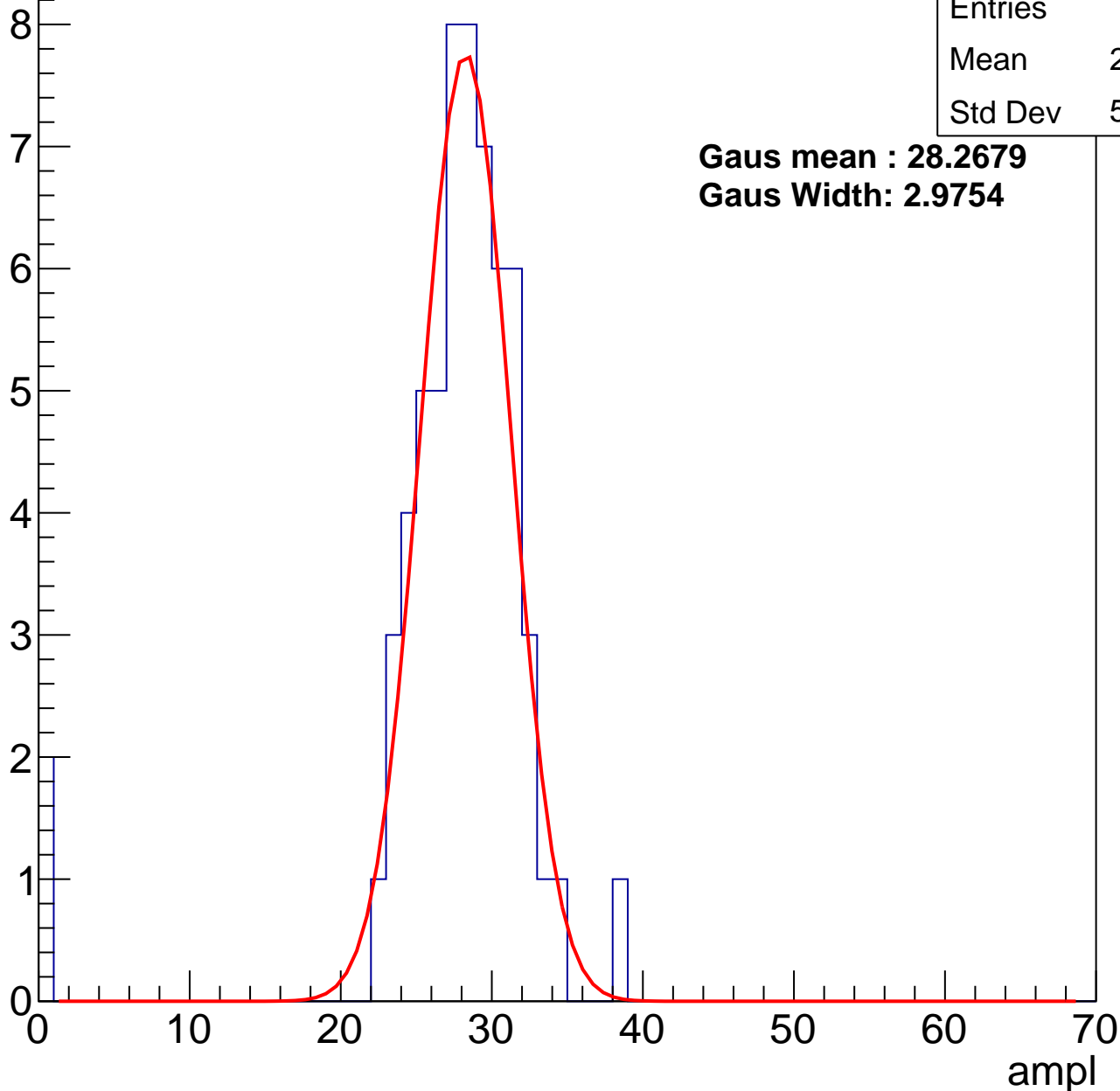
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	27.08
Std Dev	5.809

**Gaus mean : 28.2679**

**Gaus Width: 2.9754**



# B1L002S, U3-ch95, adc1

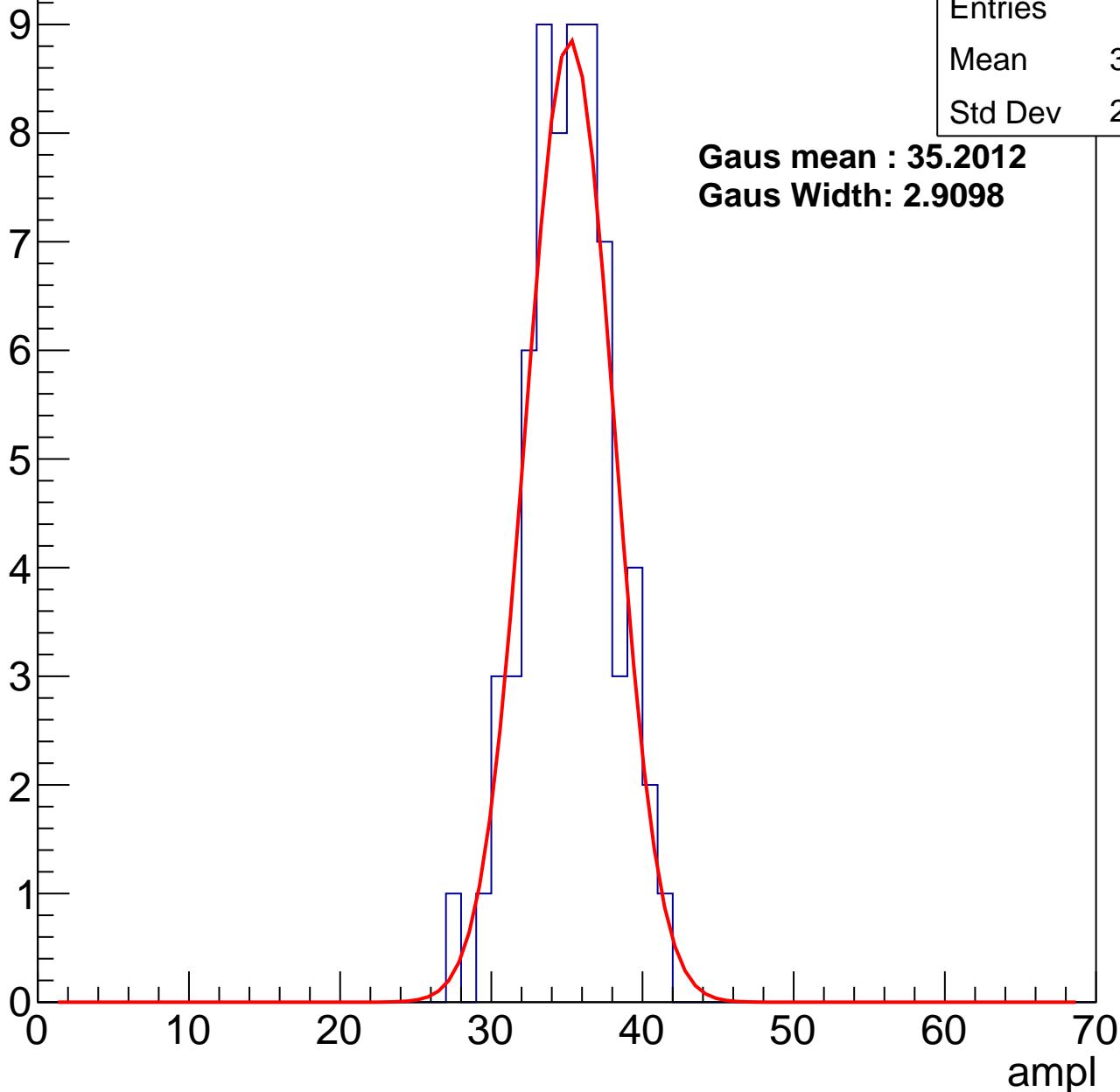
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	34.68
Std Dev	2.829

**Gaus mean : 35.2012**

**Gaus Width: 2.9098**



# B1L002S, U3-ch95, adc2

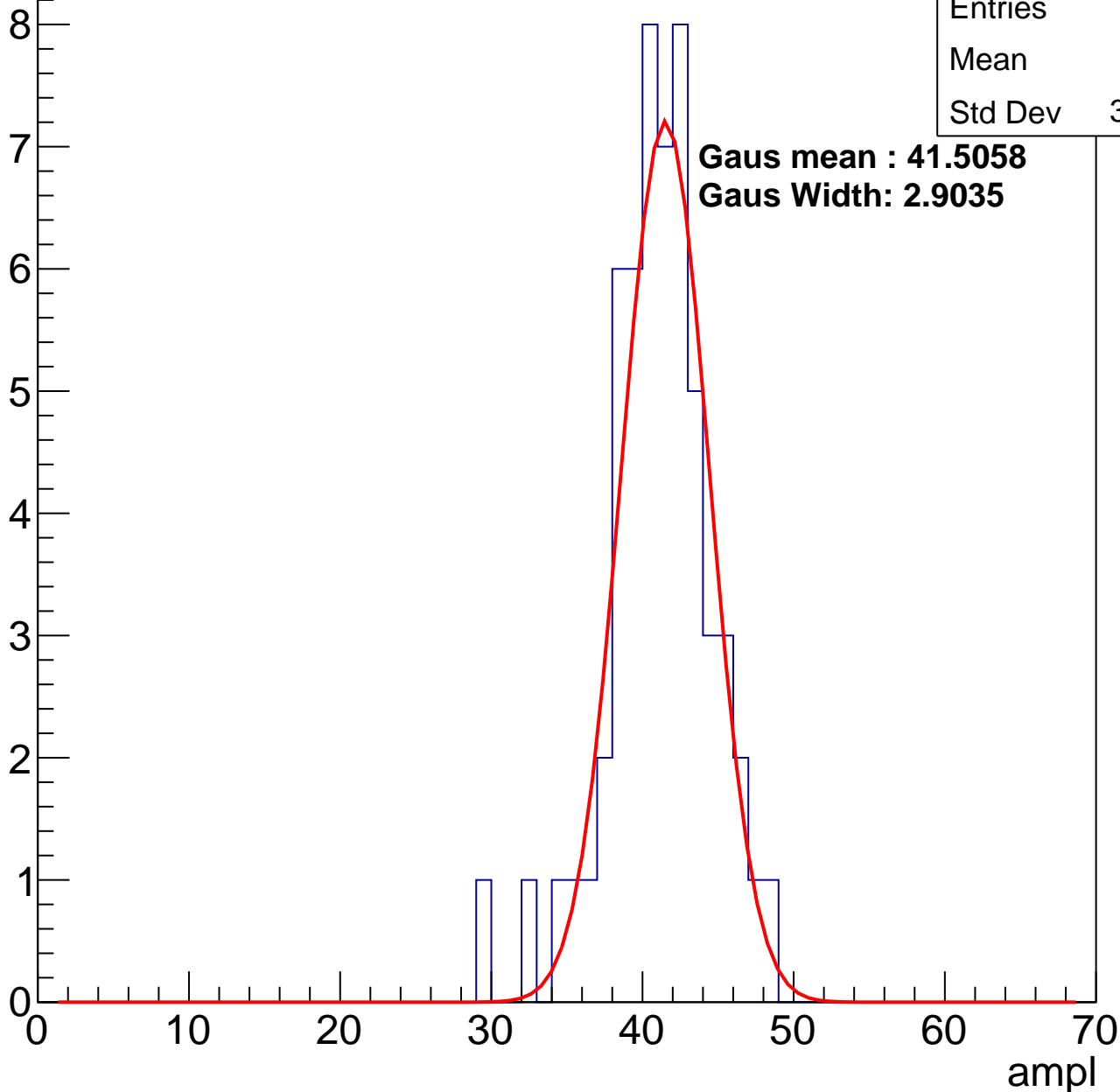
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.6
Std Dev	3.458

**Gaus mean : 41.5058**

**Gaus Width: 2.9035**

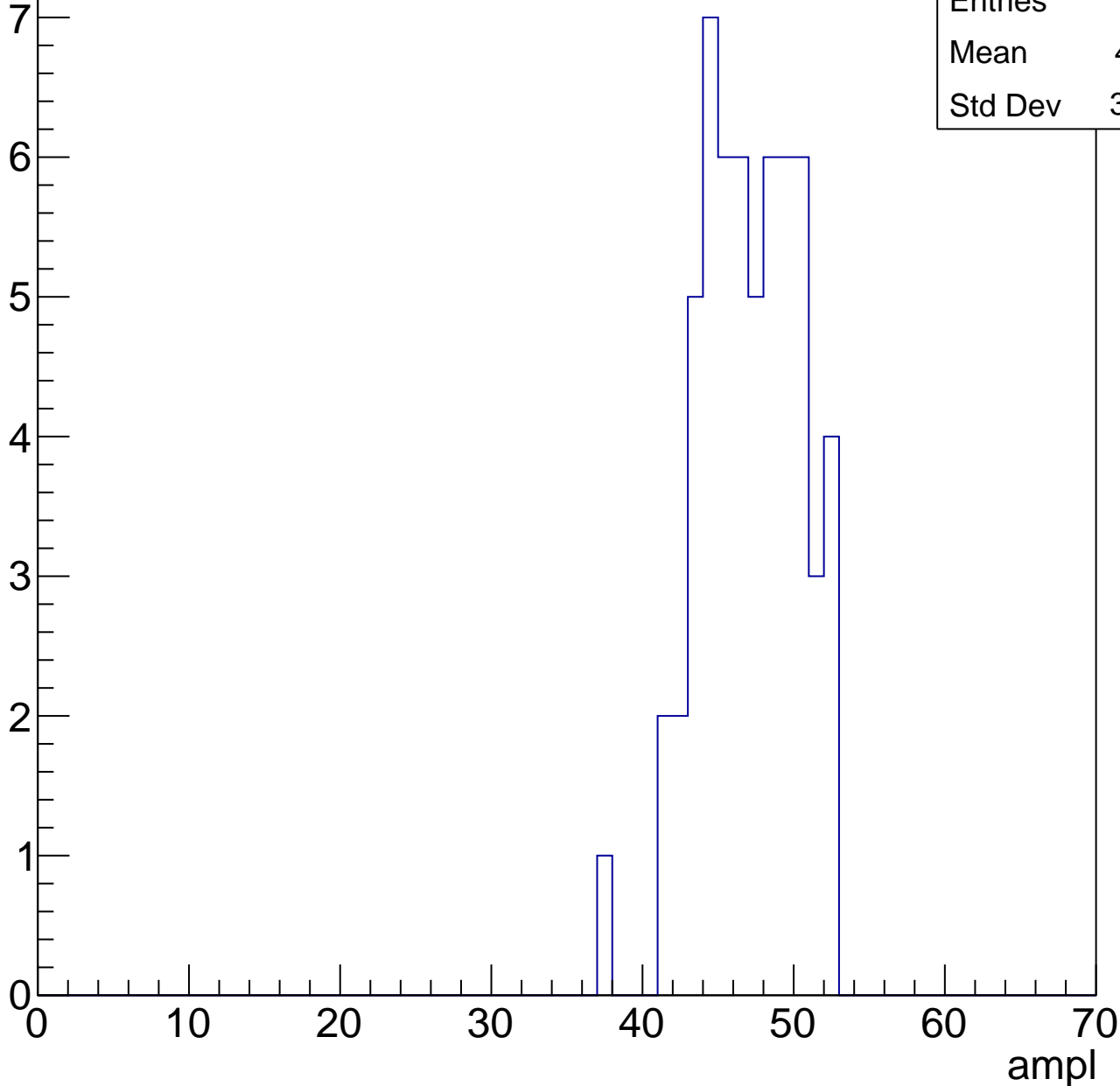


# B1L002S, U3-ch95, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	46.61
Std Dev	3.237

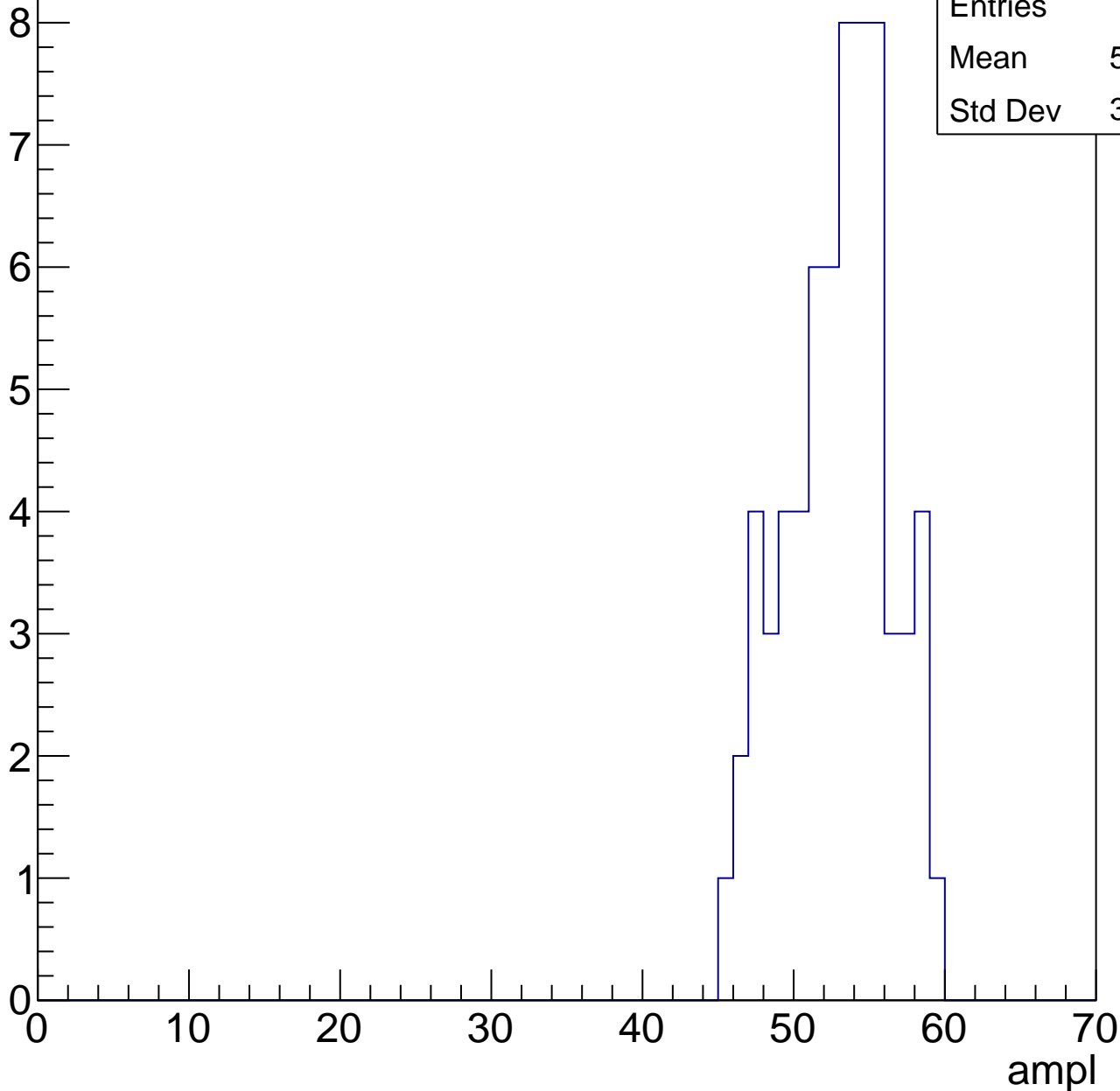


# B1L002S, U3-ch95, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	52.45
Std Dev	3.379

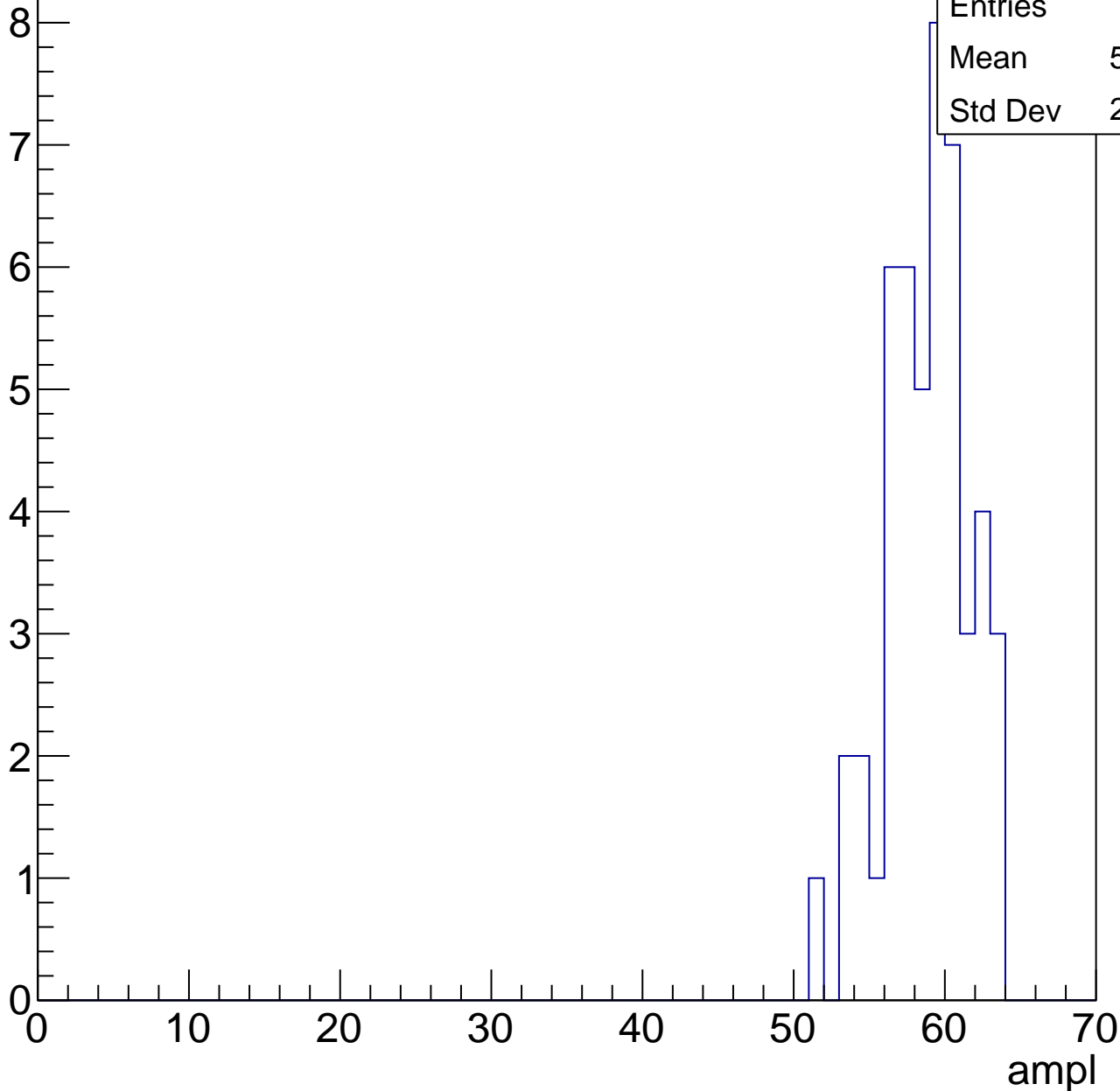


# B1L002S, U3-ch95, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	58.33
Std Dev	2.764

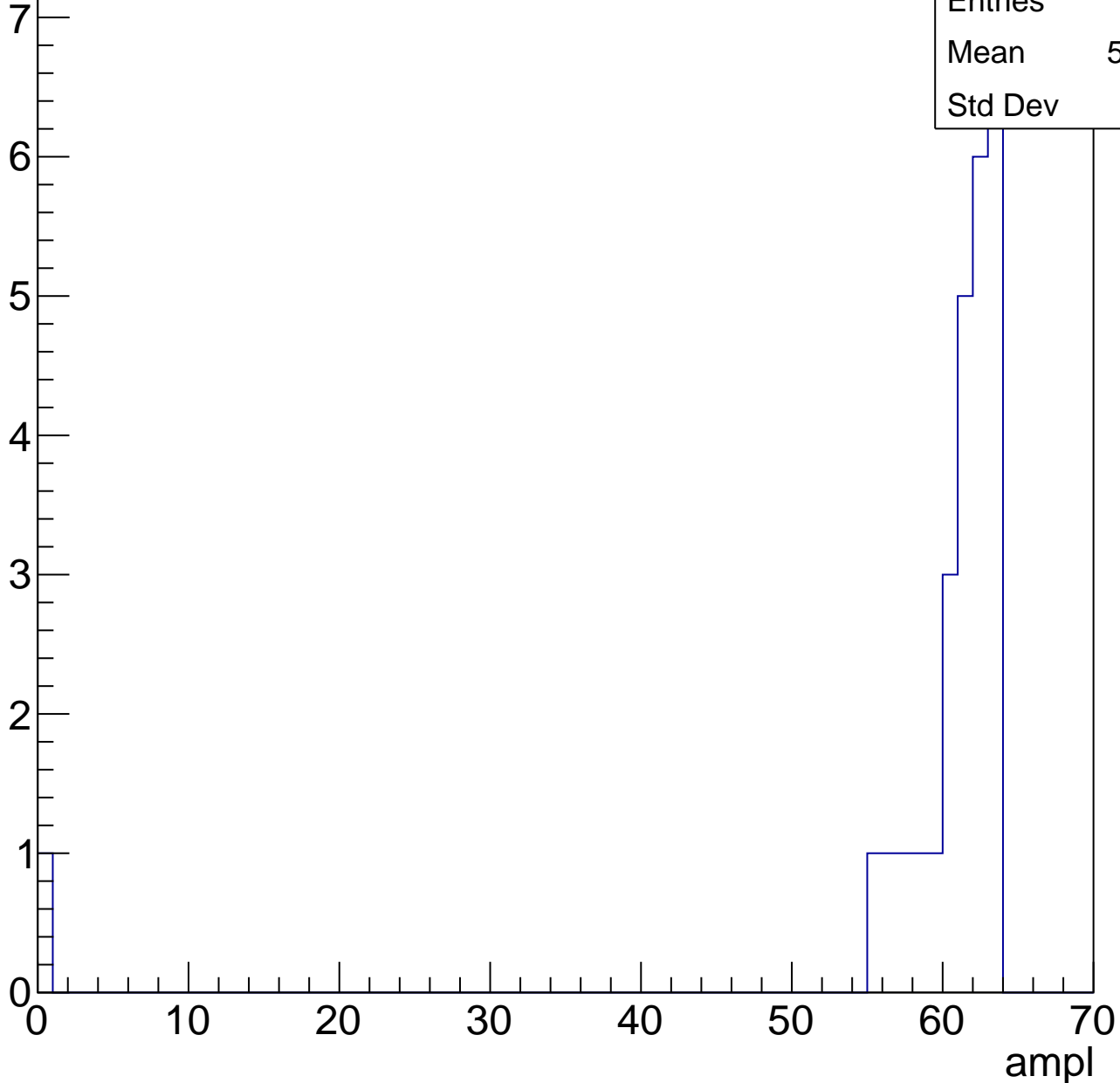


# B1L002S, U3-ch95, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	58.63
Std Dev	11.7

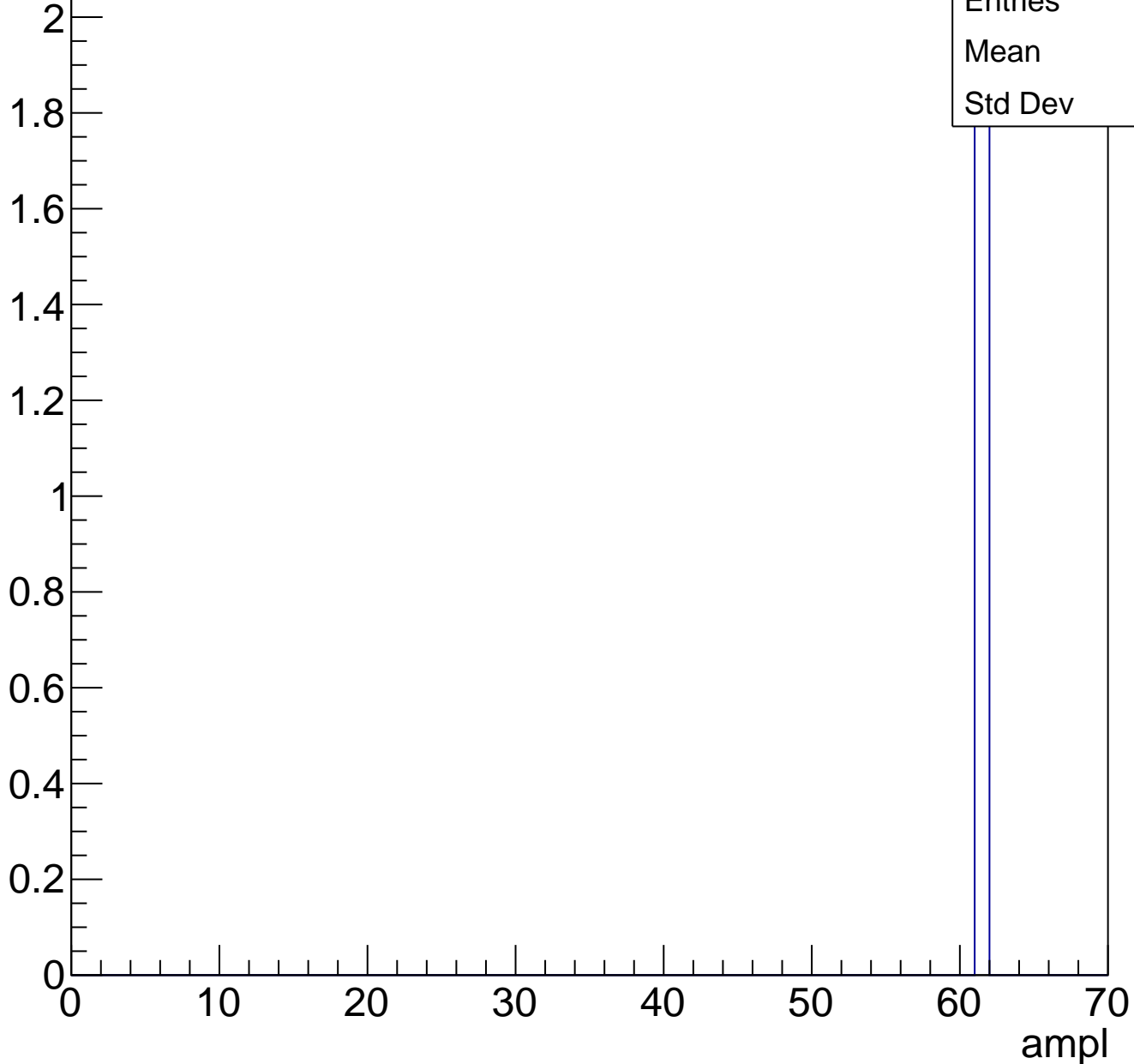




# B1L002S, U3-ch95, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch96, adc0

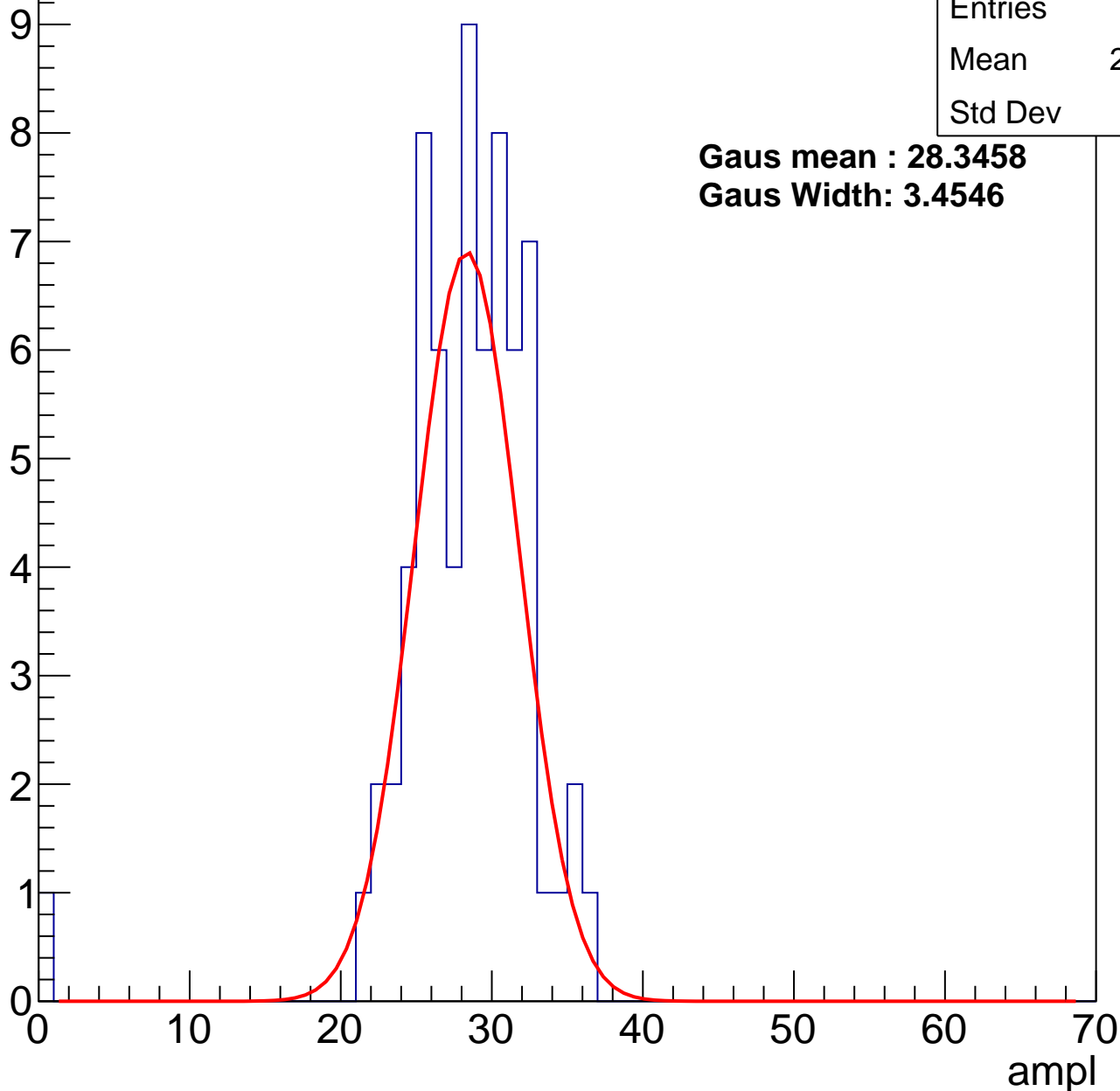
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.83
Std Dev	4.73

**Gaus mean : 28.3458**

**Gaus Width: 3.4546**



# B1L002S, U3-ch96, adc1

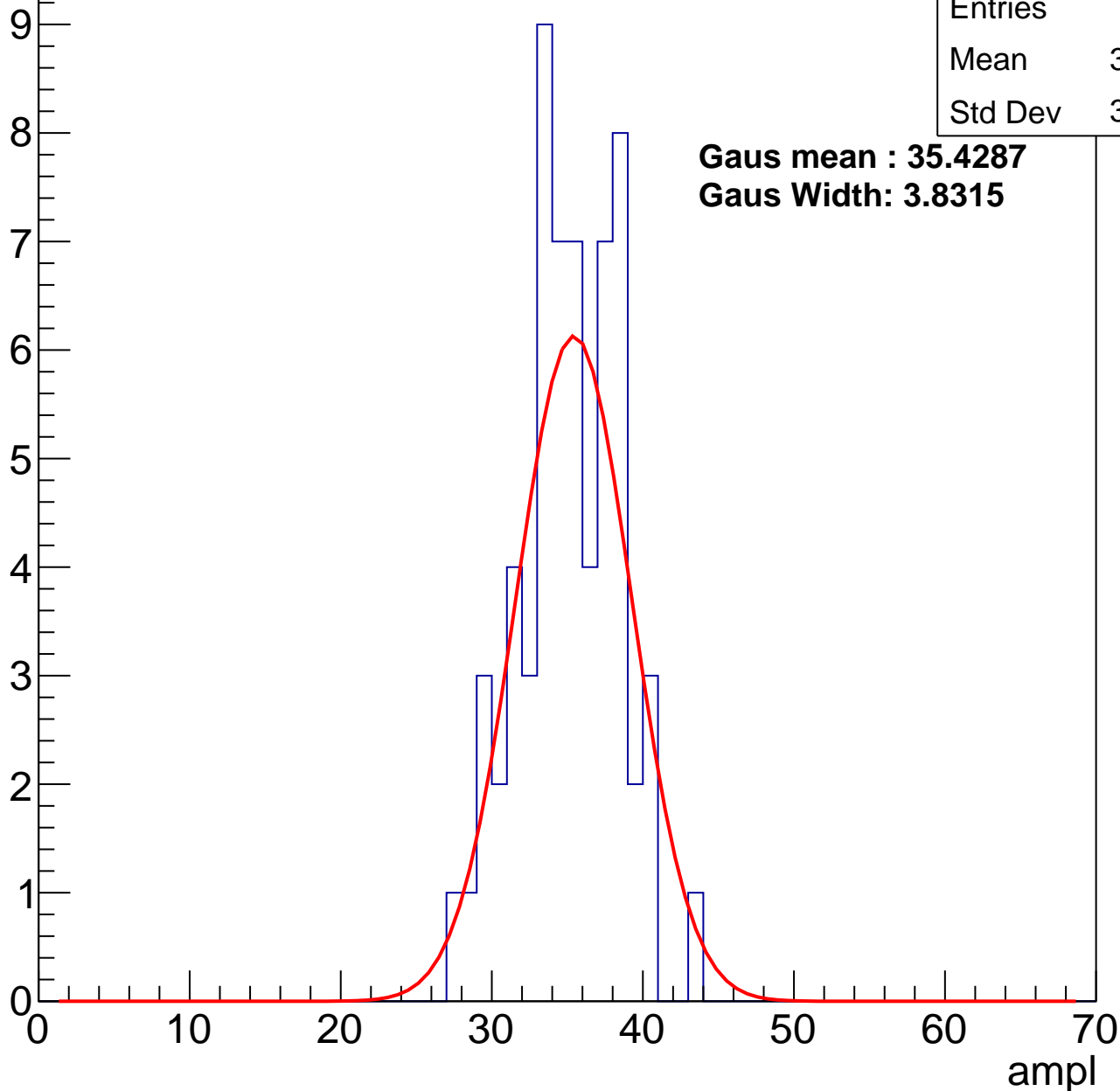
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	34.68
Std Dev	3.296

**Gaus mean : 35.4287**

**Gaus Width: 3.8315**



# B1L002S, U3-ch96, adc2

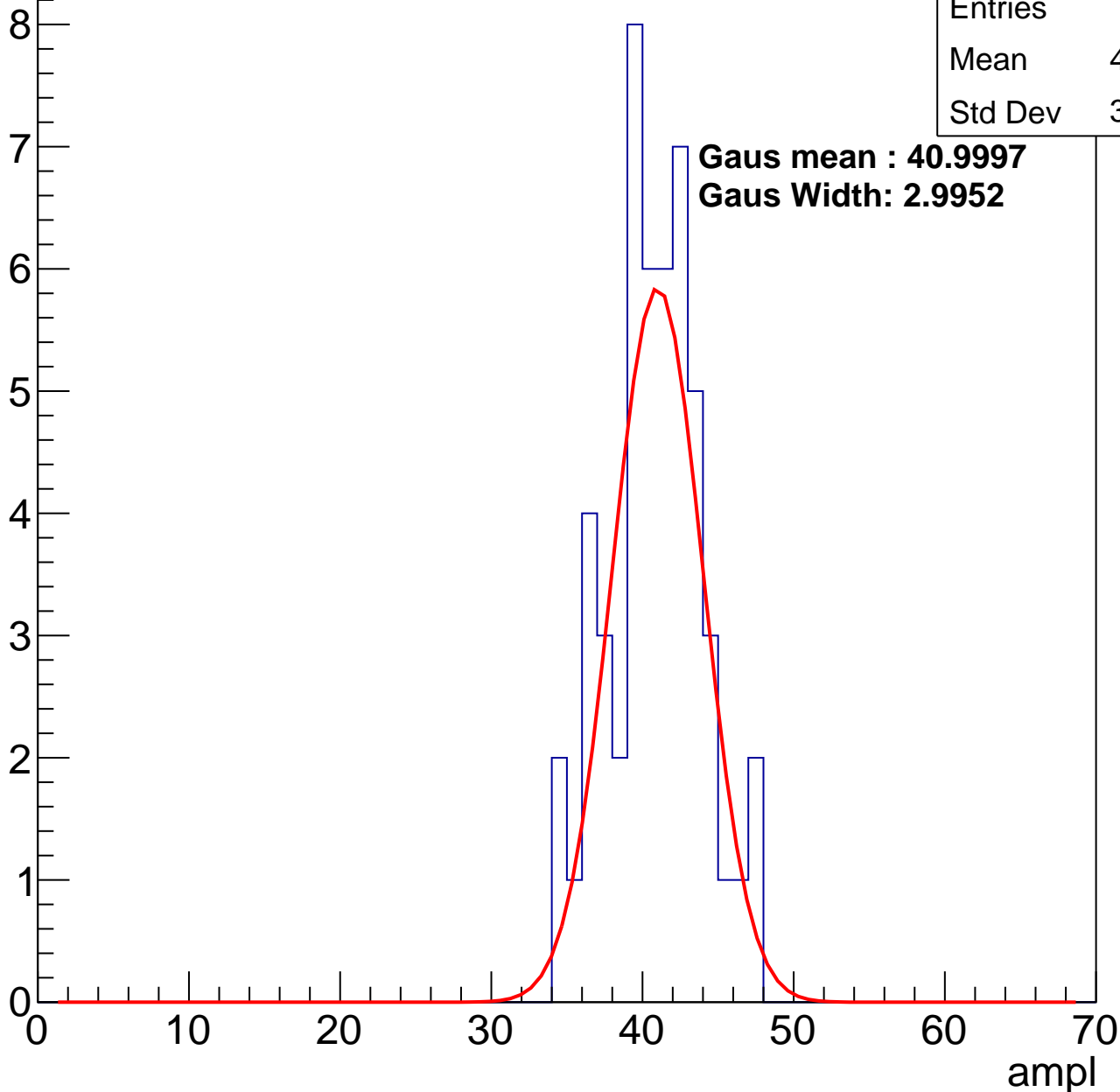
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	40.35
Std Dev	3.079

**Gaus mean : 40.9997**

**Gaus Width: 2.9952**

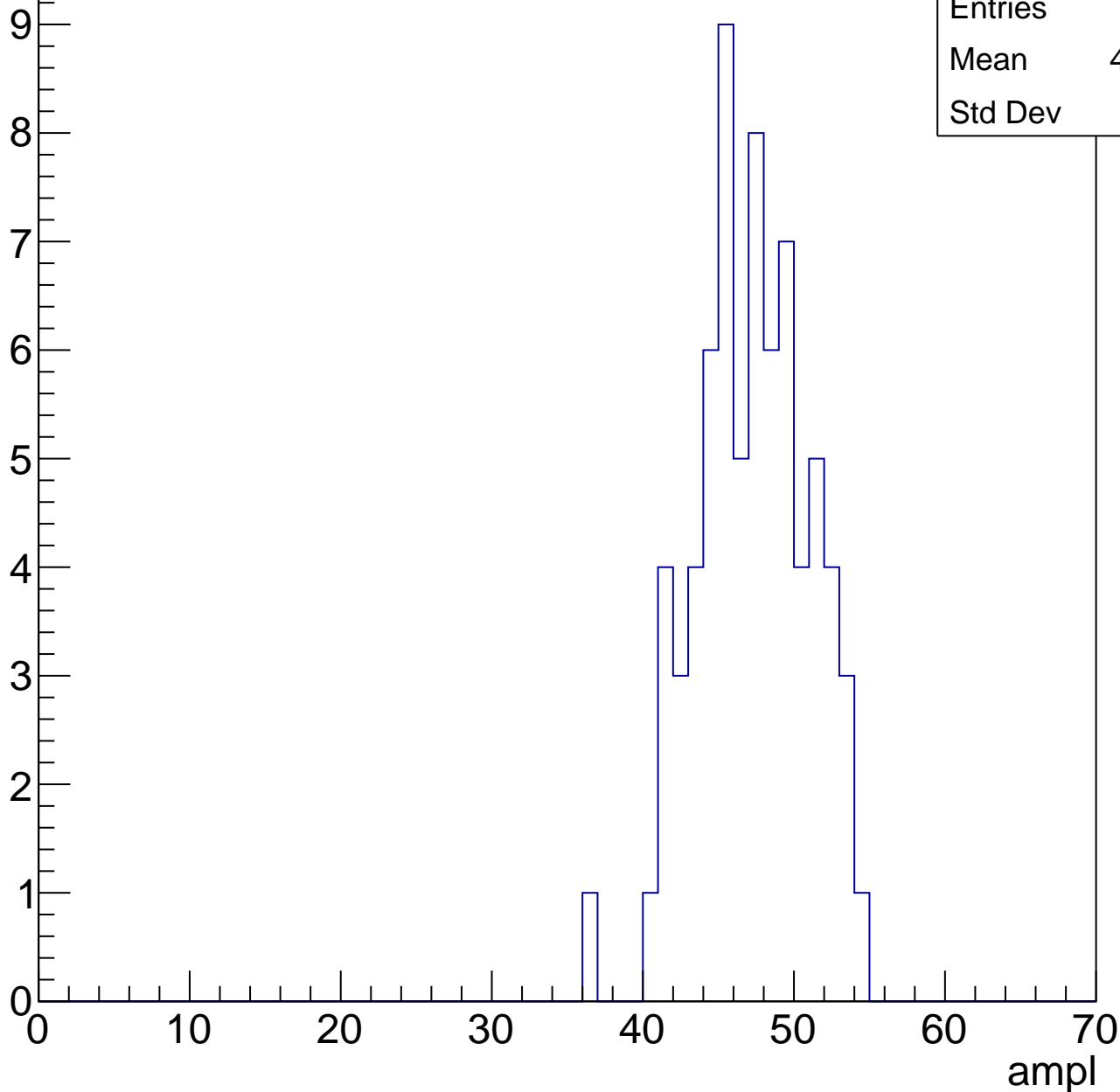


# B1L002S, U3-ch96, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	46.76
Std Dev	3.66

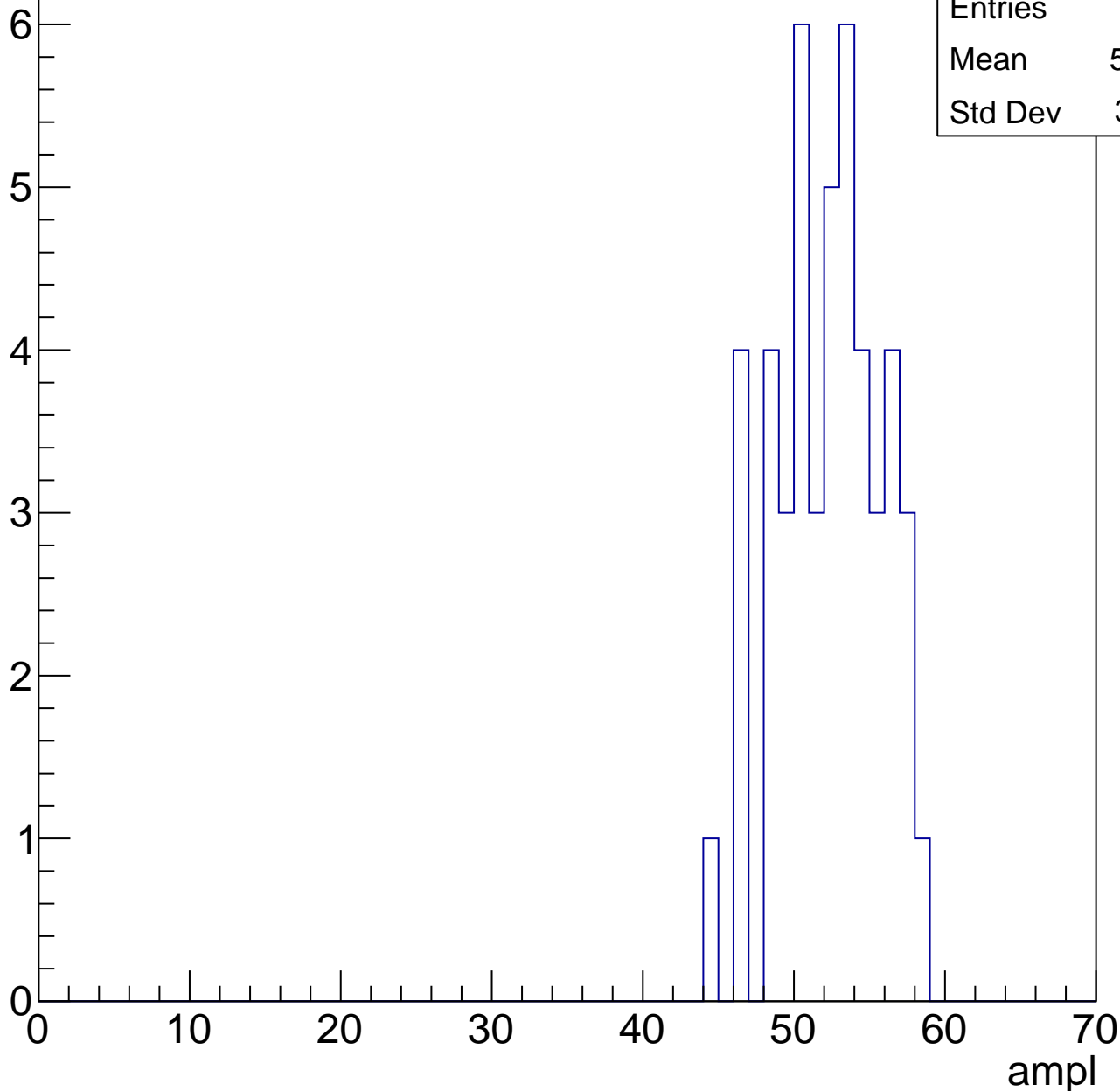


# B1L002S, U3-ch96, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	51.74
Std Dev	3.411

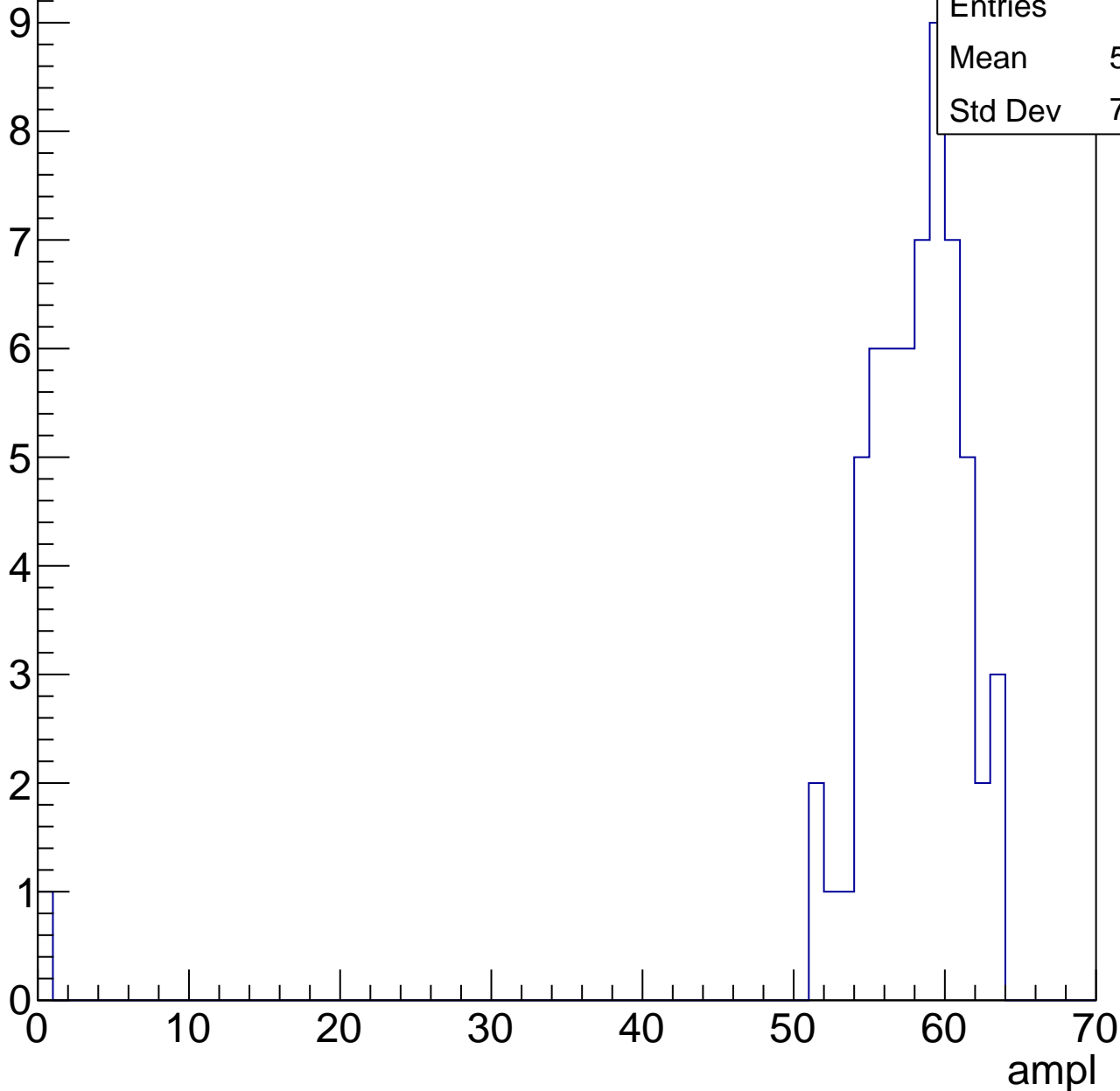


# B1L002S, U3-ch96, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	56.72
Std Dev	7.868

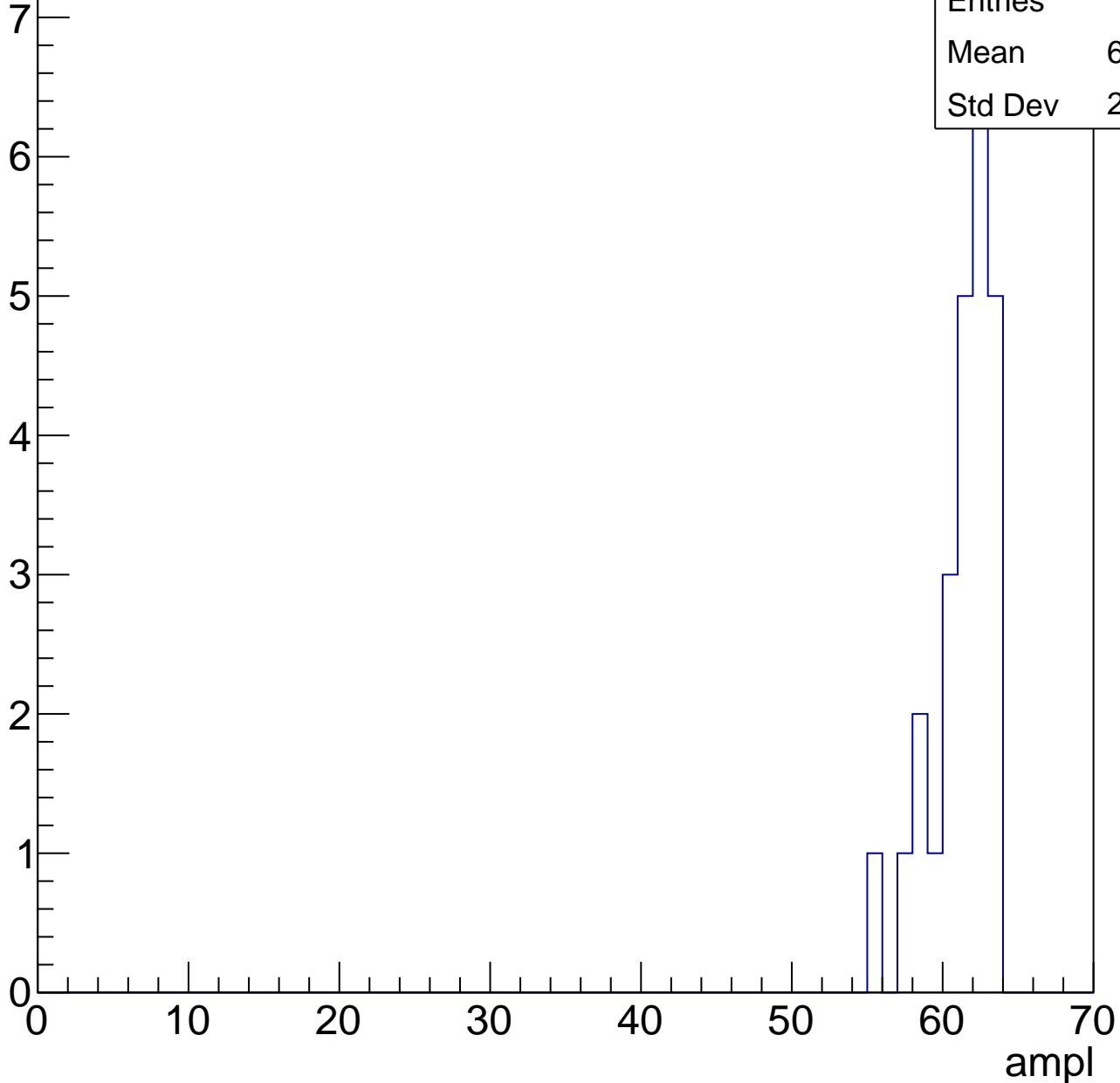


# B1L002S, U3-ch96, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	60.84
Std Dev	2.033





# B1L002S, U3-ch96, adc7

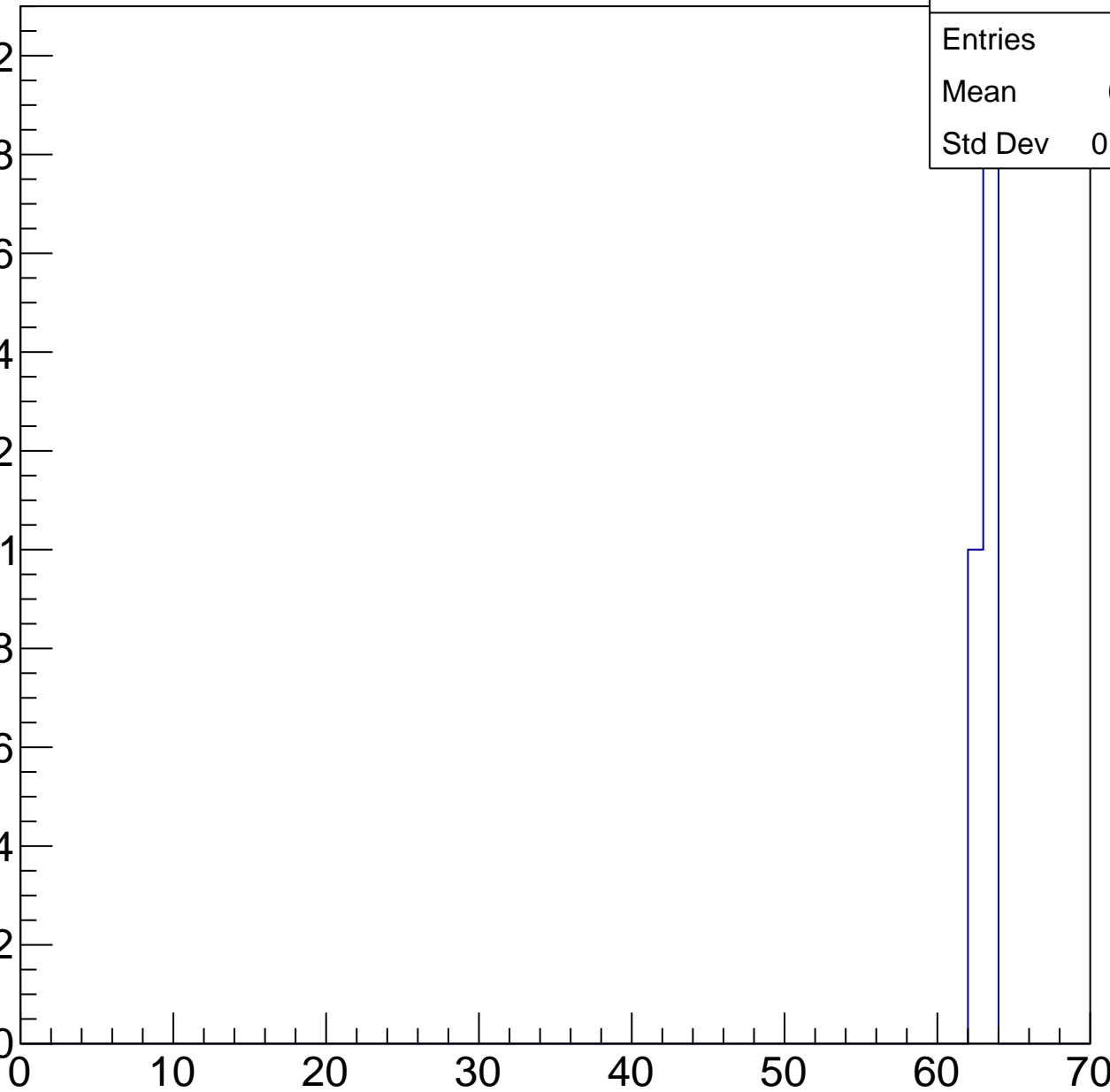
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



# B1L002S, U3-ch97, adc0

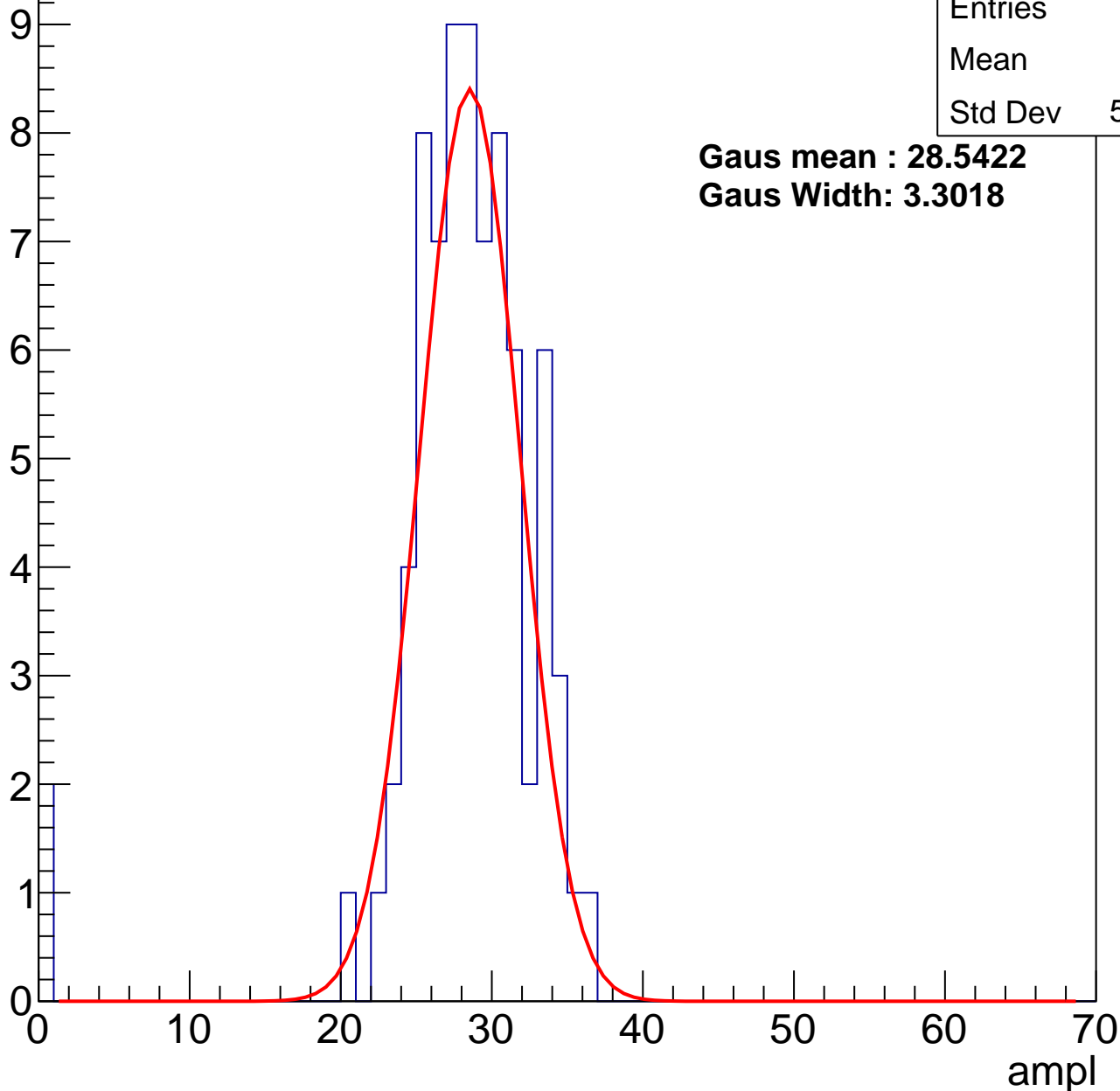
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	27.6
Std Dev	5.553

**Gaus mean : 28.5422**

**Gaus Width: 3.3018**



# B1L002S, U3-ch97, adc1

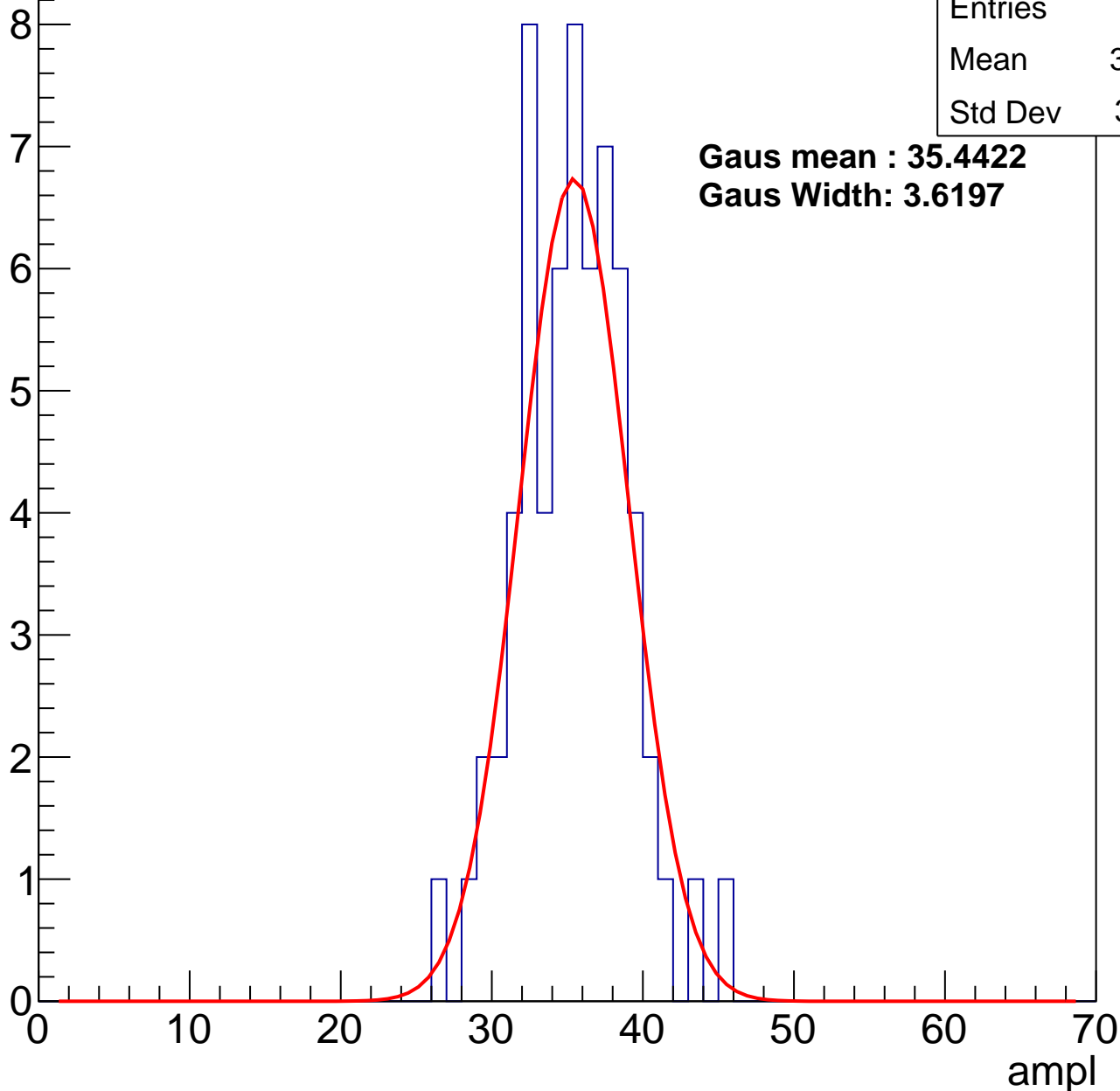
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	34.94
Std Dev	3.561

**Gaus mean : 35.4422**

**Gaus Width: 3.6197**



# B1L002S, U3-ch97, adc2

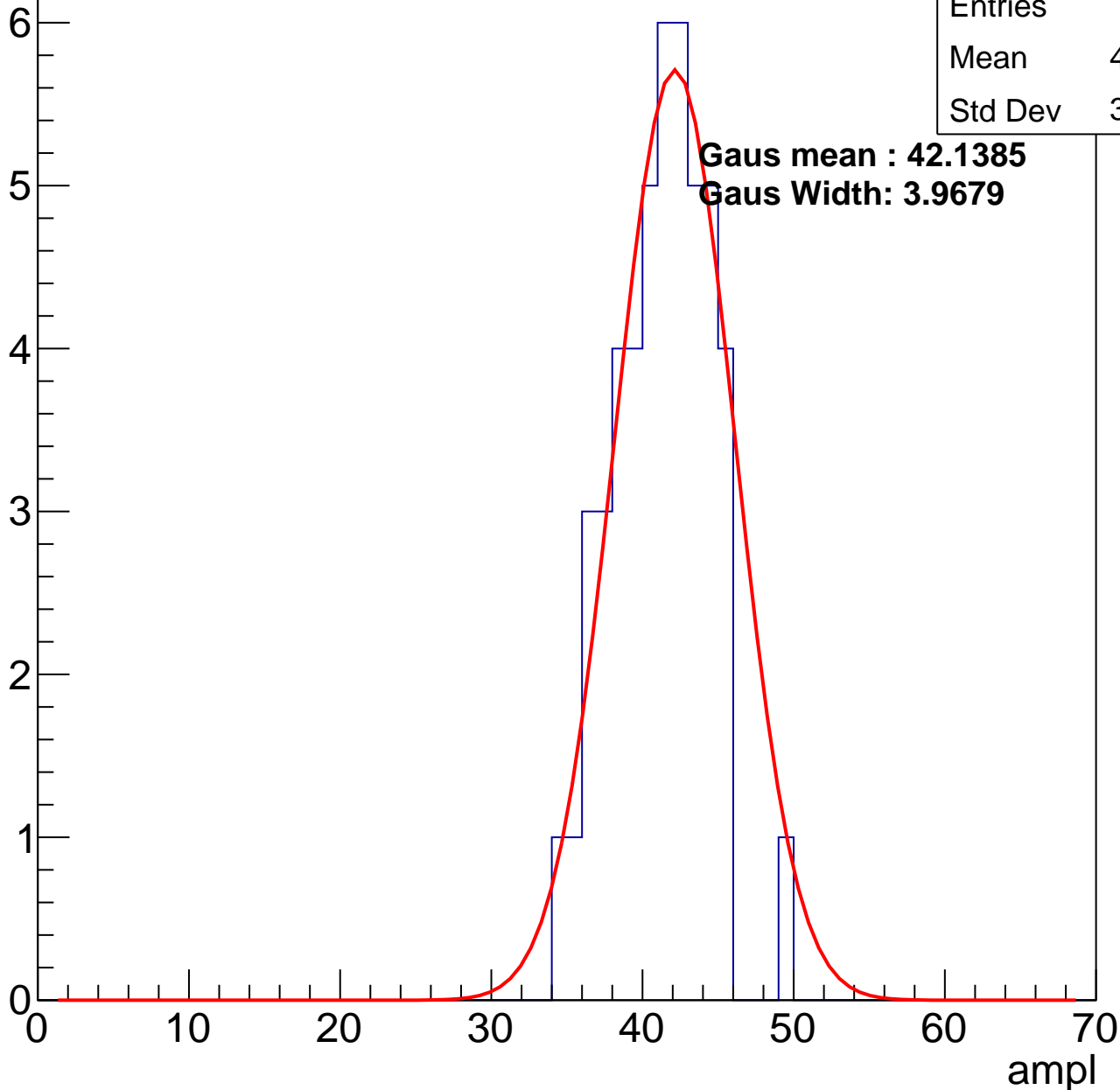
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	40.79
Std Dev	3.102

**Gaus mean : 42.1385**

**Gaus Width: 3.9679**



# B1L002S, U3-ch97, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

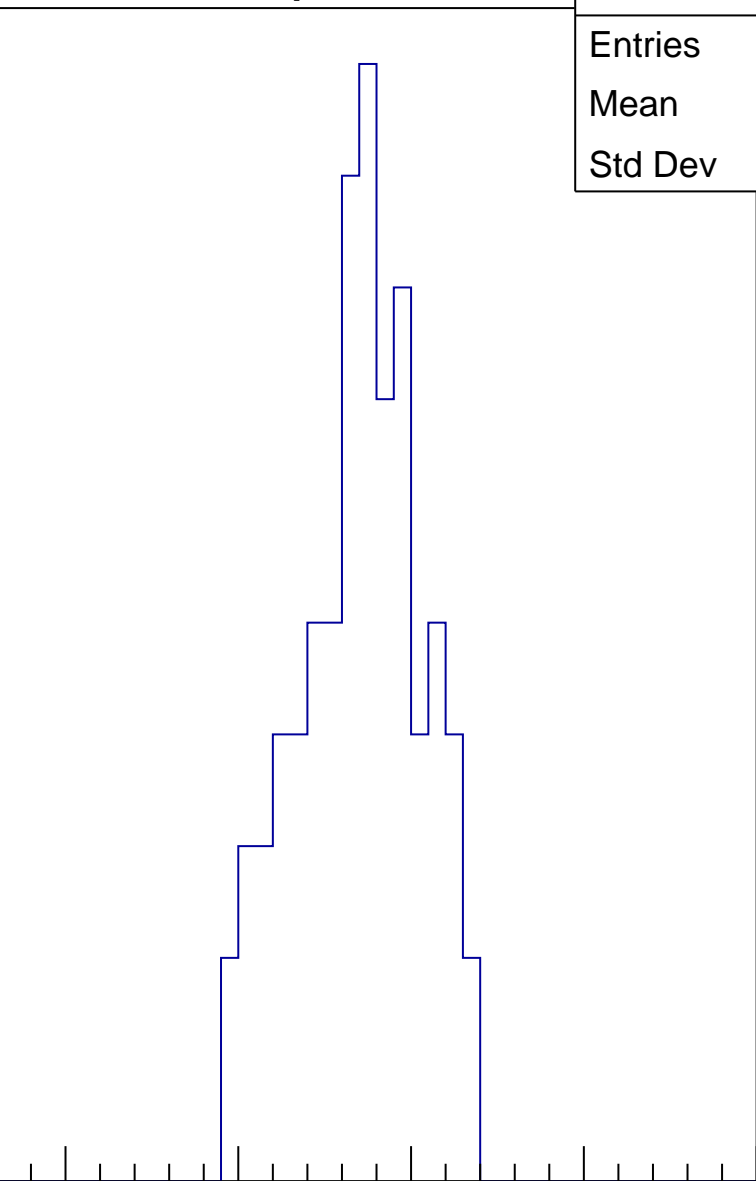
Entries	75
Mean	46.49
Std Dev	3.519

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

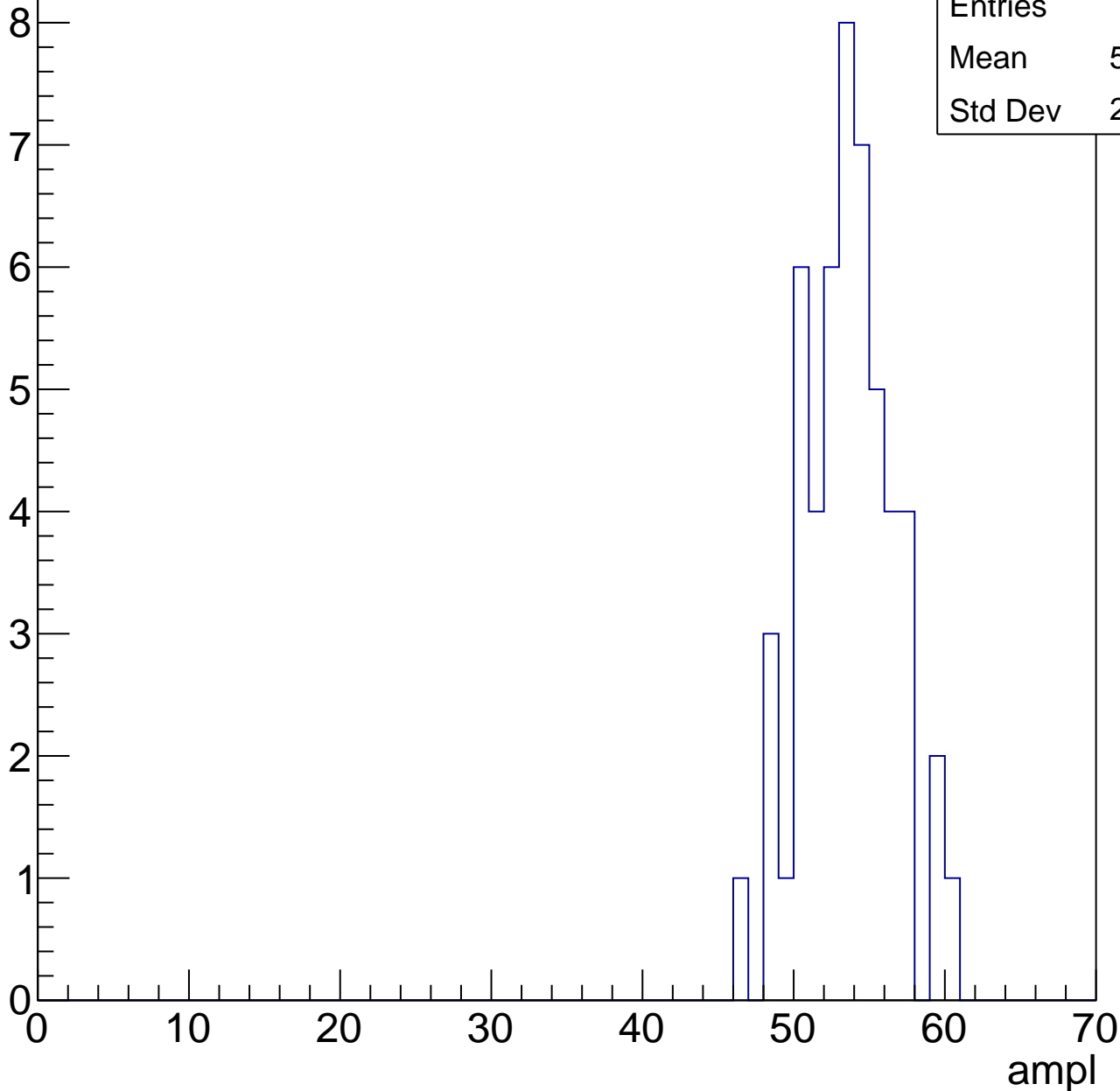


# B1L002S, U3-ch97, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	53.12
Std Dev	2.985



# B1L002S, U3-ch97, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

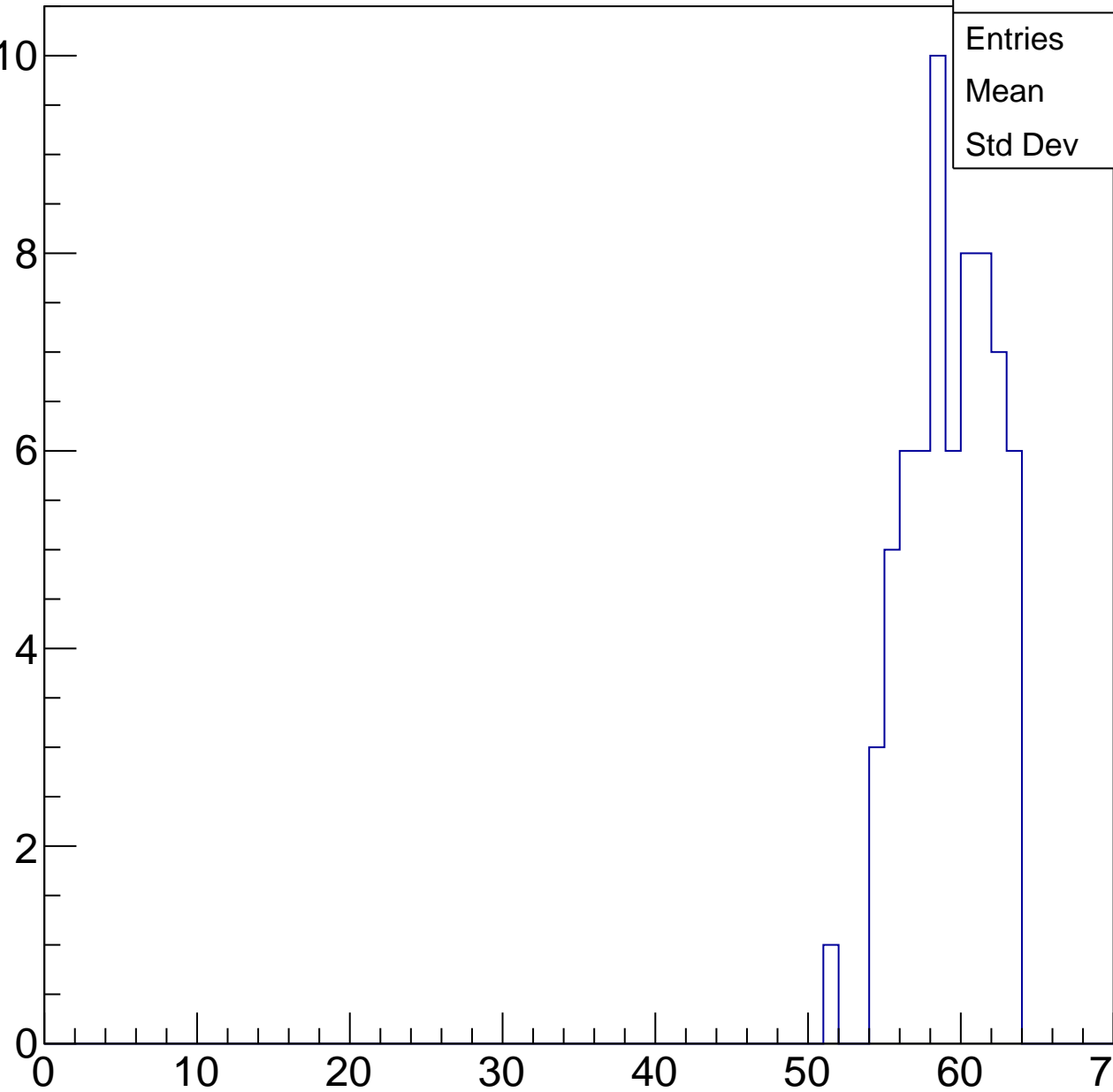
Entries	66
Mean	58.79
Std Dev	2.761

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

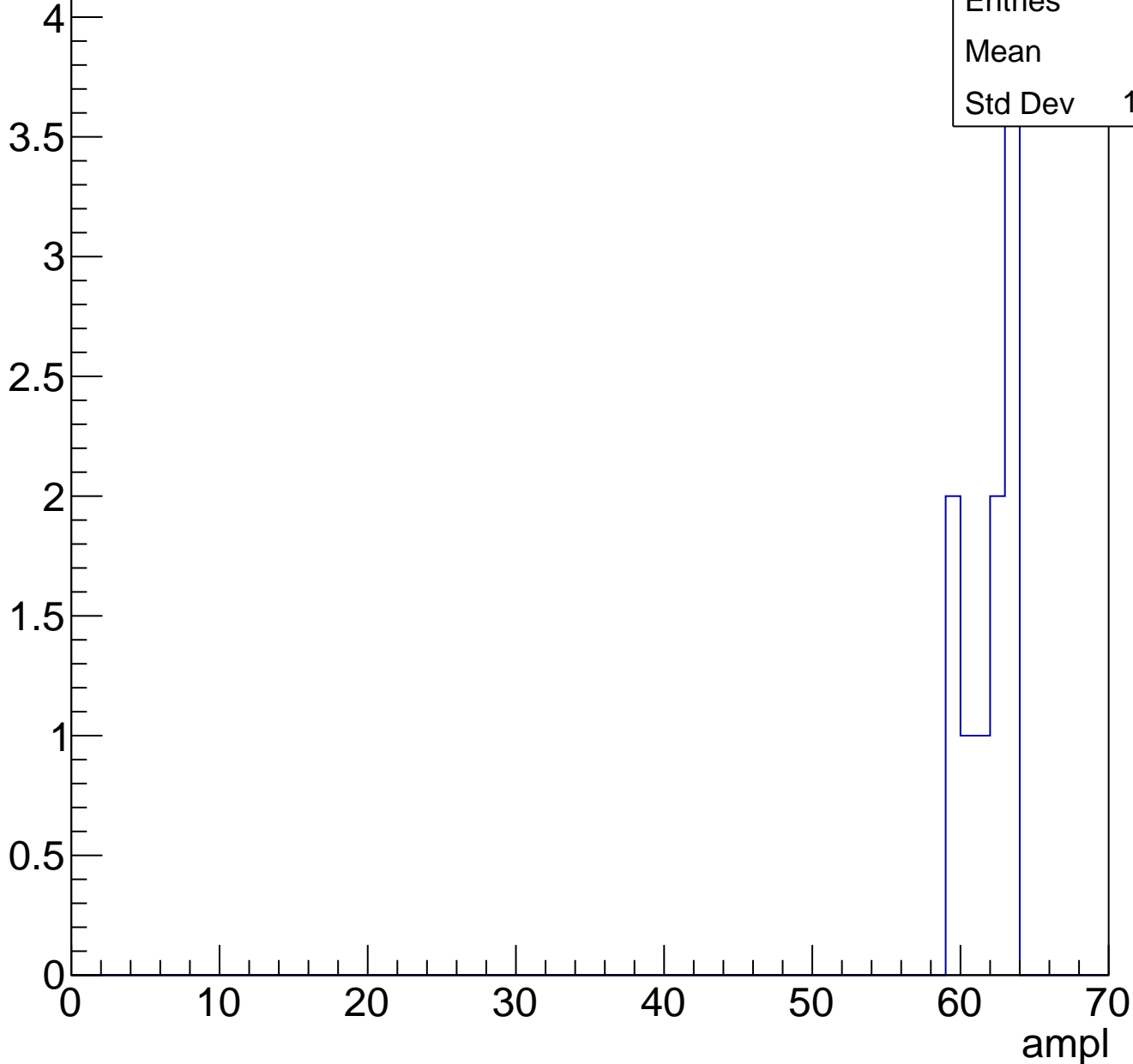
ampl



# B1L002S, U3-ch97, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

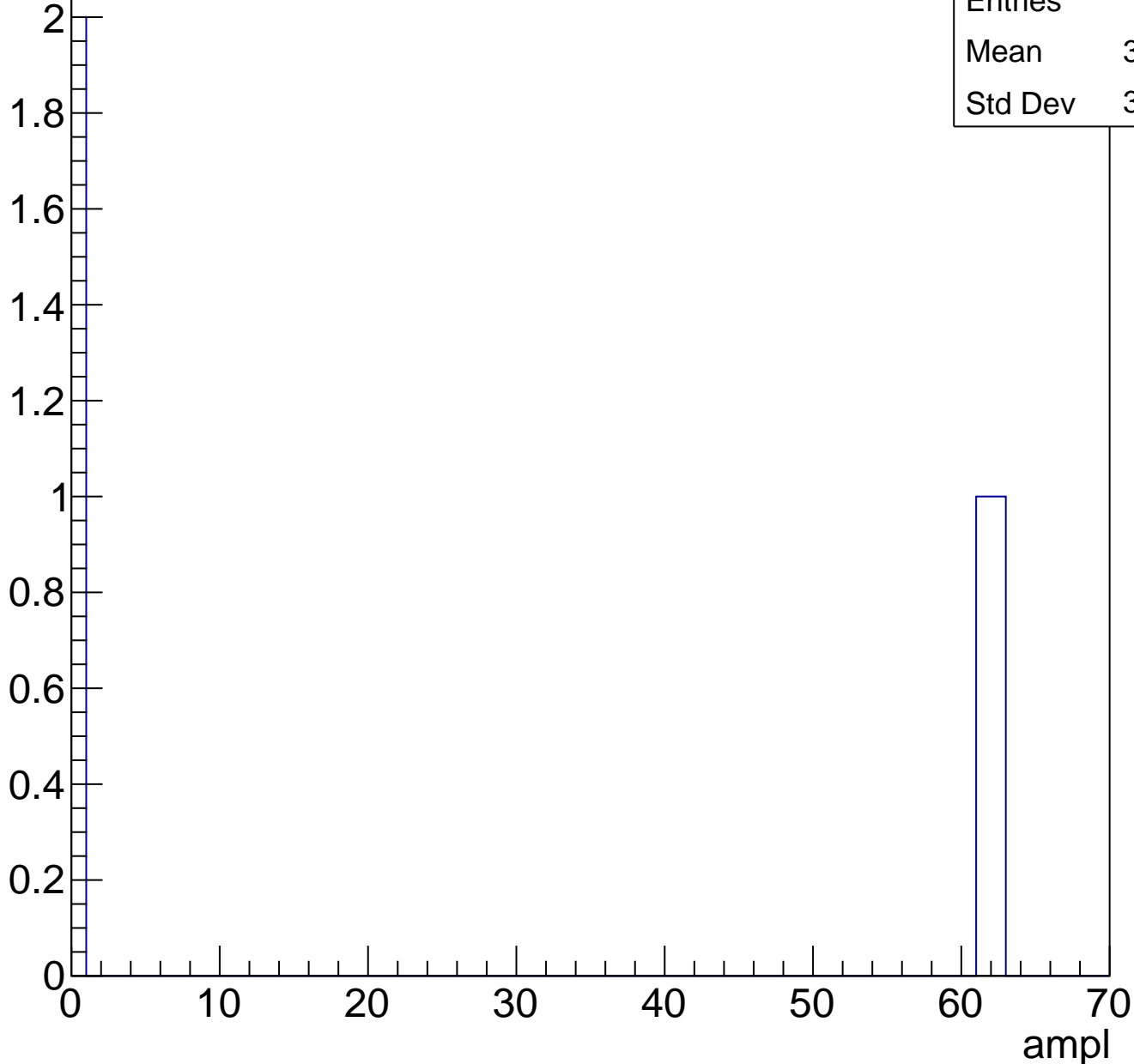




# B1L002S, U3-ch97, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch98, adc0

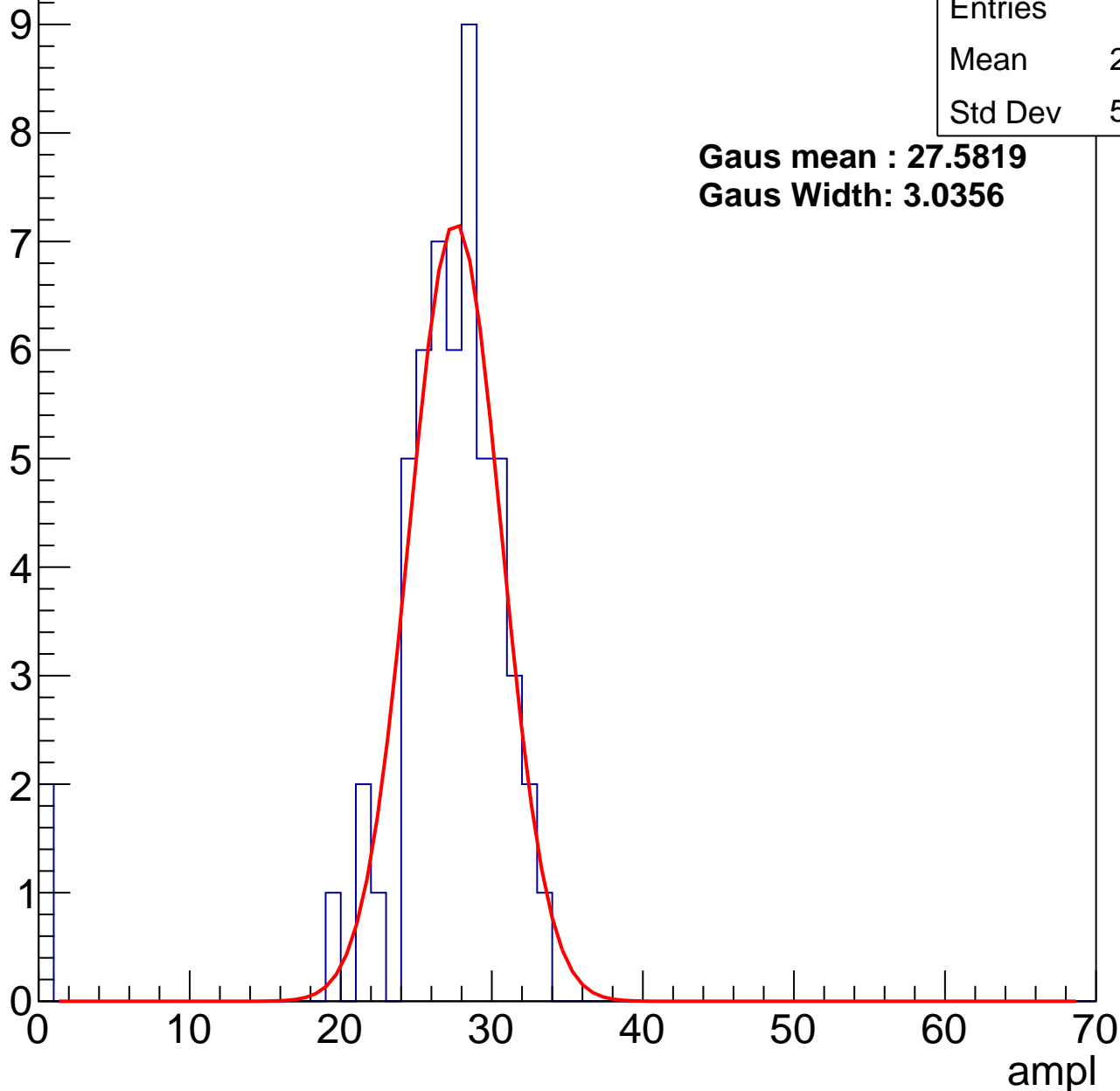
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	26.07
Std Dev	5.809

**Gaus mean : 27.5819**

**Gaus Width: 3.0356**



# B1L002S, U3-ch98, adc1

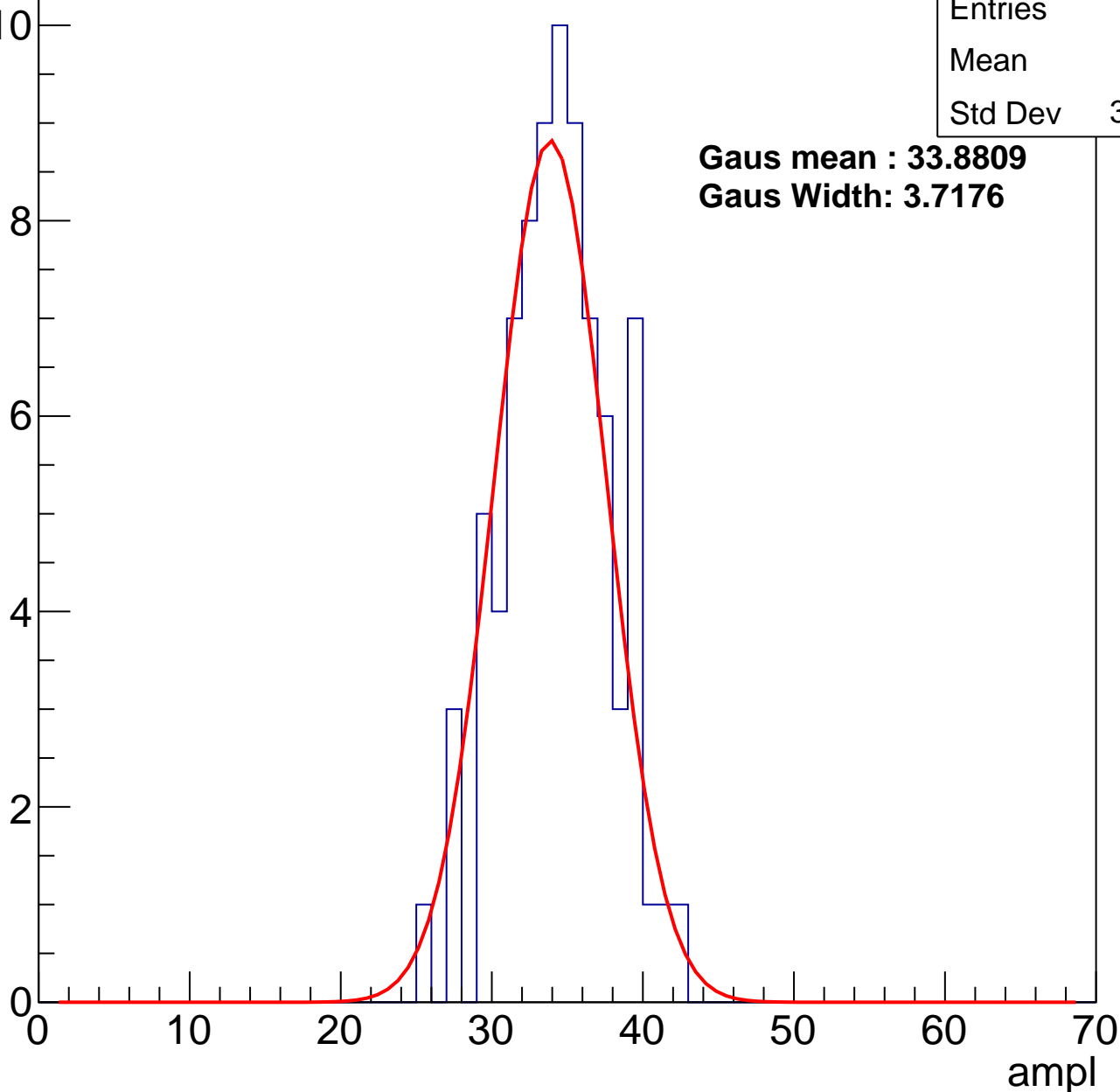
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	82
Mean	33.9
Std Dev	3.463

**Gaus mean : 33.8809**

**Gaus Width: 3.7176**



# B1L002S, U3-ch98, adc2

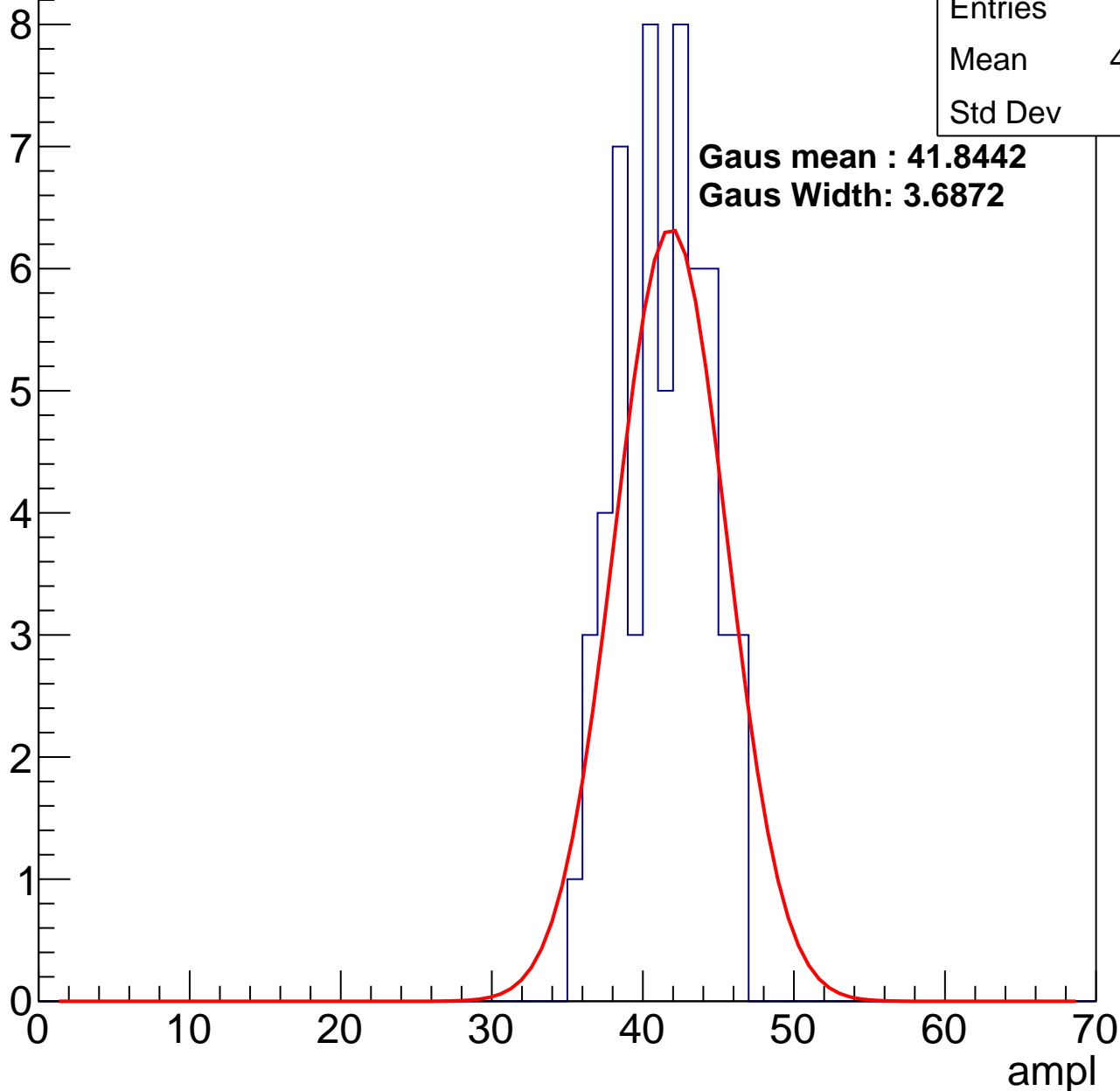
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.88
Std Dev	2.86

**Gaus mean : 41.8442**

**Gaus Width: 3.6872**

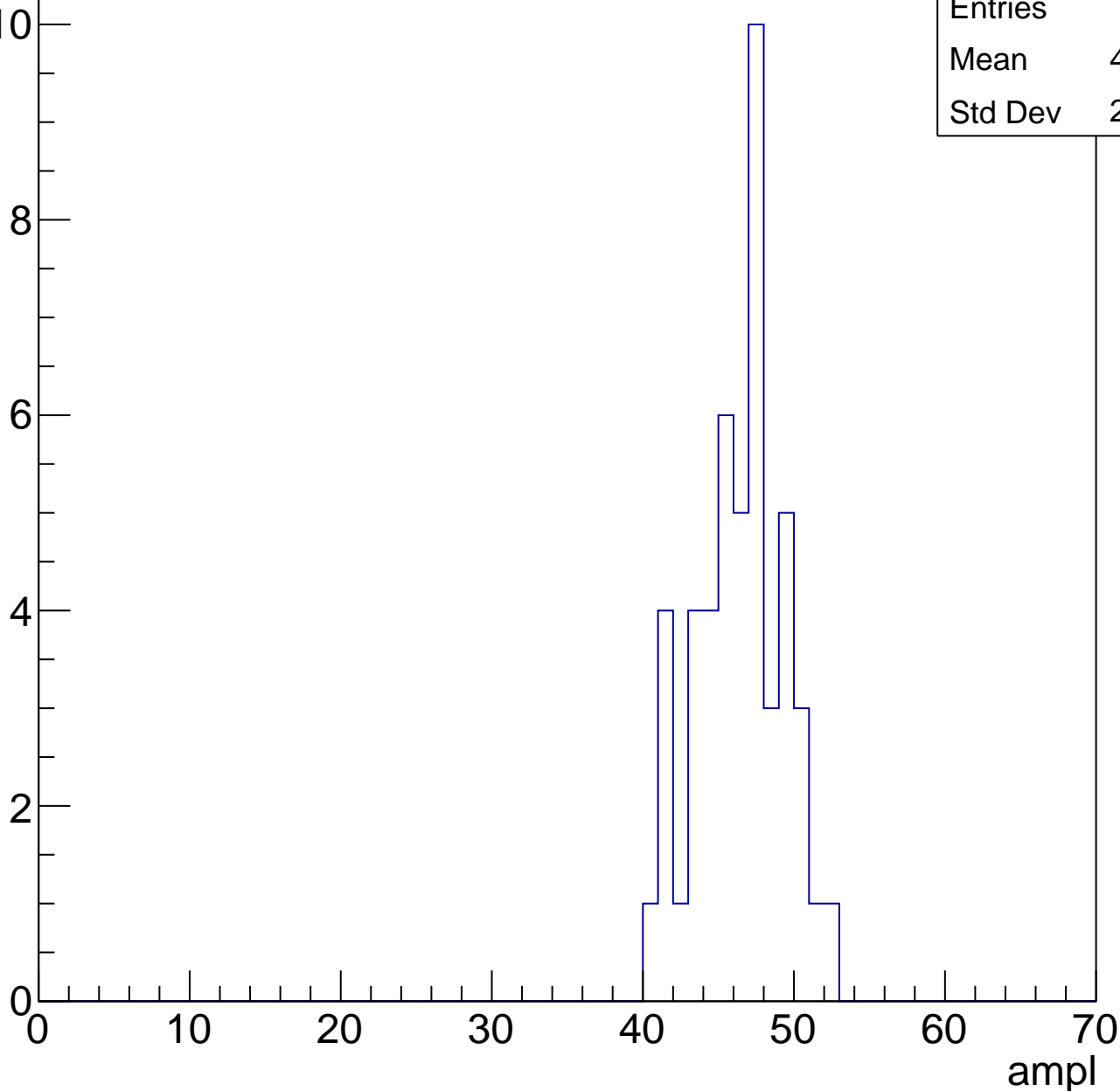


# B1L002S, U3-ch98, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	45.96
Std Dev	2.835

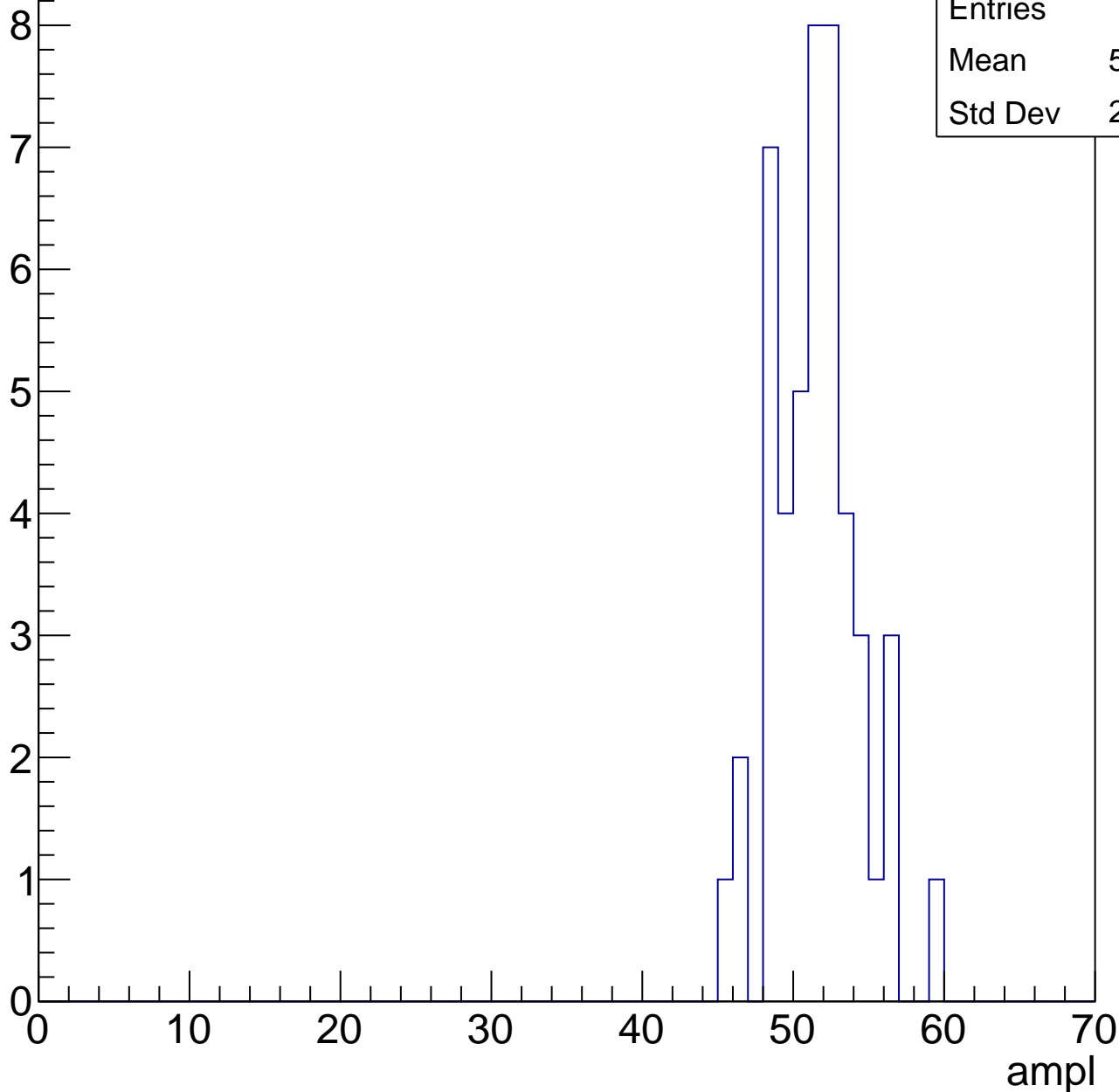


# B1L002S, U3-ch98, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

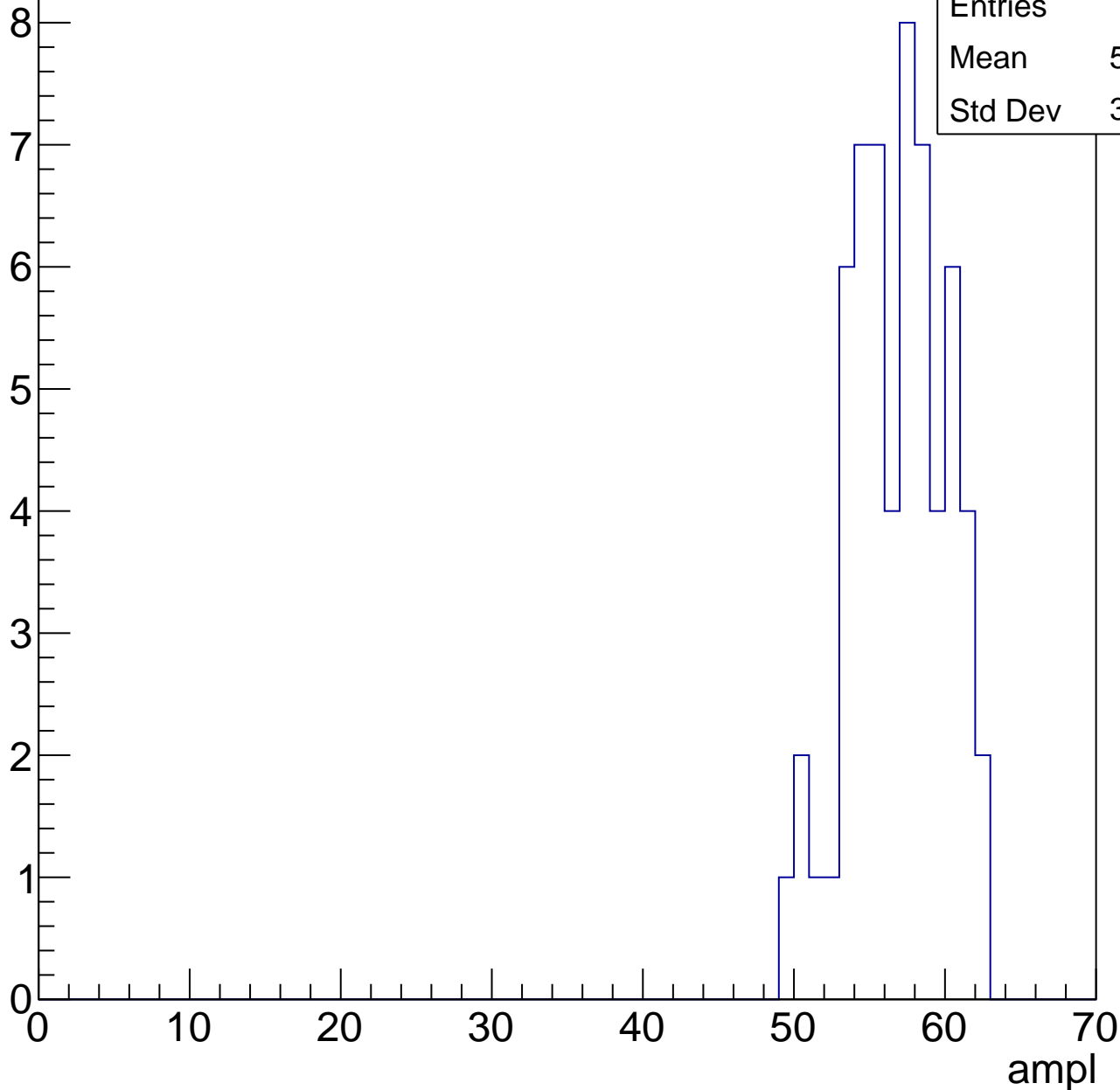
Entries	47
Mean	51.04
Std Dev	2.828



# B1L002S, U3-ch98, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

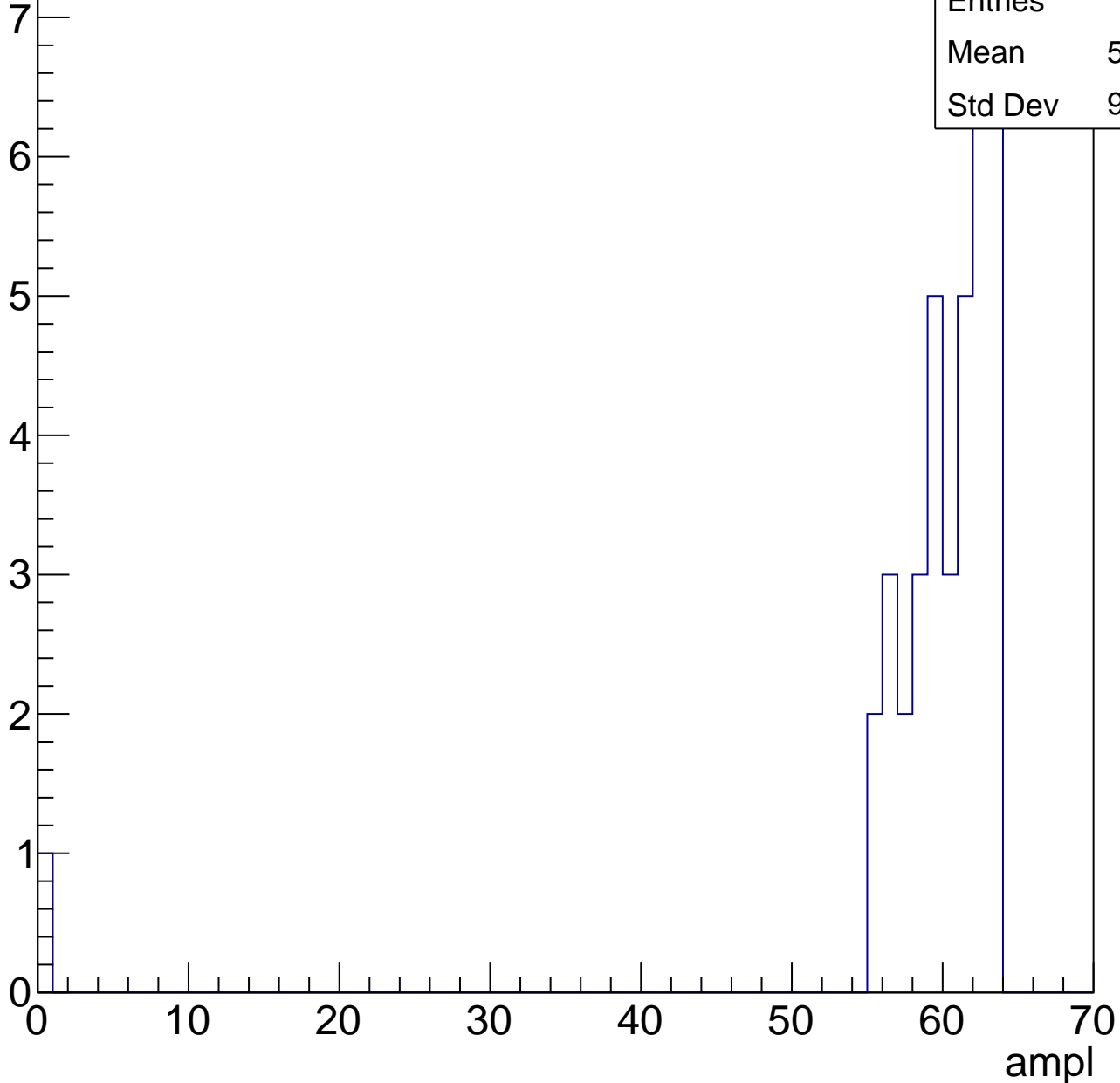


# B1L002S, U3-ch98, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	38
Mean	58.45
Std Dev	9.917

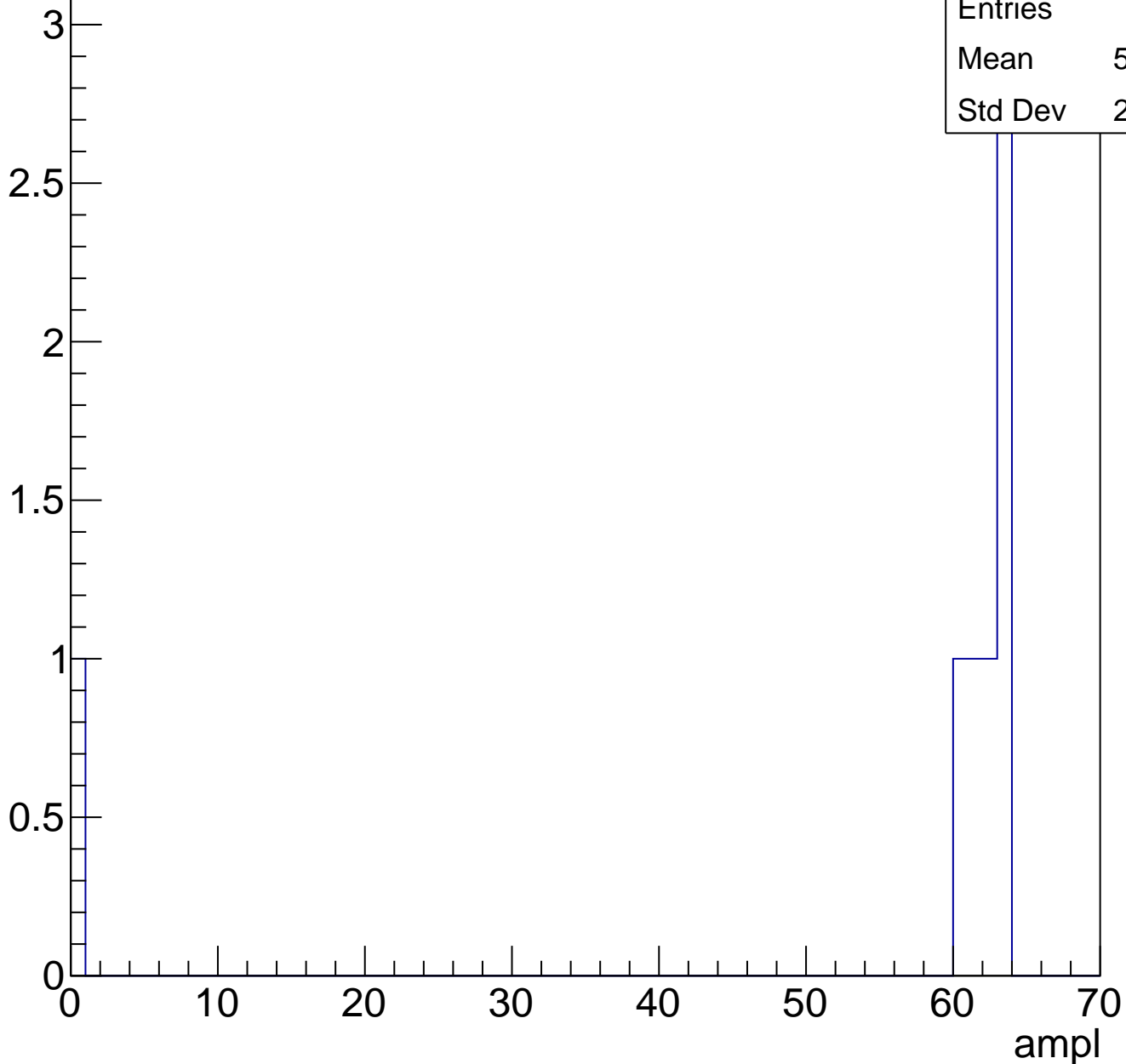




# B1L002S, U3-ch98, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch99, adc0

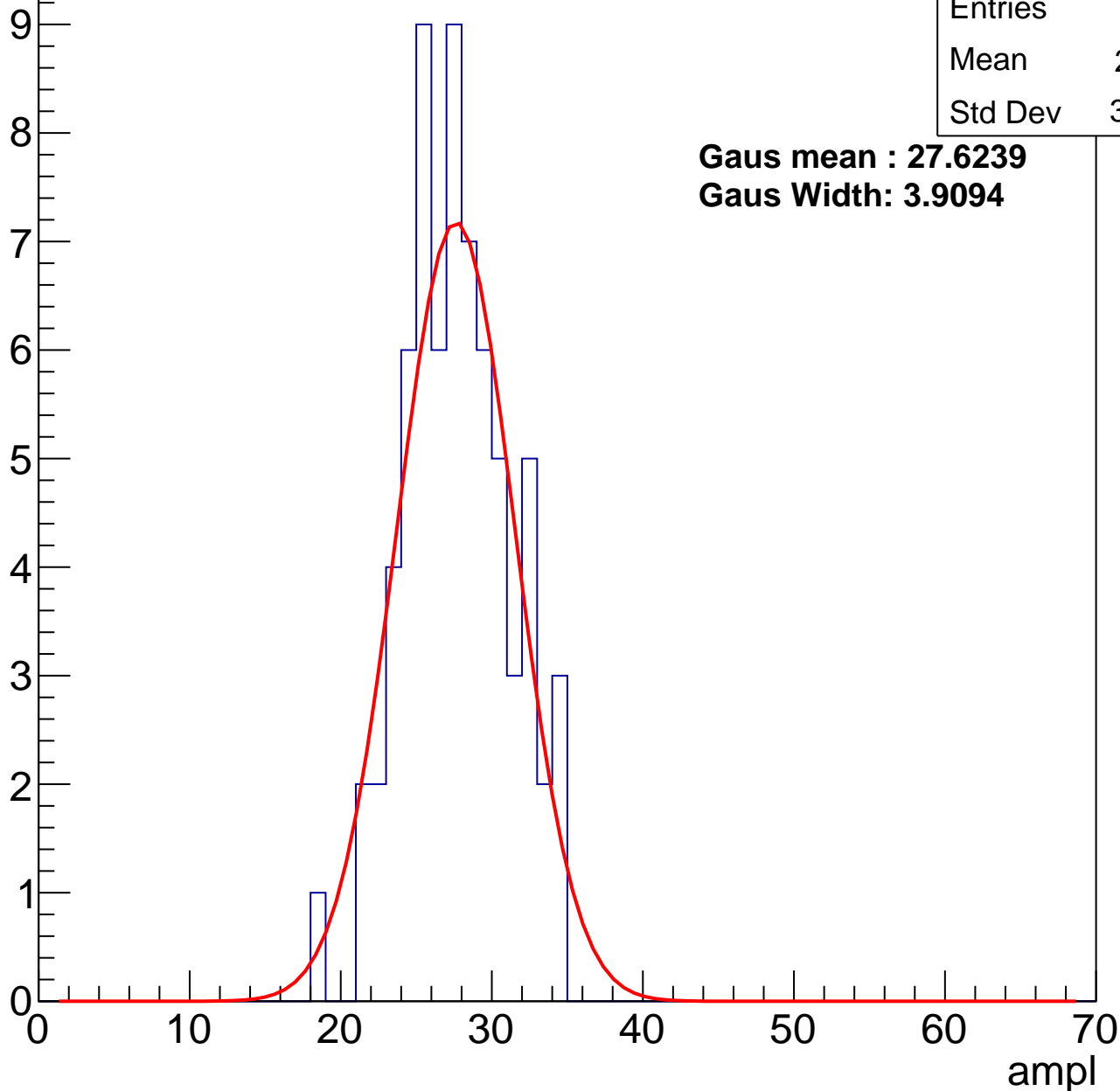
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	27.21
Std Dev	3.439

**Gaus mean : 27.6239**

**Gaus Width: 3.9094**



# B1L002S, U3-ch99, adc1

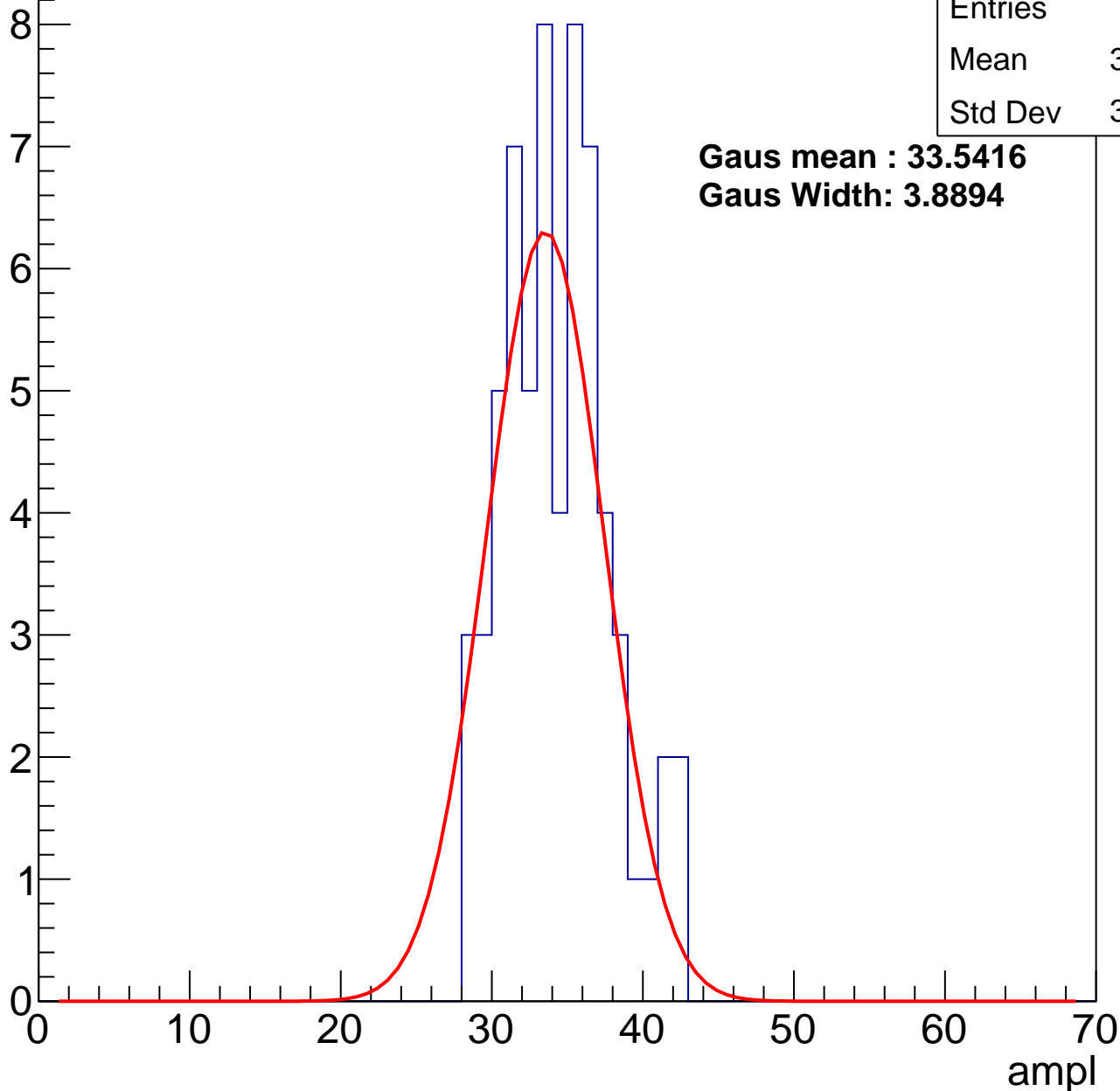
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	33.92
Std Dev	3.475

**Gaus mean : 33.5416**

**Gaus Width: 3.8894**



# B1L002S, U3-ch99, adc2

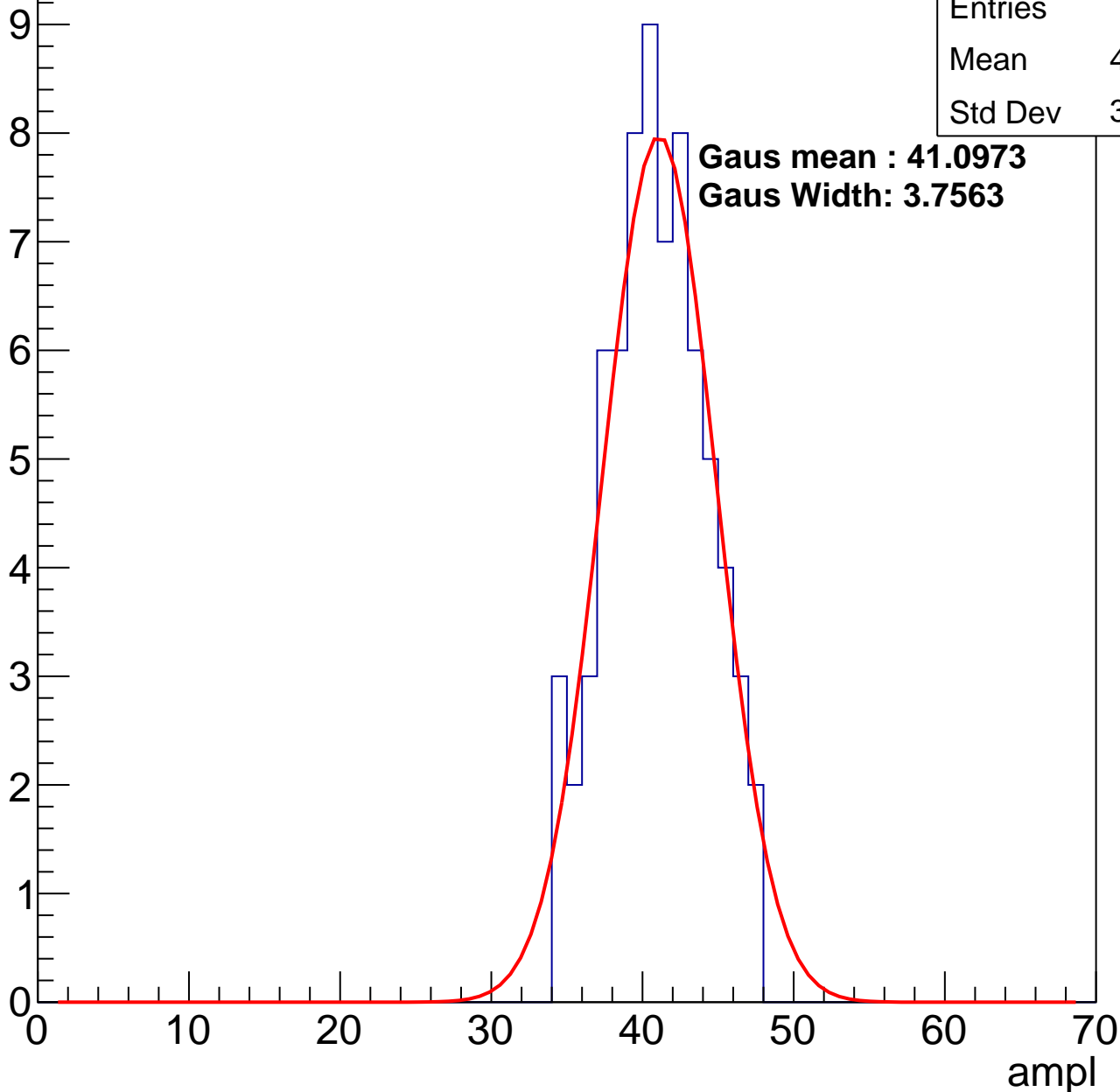
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	40.49
Std Dev	3.236

**Gaus mean : 41.0973**

**Gaus Width: 3.7563**

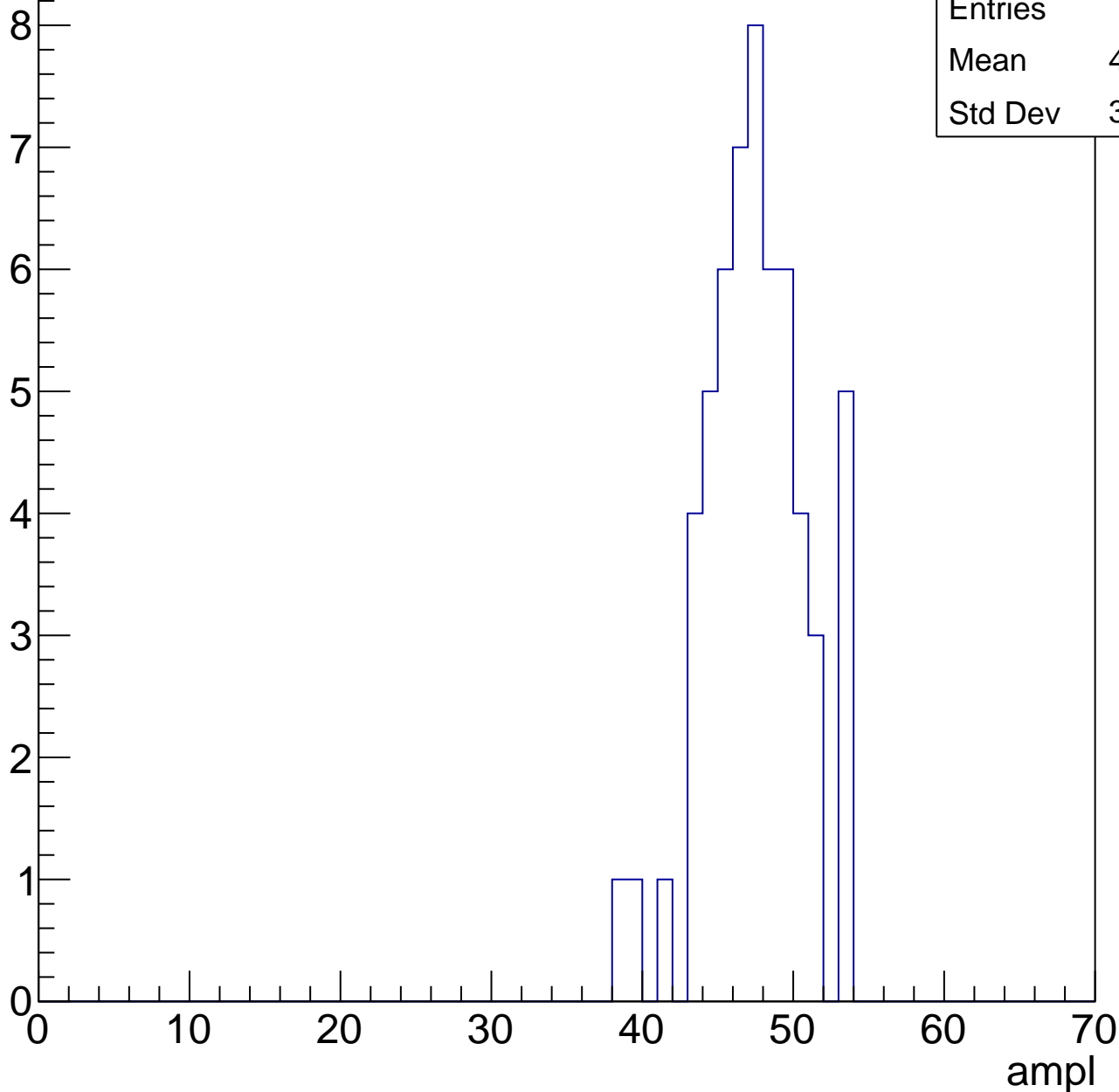


# B1L002S, U3-ch99, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

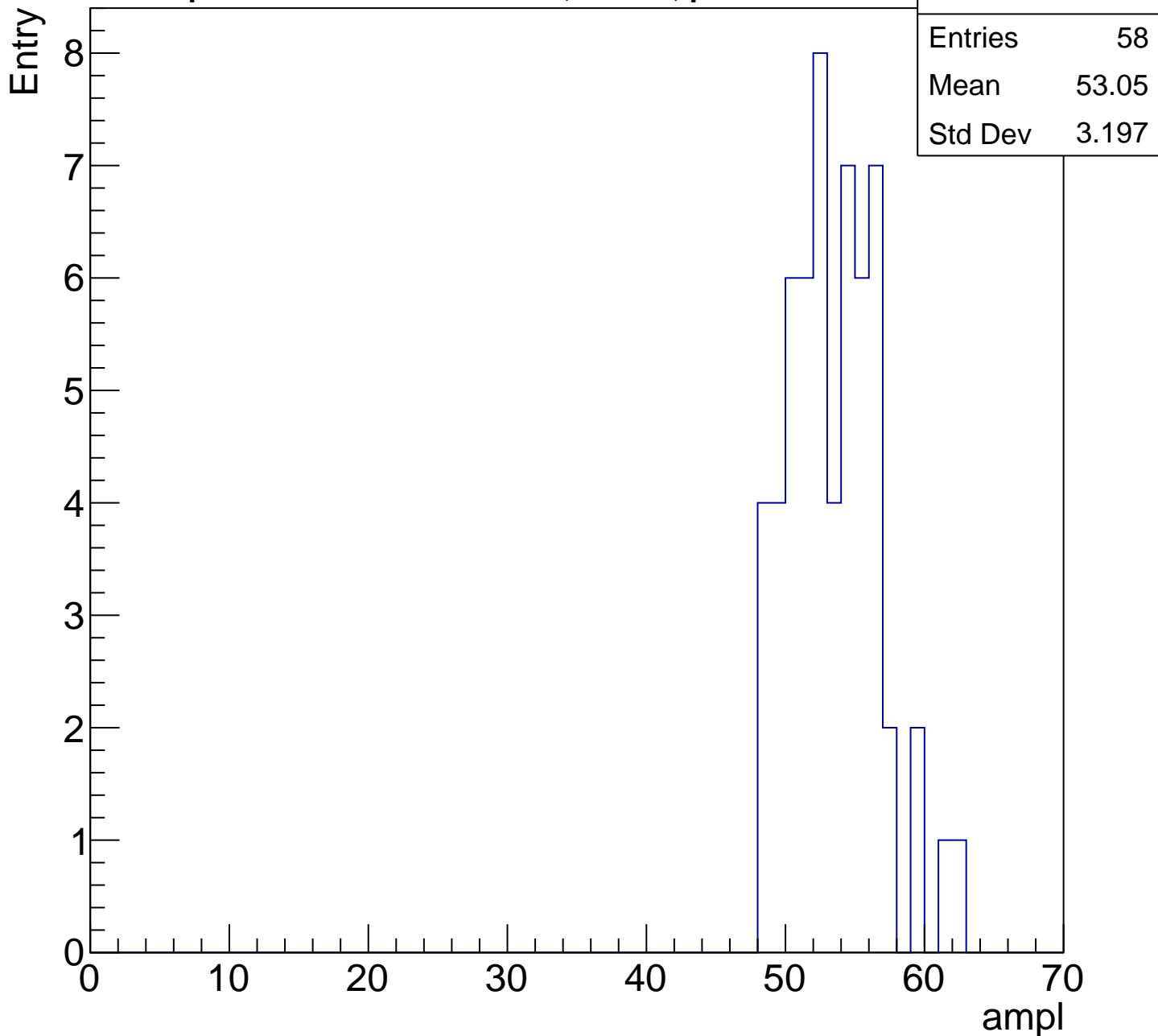
Entry

Entries	57
Mean	46.98
Std Dev	3.285



# B1L002S, U3-ch99, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

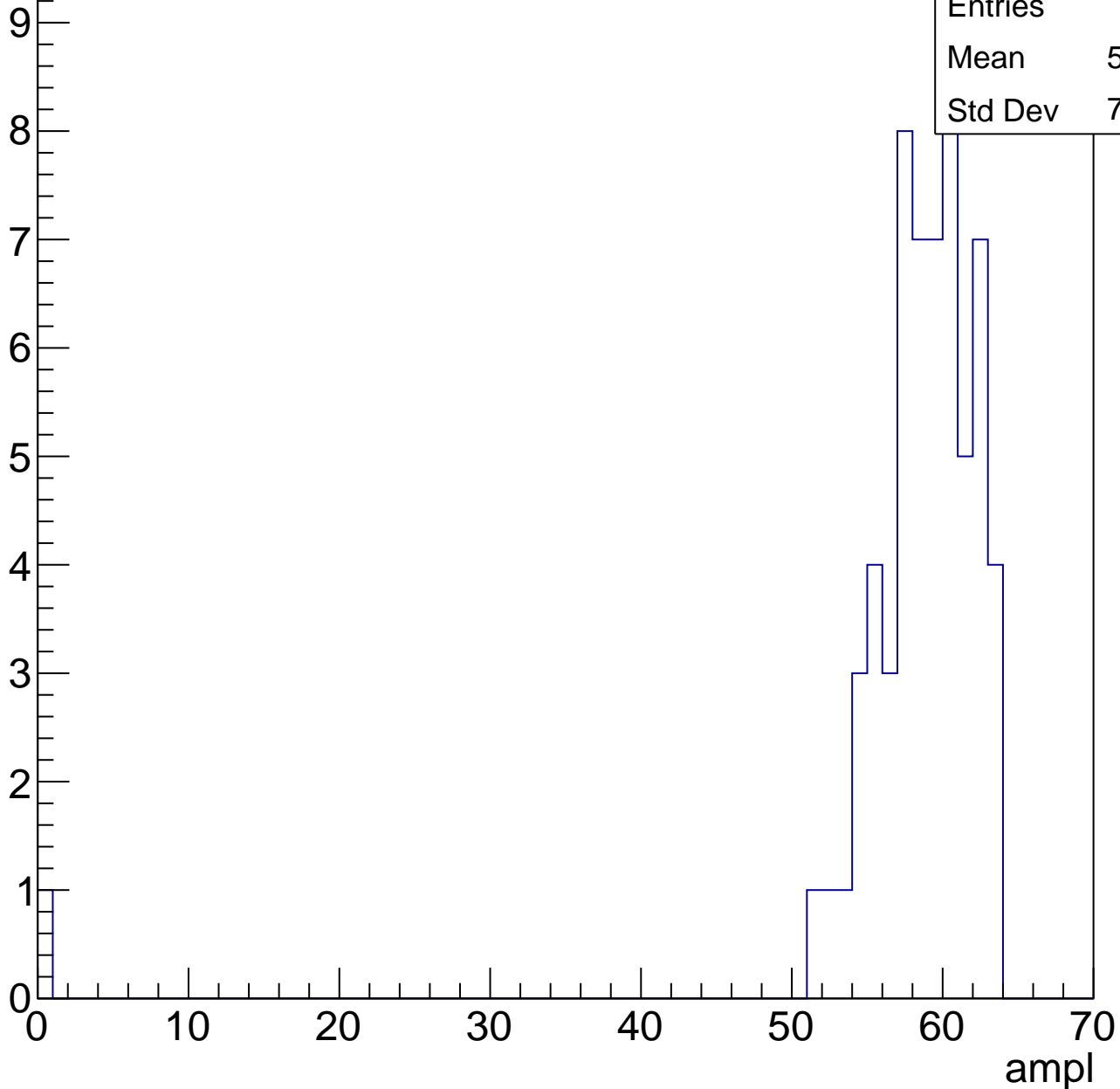


# B1L002S, U3-ch99, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

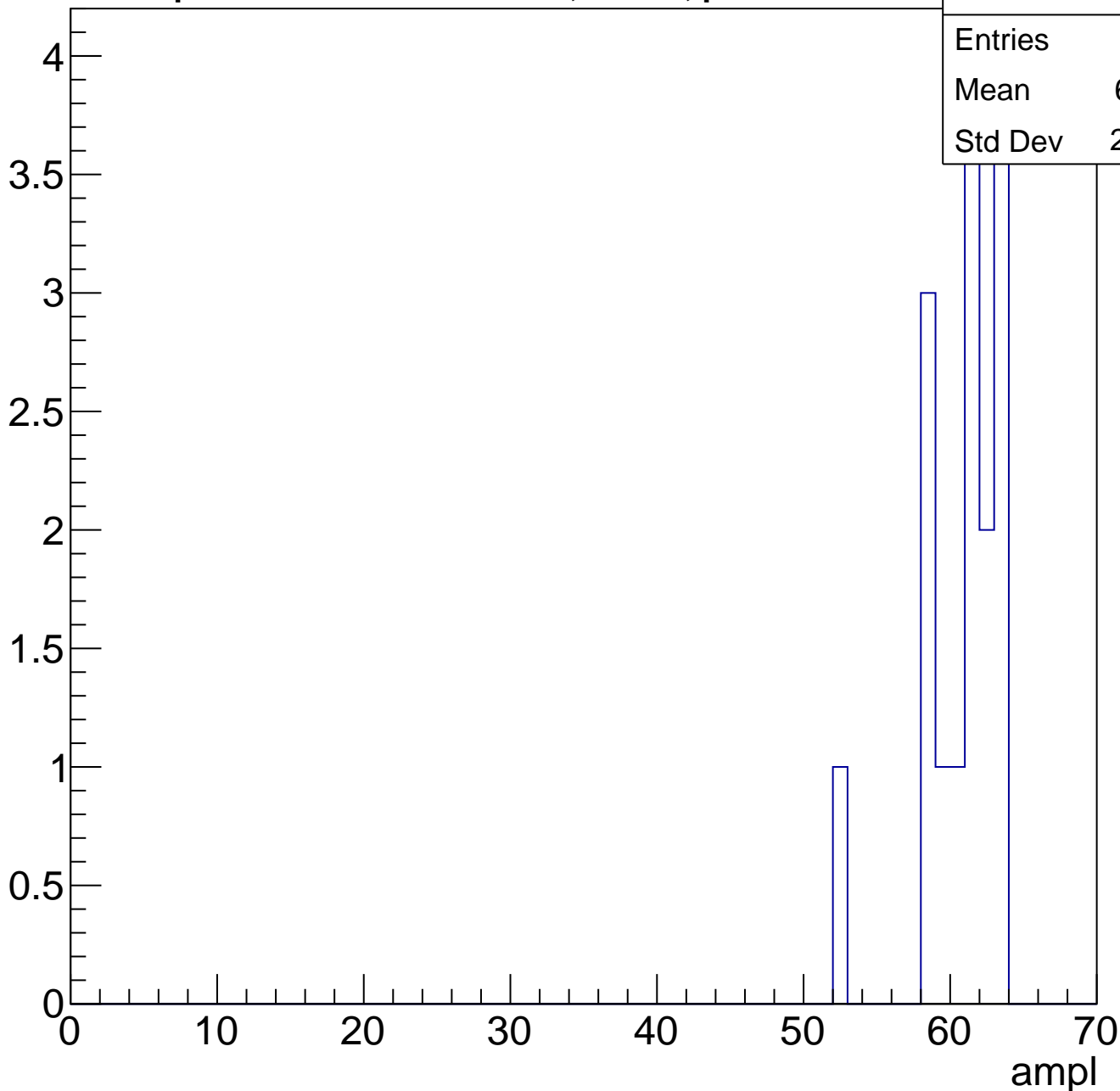
Entries	61
Mean	57.57
Std Dev	7.962



# B1L002S, U3-ch99, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	16
Mean	60.31
Std Dev	2.778



# B1L002S, U3-ch99, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch100, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	74
Mean	26.45
Std Dev	3.235

**Gaus mean : 26.7852**

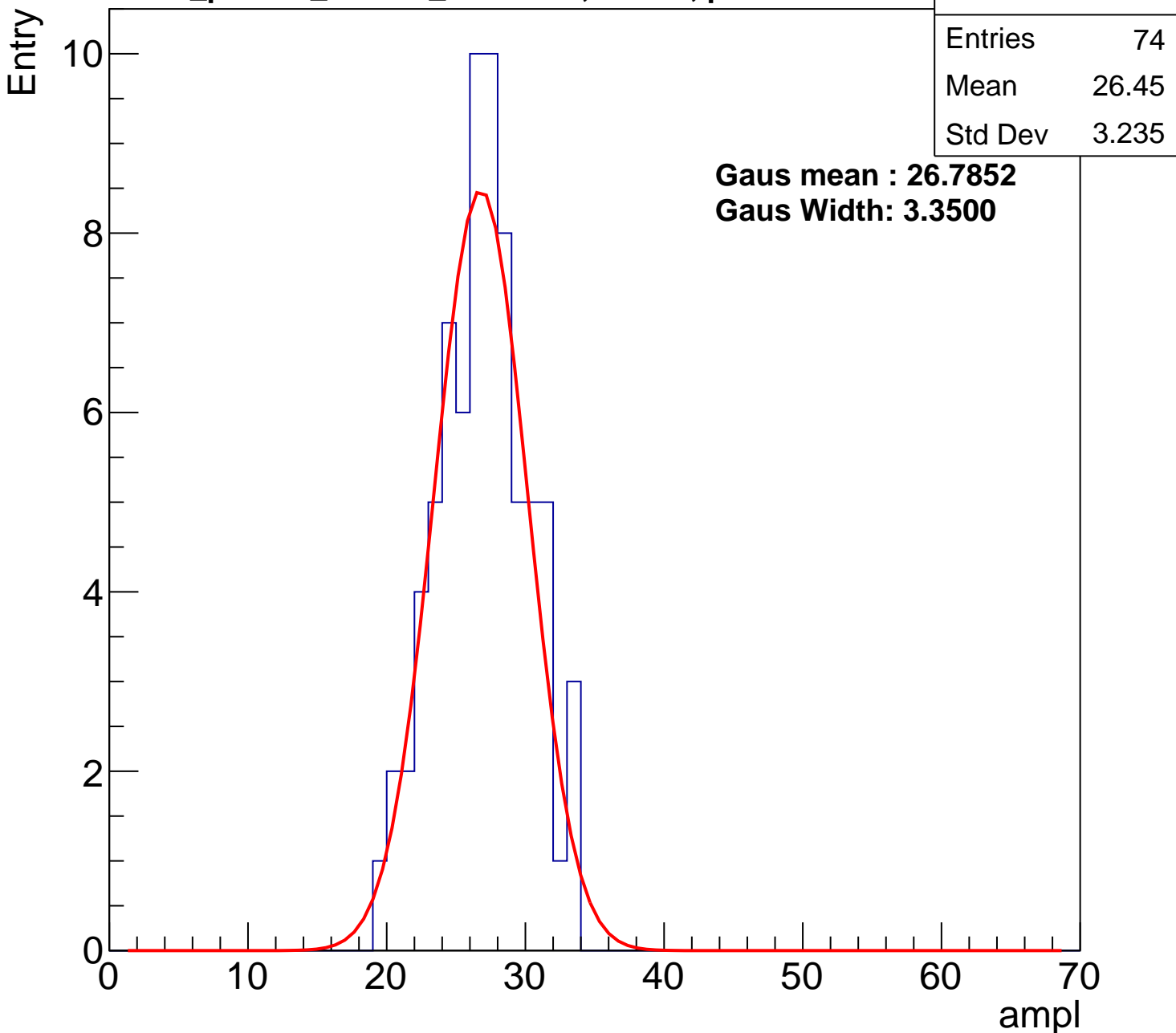
**Gaus Width: 3.3500**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch100, adc1

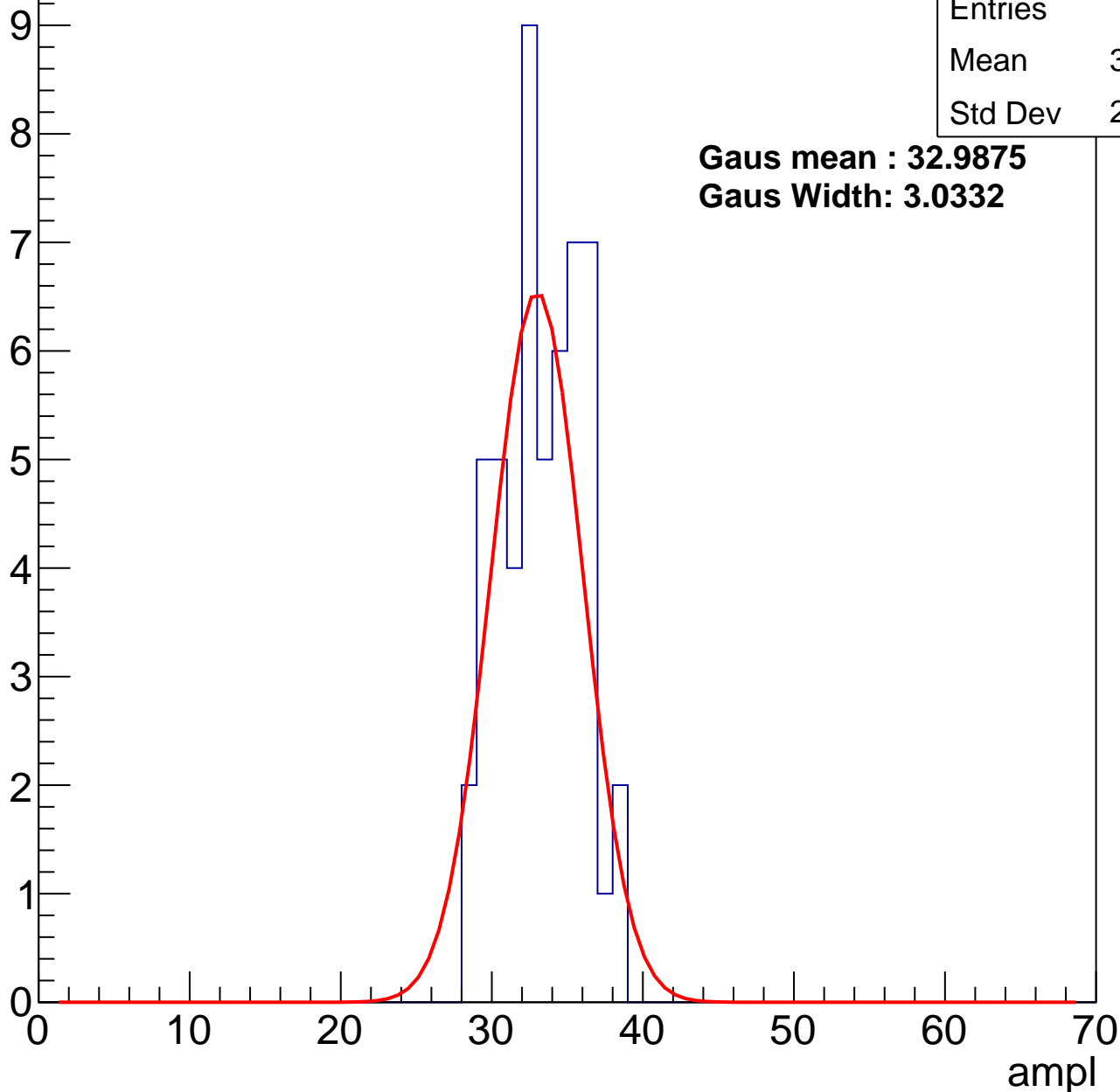
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	32.87
Std Dev	2.614

**Gaus mean : 32.9875**

**Gaus Width: 3.0332**



# B1L002S, U3-ch100, adc2

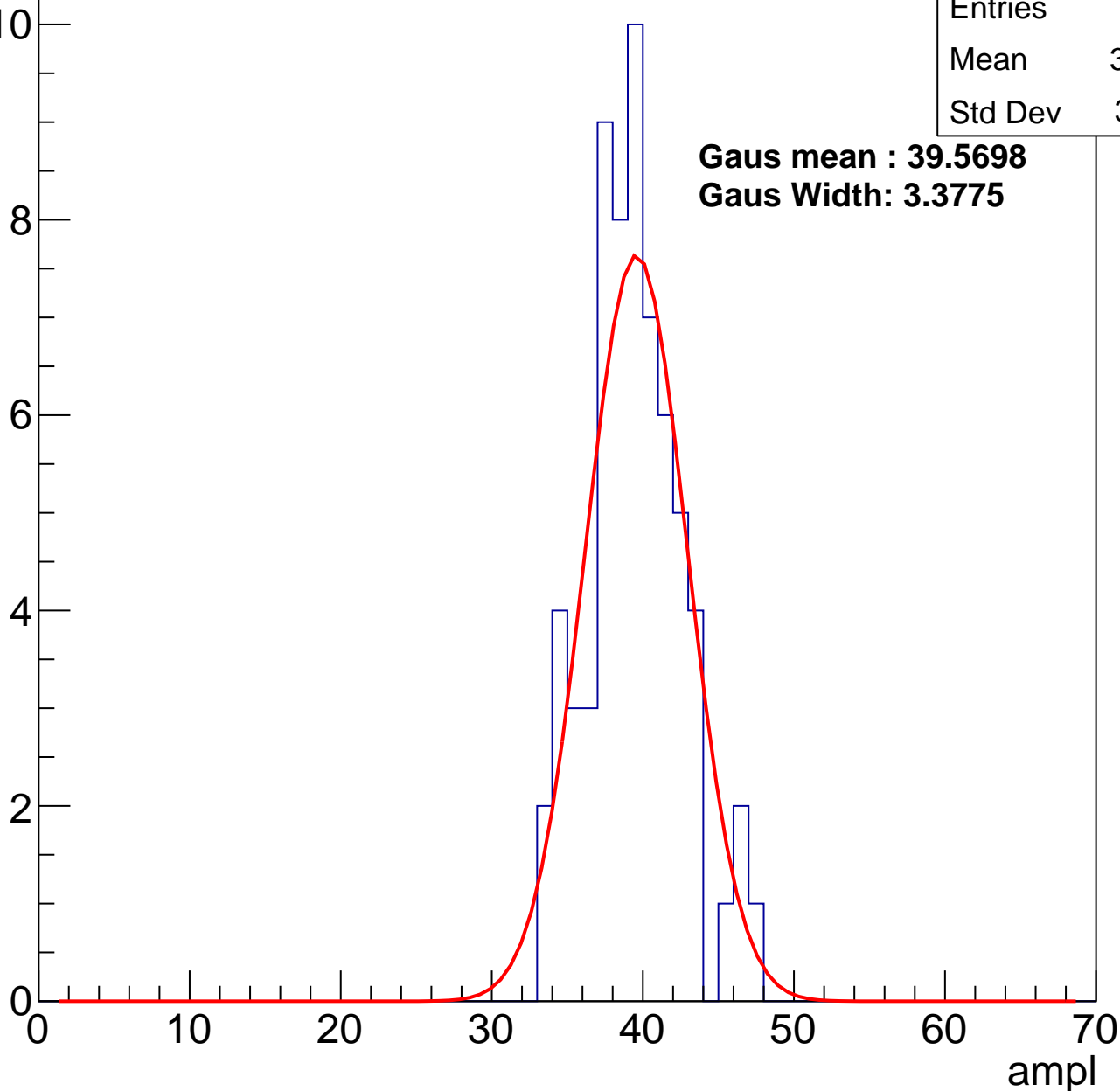
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	38.98
Std Dev	3.111

**Gaus mean : 39.5698**

**Gaus Width: 3.3775**

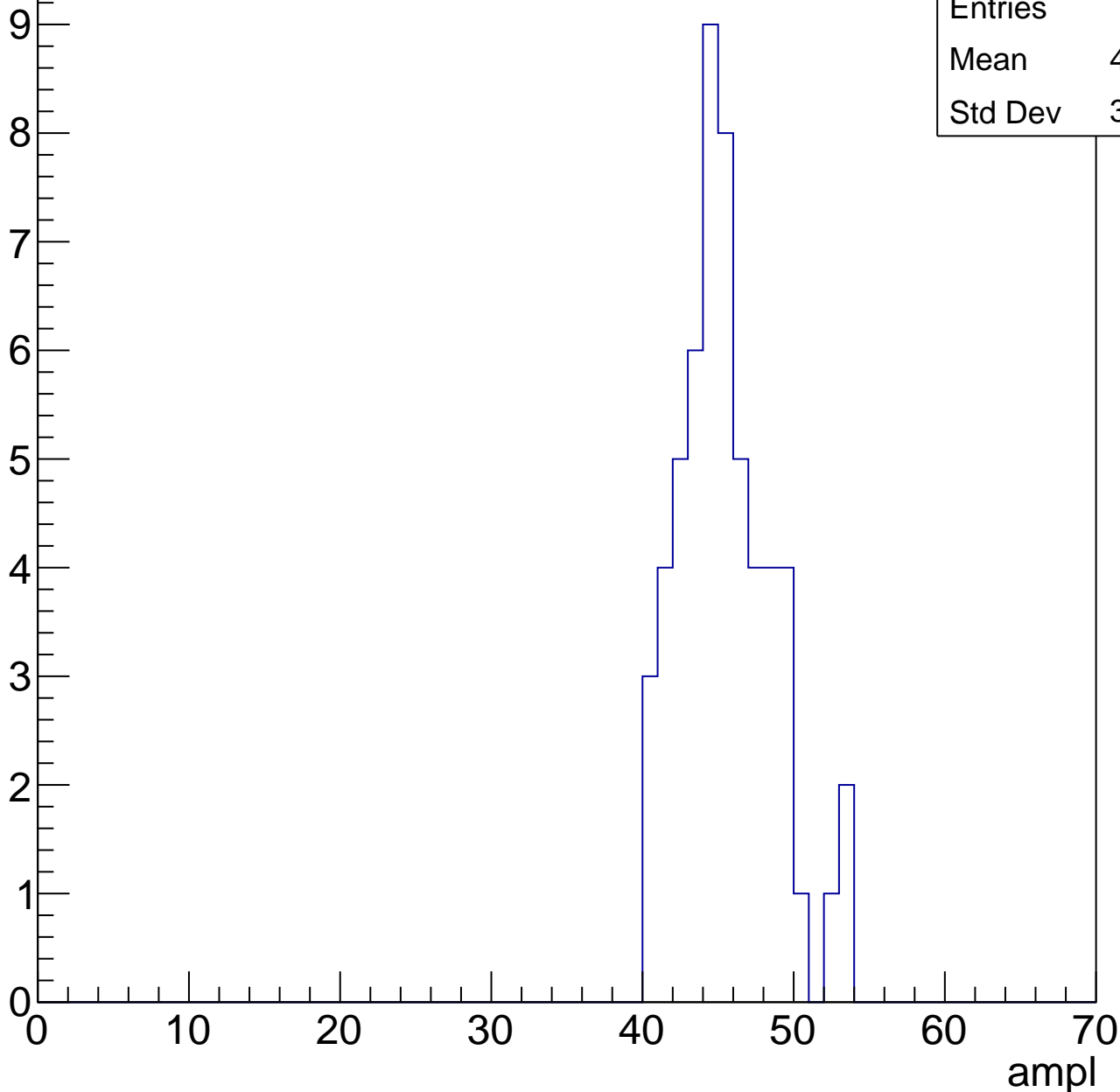


# B1L002S, U3-ch100, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	45.04
Std Dev	3.105



# B1L002S, U3-ch100, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

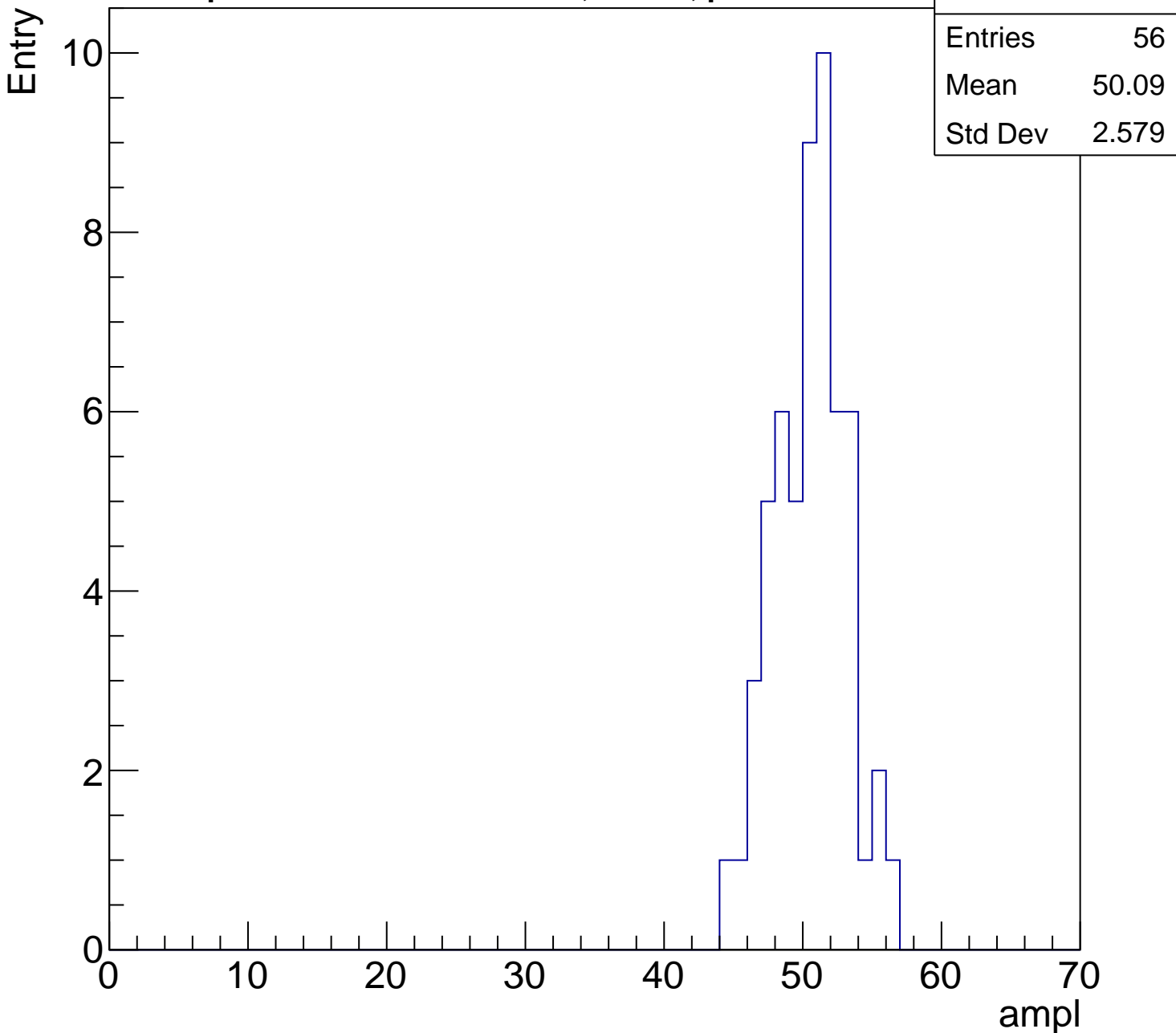
Entries	56
Mean	50.09
Std Dev	2.579

Entry

10  
8  
6  
4  
2  
0

0 10 20 30 40 50 60 70

ampl

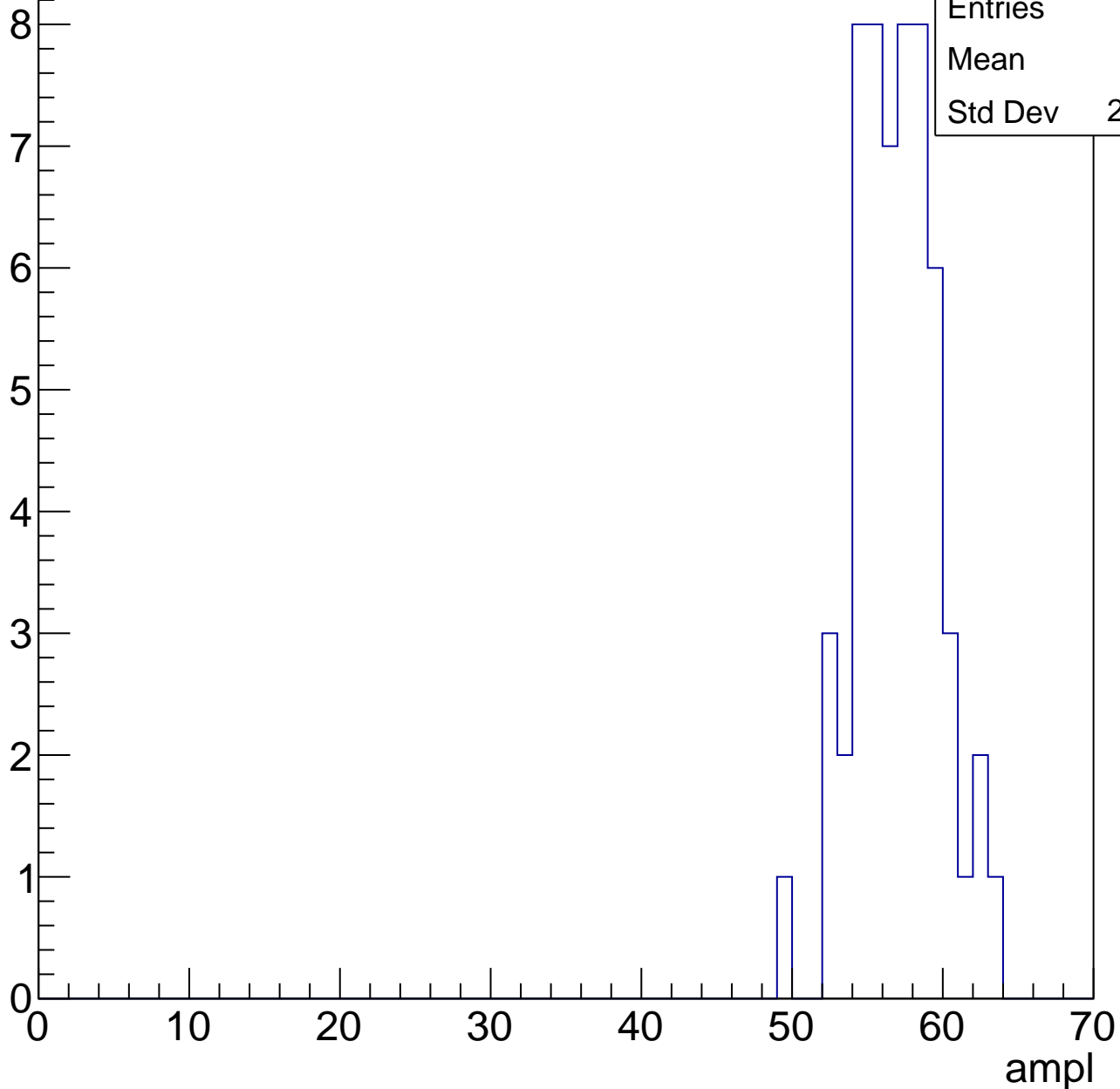


# B1L002S, U3-ch100, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	56.5
Std Dev	2.718

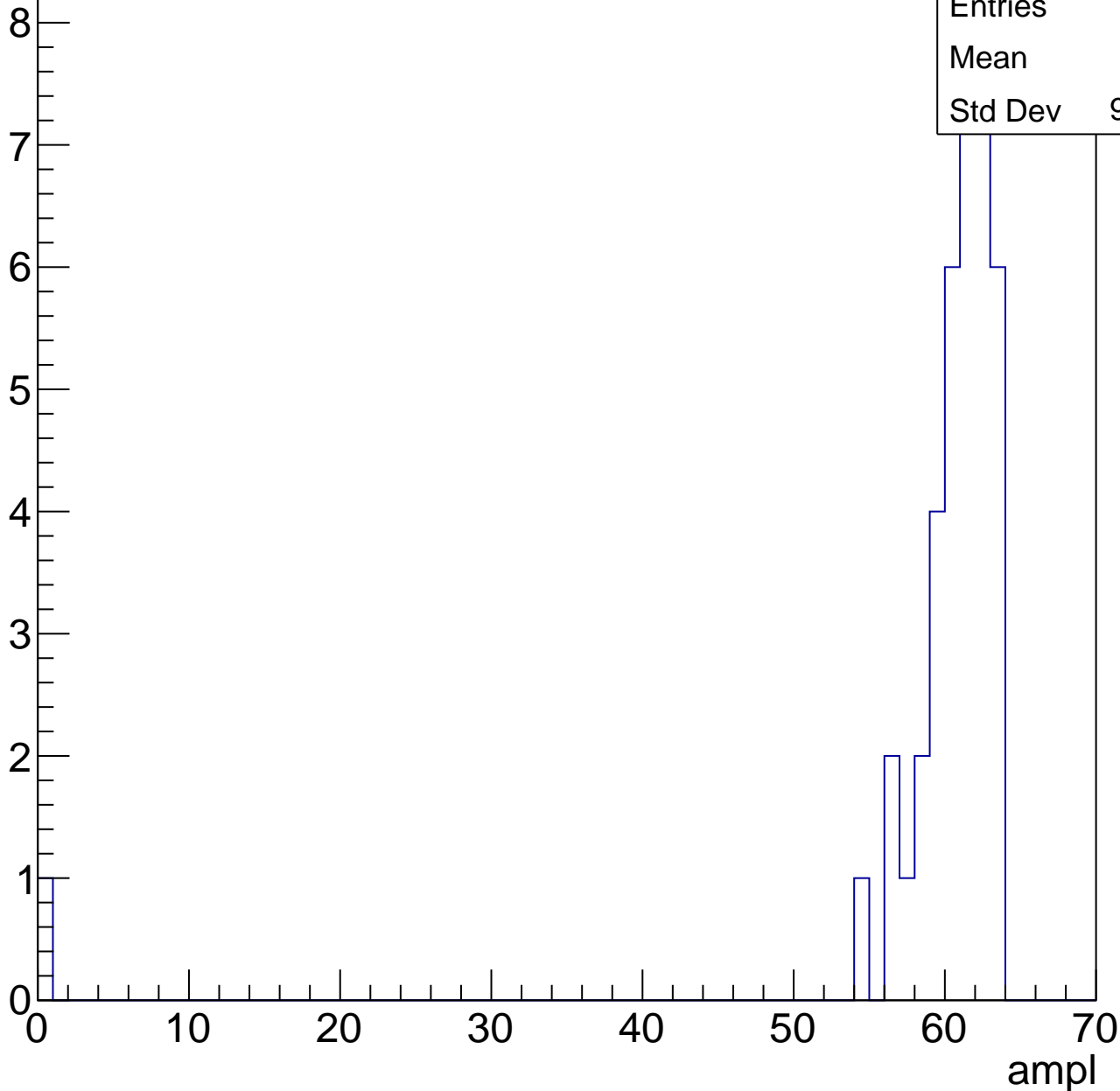


# B1L002S, U3-ch100, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	58.9
Std Dev	9.787

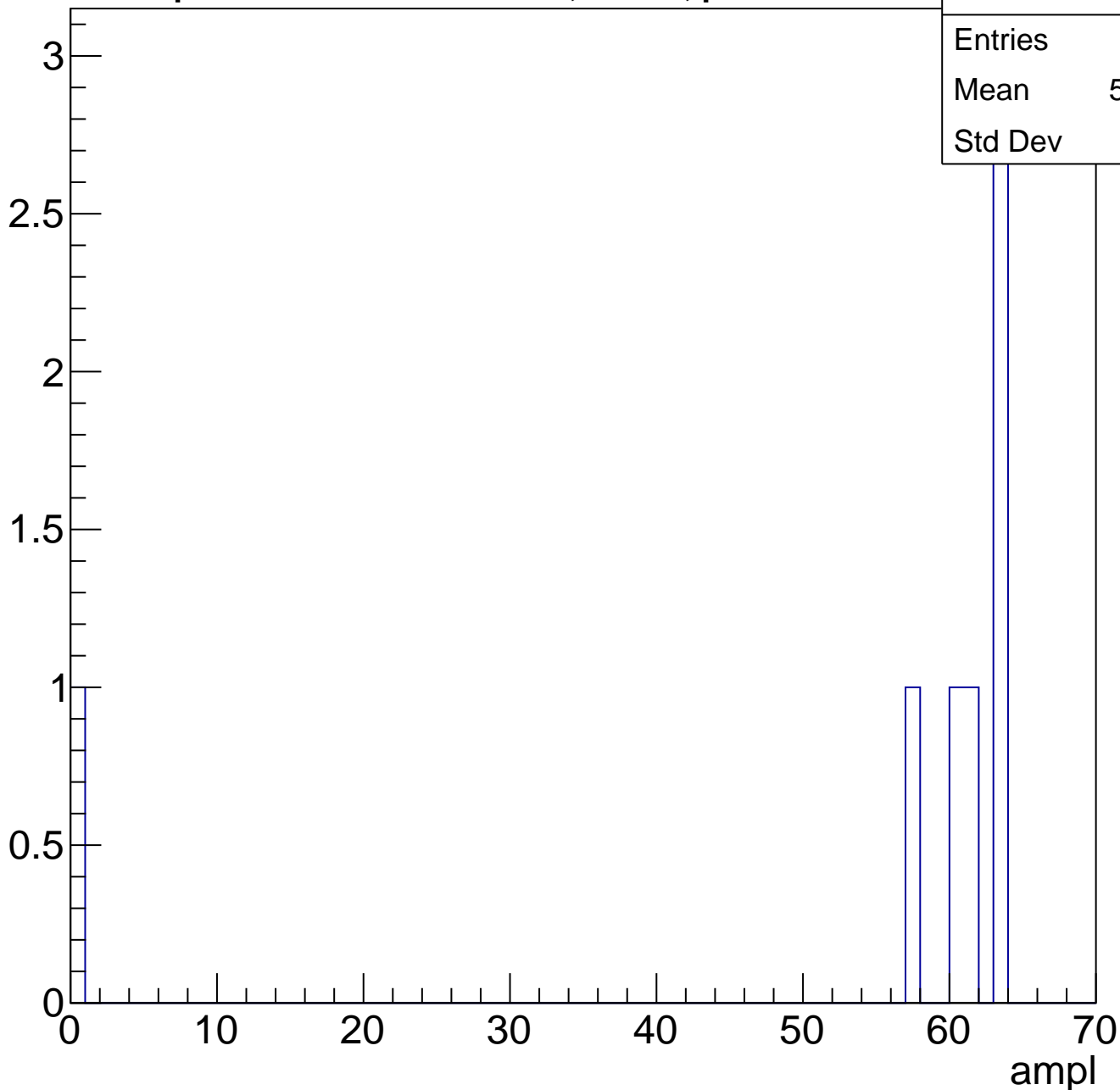




# B1L002S, U3-ch100, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch101, adc0

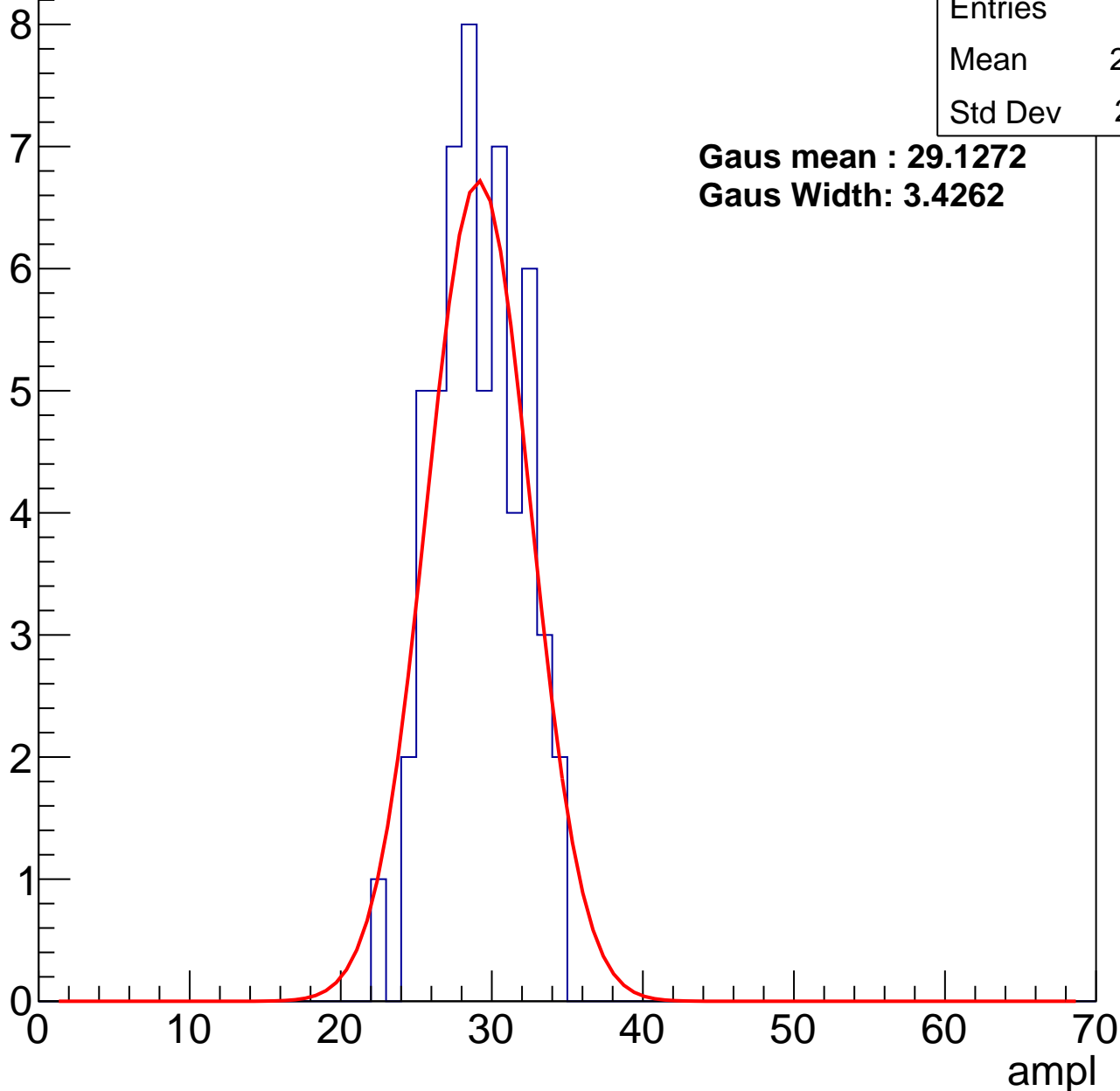
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	28.65
Std Dev	2.791

**Gaus mean : 29.1272**

**Gaus Width: 3.4262**



# B1L002S, U3-ch101, adc1

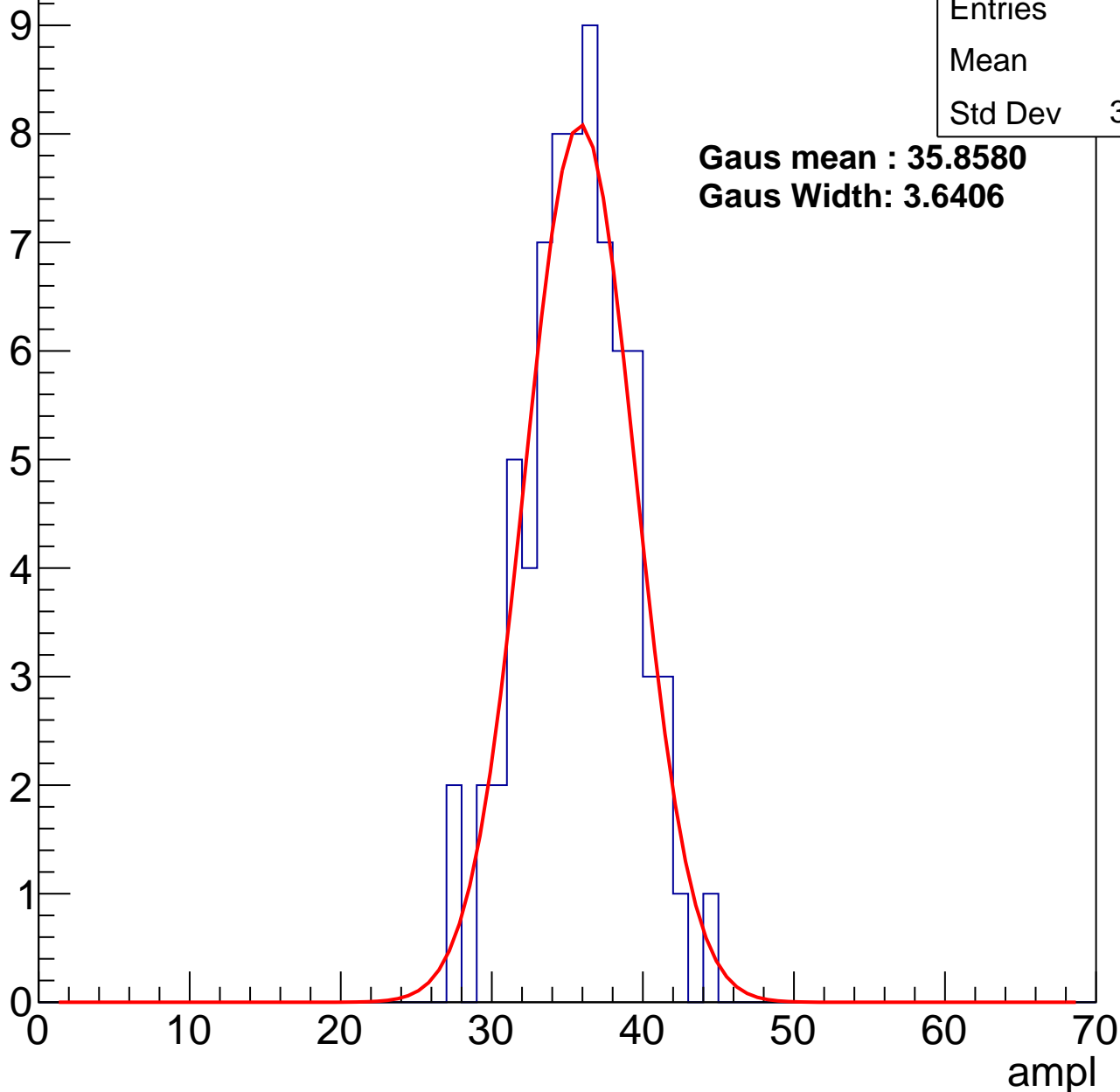
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	74
Mean	35.3
Std Dev	3.479

**Gaus mean : 35.8580**

**Gaus Width: 3.6406**



# B1L002S, U3-ch101, adc2

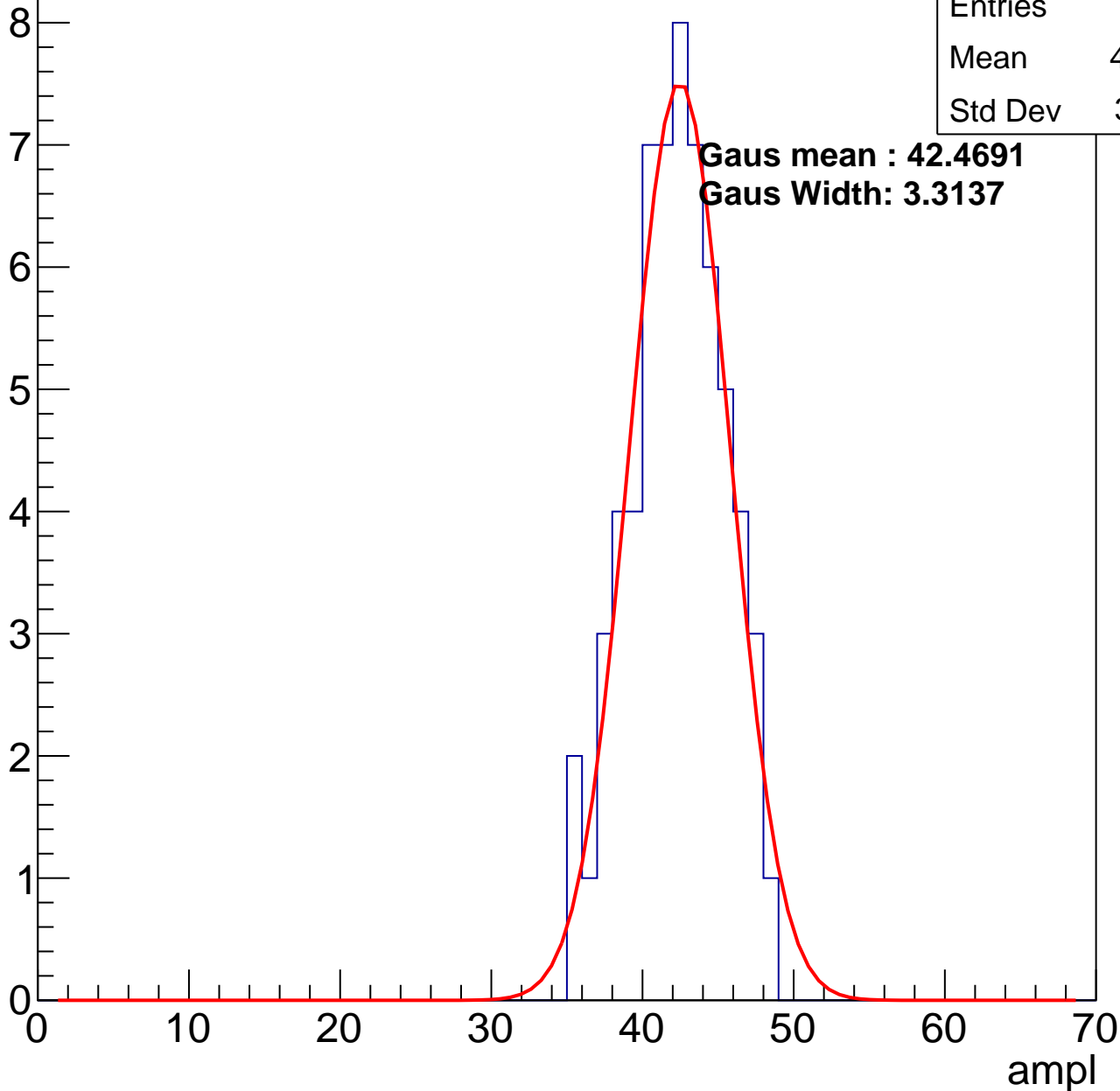
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.79
Std Dev	3.091

**Gaus mean : 42.4691**

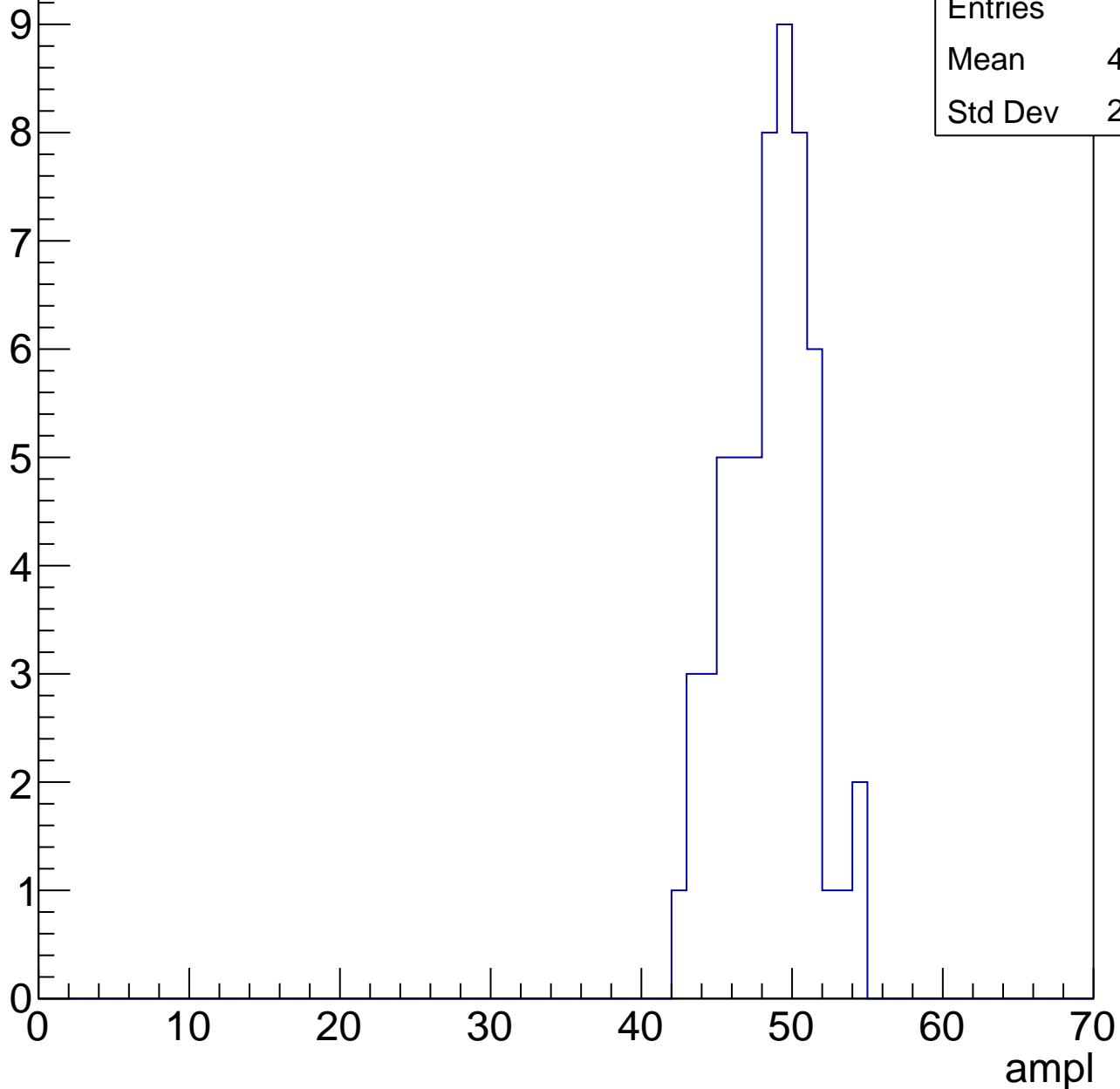
**Gaus Width: 3.3137**



# B1L002S, U3-ch101, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

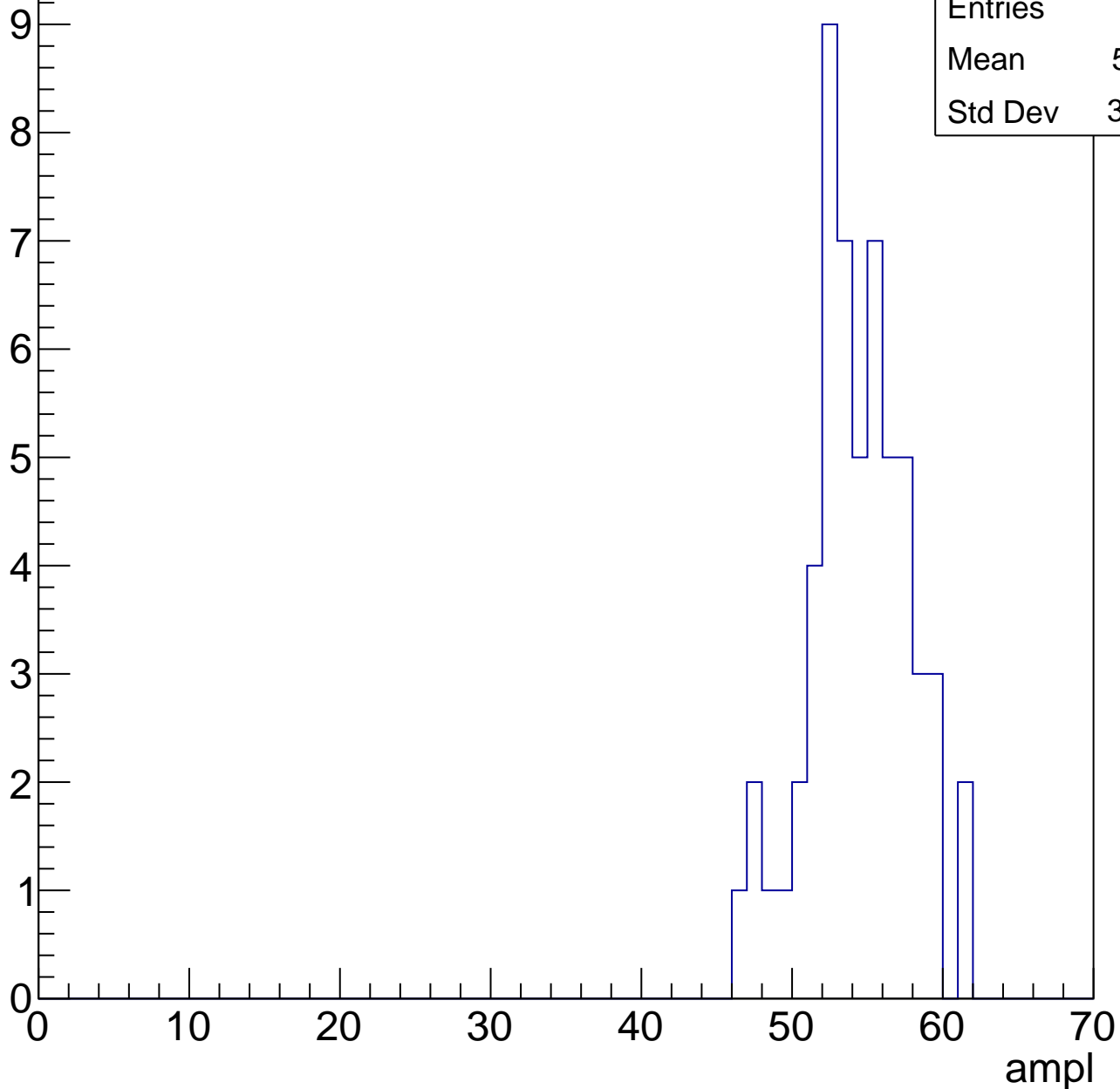


Entries	57
Mean	48.02
Std Dev	2.769

# B1L002S, U3-ch101, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	57
Mean	53.91
Std Dev	3.315

# B1L002S, U3-ch101, adc5

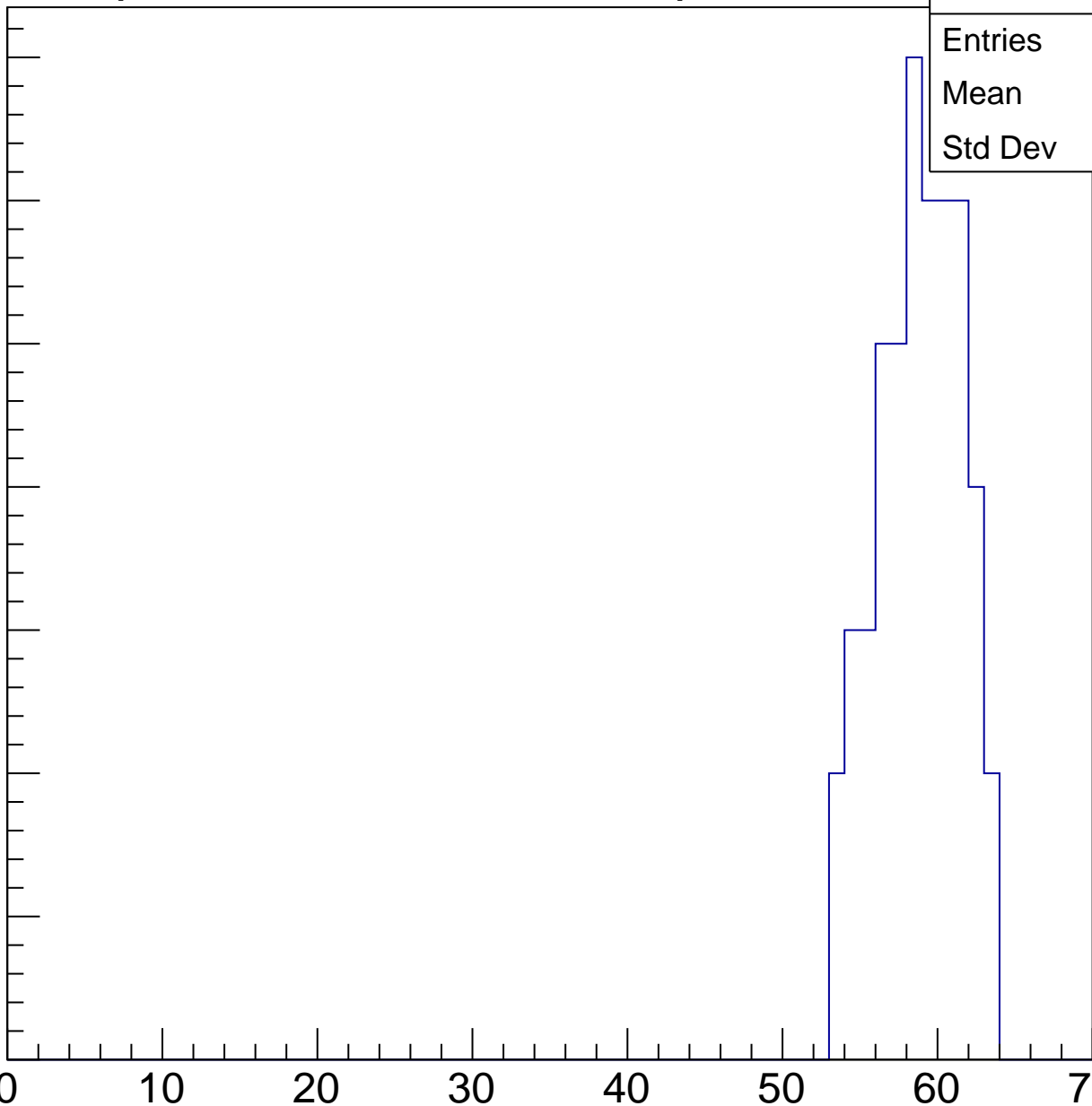
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	49
Mean	58.33
Std Dev	2.645

ampl

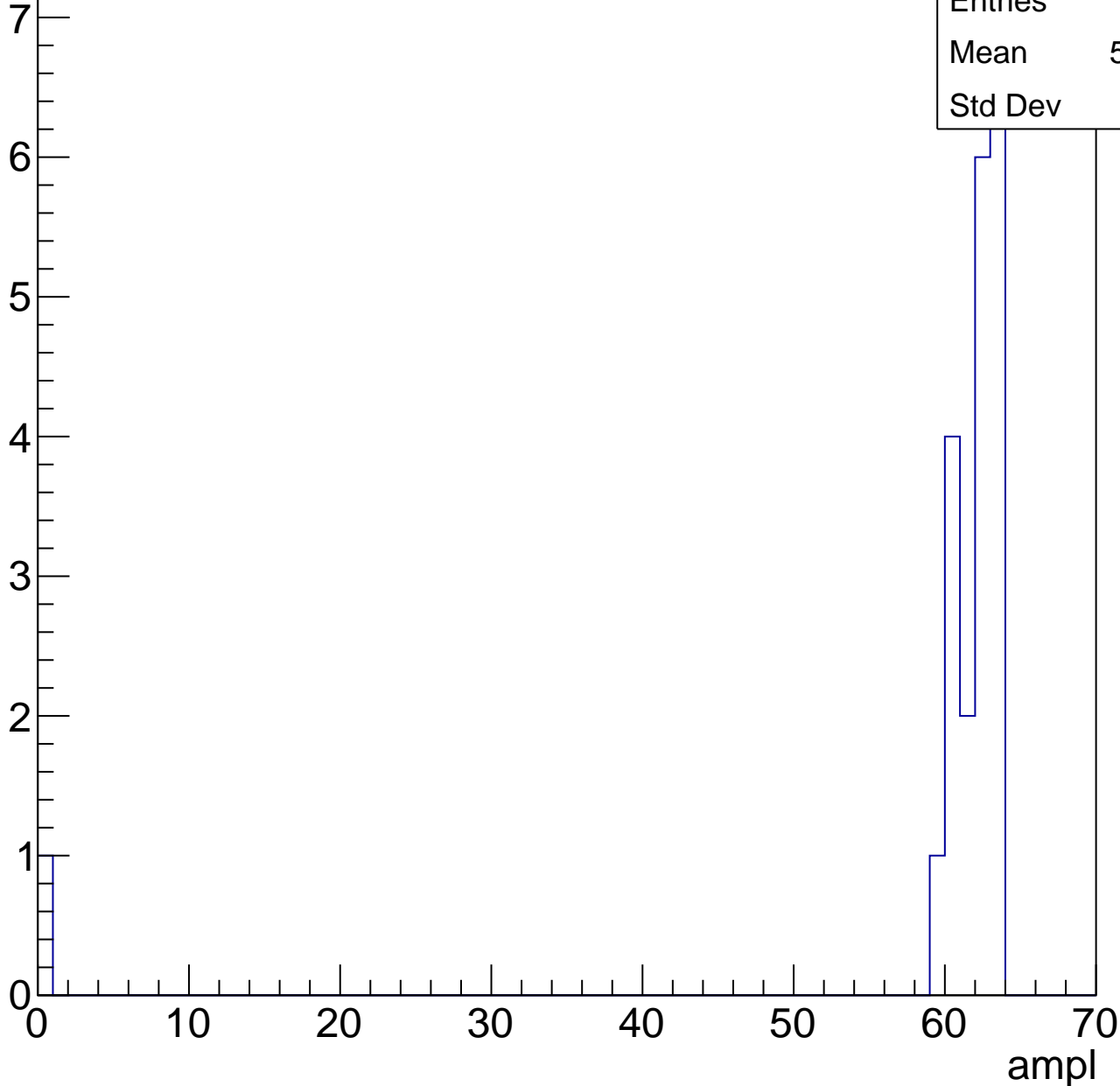


# B1L002S, U3-ch101, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	58.76
Std Dev	13.2

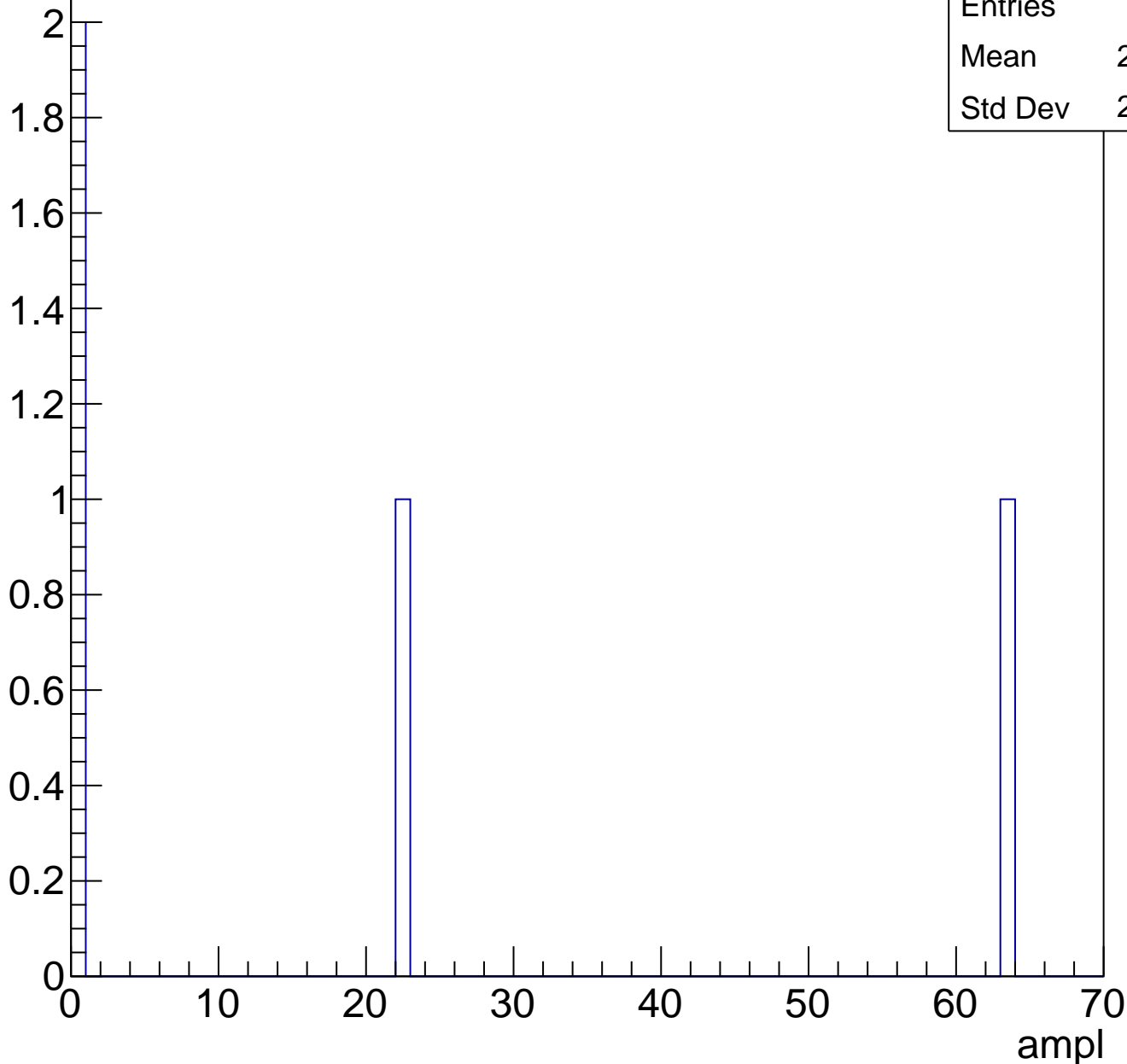




# B1L002S, U3-ch101, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

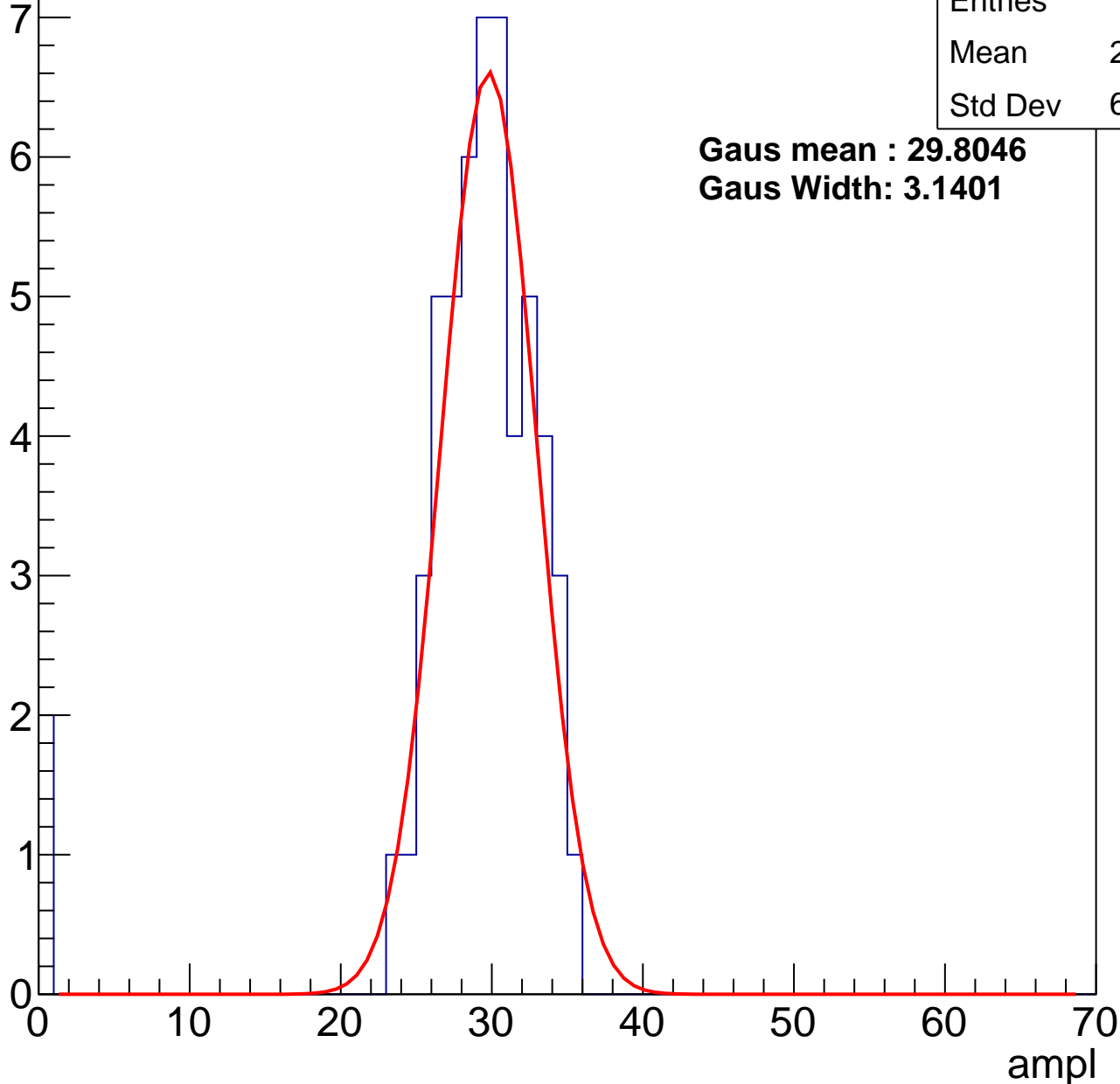


Entries	4
Mean	21.25
Std Dev	25.72

# B1L002S, U3-ch102, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch102, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	64
Mean	34.59
Std Dev	3.066

**Gaus mean : 35.3162**

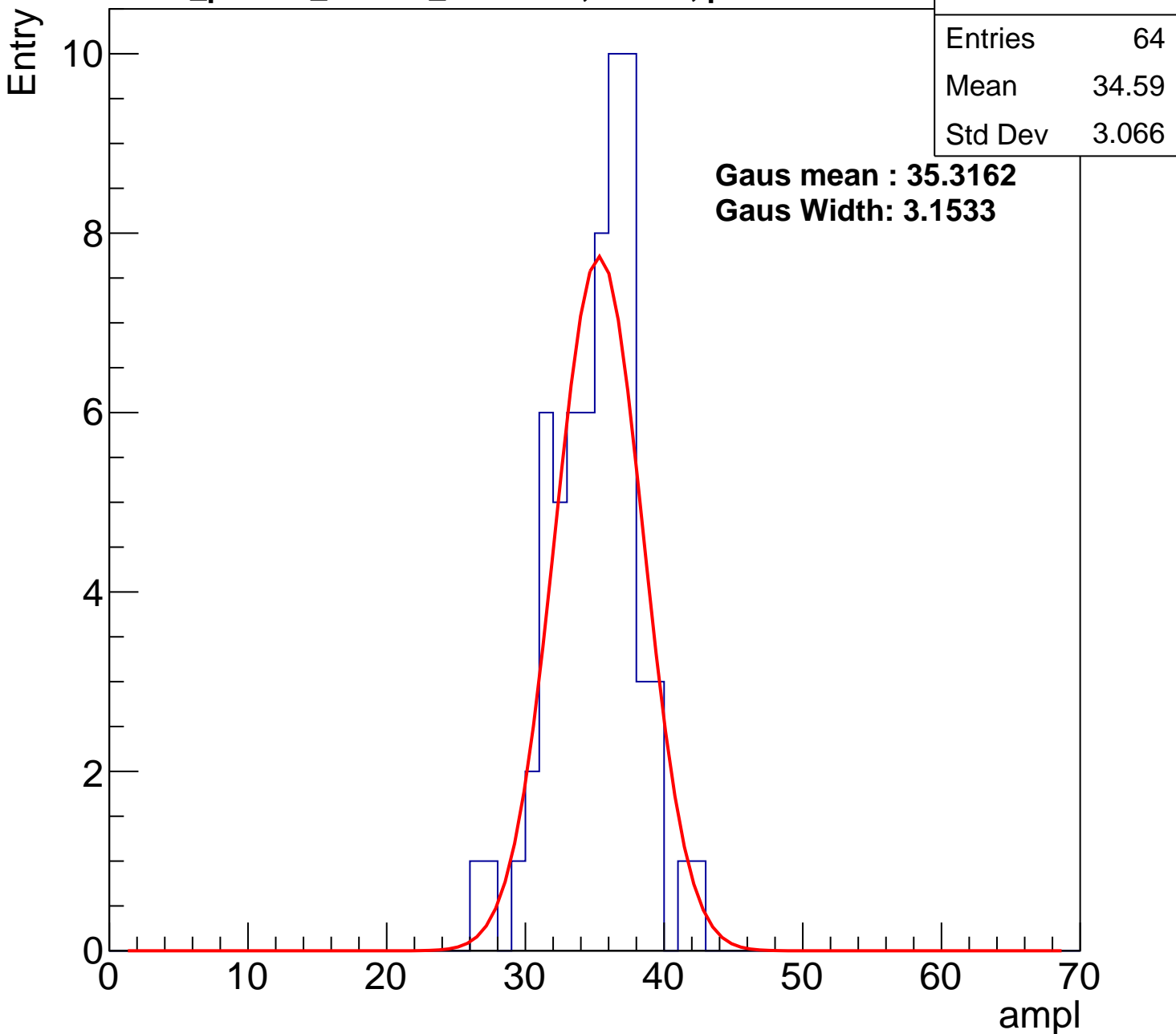
**Gaus Width: 3.1533**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch102, adc2

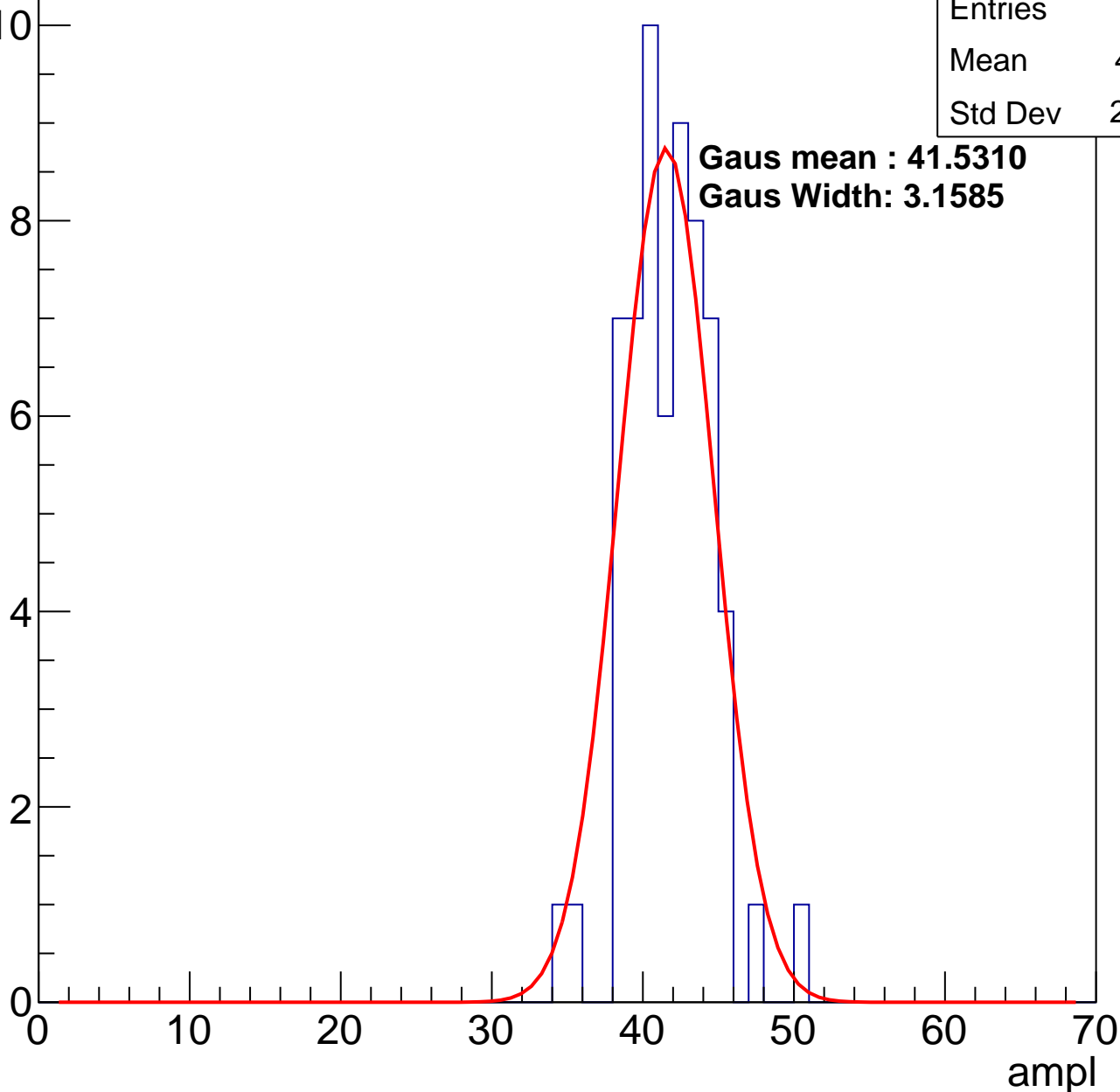
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	41.31
Std Dev	2.739

**Gaus mean : 41.5310**

**Gaus Width: 3.1585**

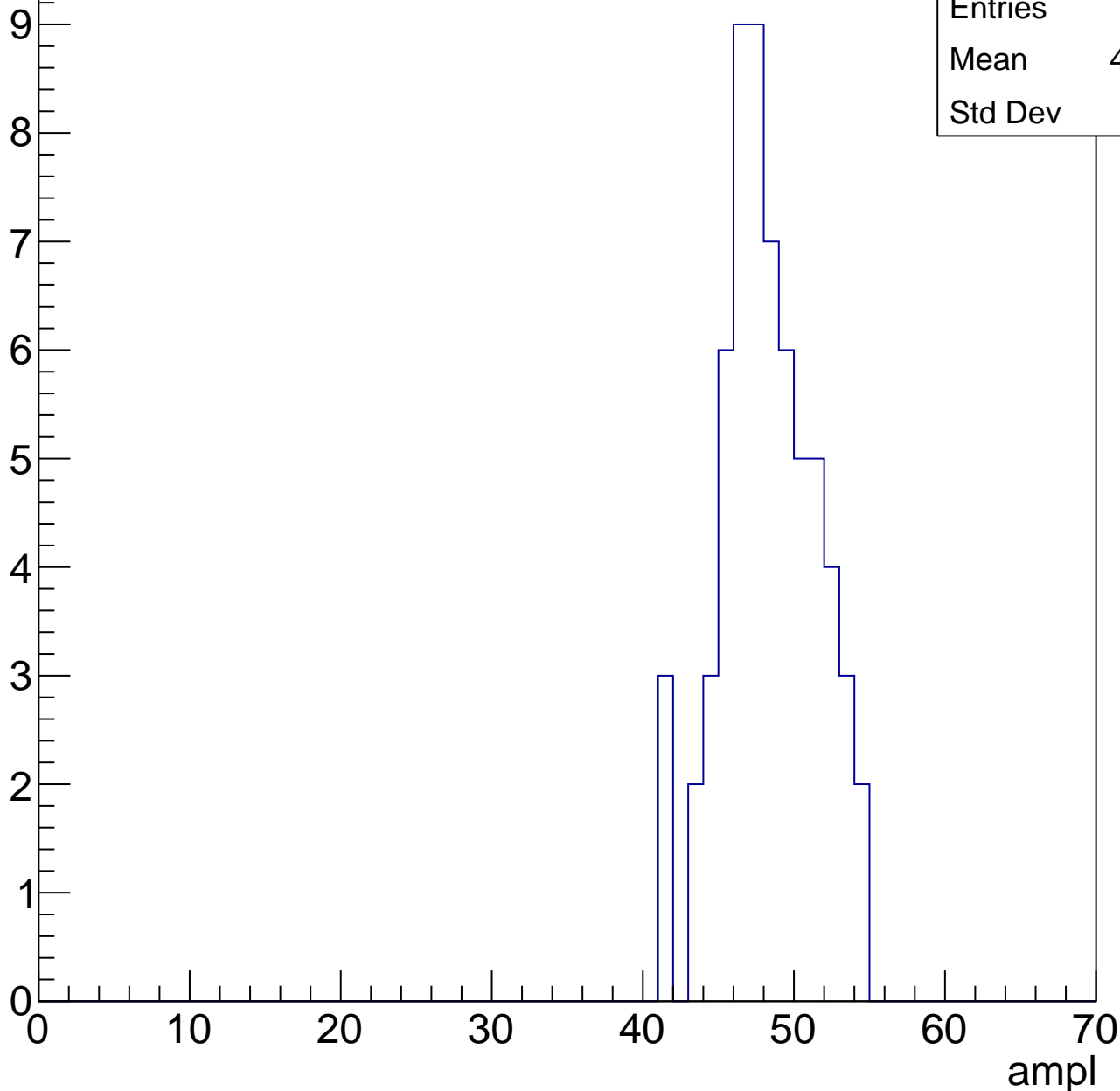


# B1L002S, U3-ch102, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	47.78
Std Dev	3.12

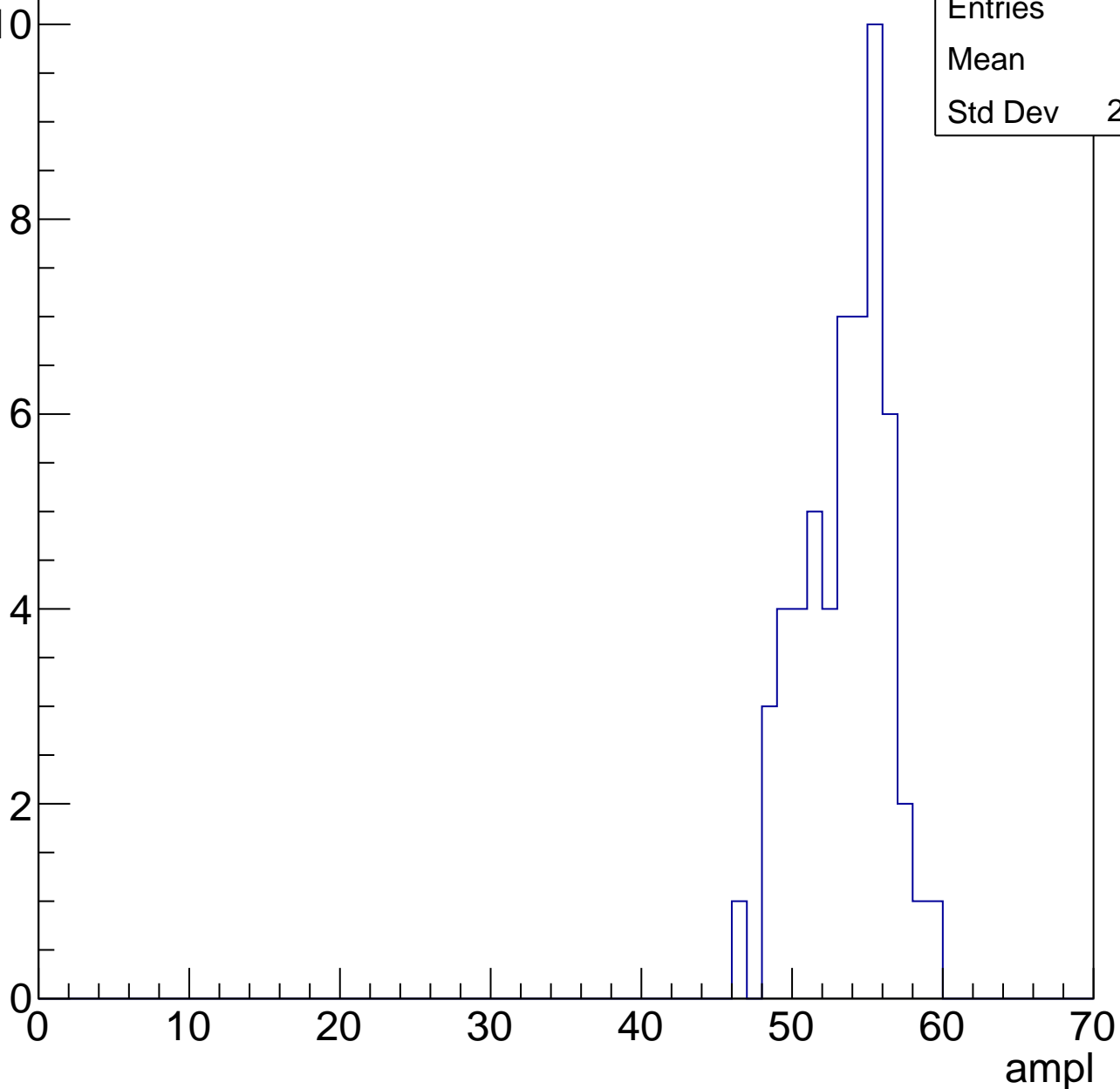


# B1L002S, U3-ch102, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

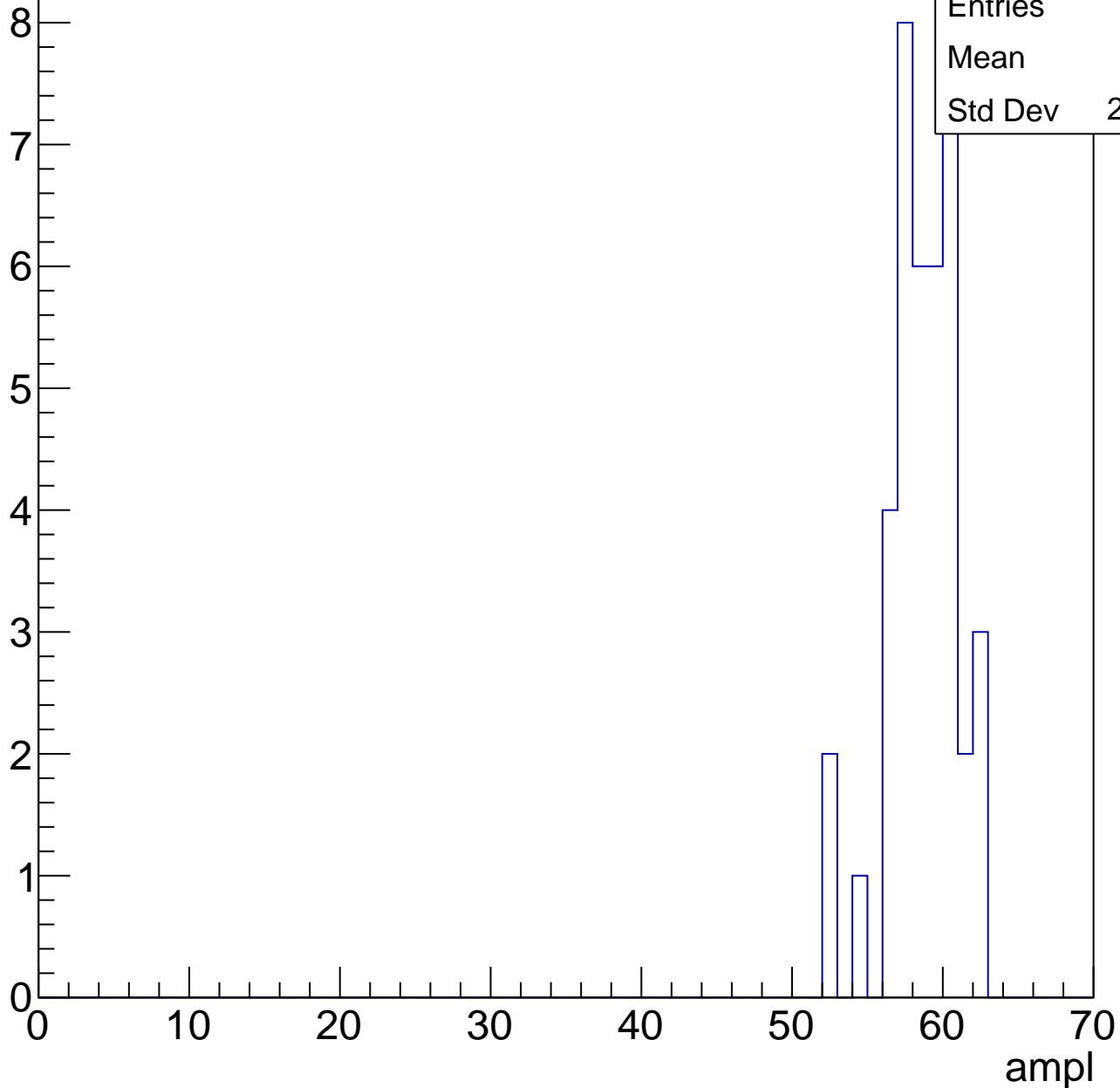
Entries	55
Mean	53
Std Dev	2.835



# B1L002S, U3-ch102, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

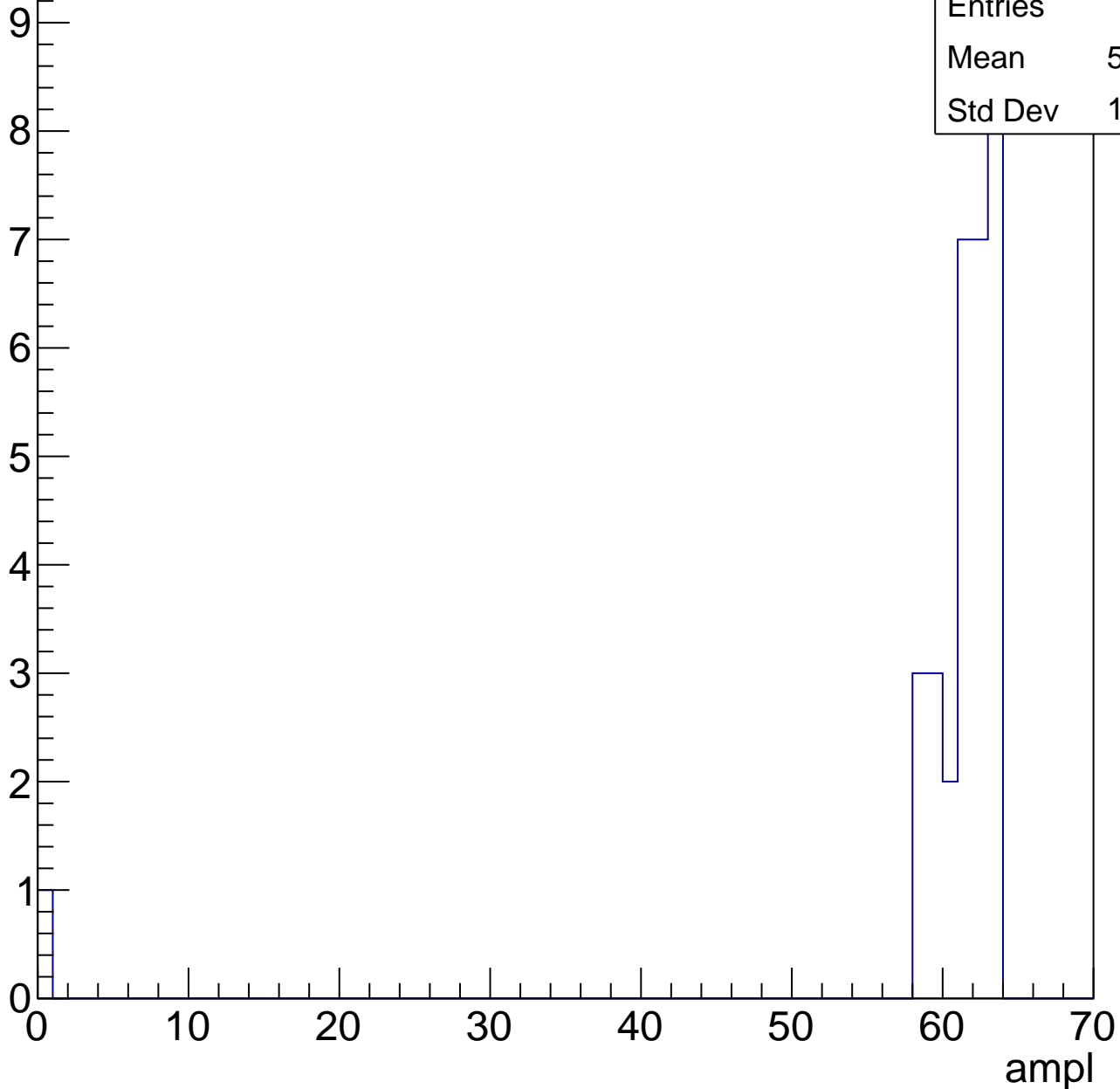


# B1L002S, U3-ch102, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.34
Std Dev	10.78





# B1L002S, U3-ch102, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	49
Std Dev	18.4

ampl

0 10 20 30 40 50 60 70

# B1L002S, U3-ch103, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	28.78
Std Dev	4.619

**Gaus mean : 30.0276**

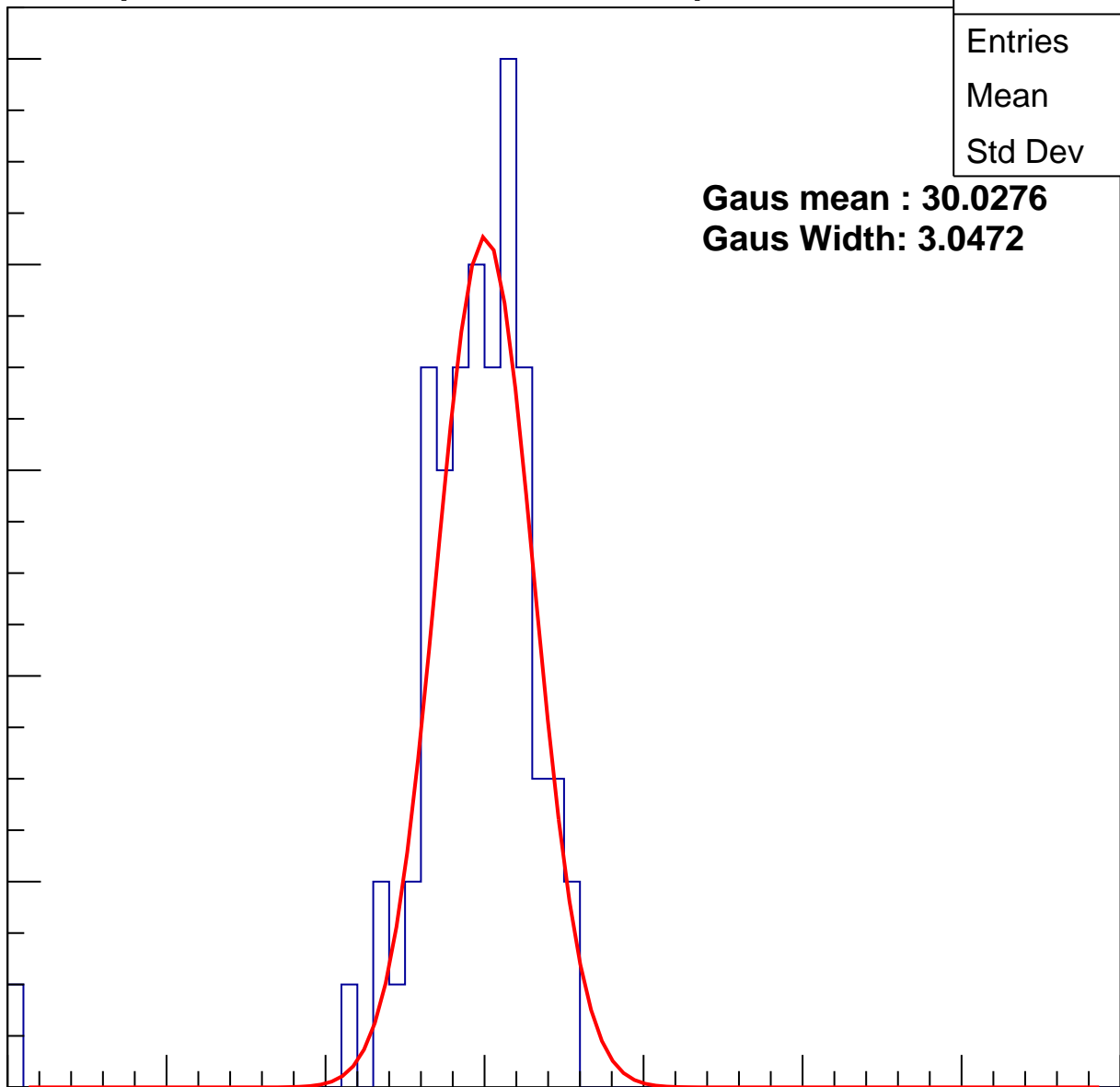
**Gaus Width: 3.0472**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



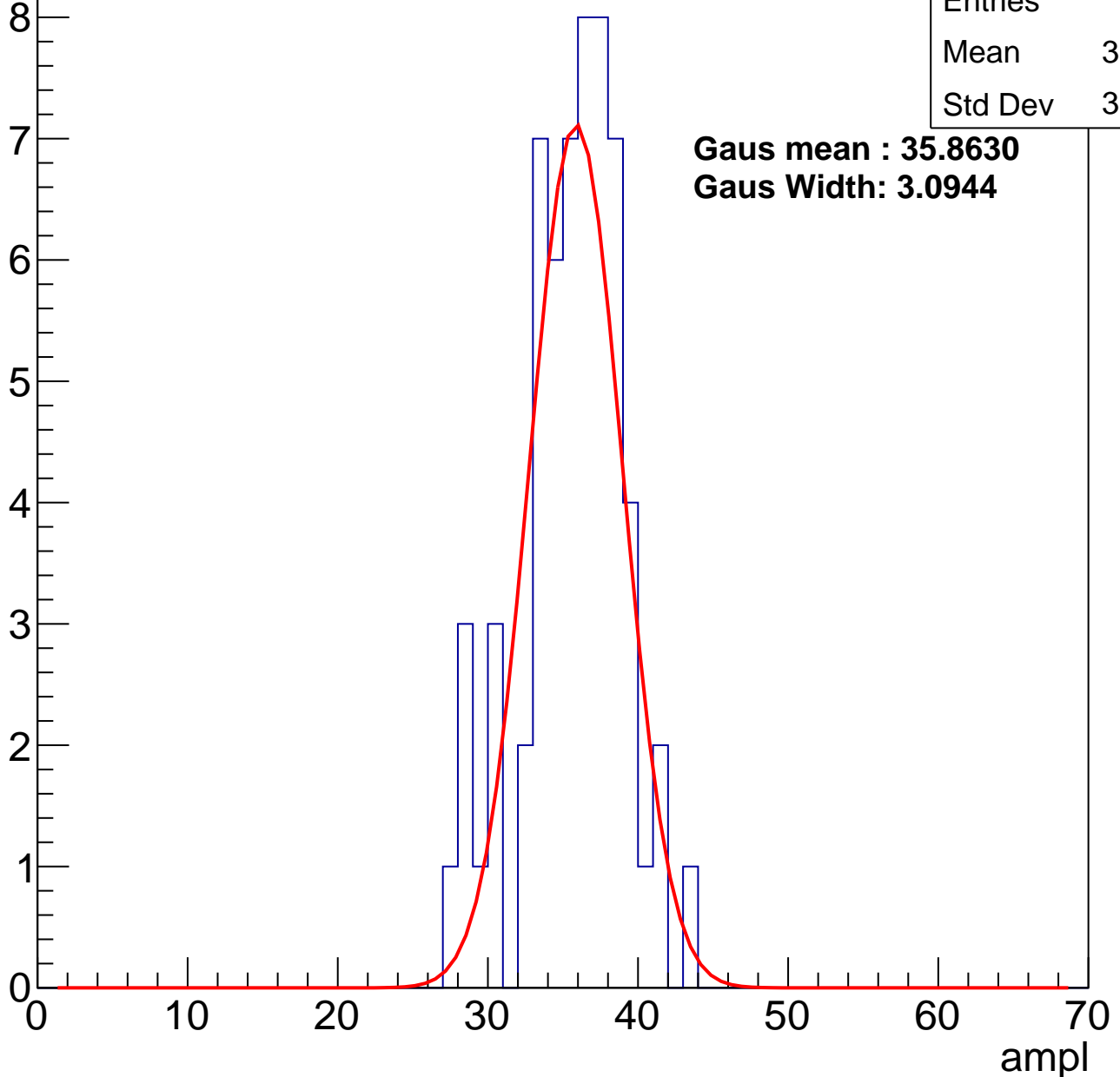
# B1L002S, U3-ch103, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	35.16
Std Dev	3.388

**Gaus mean : 35.8630**  
**Gaus Width: 3.0944**



# B1L002S, U3-ch103, adc2

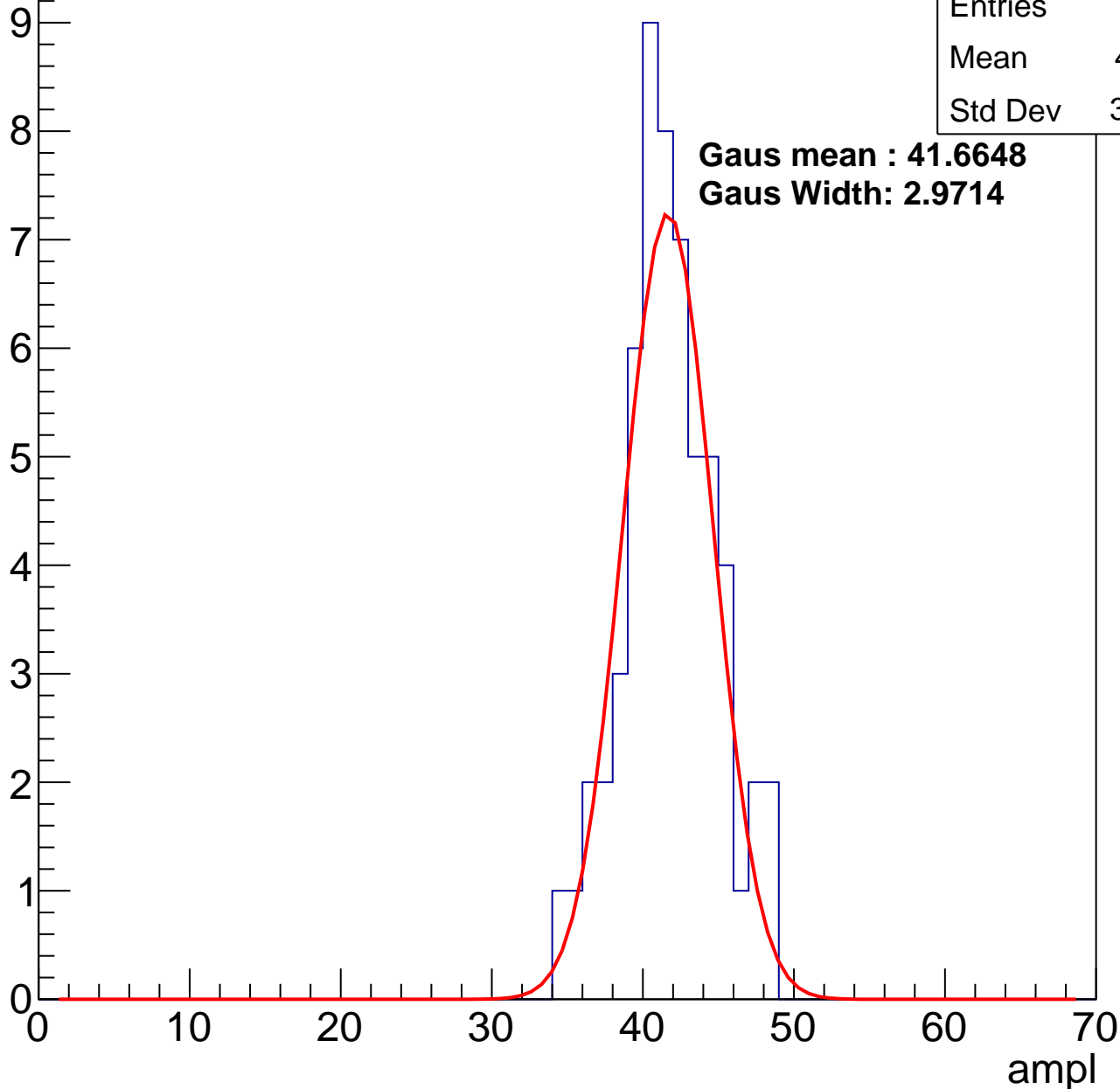
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	41.31
Std Dev	3.086

**Gaus mean : 41.6648**

**Gaus Width: 2.9714**

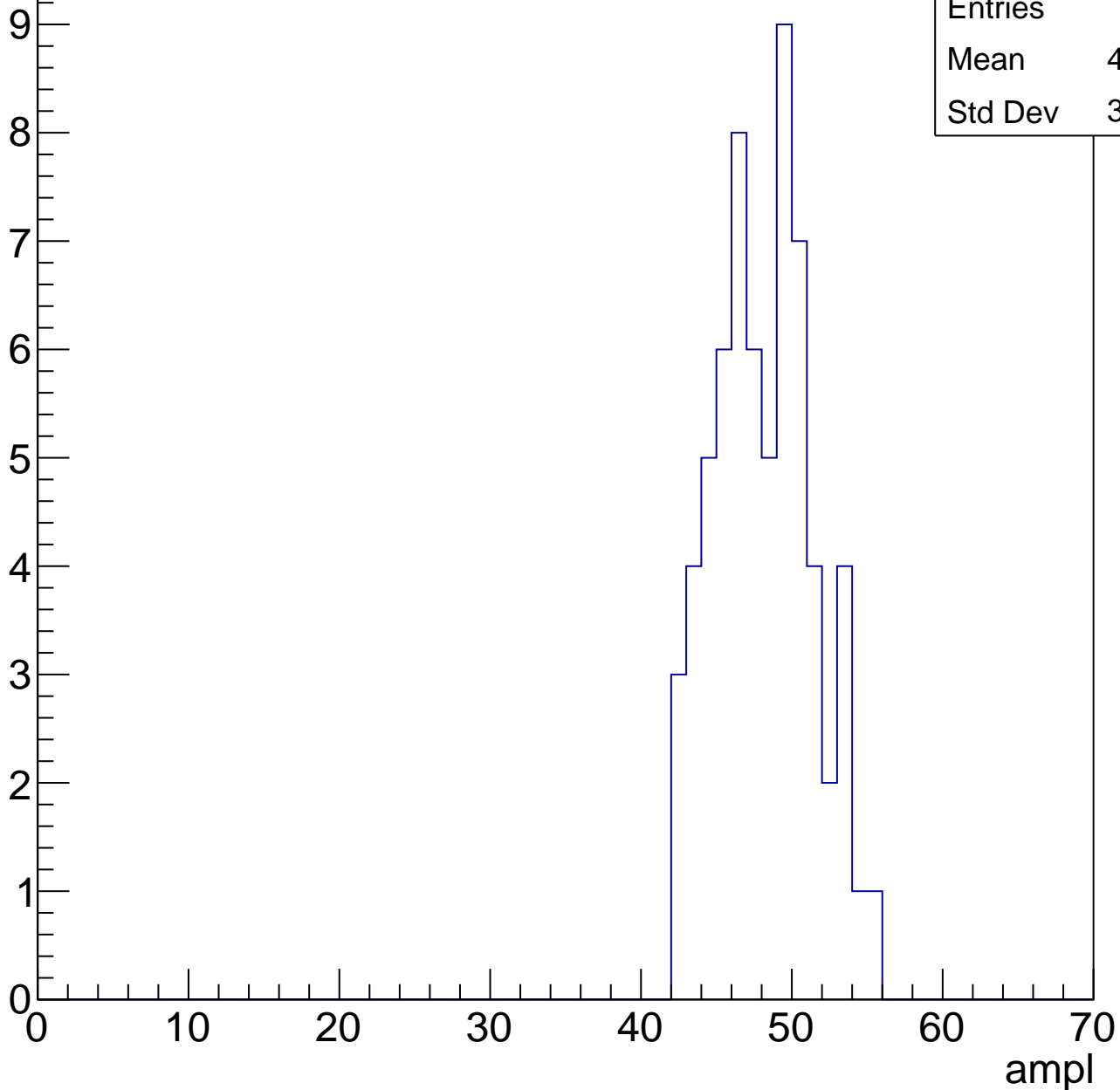


# B1L002S, U3-ch103, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	47.66
Std Dev	3.193

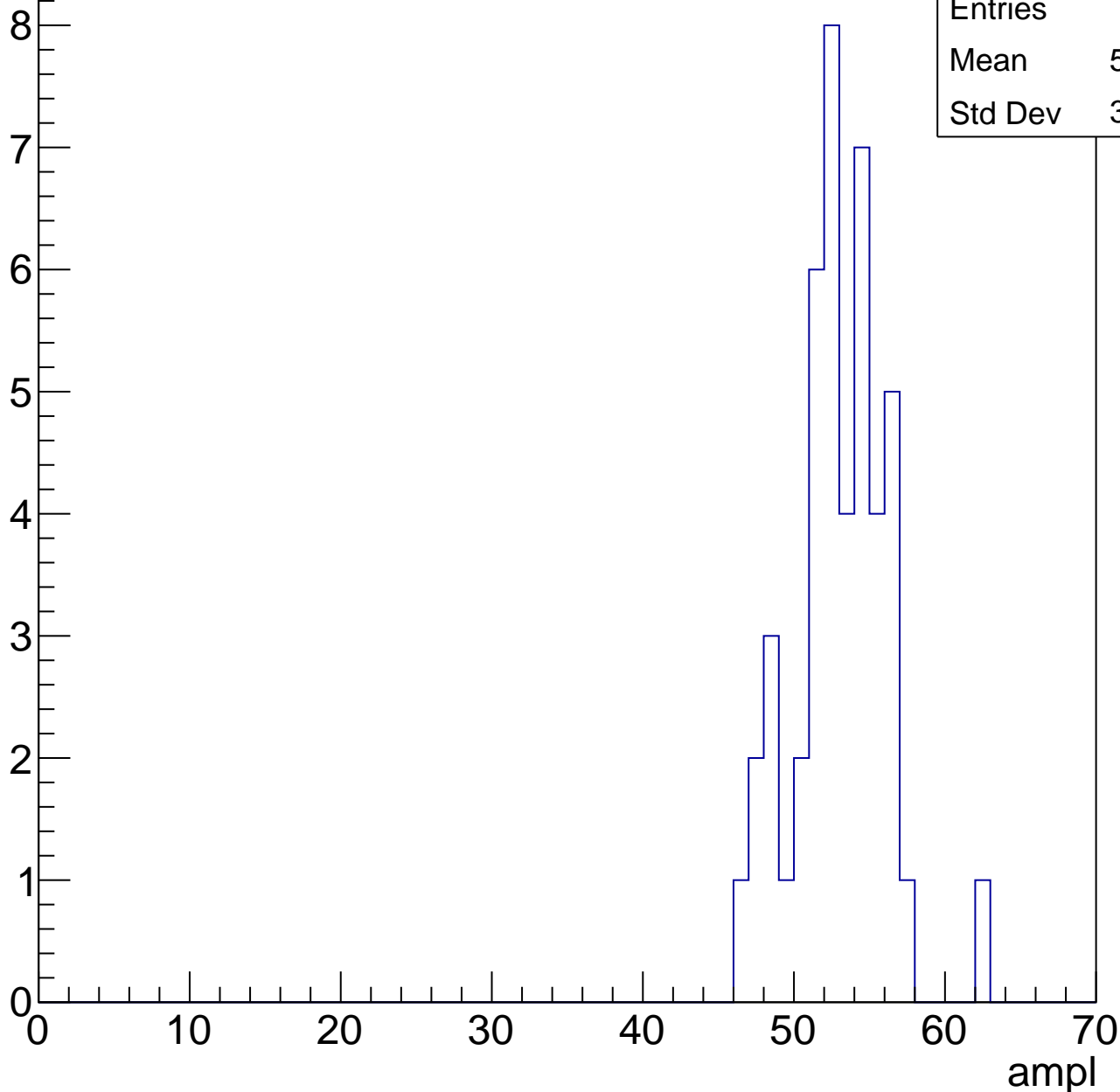


# B1L002S, U3-ch103, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	52.53
Std Dev	3.045

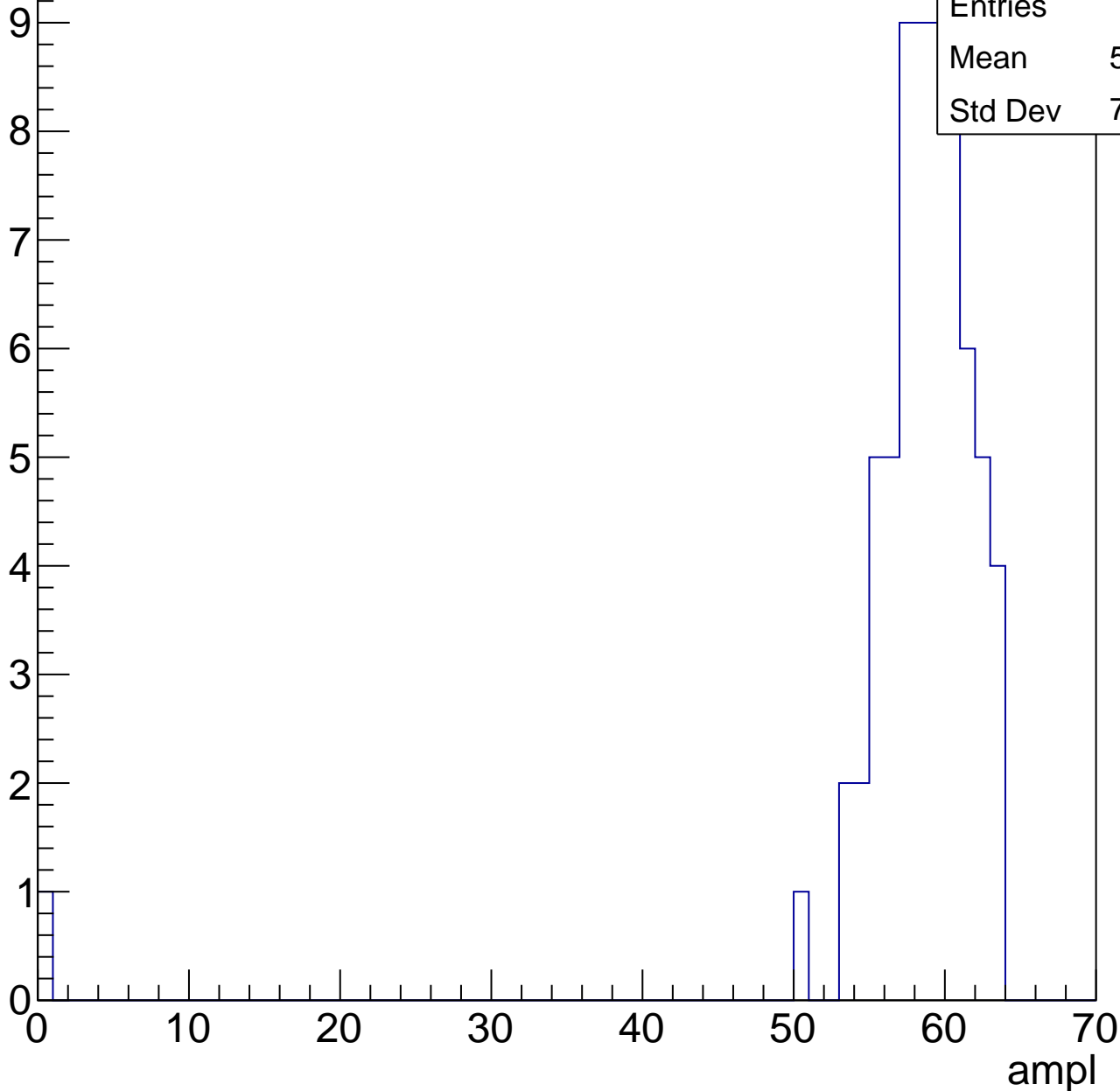


# B1L002S, U3-ch103, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	57.47
Std Dev	7.628

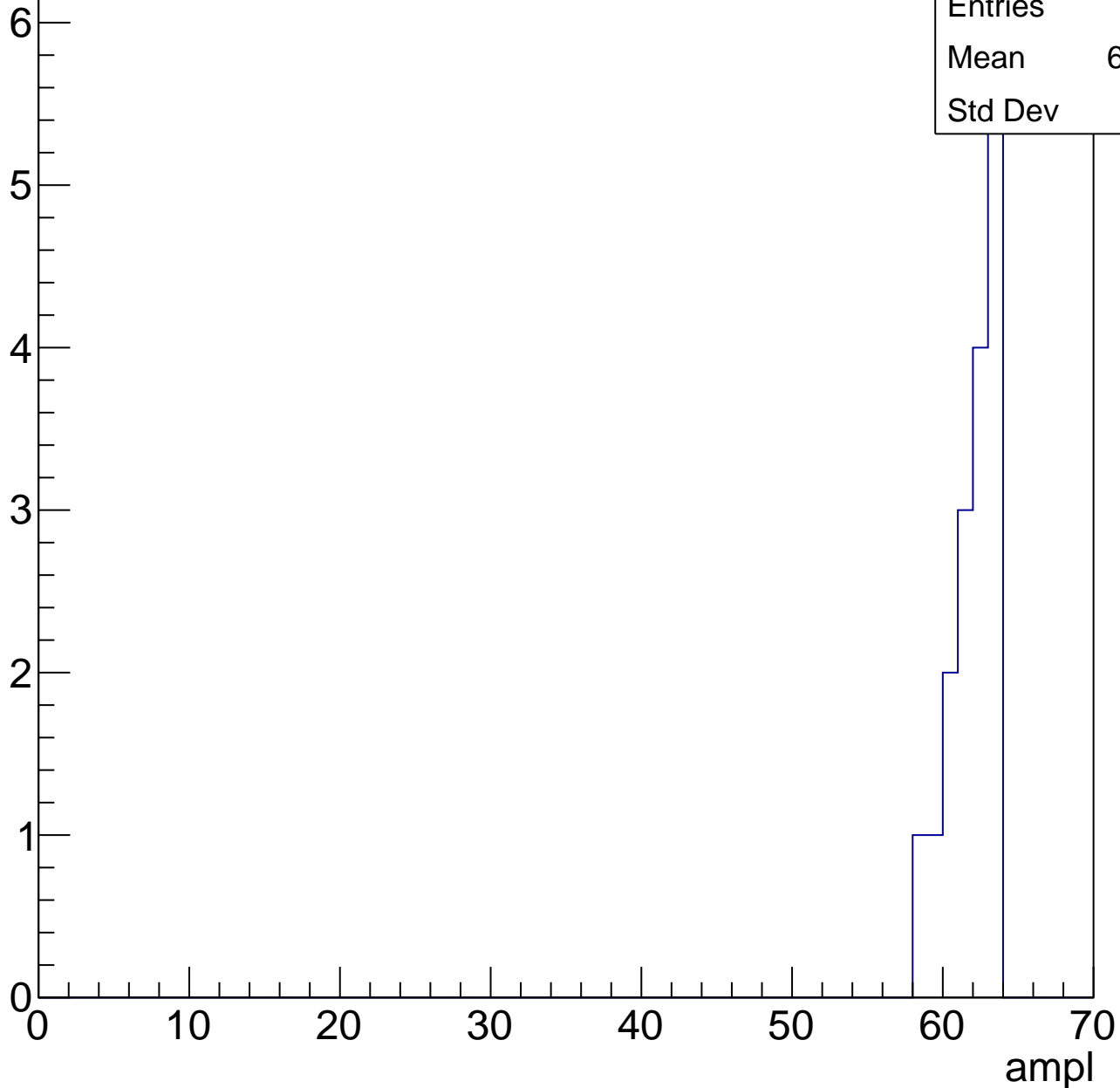


# B1L002S, U3-ch103, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	17
Mean	61.53
Std Dev	1.5

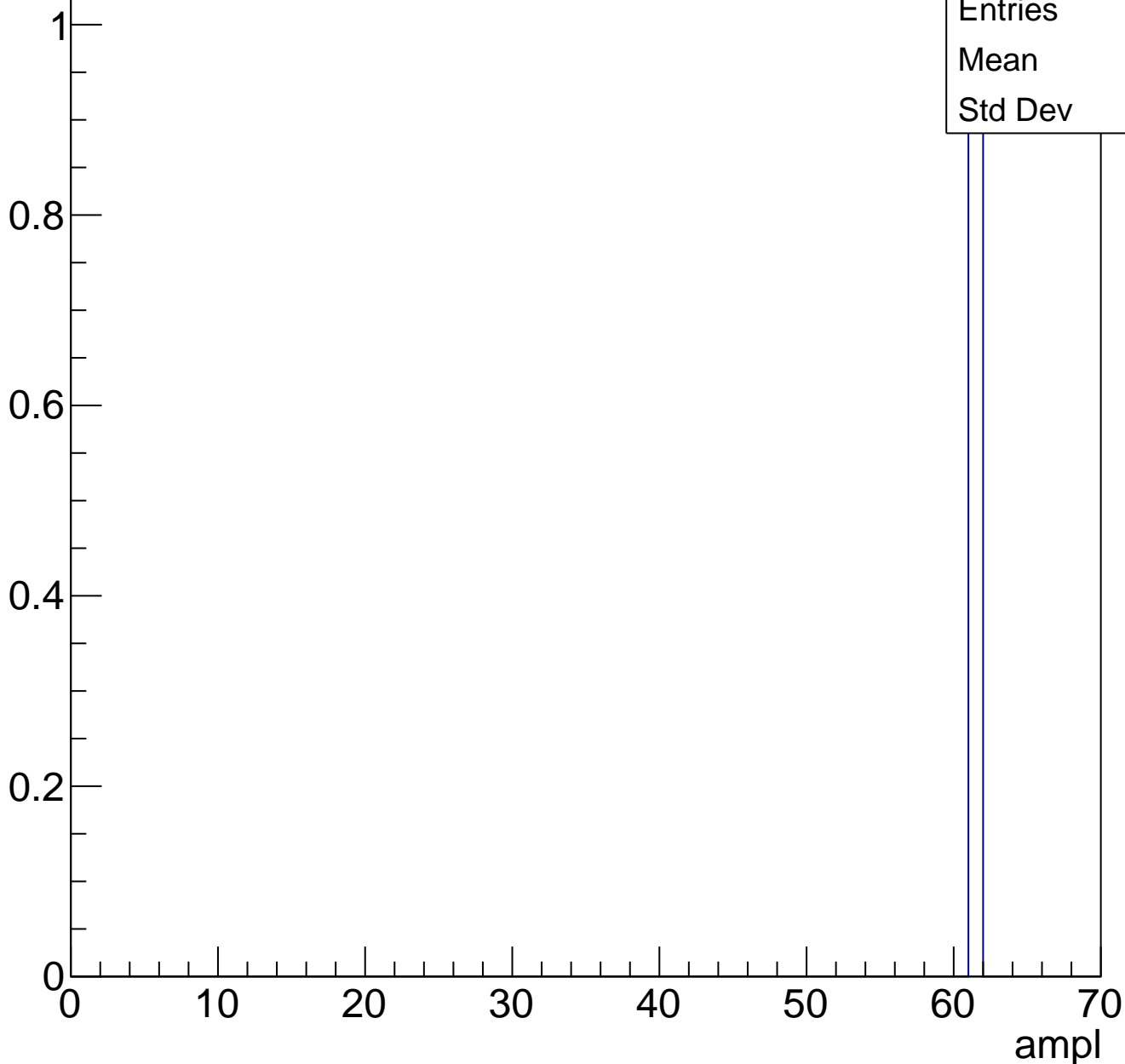




# B1L002S, U3-ch103, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch104, adc0

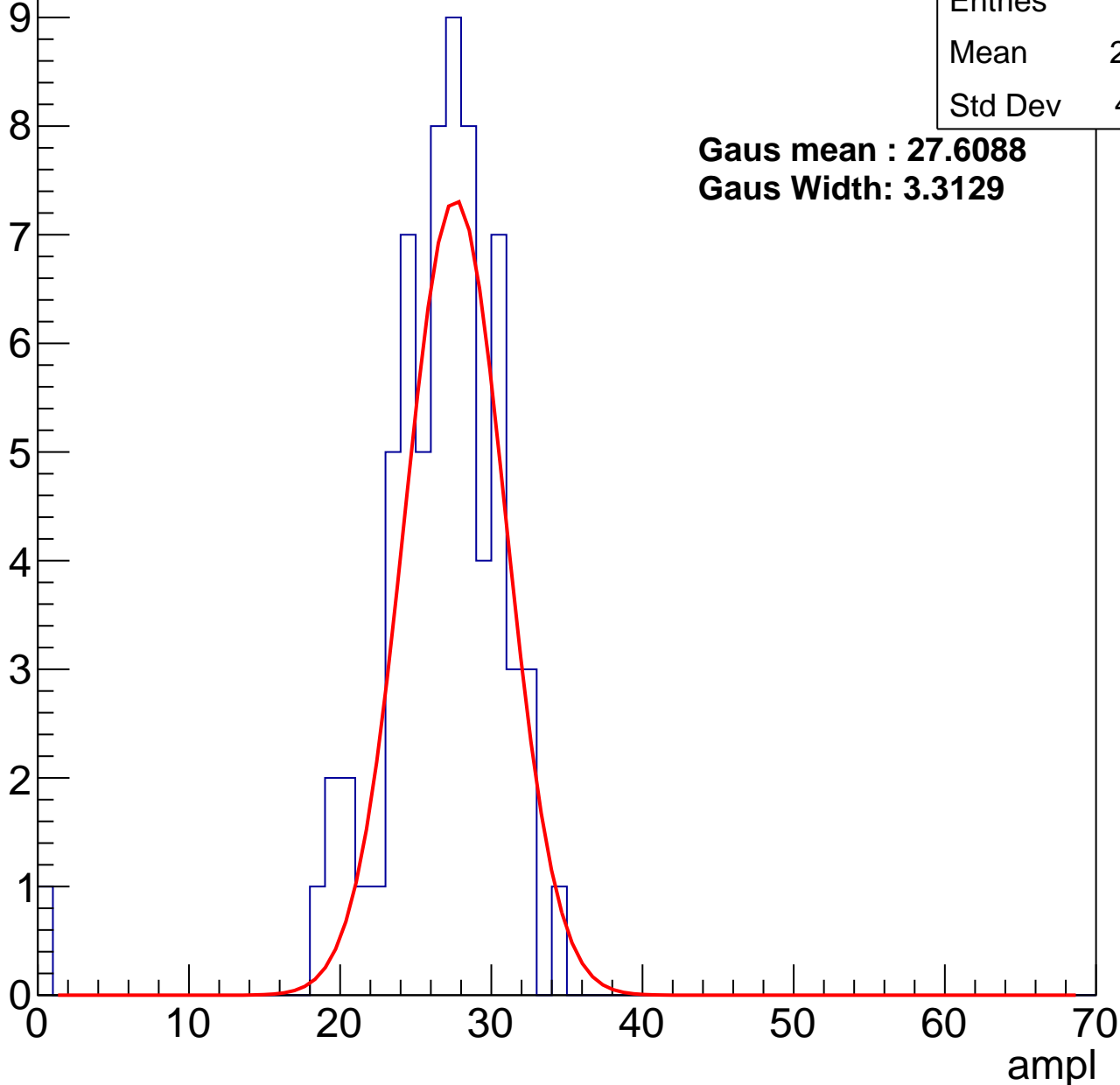
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	26.04
Std Dev	4.641

**Gaus mean : 27.6088**

**Gaus Width: 3.3129**



# B1L002S, U3-ch104, adc1

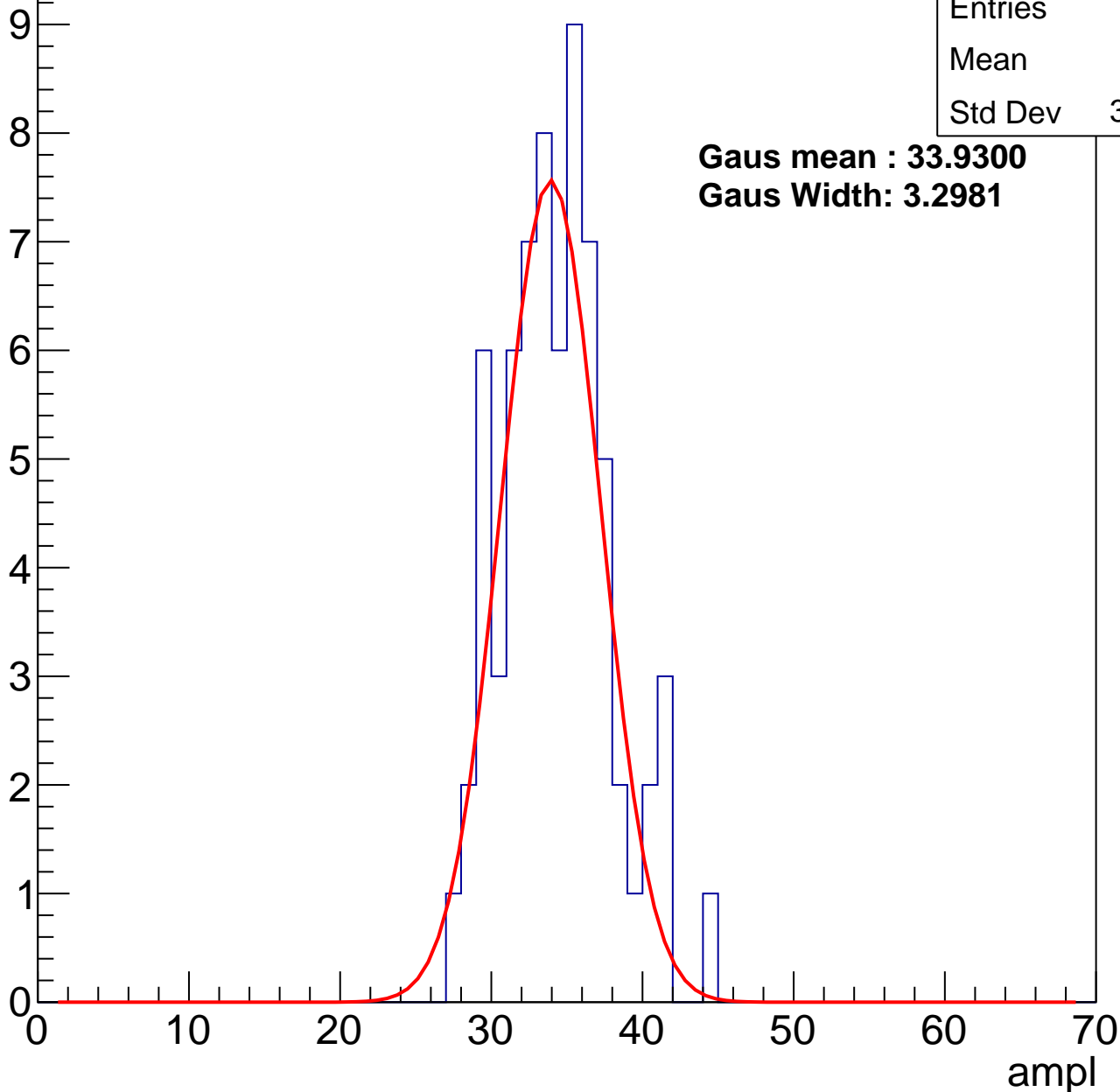
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	33.9
Std Dev	3.543

**Gaus mean : 33.9300**

**Gaus Width: 3.2981**



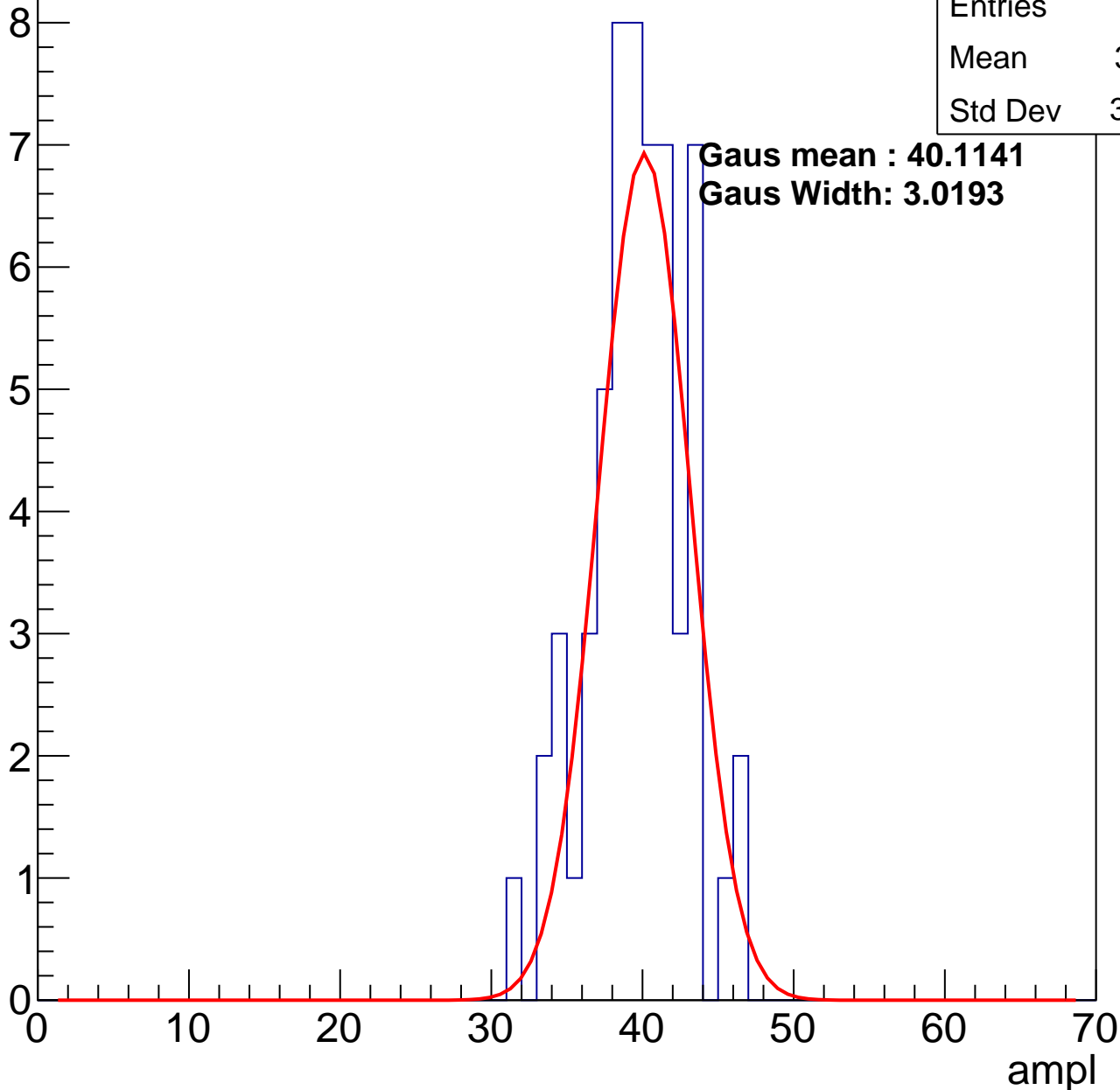
# B1L002S, U3-ch104, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	39.21
Std Dev	3.183

**Gaus mean : 40.1141**  
**Gaus Width: 3.0193**

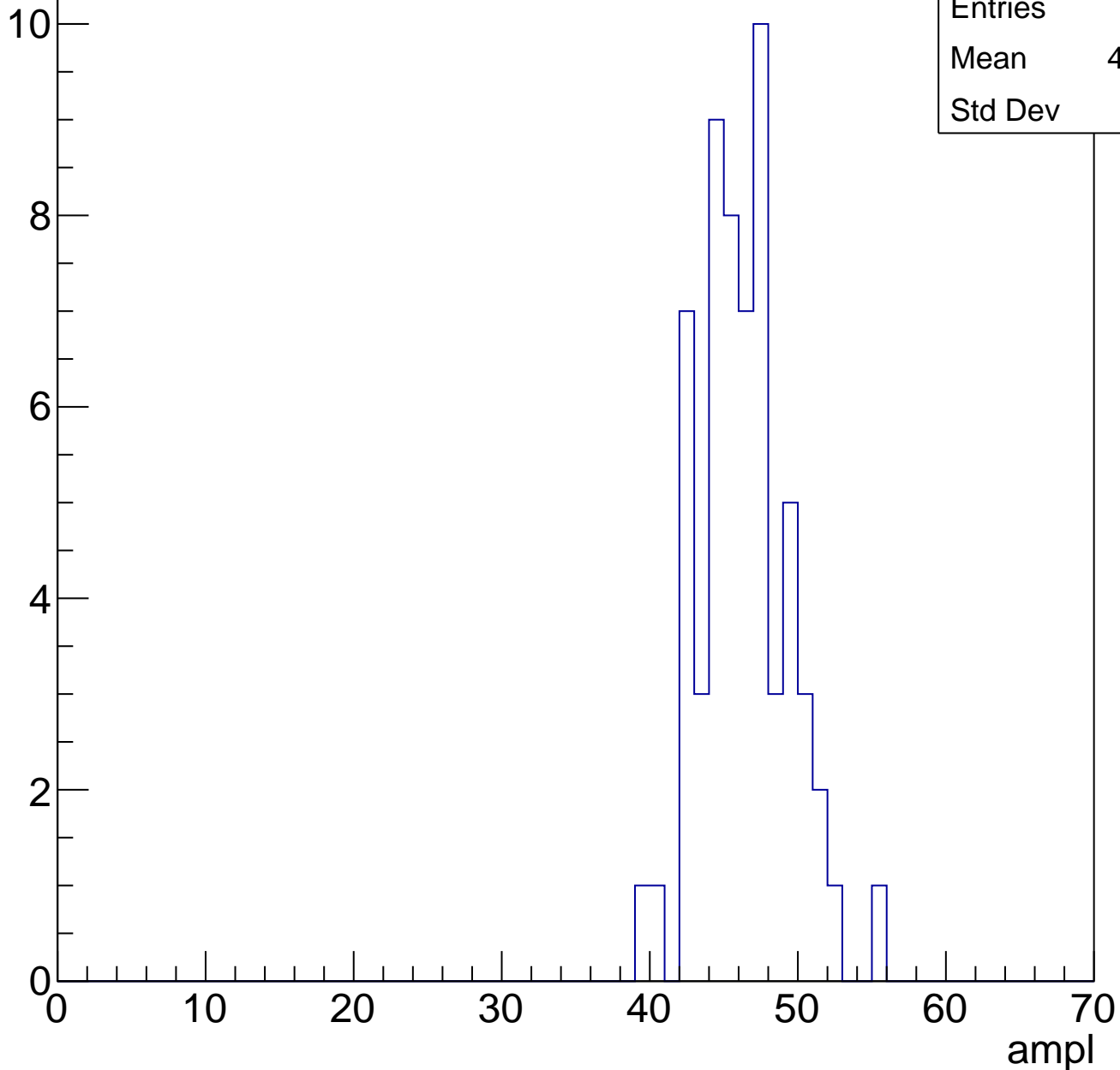


# B1L002S, U3-ch104, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	61
Mean	45.87
Std Dev	3

Entry

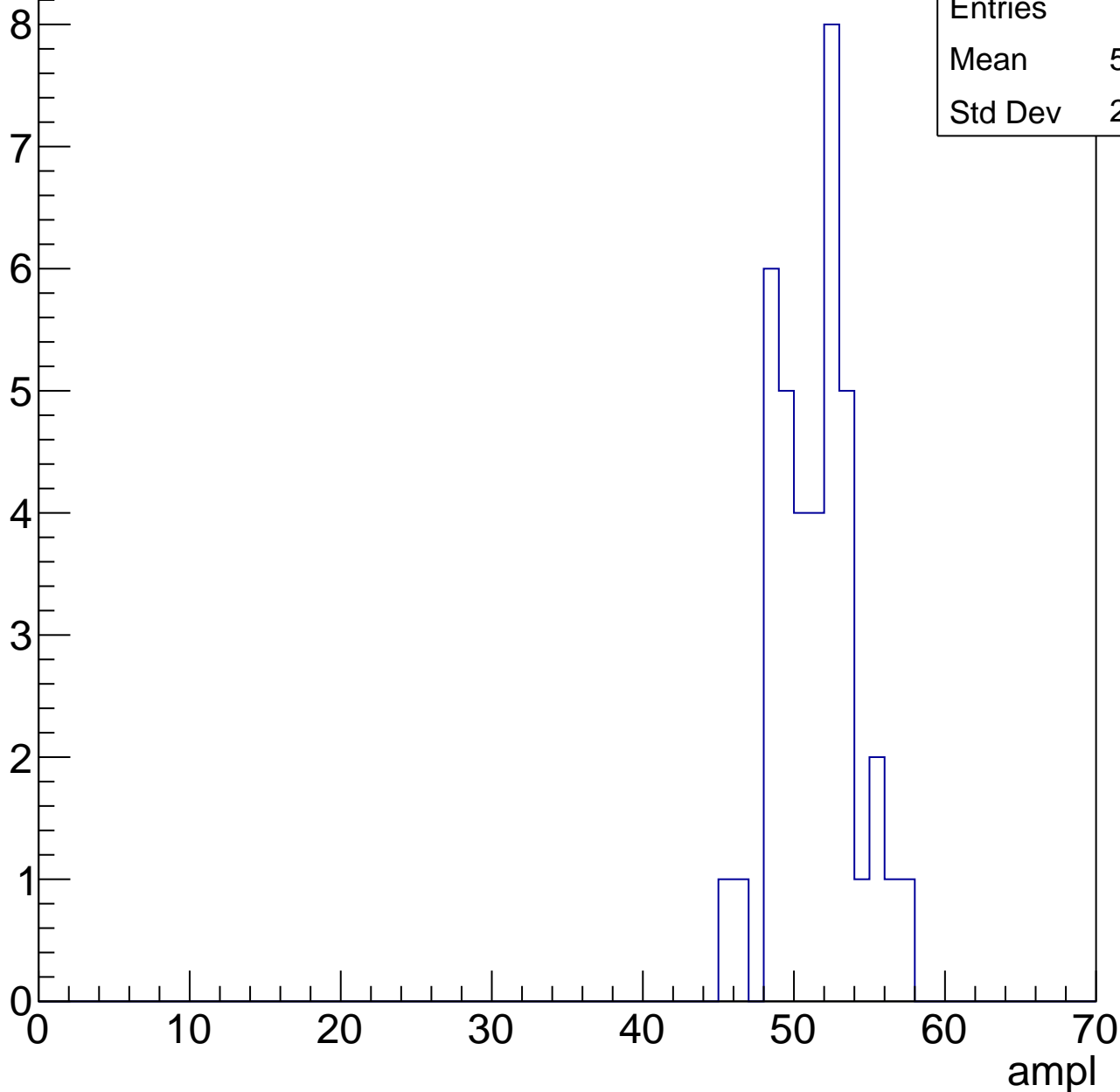


# B1L002S, U3-ch104, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	50.92
Std Dev	2.625

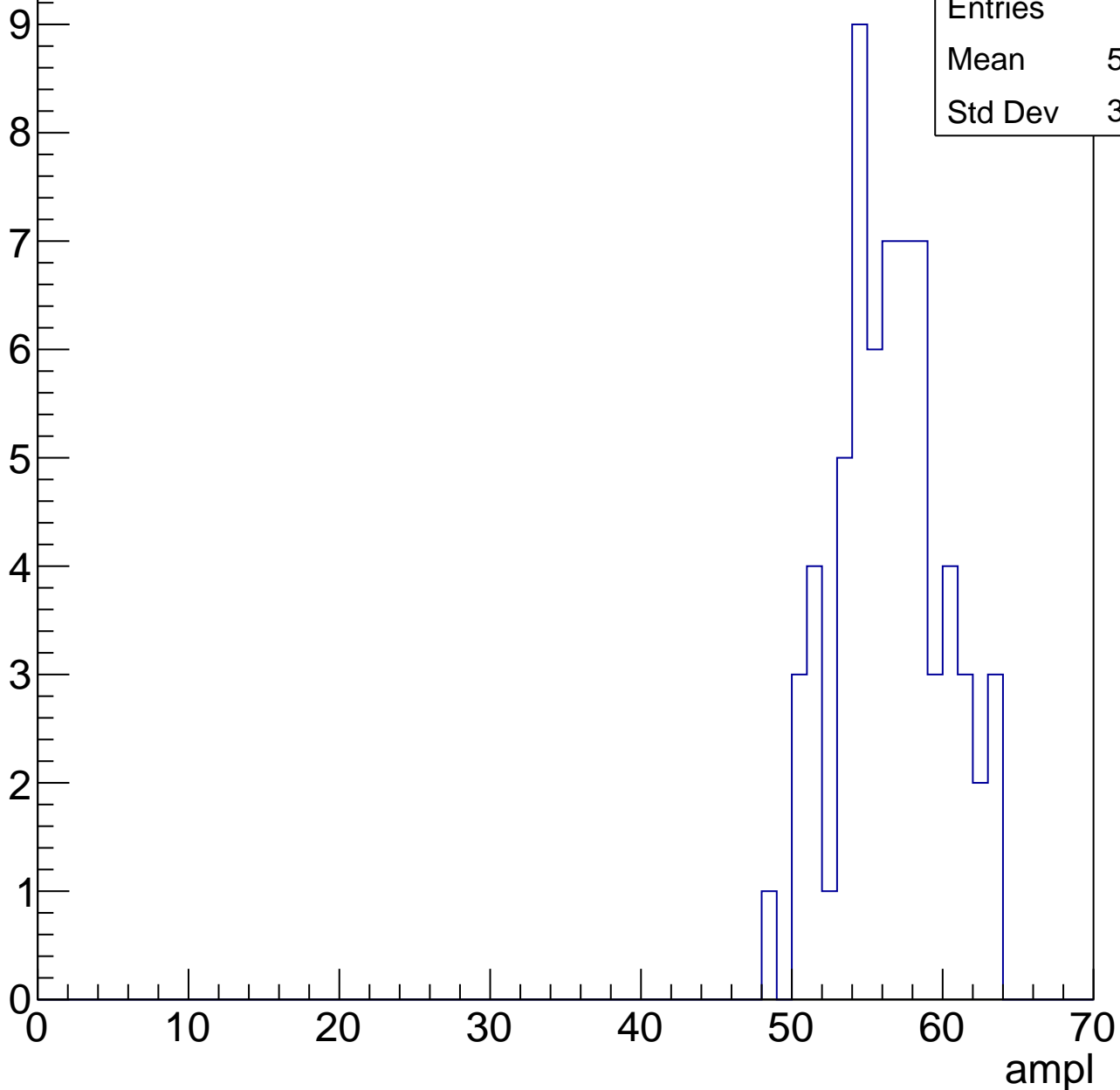


# B1L002S, U3-ch104, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	56.08
Std Dev	3.496

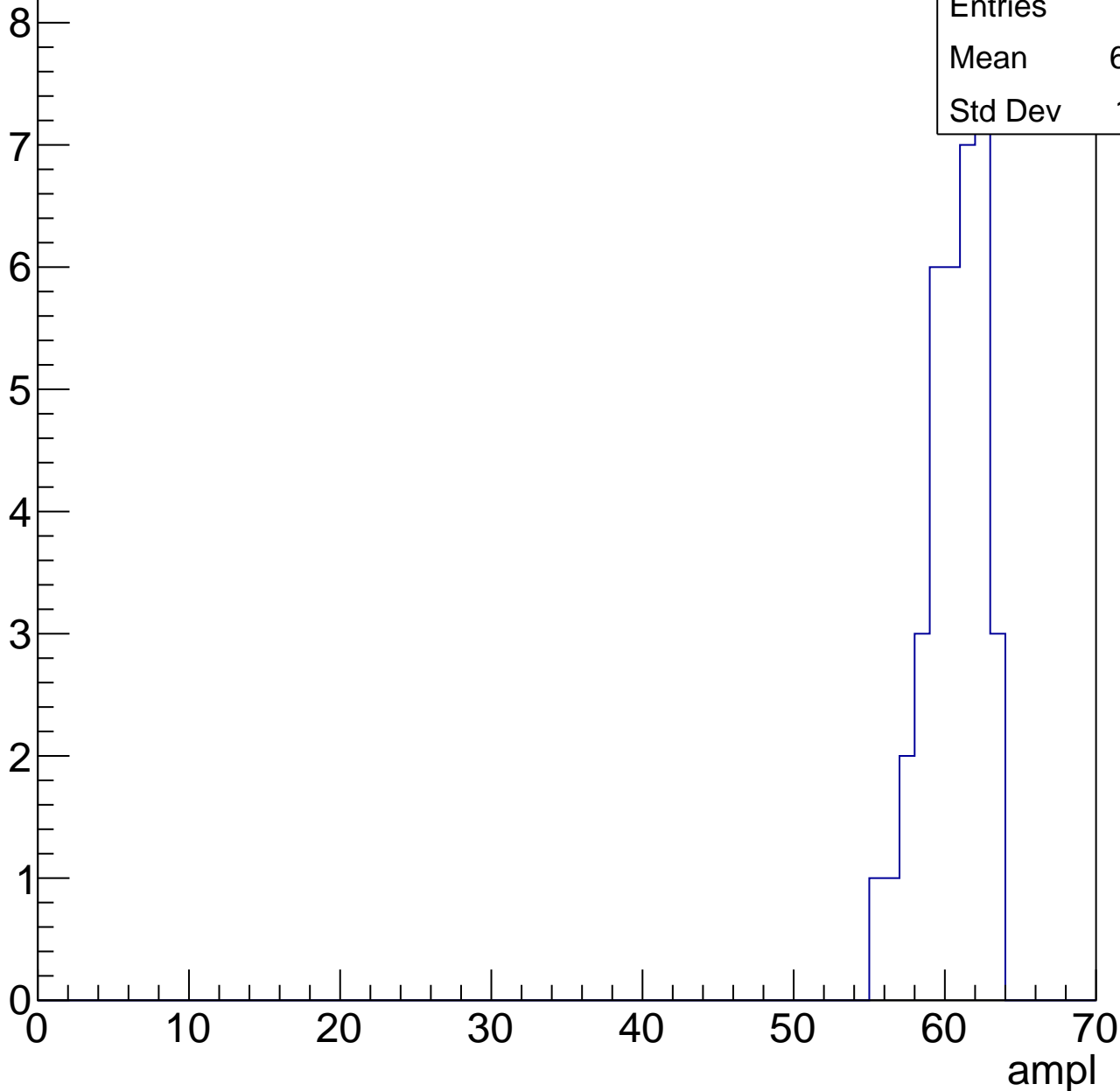


# B1L002S, U3-ch104, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	37
Mean	60.14
Std Dev	1.961

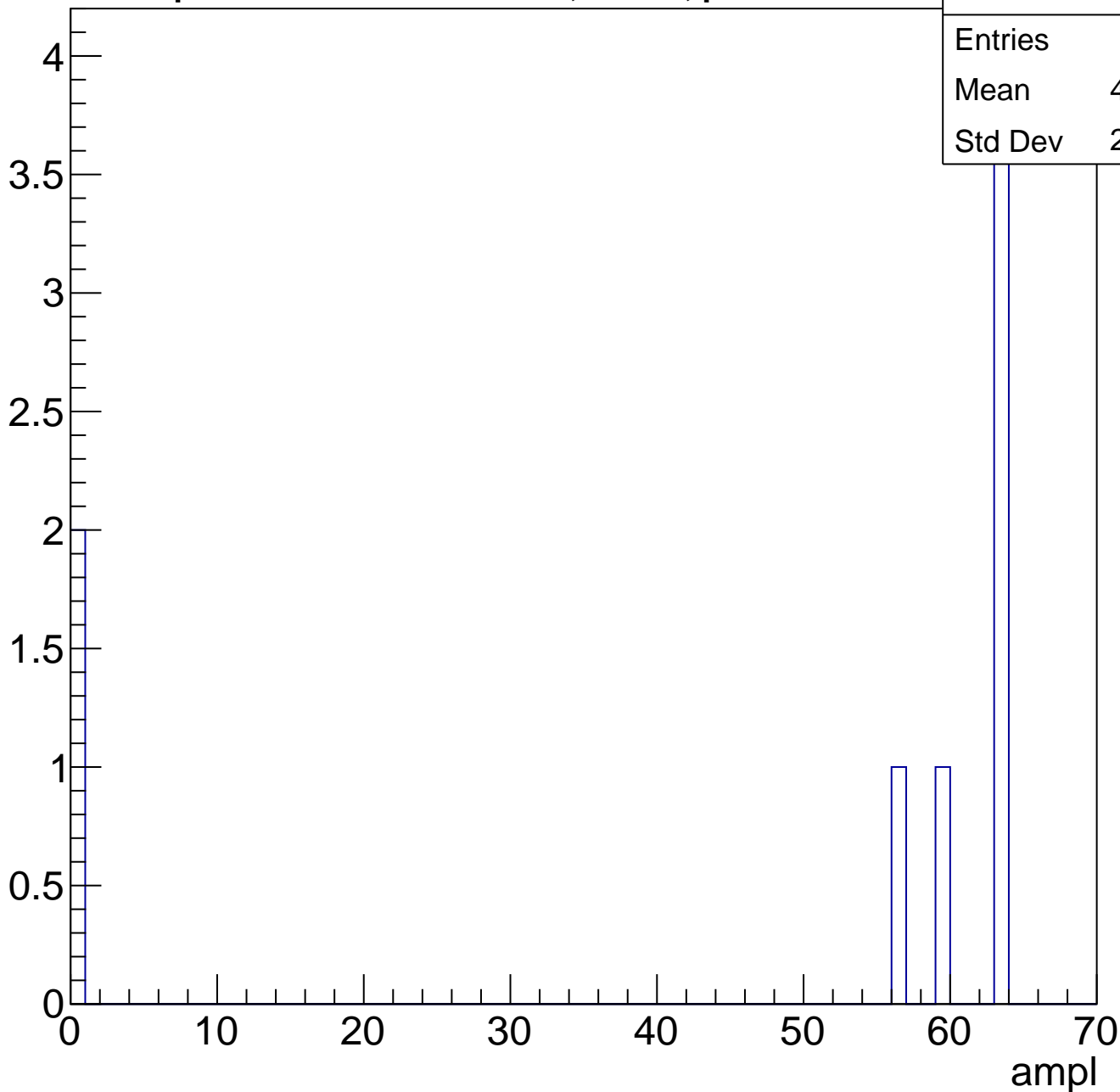




# B1L002S, U3-ch104, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch105, adc0

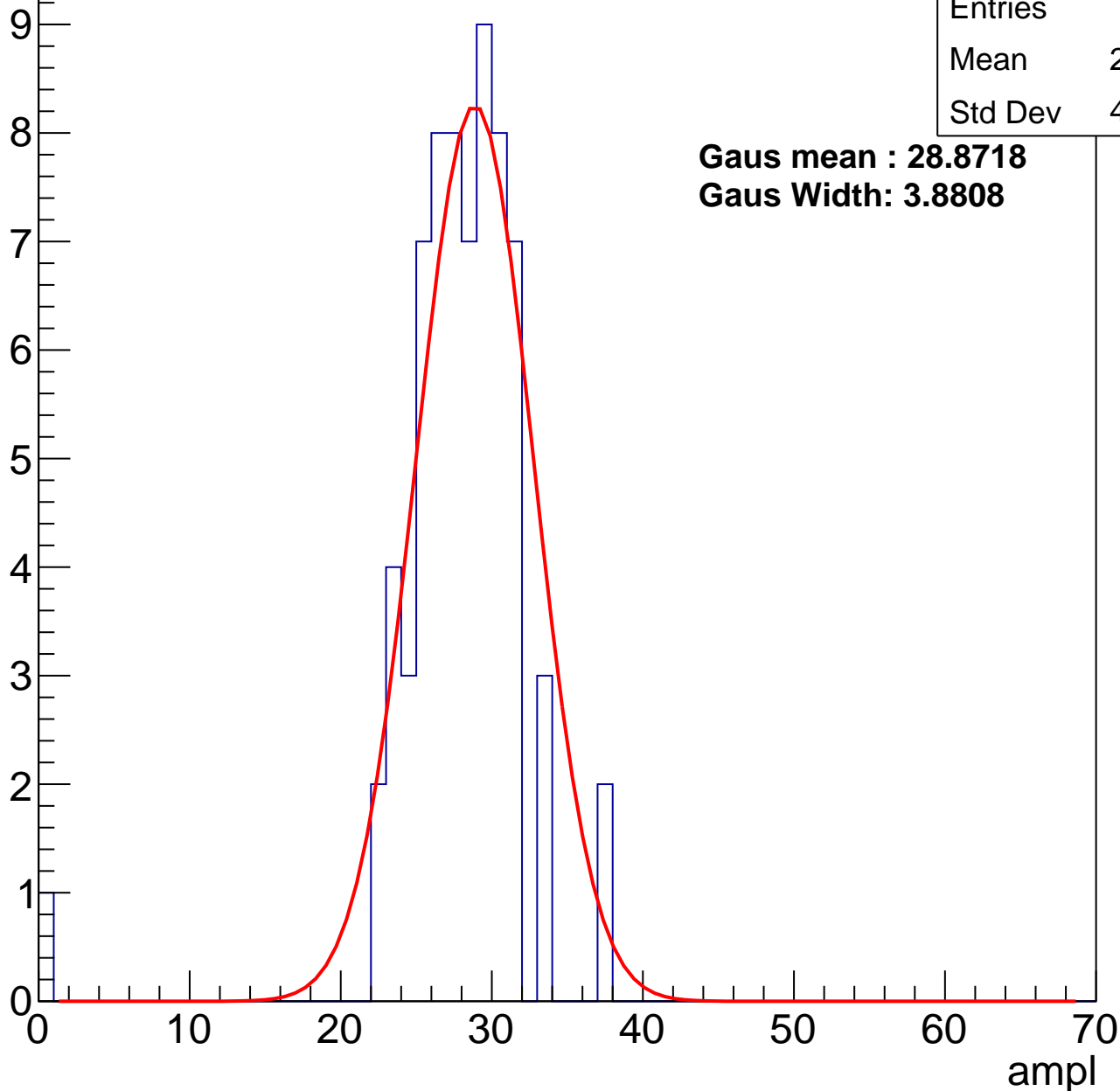
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	27.45
Std Dev	4.548

**Gaus mean : 28.8718**

**Gaus Width: 3.8808**



# B1L002S, U3-ch105, adc1

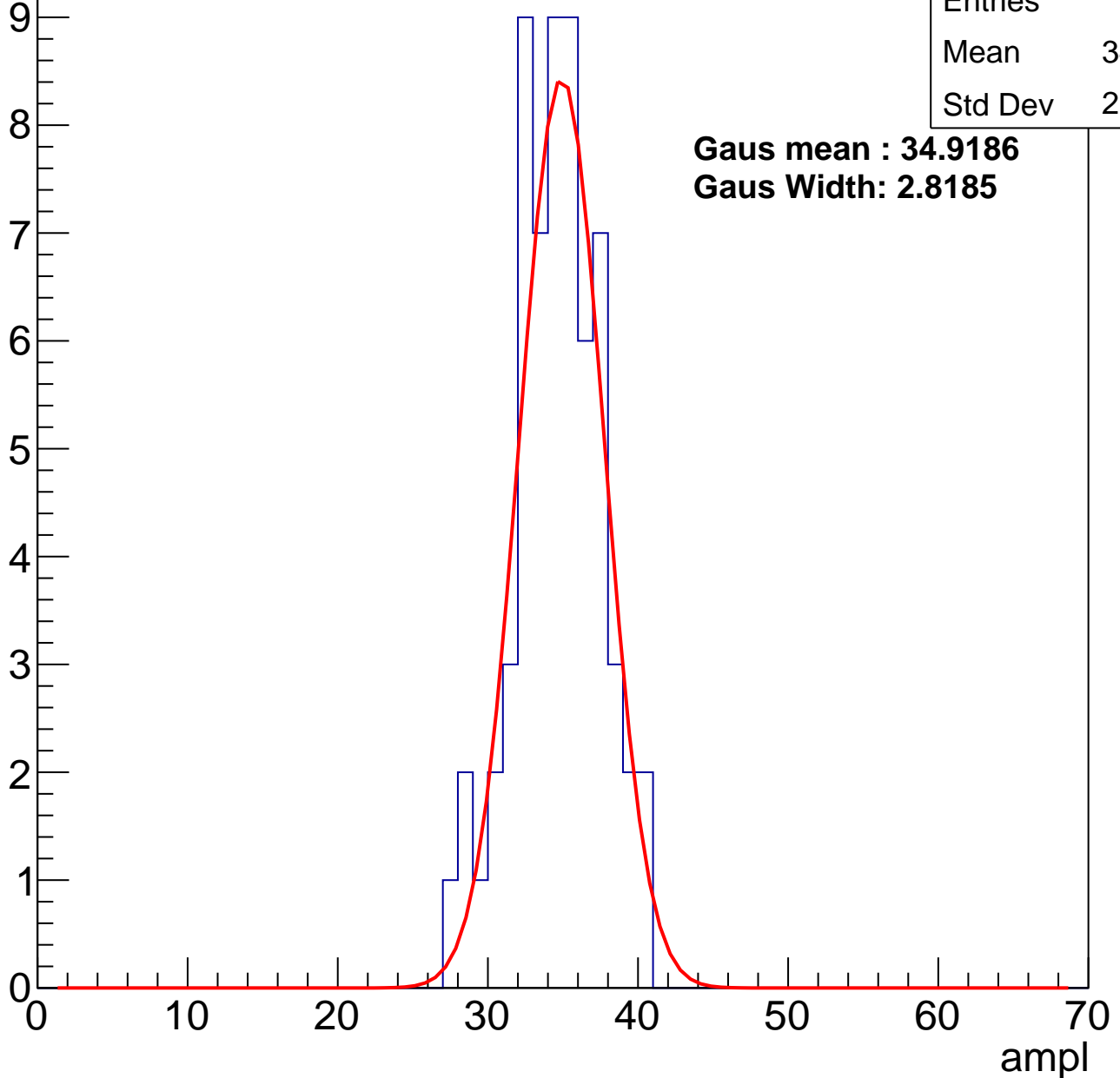
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.16
Std Dev	2.852

**Gaus mean : 34.9186**

**Gaus Width: 2.8185**



# B1L002S, U3-ch105, adc2

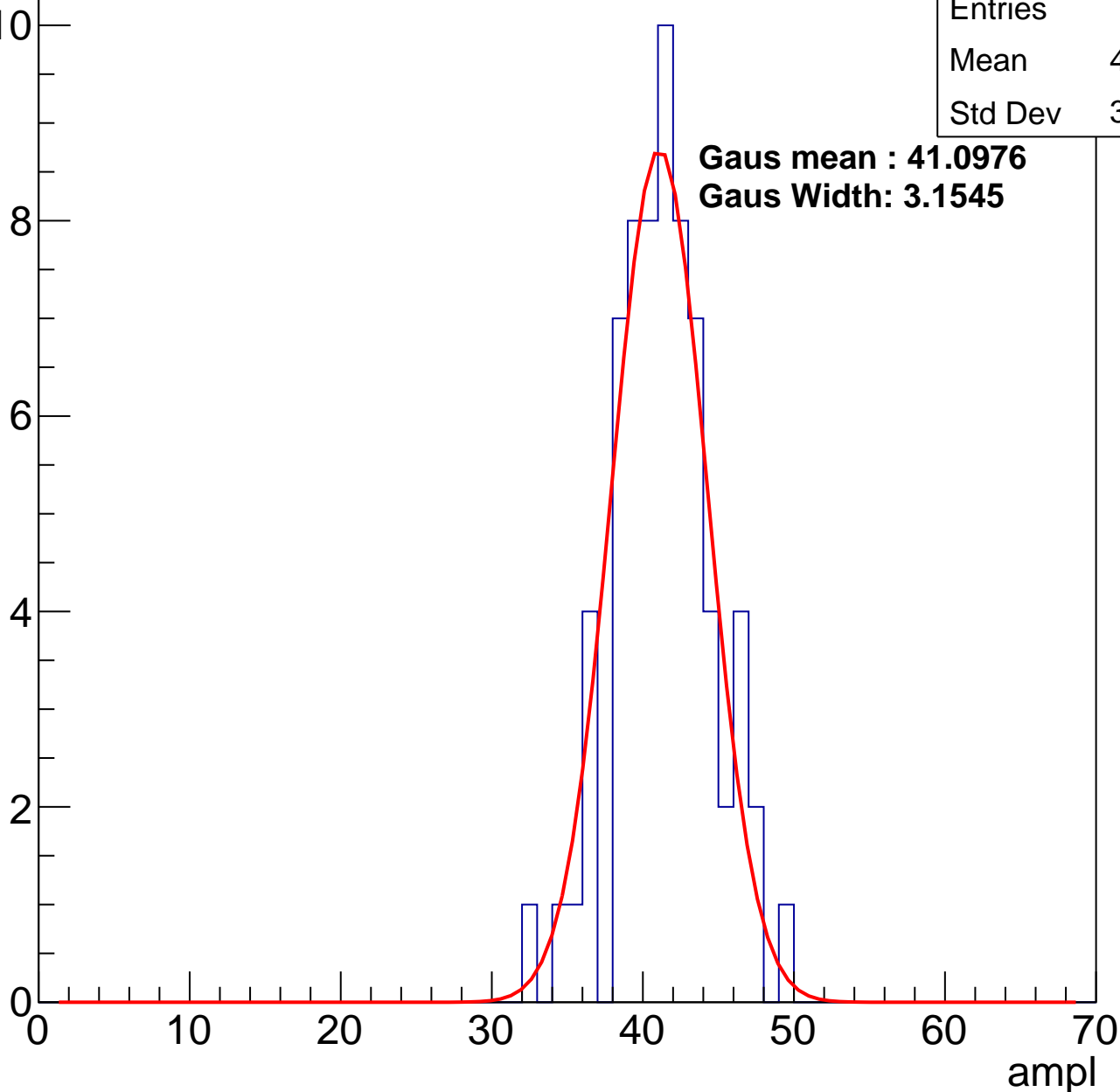
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	40.93
Std Dev	3.228

**Gaus mean : 41.0976**

**Gaus Width: 3.1545**

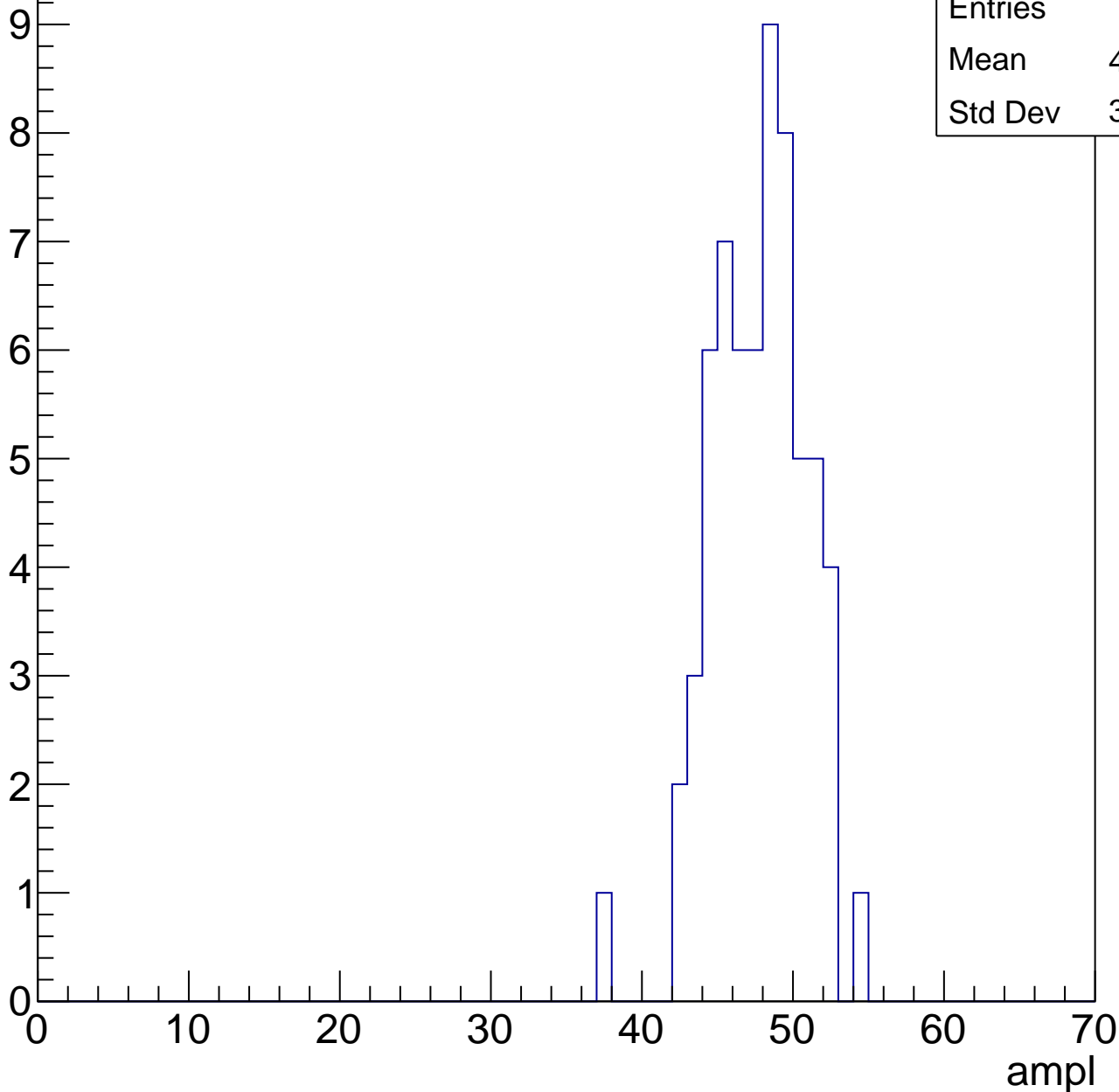


# B1L002S, U3-ch105, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	47.27
Std Dev	3.077

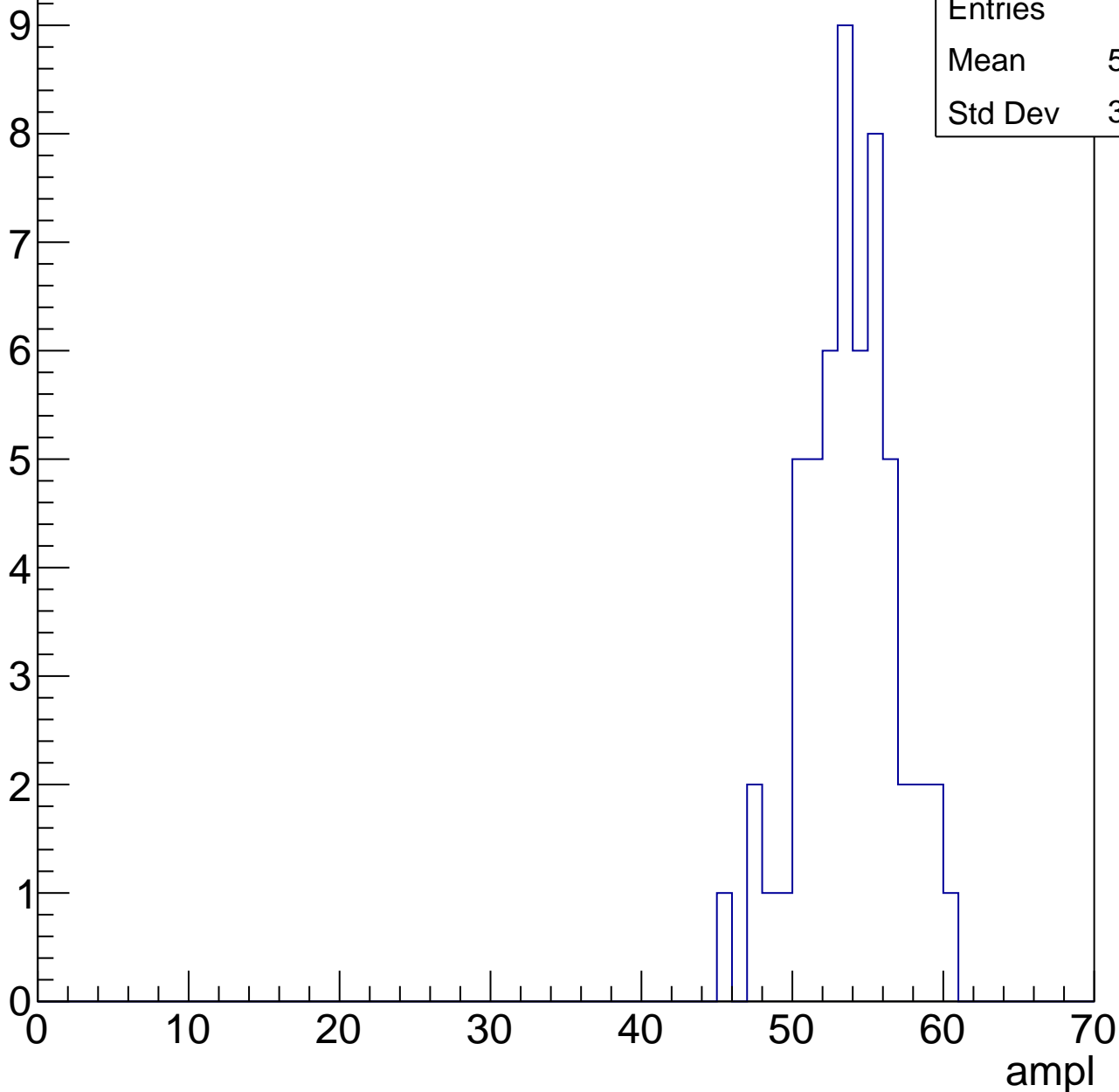


# B1L002S, U3-ch105, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.25
Std Dev	3.078

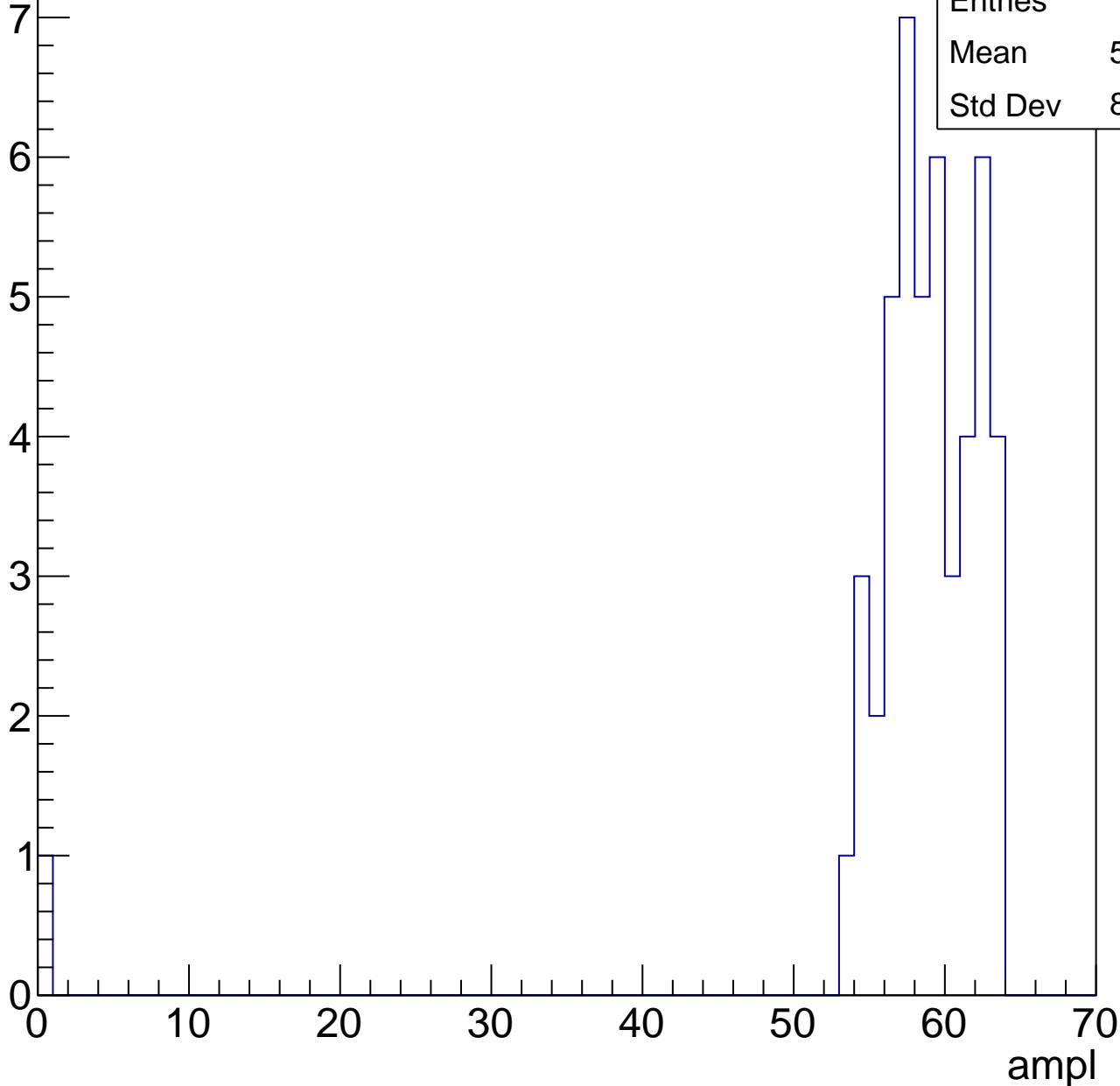


# B1L002S, U3-ch105, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	57.36
Std Dev	8.888

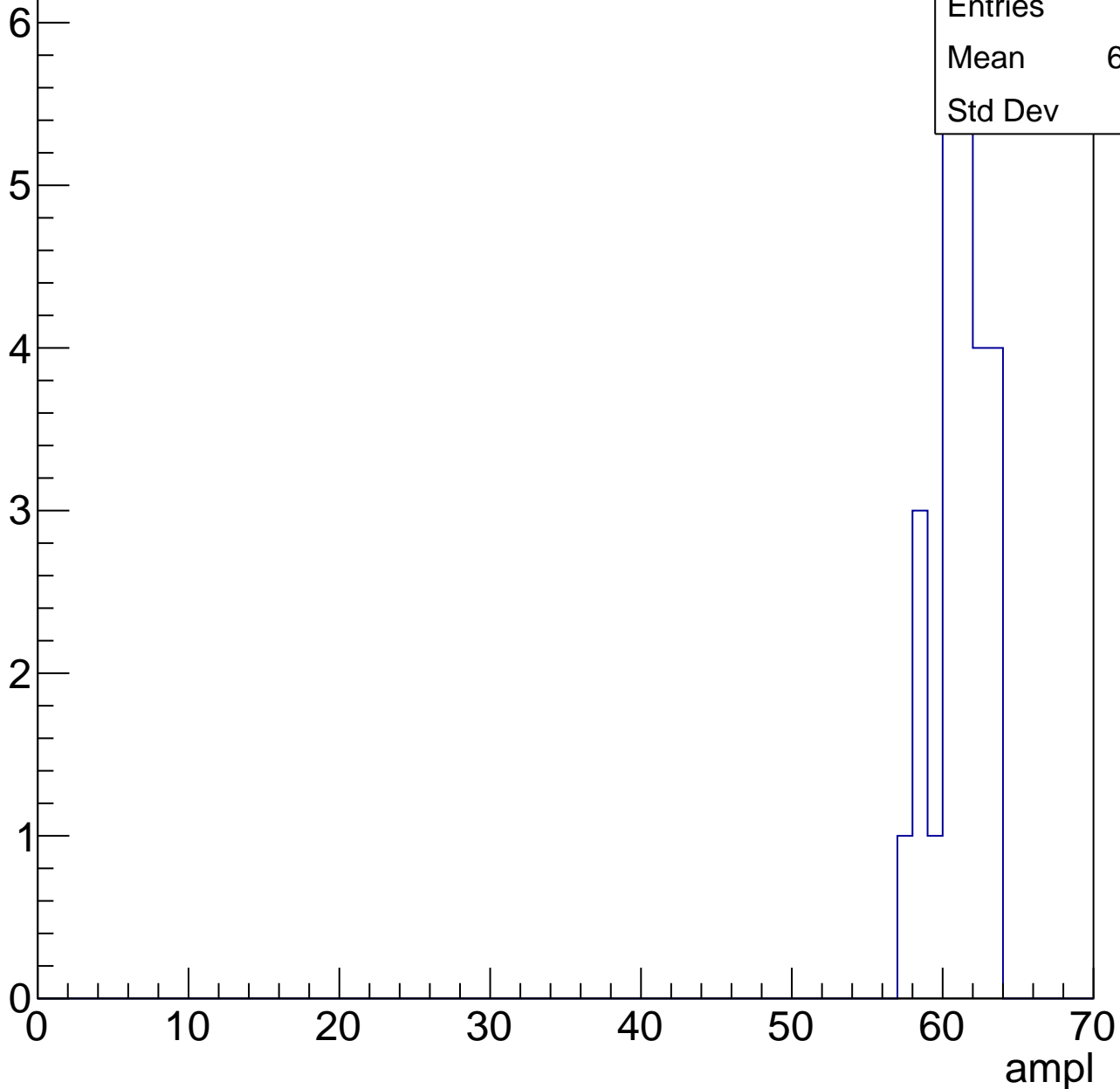


# B1L002S, U3-ch105, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	60.64
Std Dev	1.67

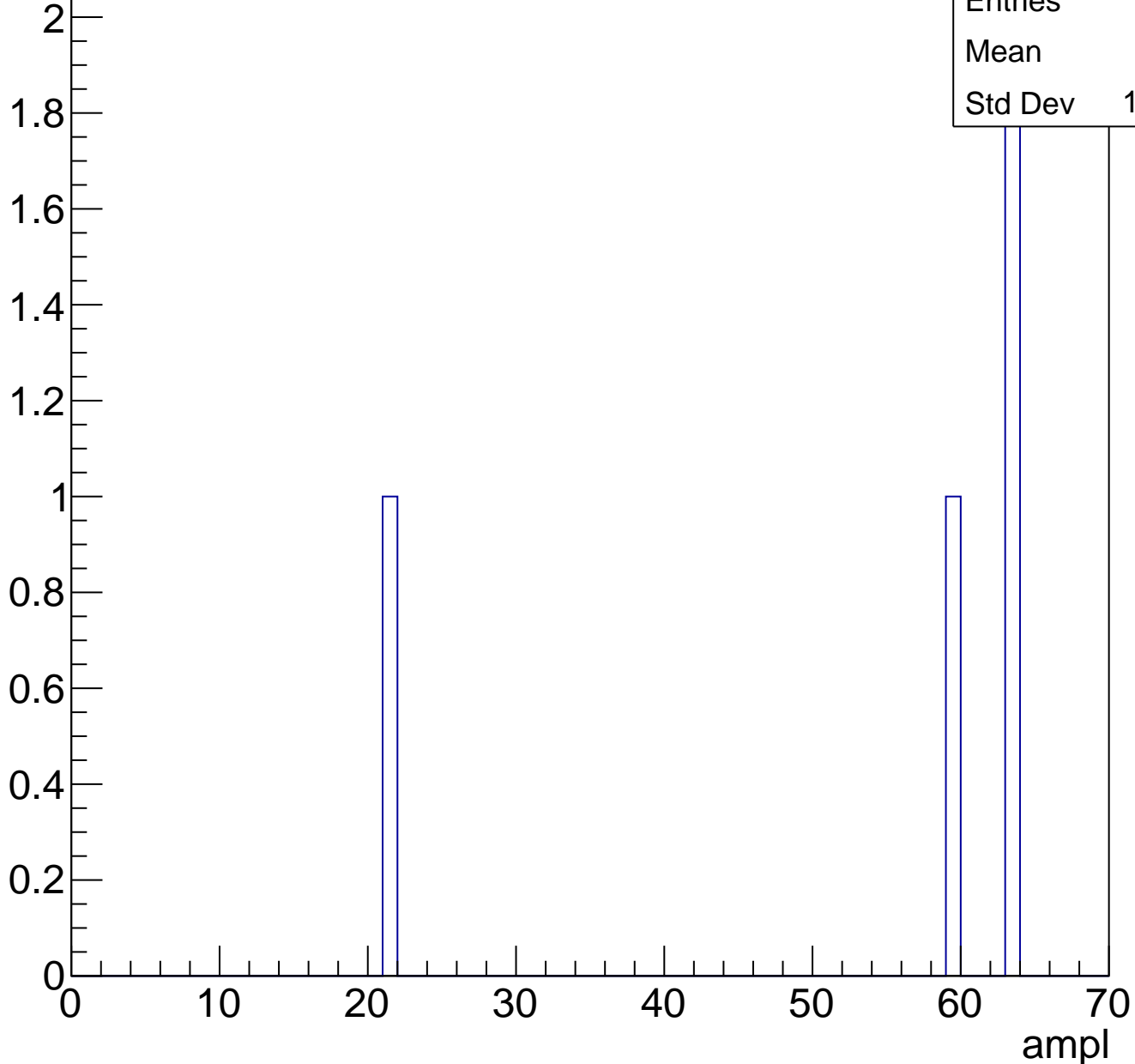




# B1L002S, U3-ch105, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	4
Mean	51.5
Std Dev	17.68

# B1L002S, U3-ch106, adc0

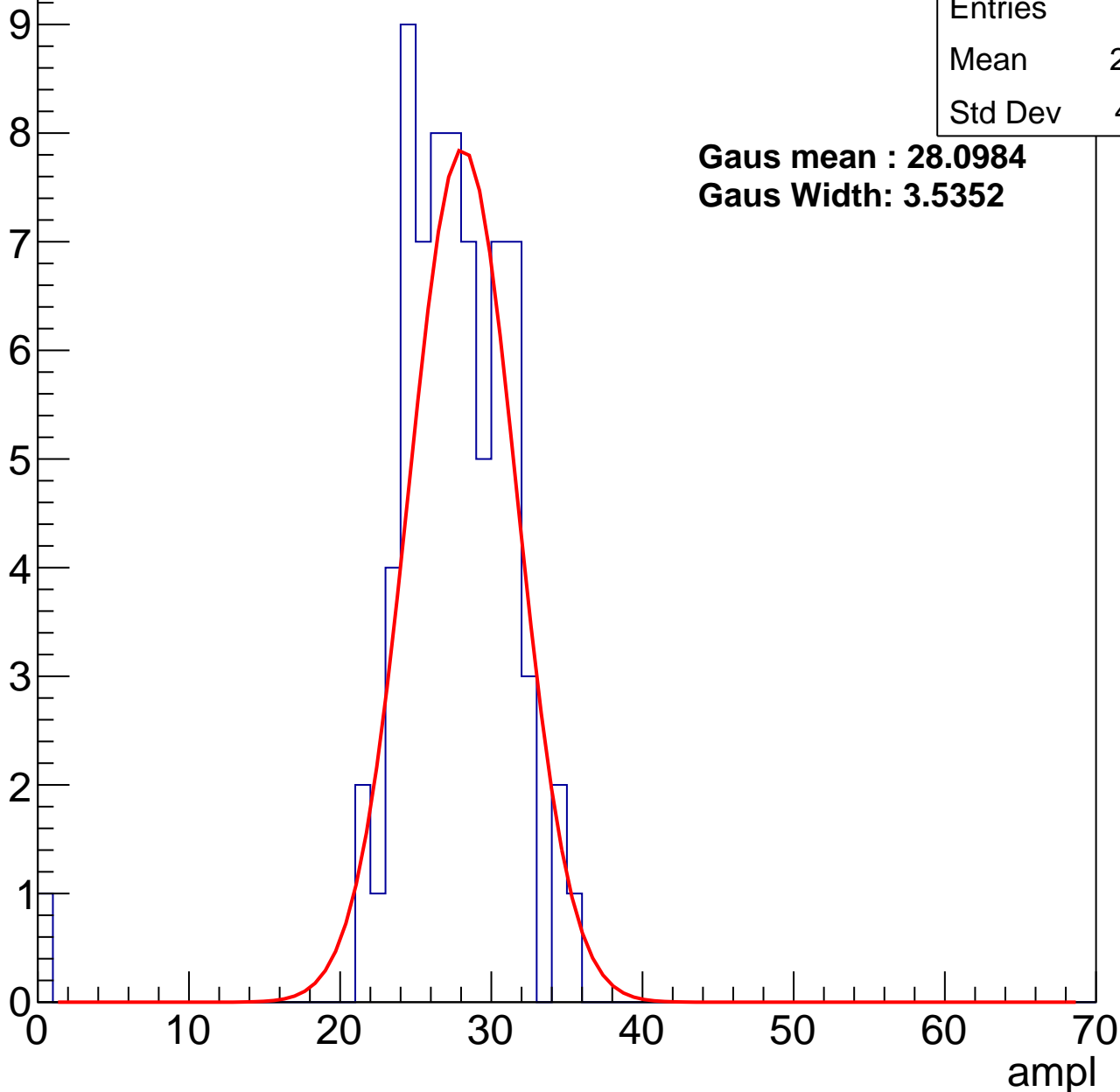
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	26.92
Std Dev	4.481

**Gaus mean : 28.0984**

**Gaus Width: 3.5352**



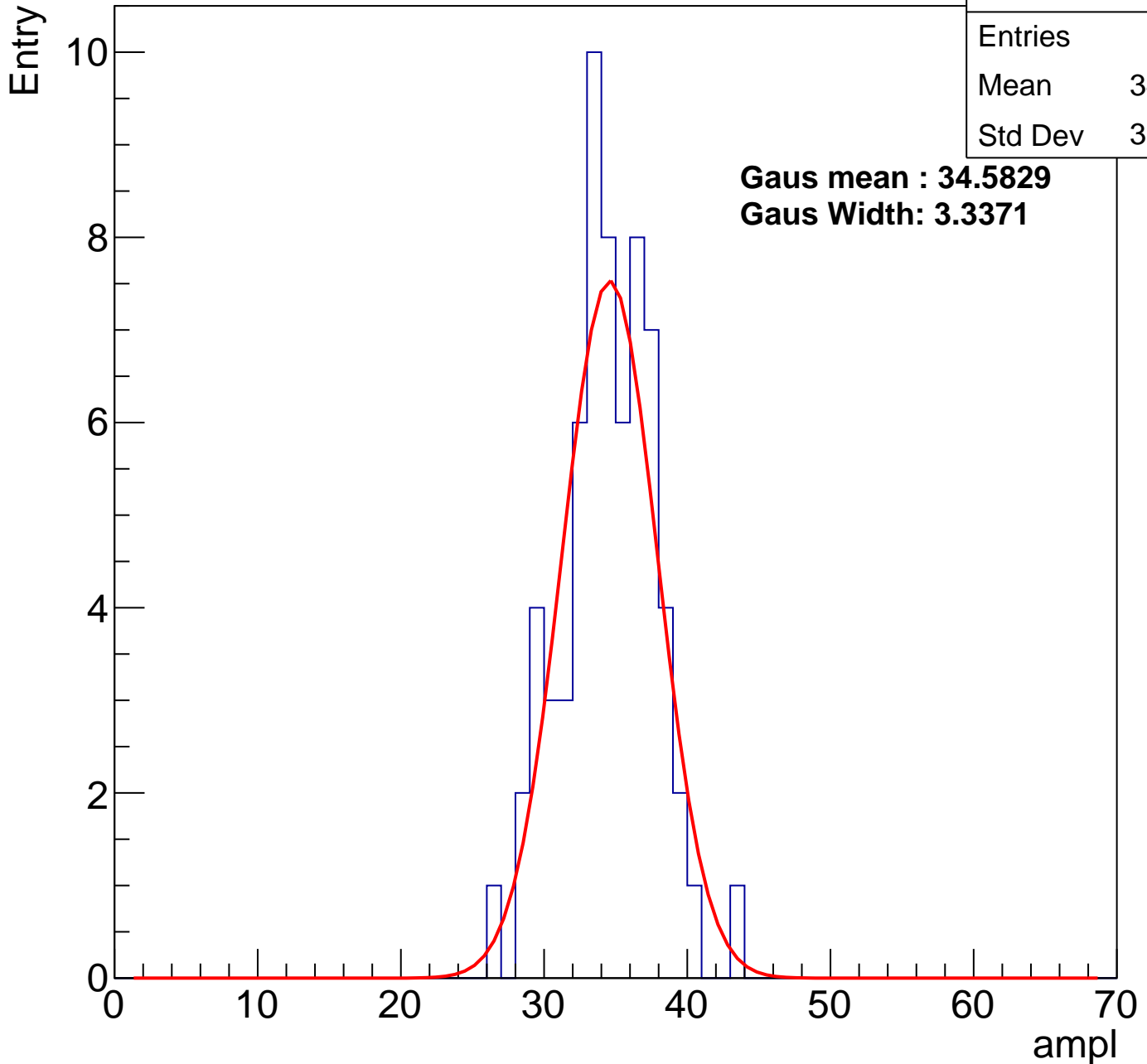
# B1L002S, U3-ch106, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	66
Mean	34.02
Std Dev	3.203

**Gaus mean : 34.5829**

**Gaus Width: 3.3371**



# B1L002S, U3-ch106, adc2

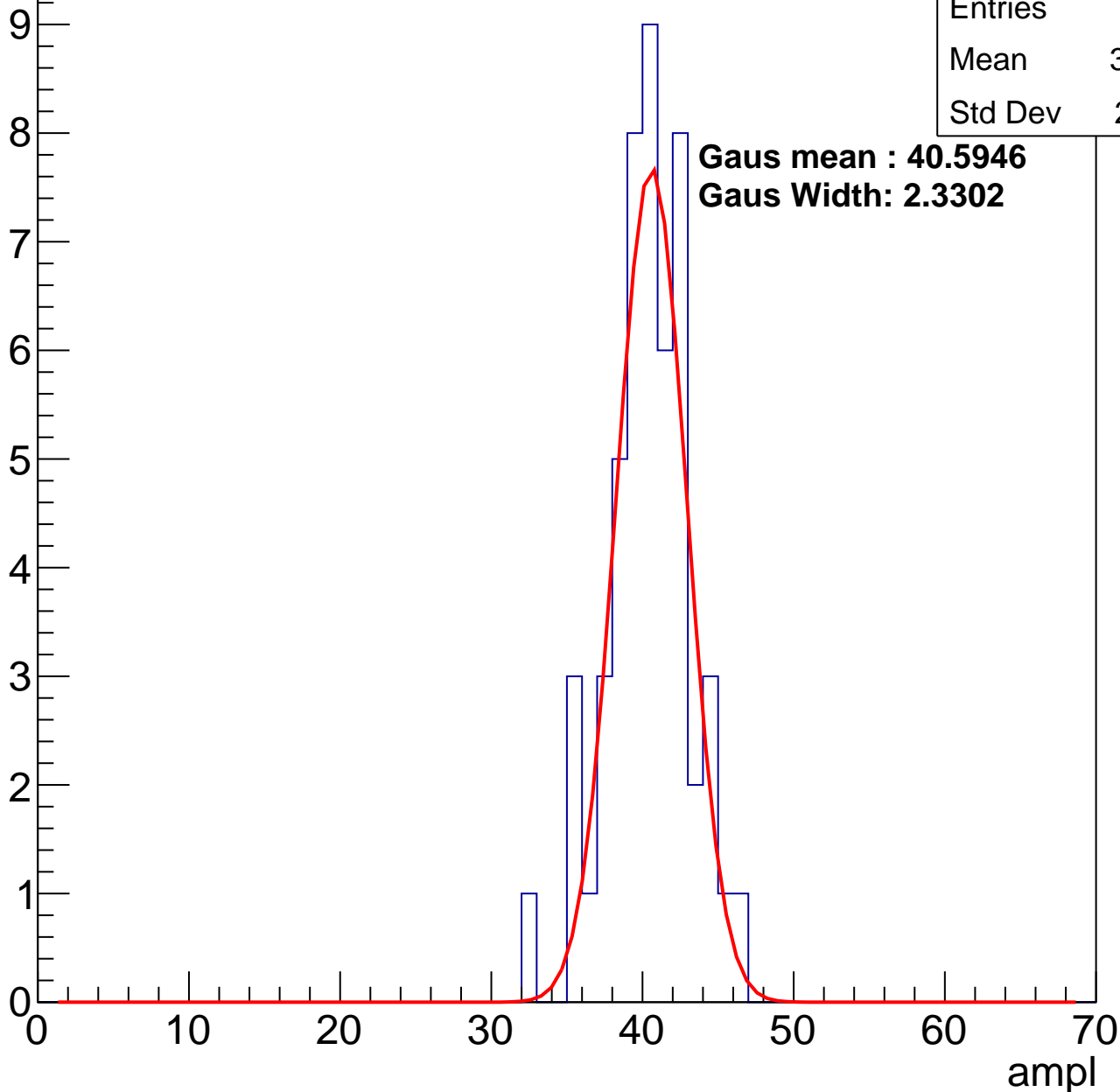
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	39.94
Std Dev	2.711

**Gaus mean : 40.5946**

**Gaus Width: 2.3302**

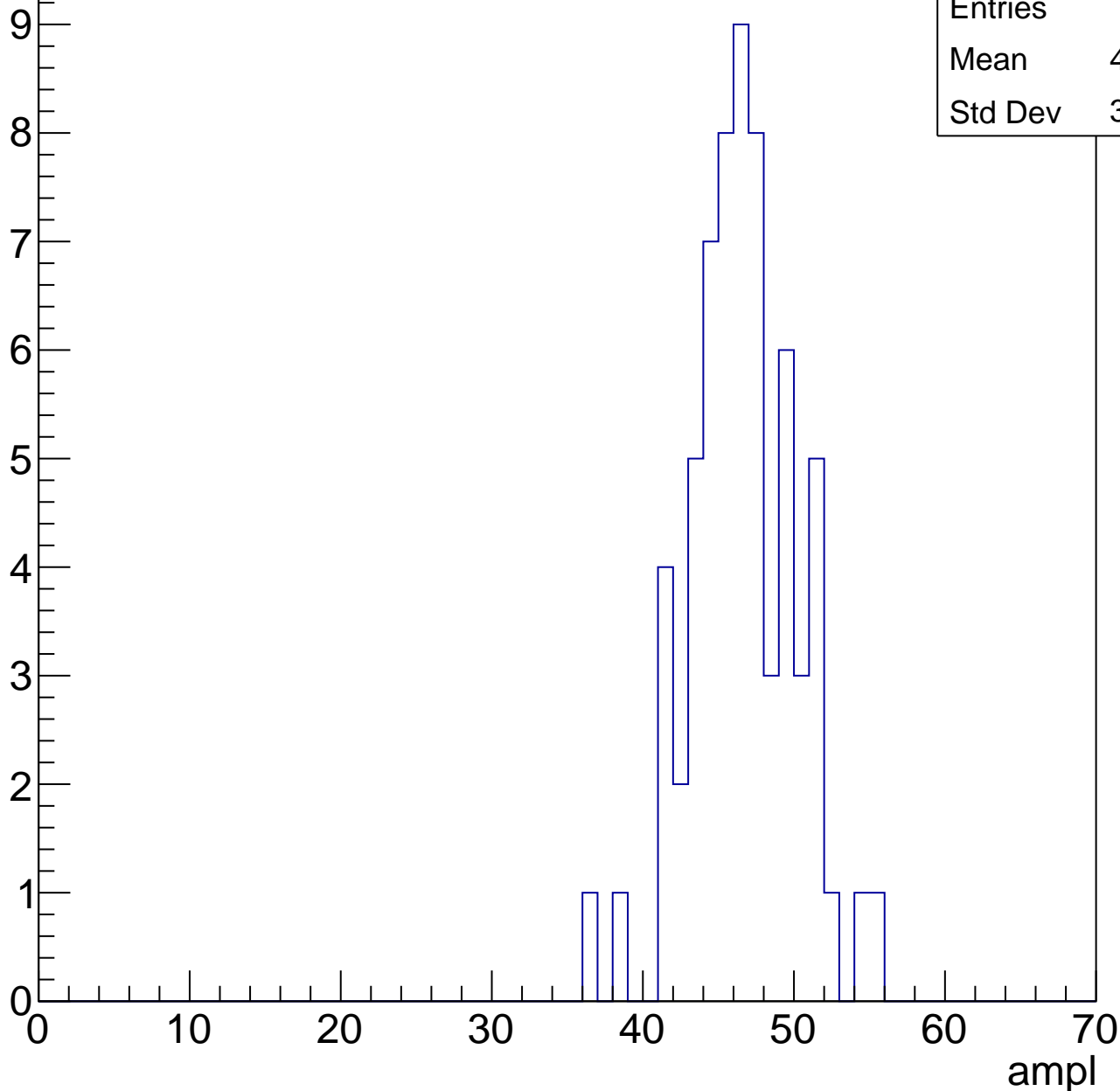


# B1L002S, U3-ch106, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	46.14
Std Dev	3.516

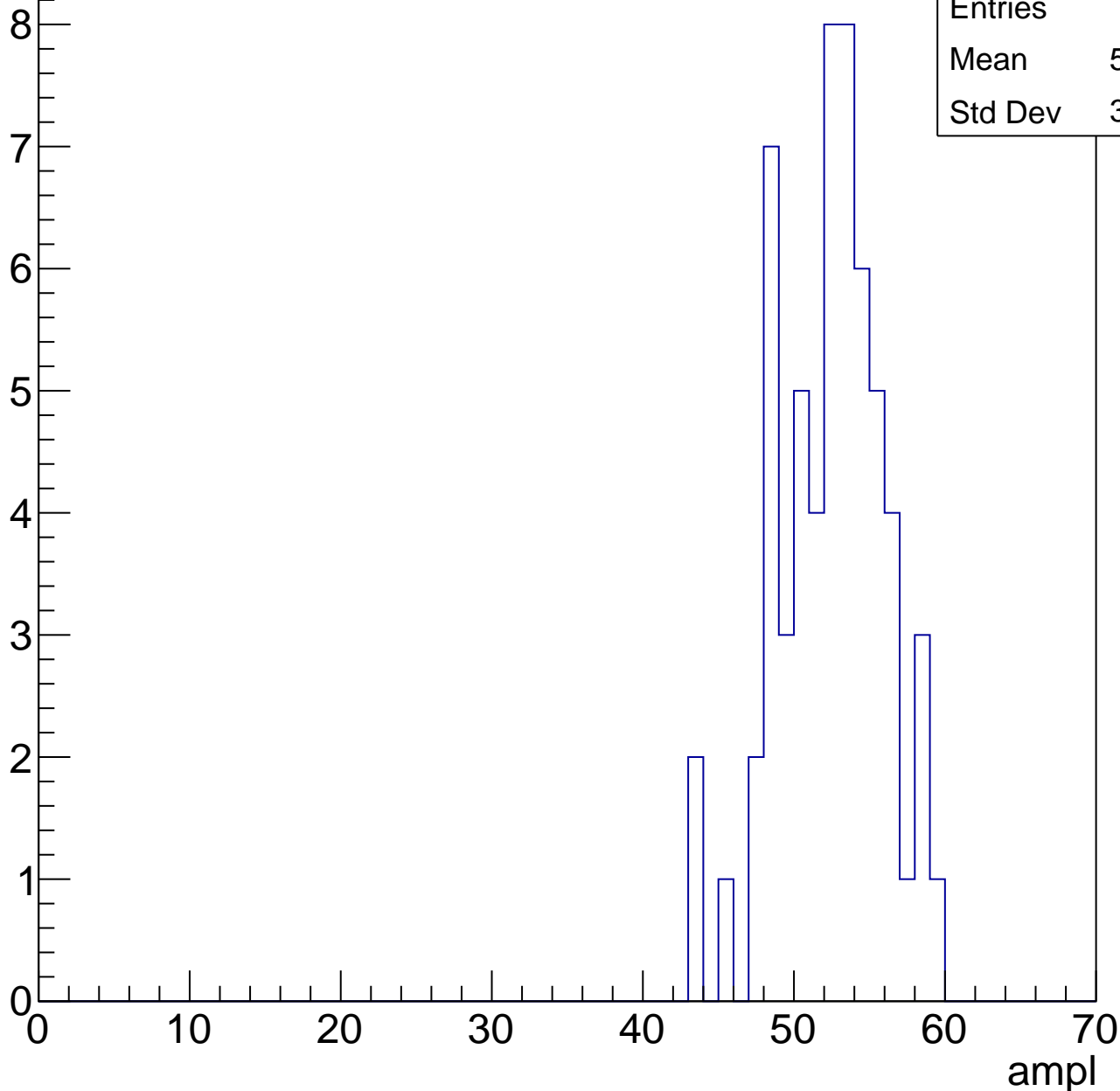


# B1L002S, U3-ch106, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

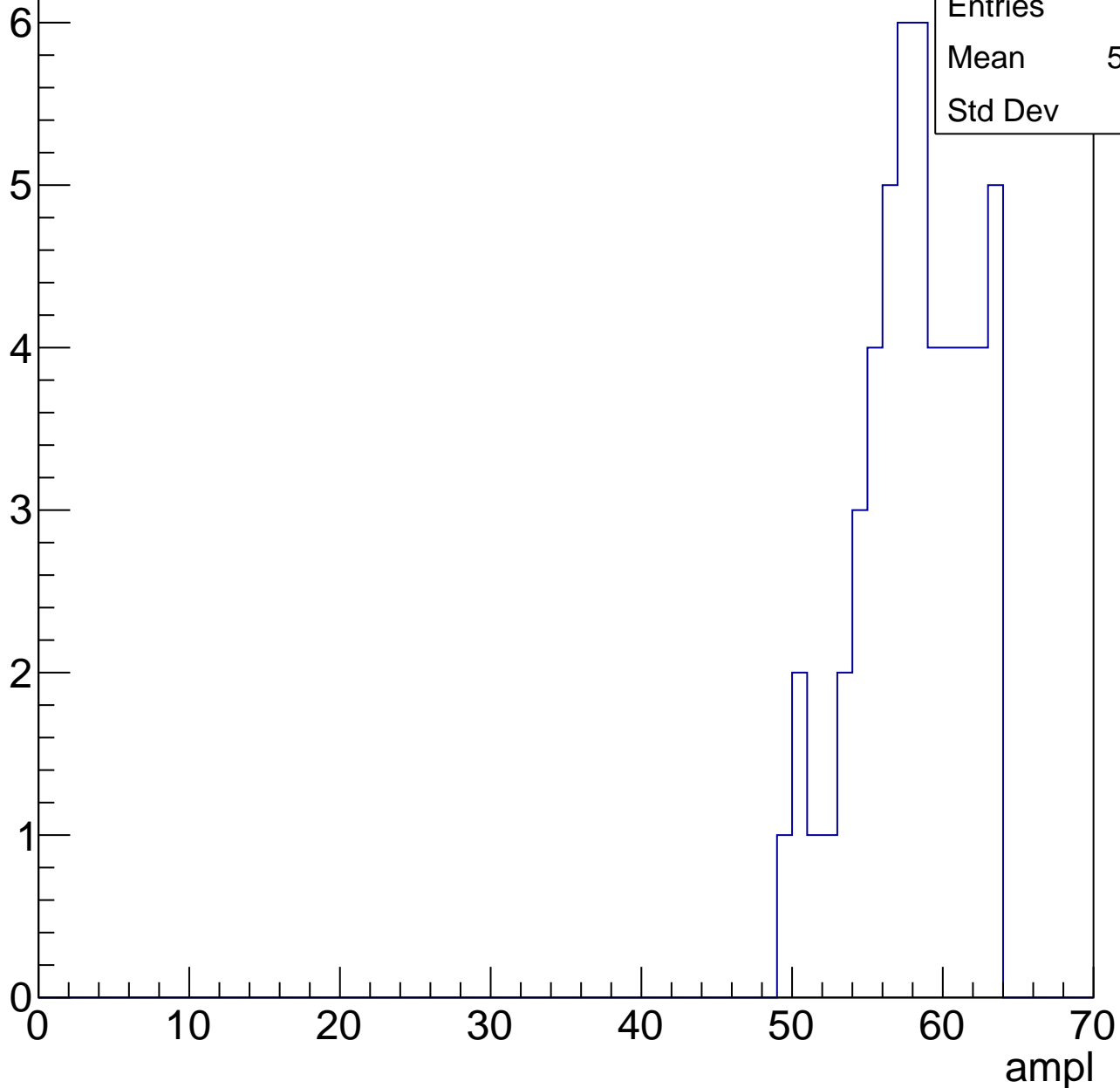
Entries	60
Mean	51.92
Std Dev	3.527



# B1L002S, U3-ch106, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

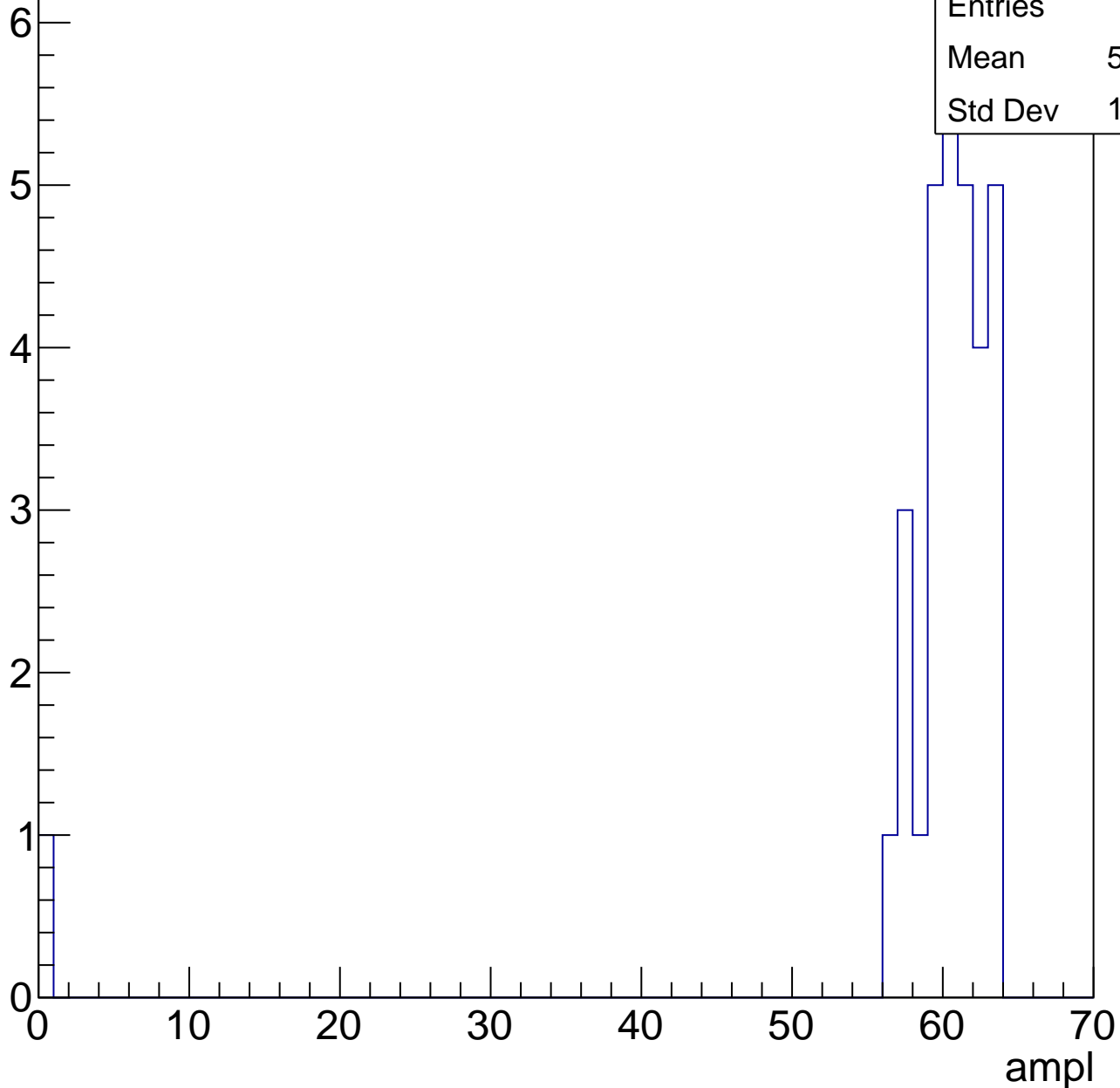


Entries	52
Mean	57.56
Std Dev	3.64

# B1L002S, U3-ch106, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch106, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

2  
1.8  
1.6  
1.4  
1.2  
1  
0.8  
0.6  
0.4  
0.2  
0

Entries	3
Mean	61.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

# B1L002S, U3-ch107, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	77
Mean	27.77
Std Dev	5.422

**Gaus mean : 29.2391**

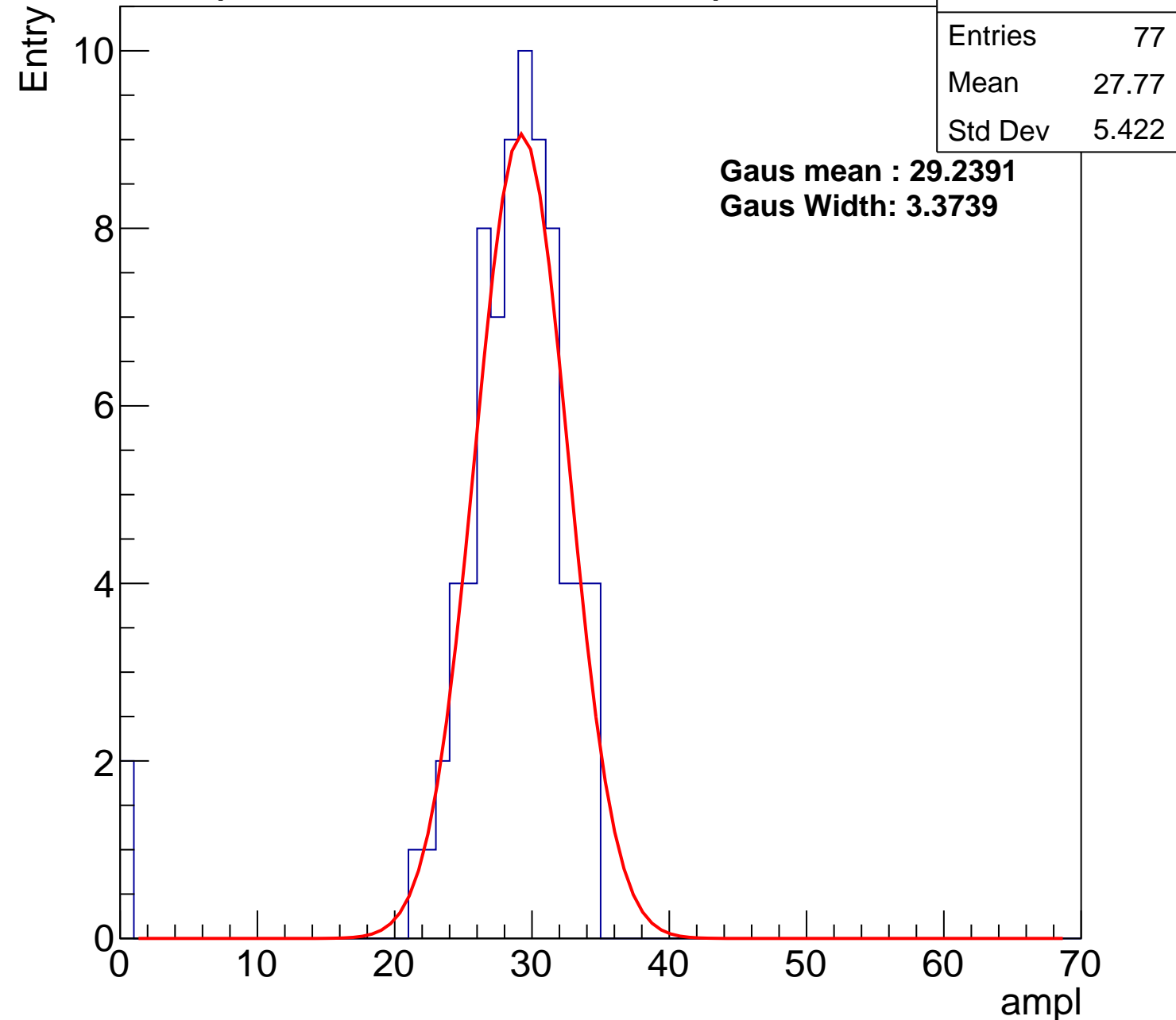
**Gaus Width: 3.3739**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch107, adc1

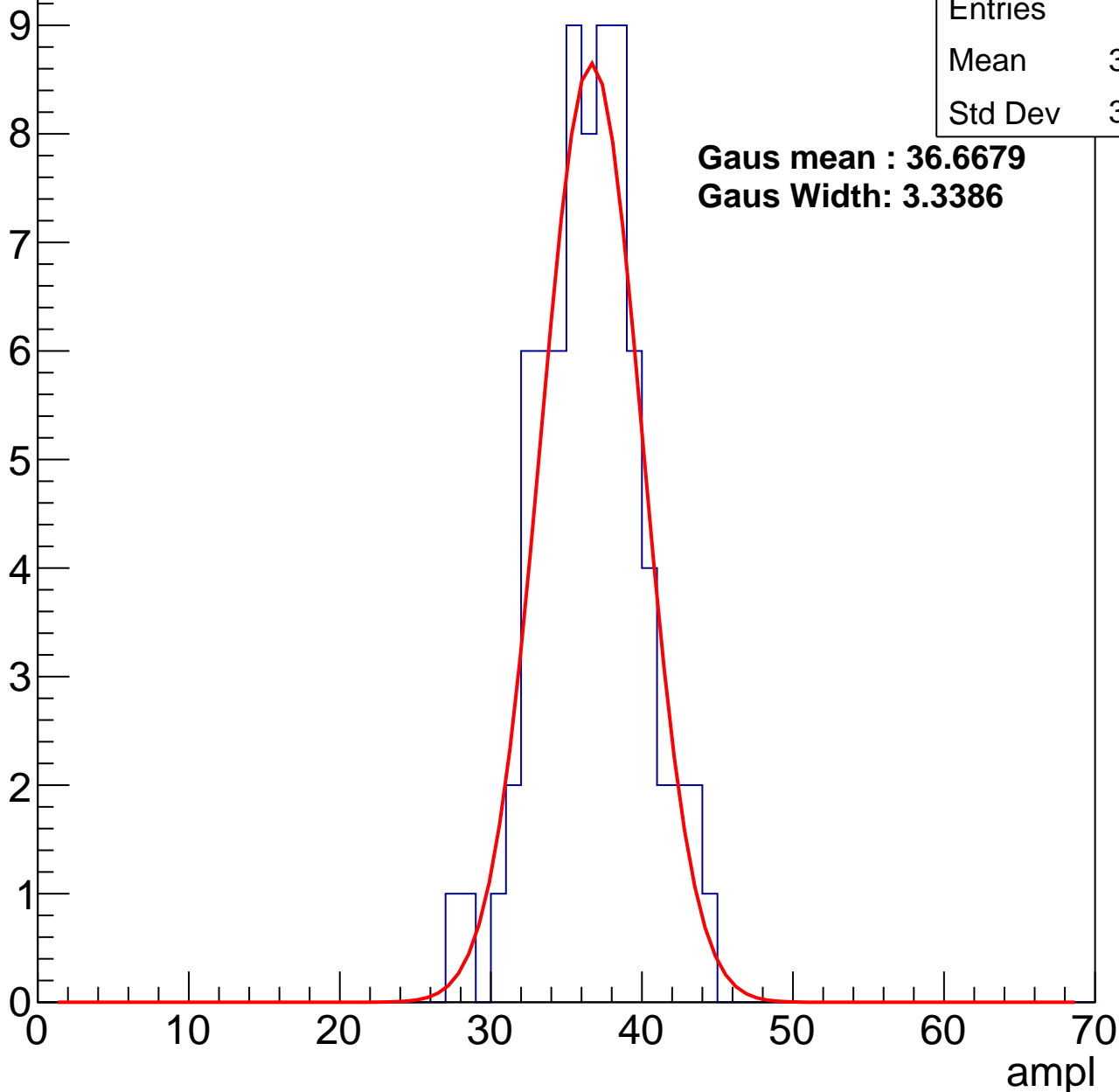
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	36.12
Std Dev	3.386

**Gaus mean : 36.6679**

**Gaus Width: 3.3386**



# B1L002S, U3-ch107, adc2

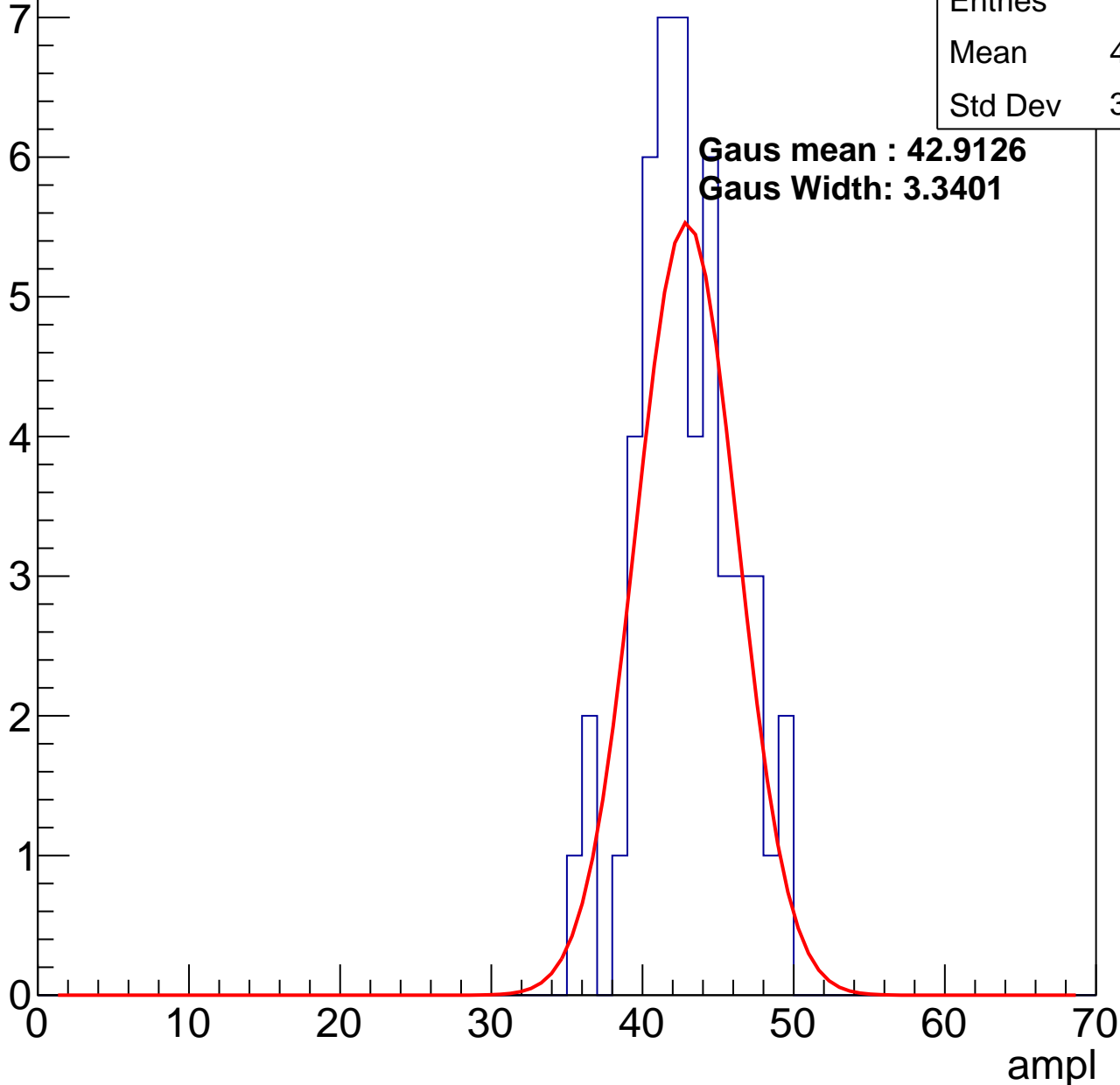
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	42.36
Std Dev	3.192

**Gaus mean : 42.9126**

**Gaus Width: 3.3401**

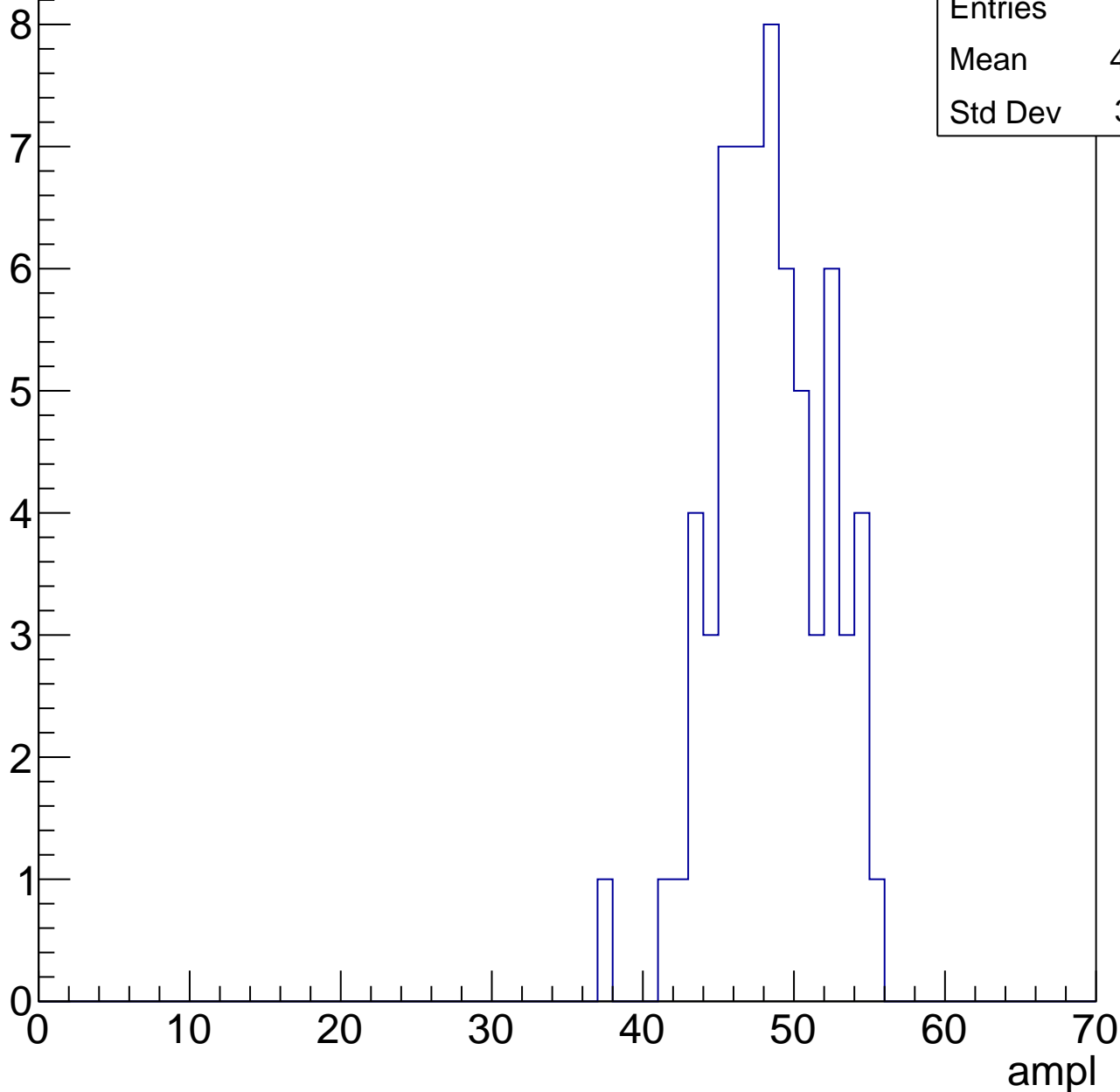


# B1L002S, U3-ch107, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	47.96
Std Dev	3.601



# B1L002S, U3-ch107, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

7  
6  
5  
4  
3  
2  
1  
0

Entries	47
Mean	53.57
Std Dev	2.923

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

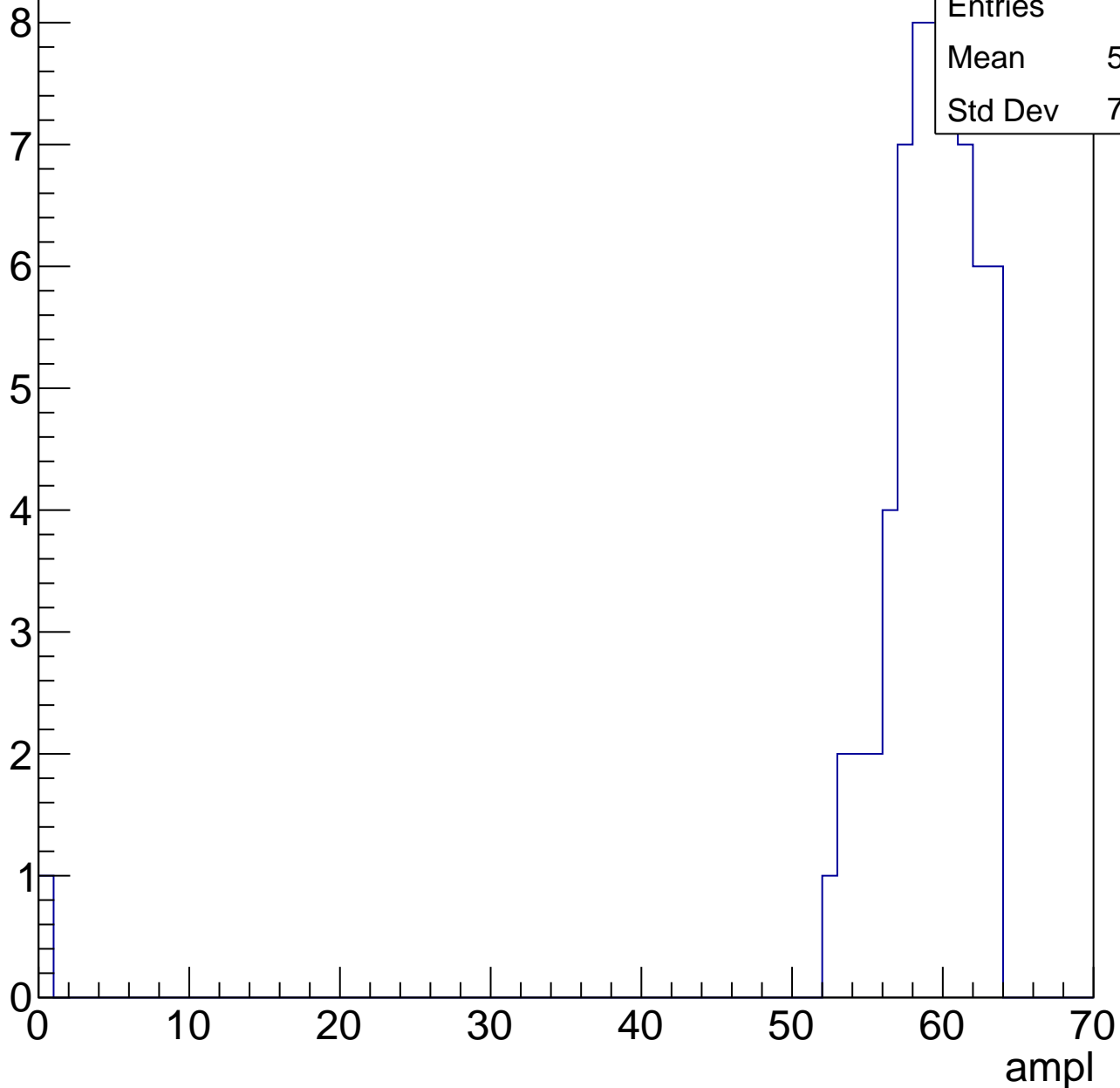
7

# B1L002S, U3-ch107, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

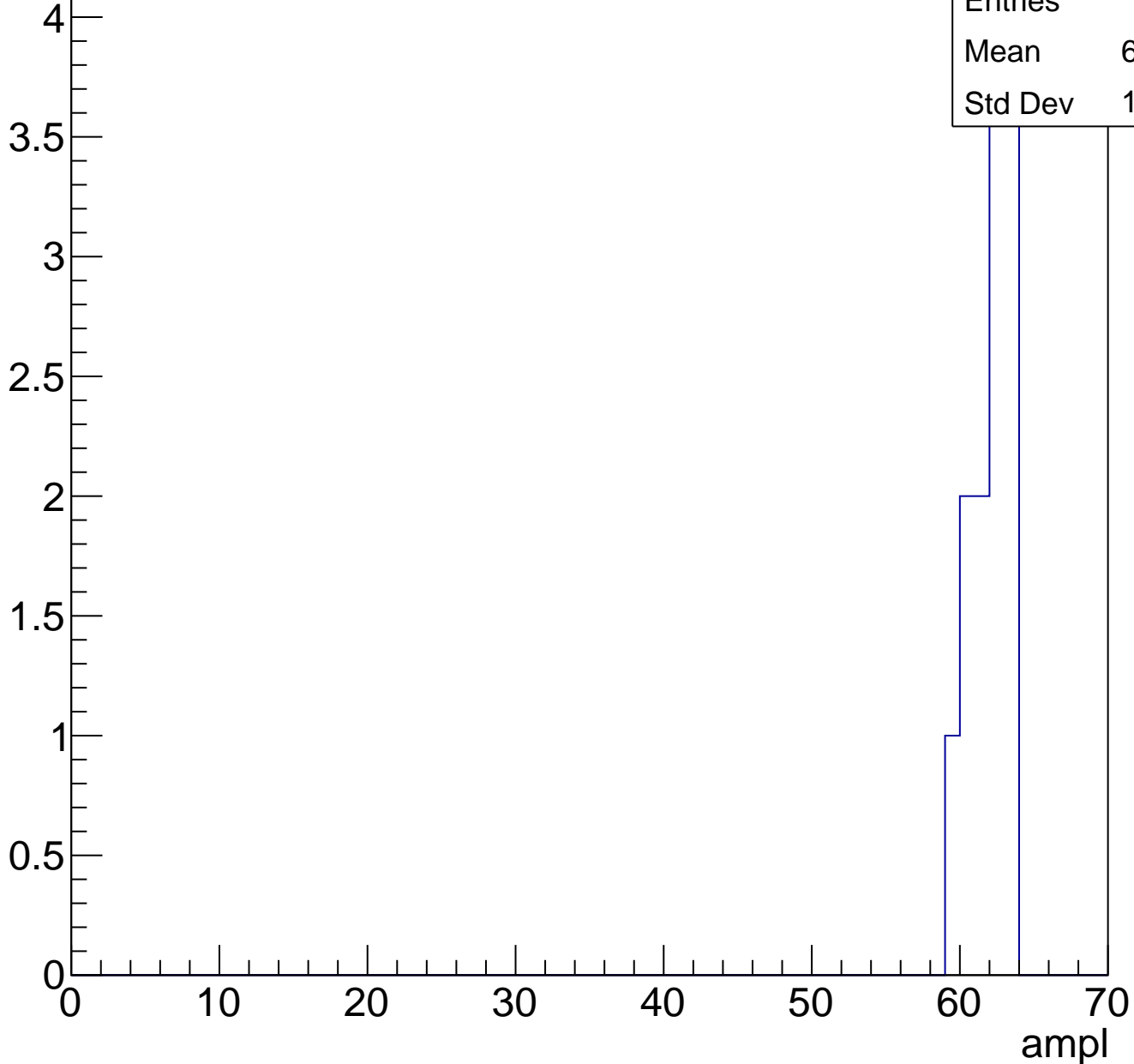
Entries	62
Mean	57.94
Std Dev	7.902



# B1L002S, U3-ch107, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry





# B1L002S, U3-ch107, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch108, adc0

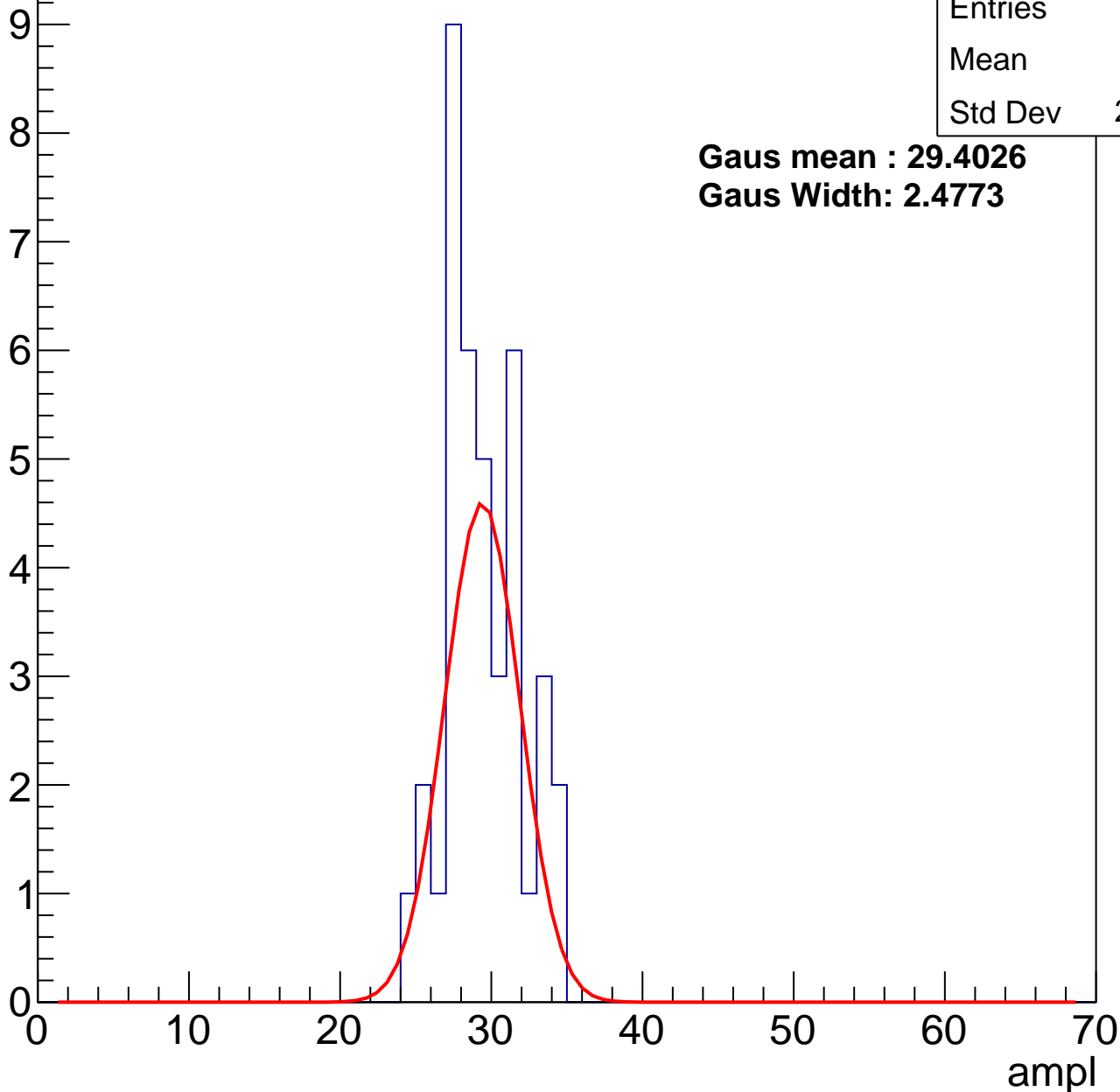
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	39
Mean	29
Std Dev	2.491

**Gaus mean : 29.4026**

**Gaus Width: 2.4773**



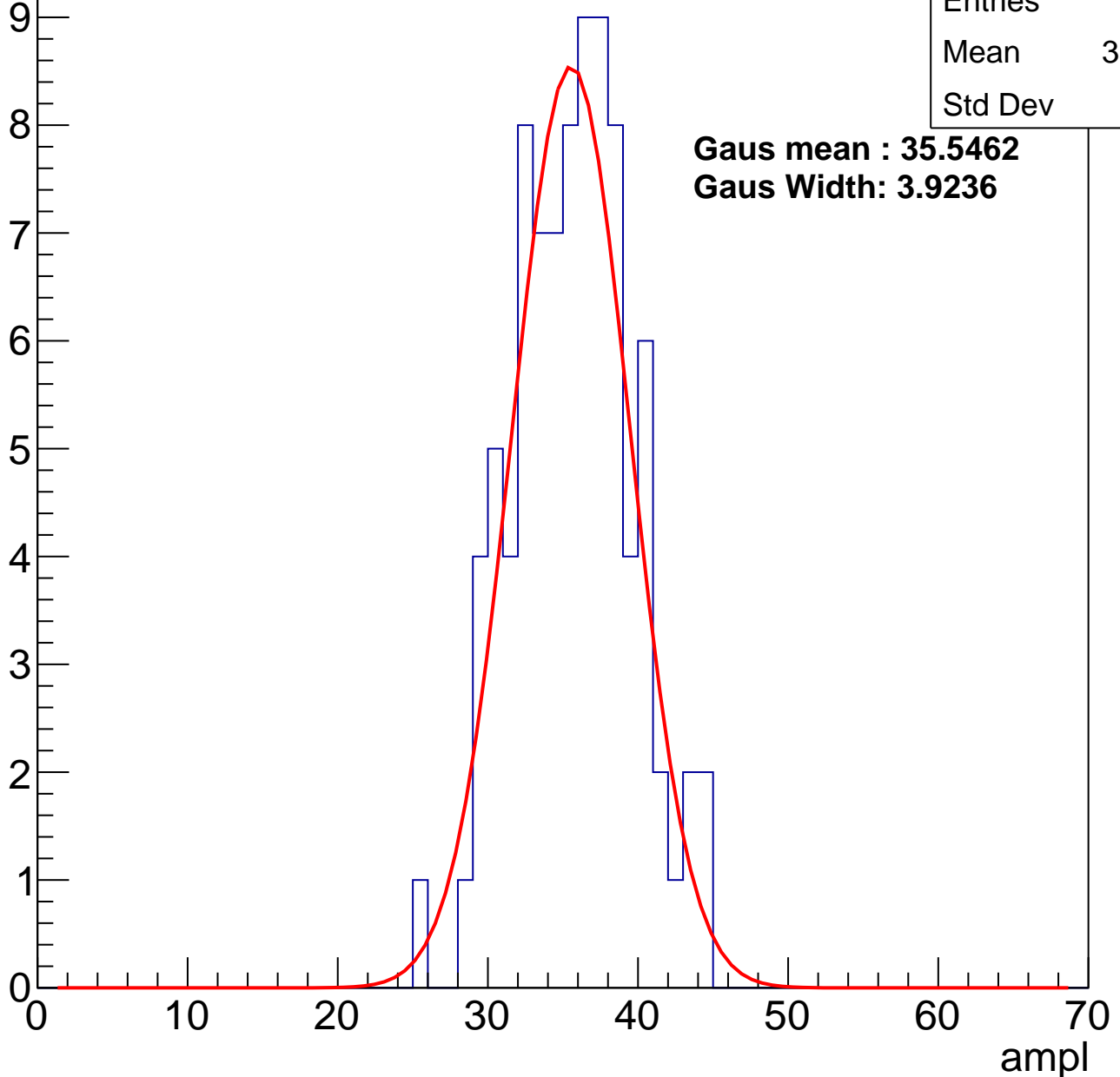
# B1L002S, U3-ch108, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	88
Mean	35.26
Std Dev	3.88

**Gaus mean : 35.5462**  
**Gaus Width: 3.9236**



# B1L002S, U3-ch108, adc2

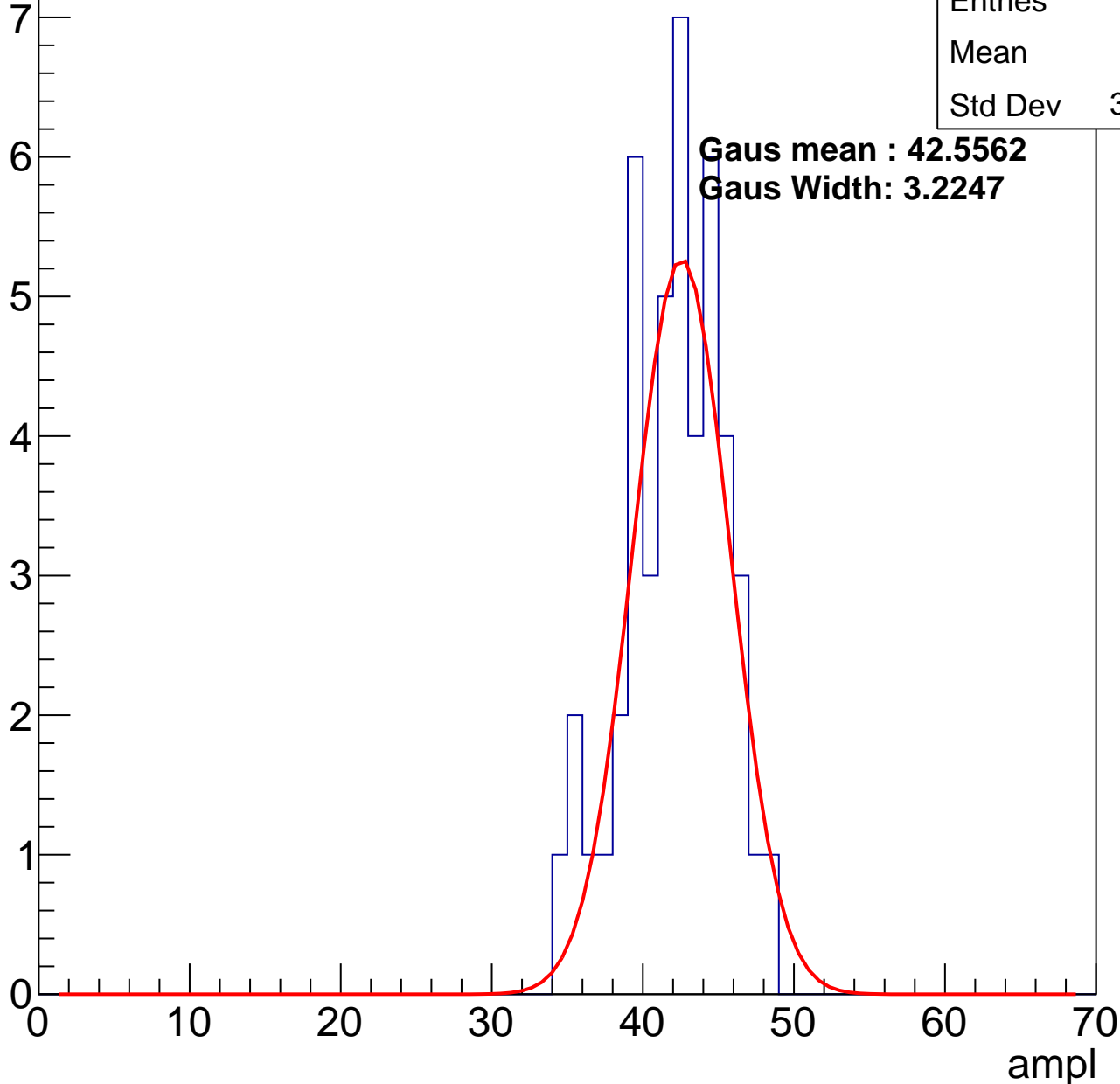
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	41.6
Std Dev	3.233

**Gaus mean : 42.5562**

**Gaus Width: 3.2247**

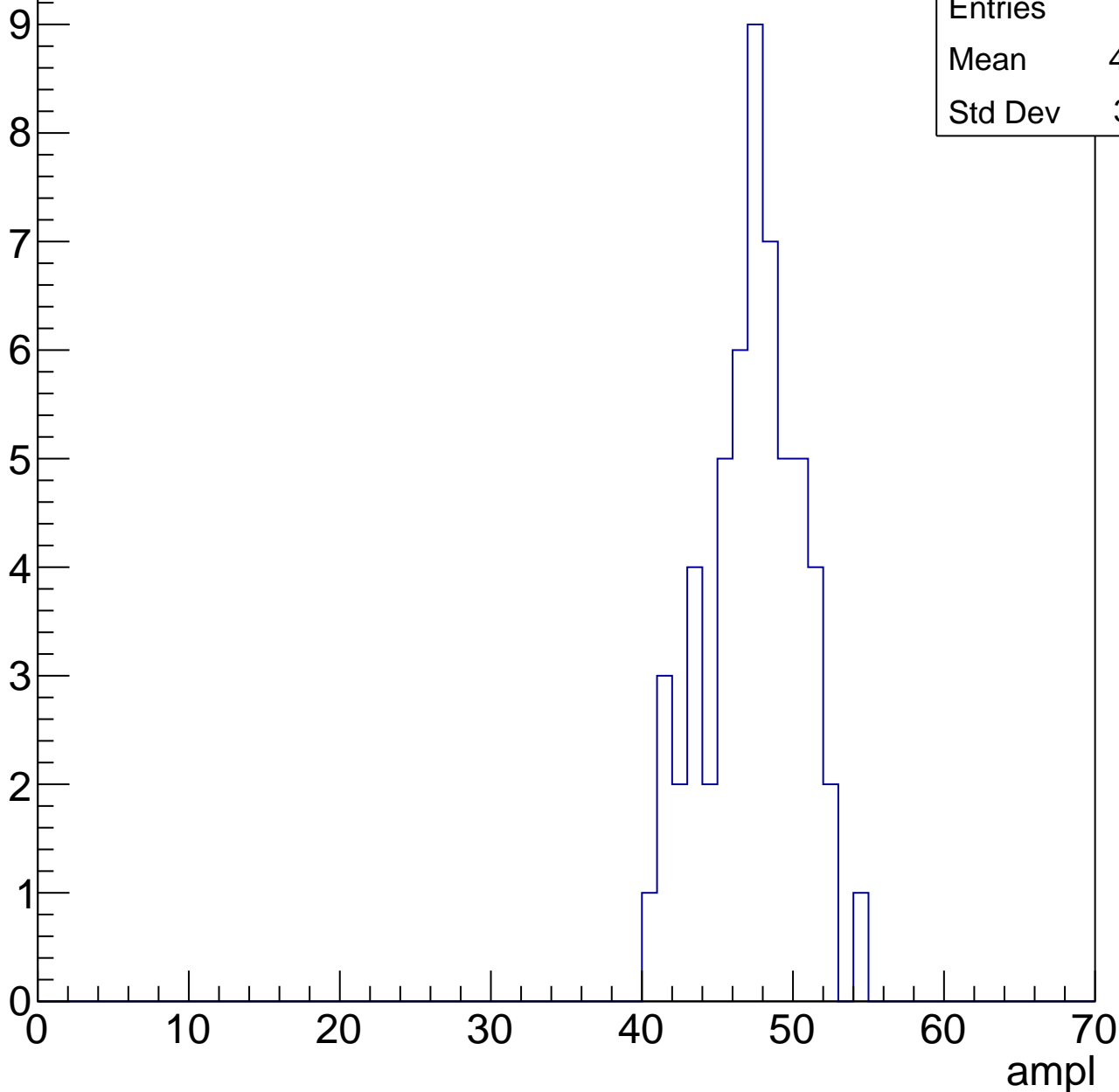


# B1L002S, U3-ch108, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

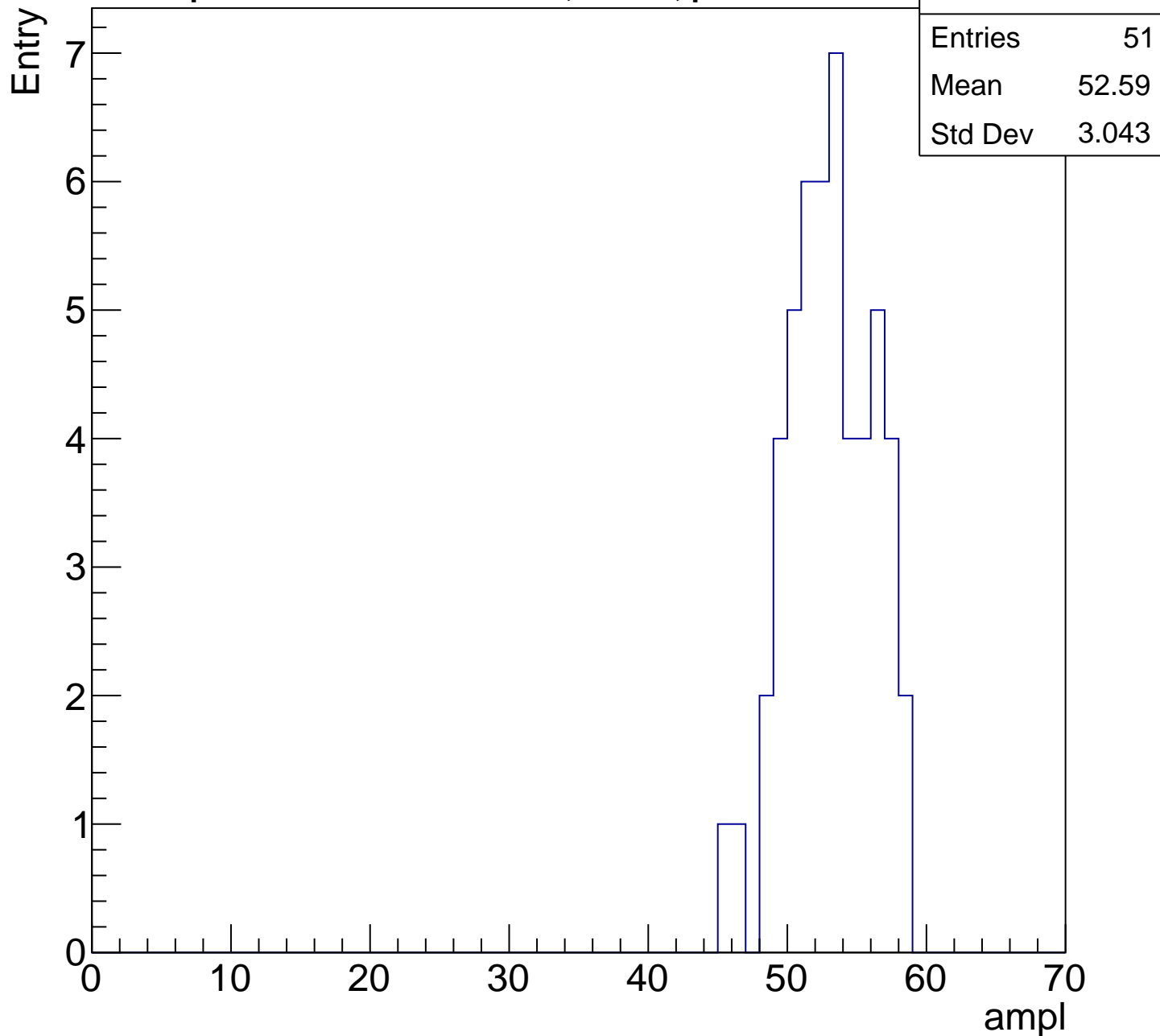
Entry

Entries	56
Mean	46.86
Std Dev	3.131



# B1L002S, U3-ch108, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

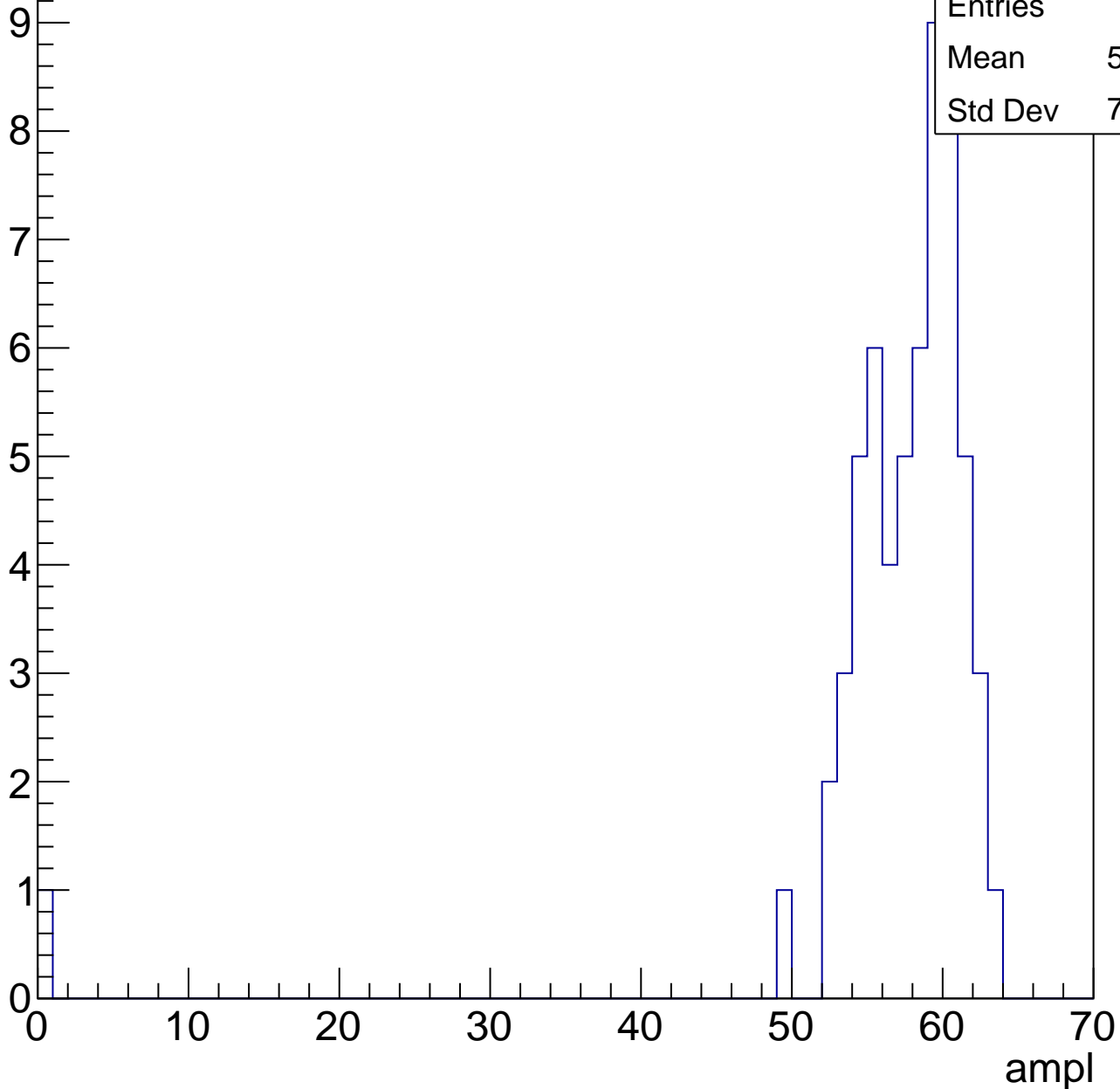


# B1L002S, U3-ch108, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	56.57
Std Dev	7.944

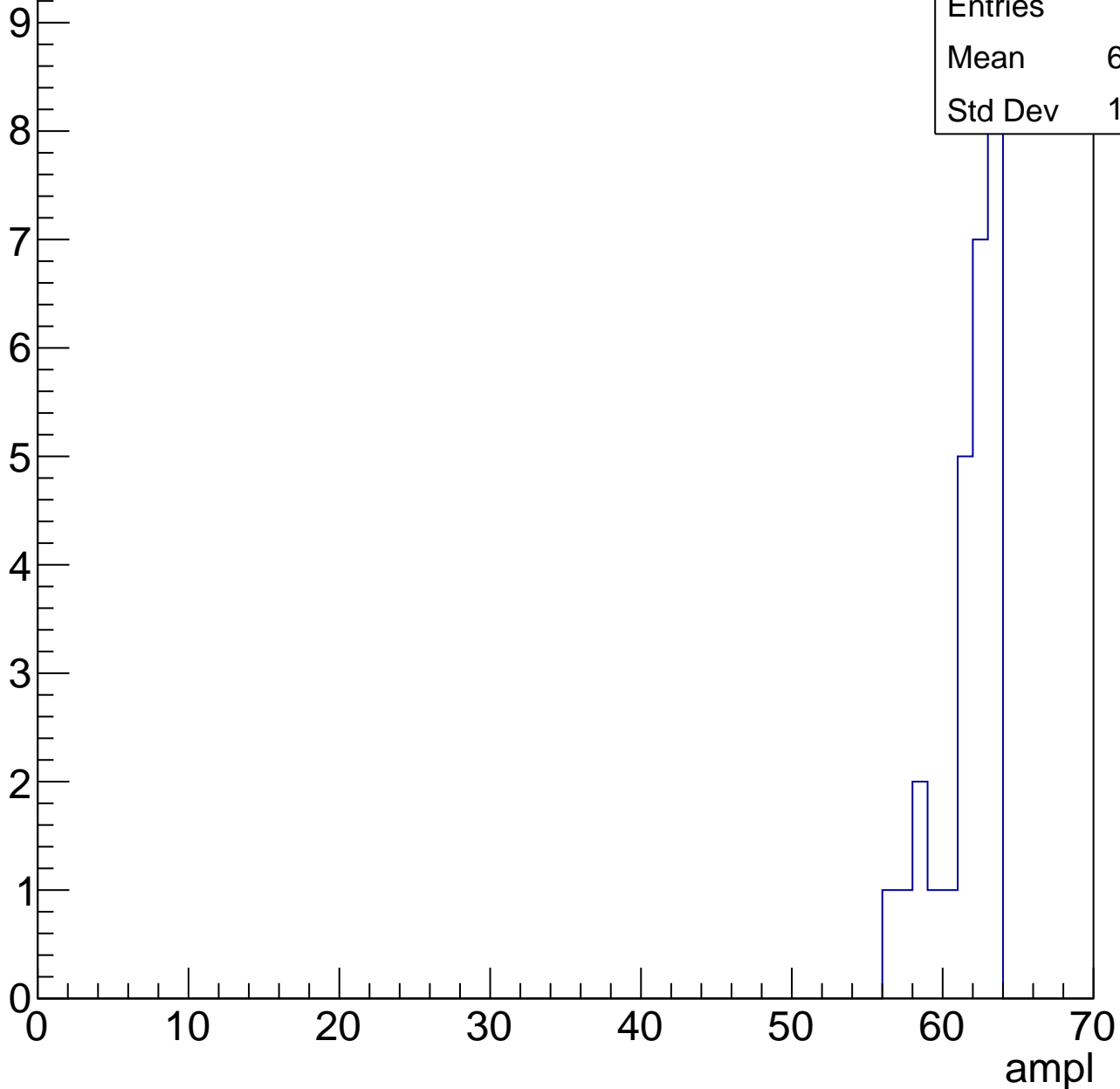


# B1L002S, U3-ch108, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	61.26
Std Dev	1.974





# B1L002S, U3-ch108, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	1
Mean	0
Std Dev	0

# B1L002S, U3-ch109, adc0

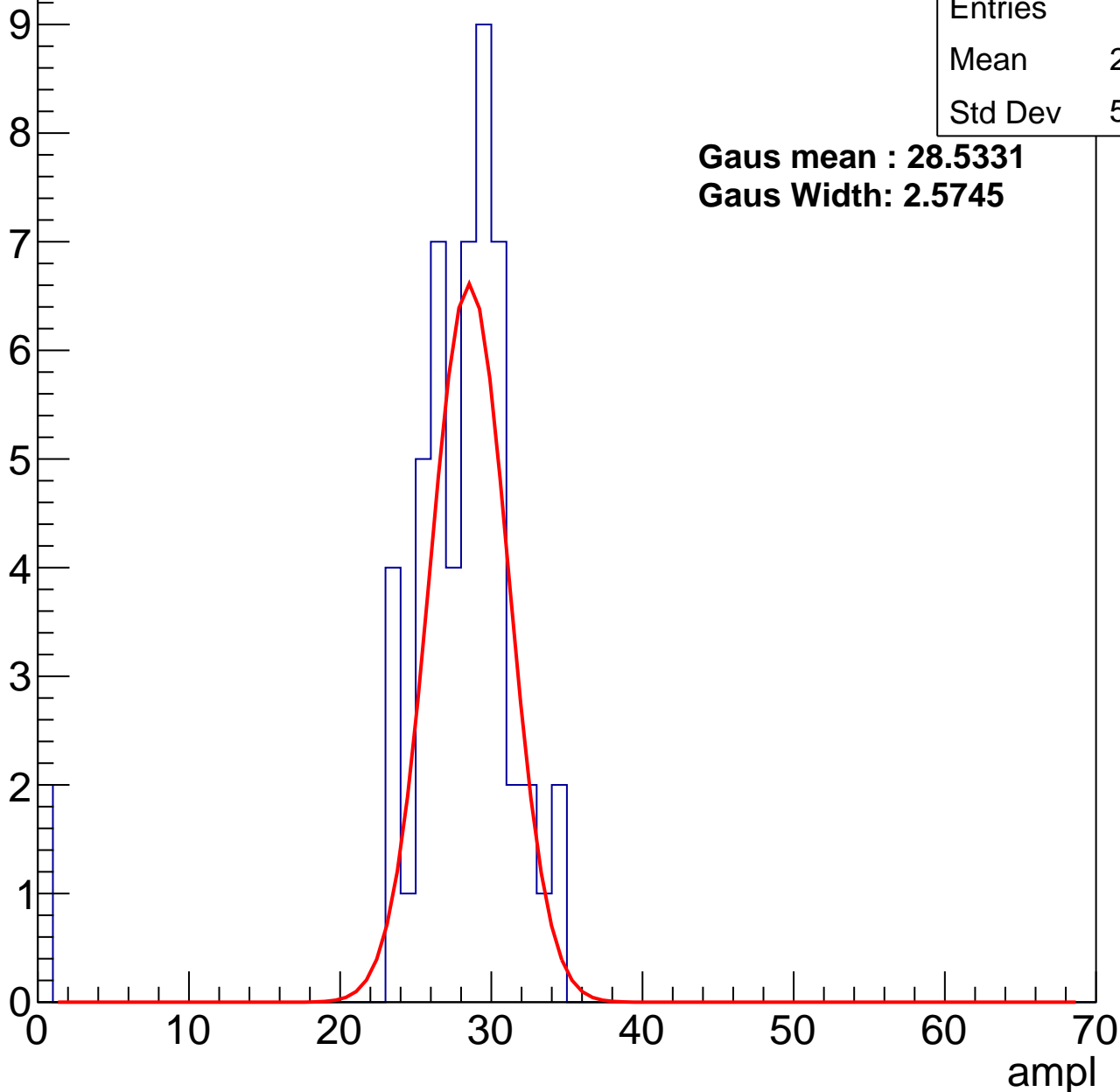
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	26.89
Std Dev	5.955

**Gaus mean : 28.5331**

**Gaus Width: 2.5745**



# B1L002S, U3-ch109, adc1

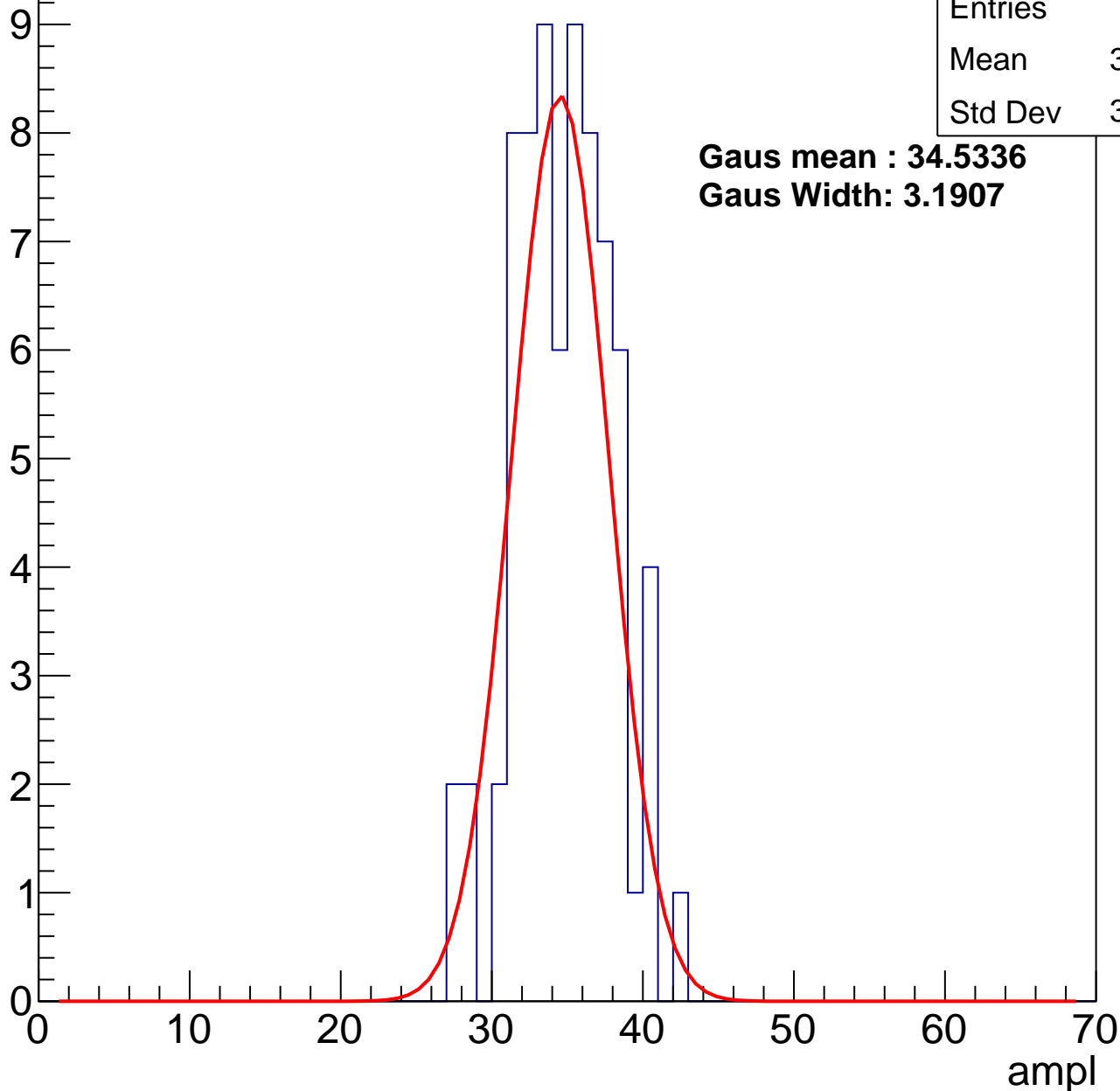
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	73
Mean	34.33
Std Dev	3.184

**Gaus mean : 34.5336**

**Gaus Width: 3.1907**



# B1L002S, U3-ch109, adc2

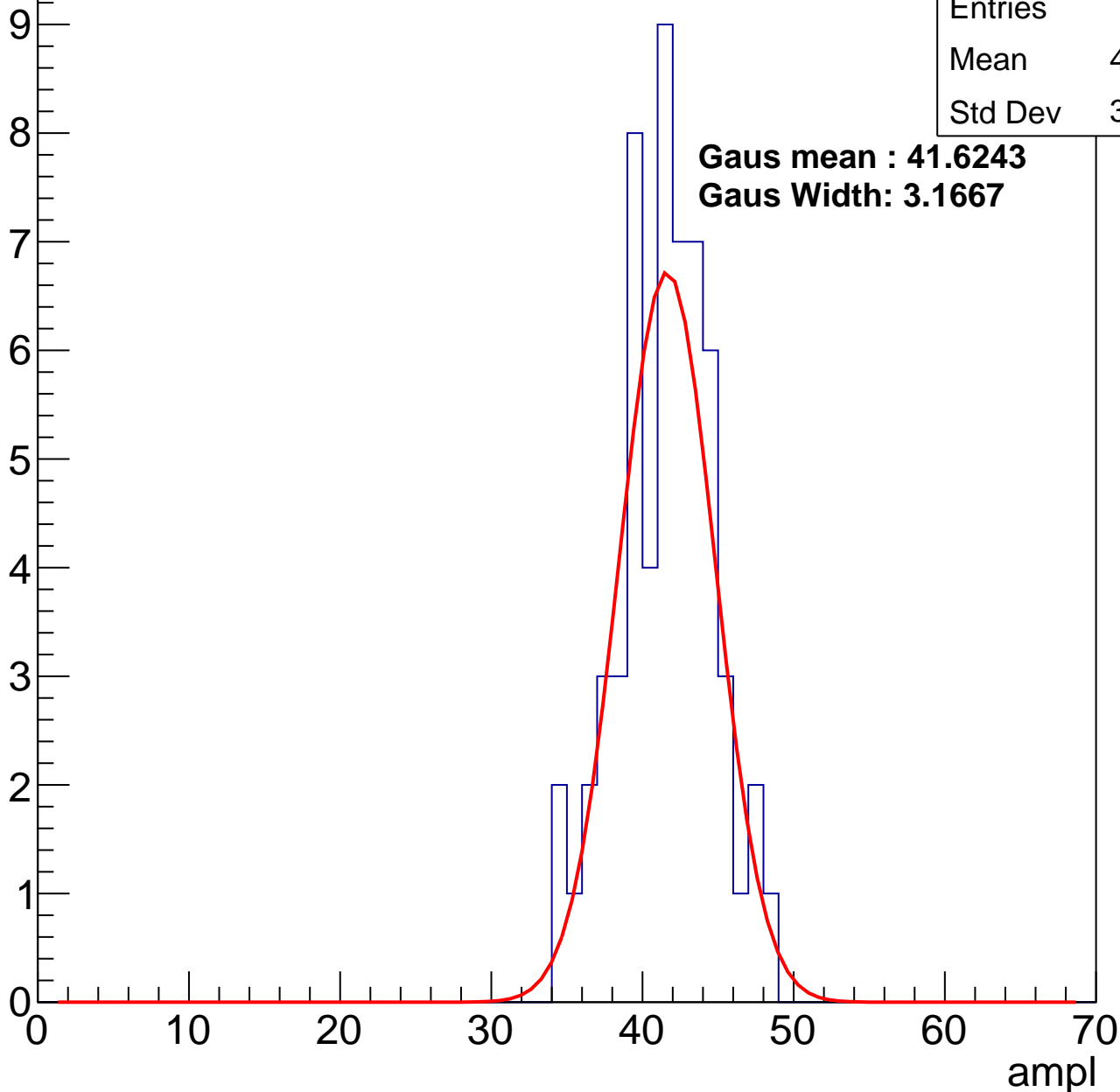
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	41.07
Std Dev	3.129

**Gaus mean : 41.6243**

**Gaus Width: 3.1667**

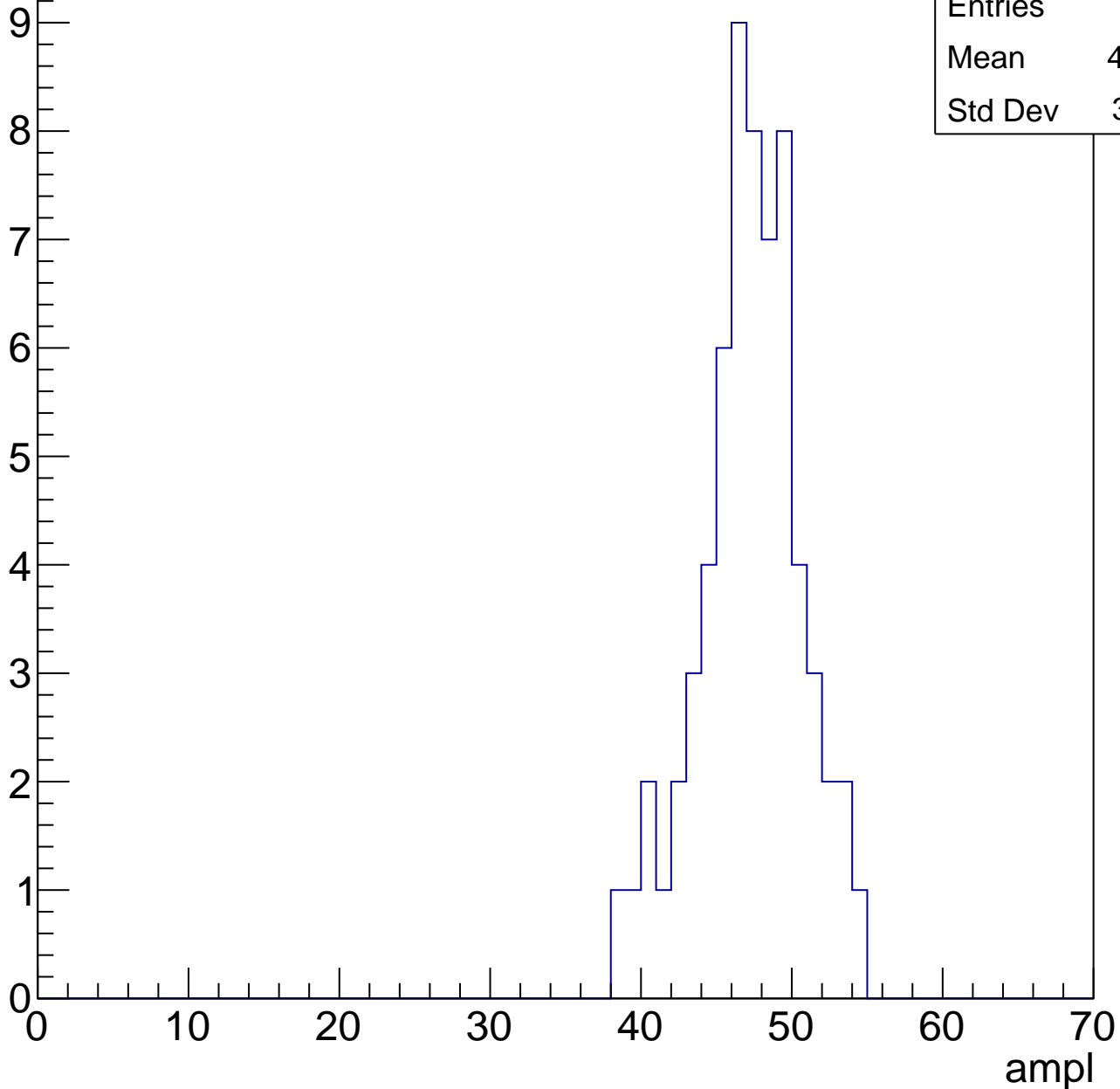


# B1L002S, U3-ch109, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	46.75
Std Dev	3.391

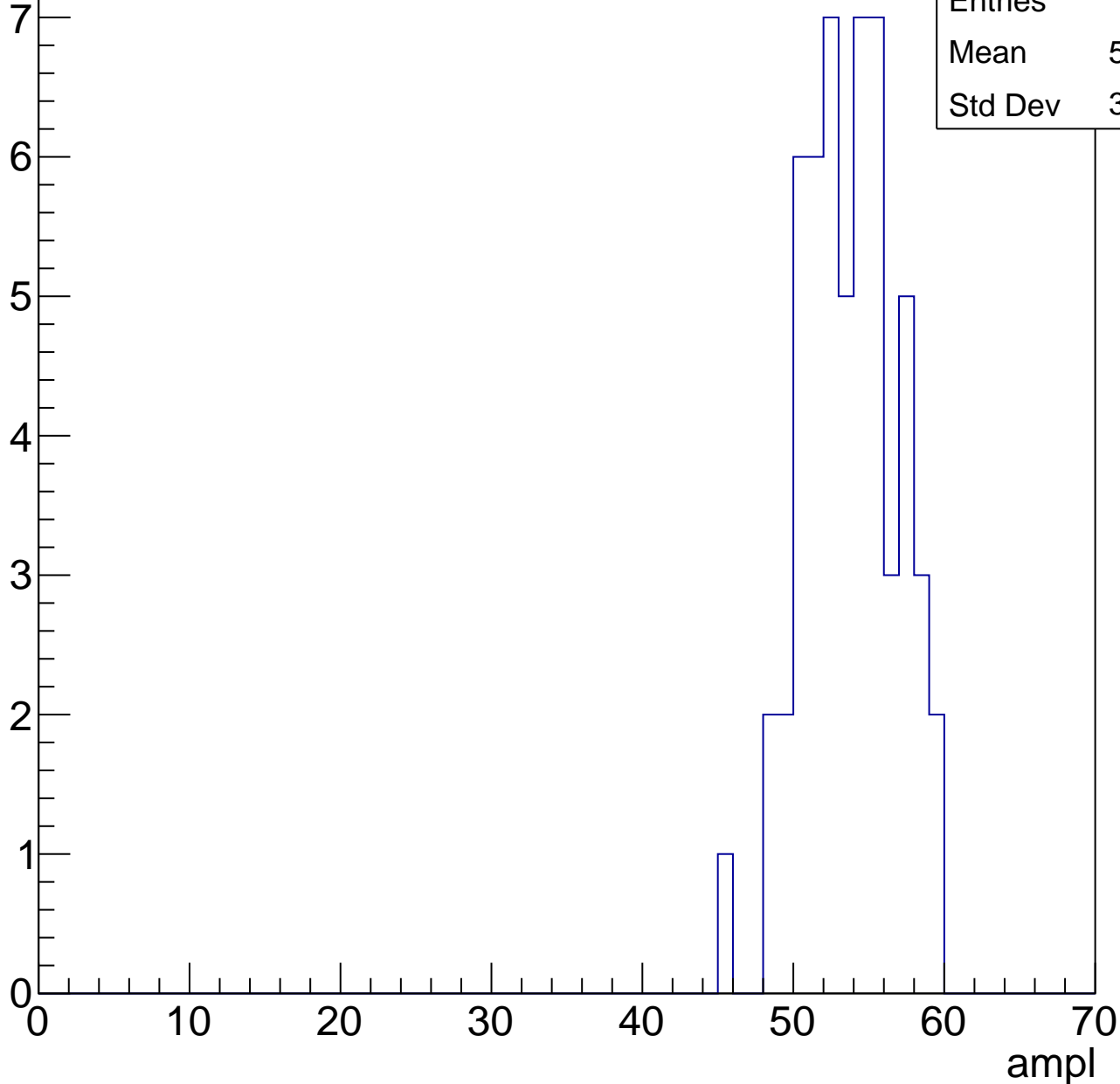


# B1L002S, U3-ch109, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	53.25
Std Dev	3.037

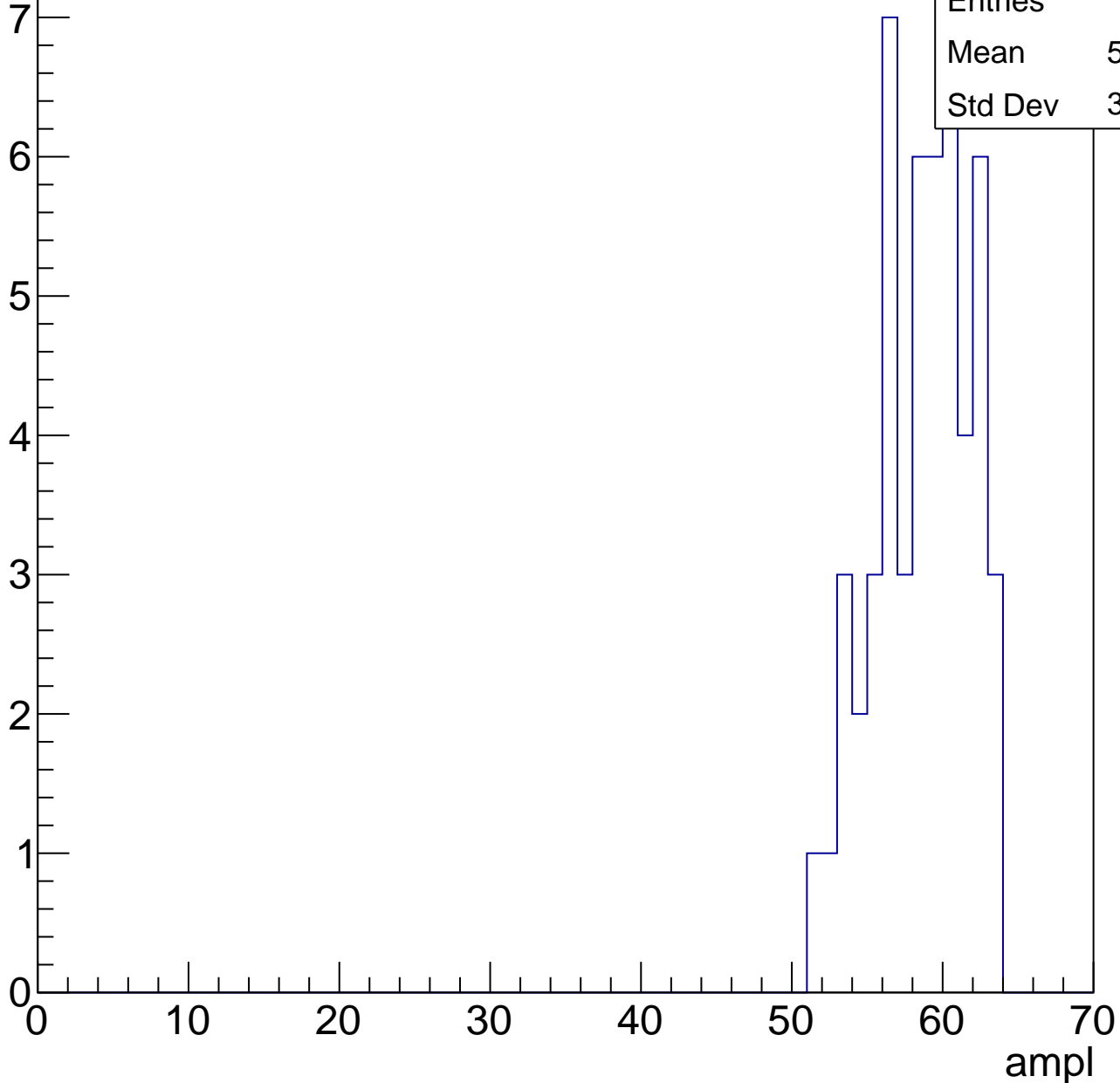


# B1L002S, U3-ch109, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	58.17
Std Dev	3.068

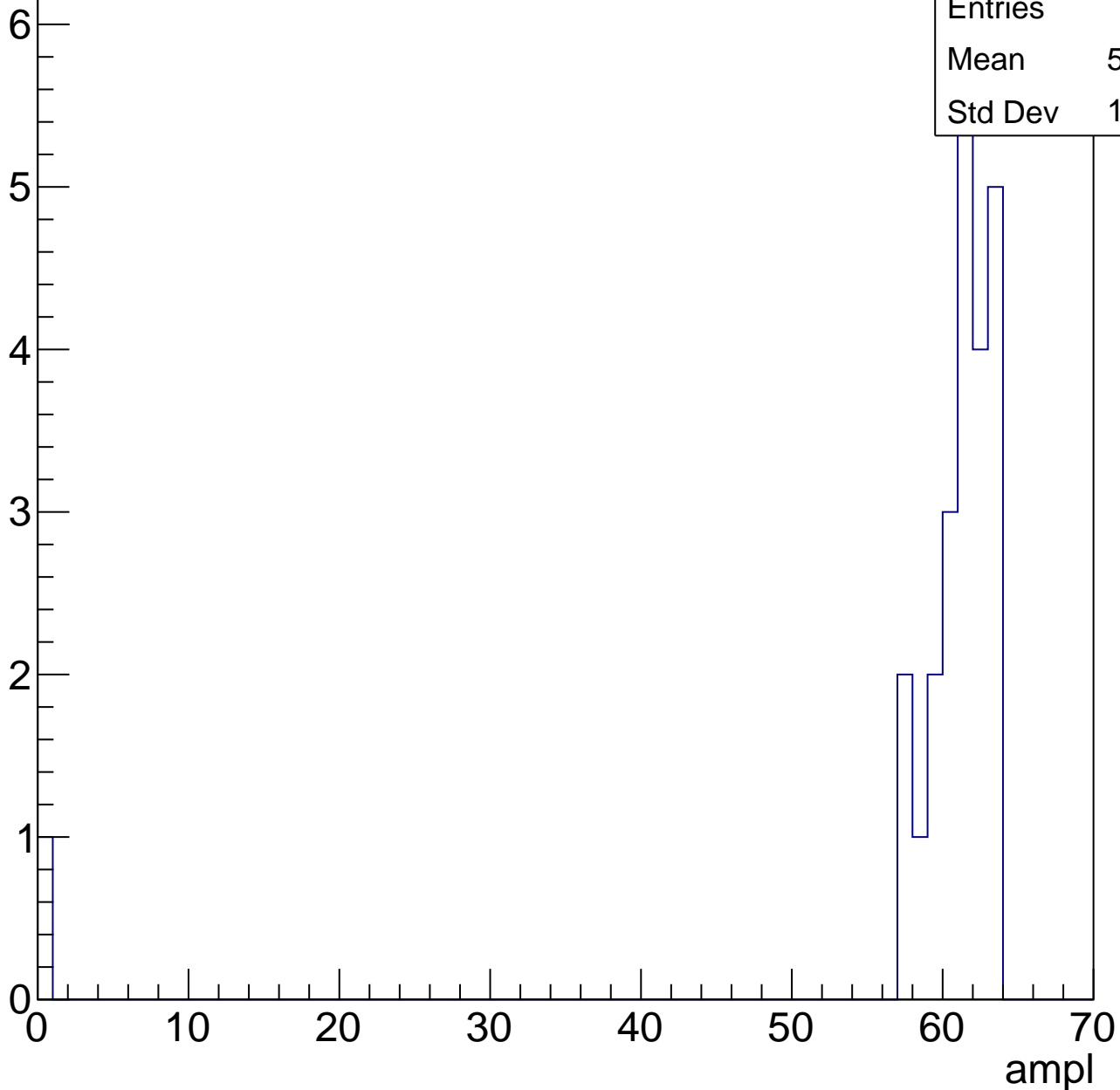


# B1L002S, U3-ch109, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	58.29
Std Dev	12.28





# B1L002S, U3-ch109, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	2
Mean	63
Std Dev	0

# B1L002S, U3-ch110, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	55
Mean	27.67
Std Dev	2.711

**Gaus mean : 28.4632**

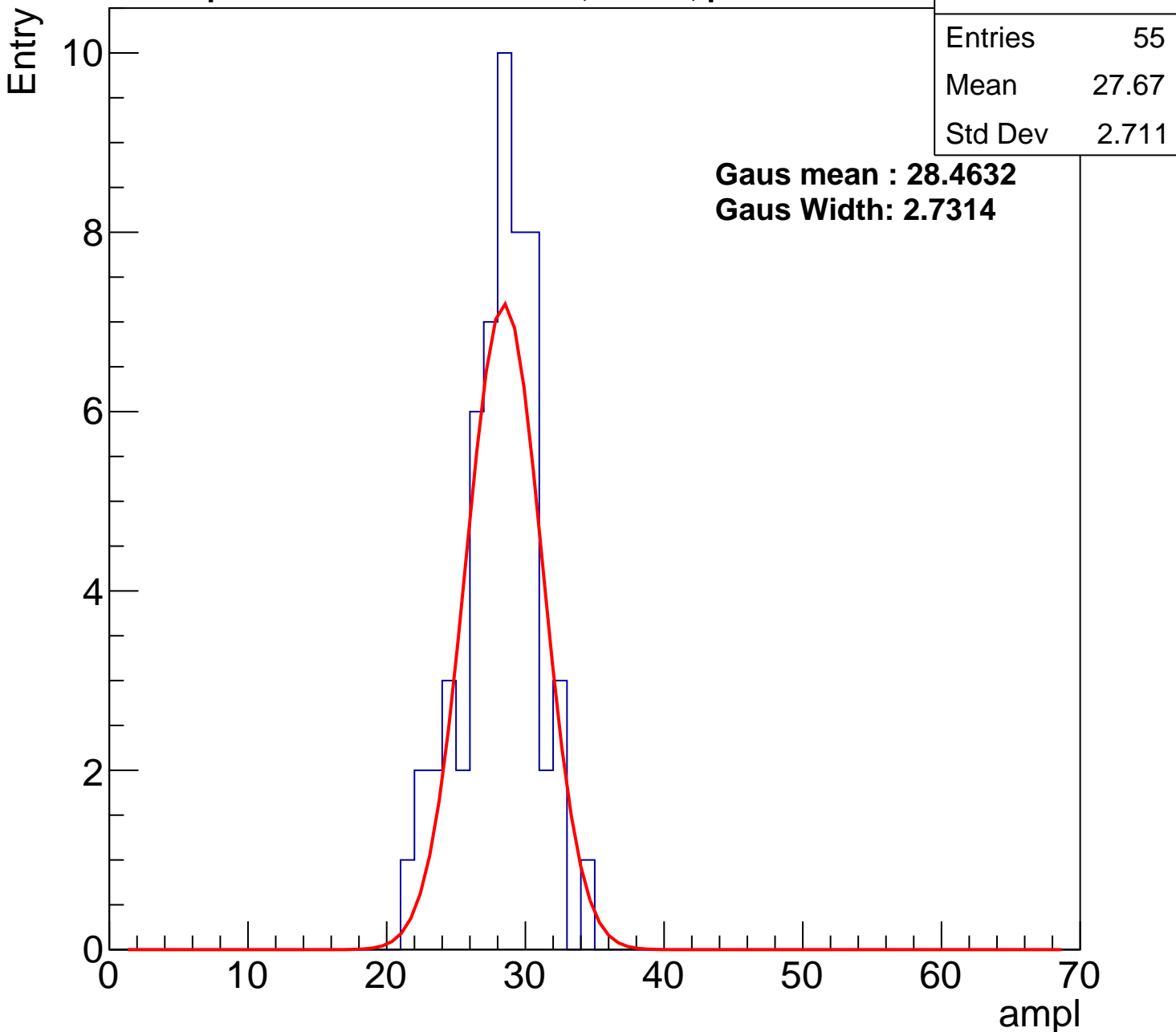
**Gaus Width: 2.7314**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch110, adc1

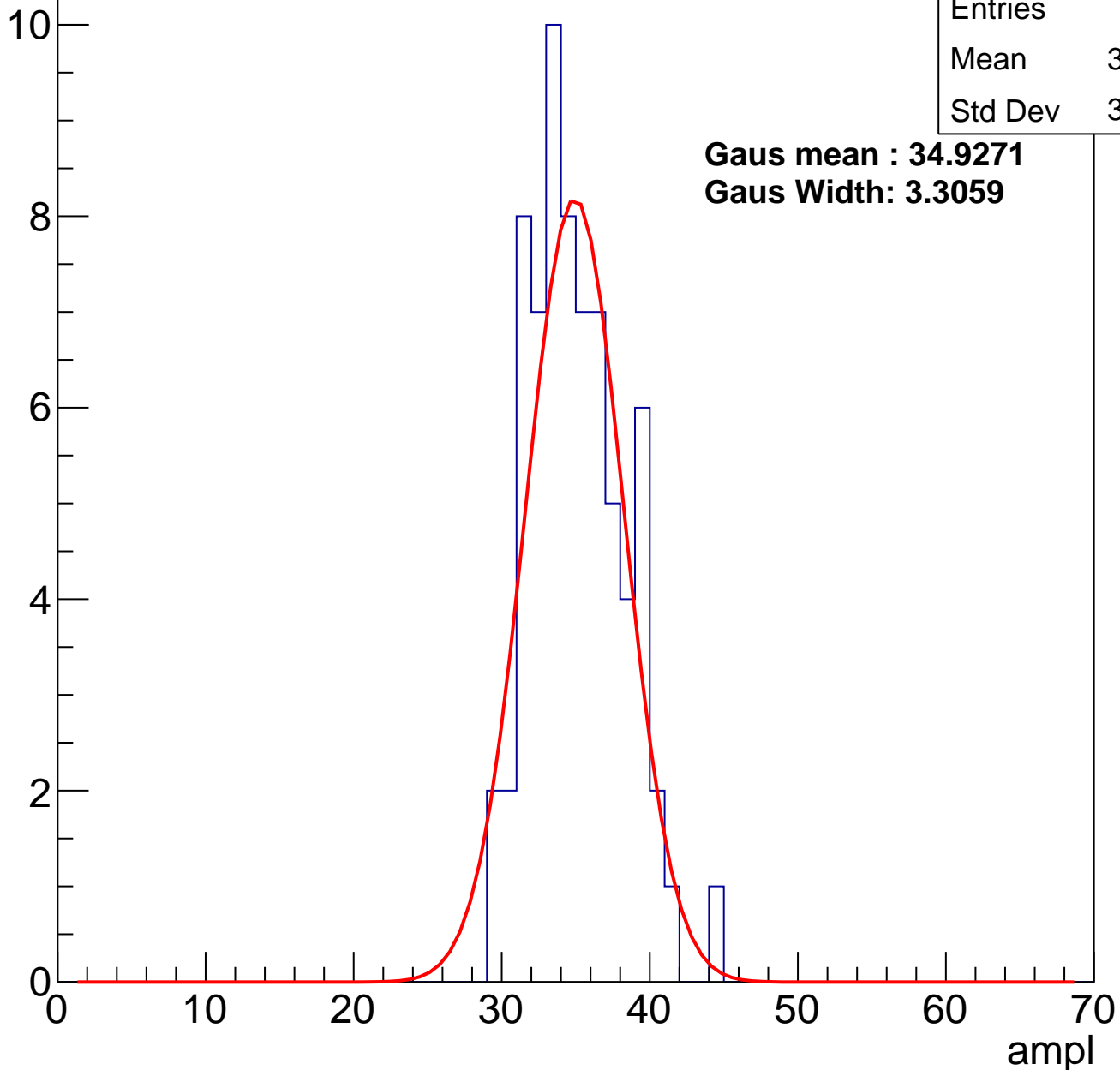
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	70
Mean	34.64
Std Dev	3.112

**Gaus mean : 34.9271**

**Gaus Width: 3.3059**

Entry



# B1L002S, U3-ch110, adc2

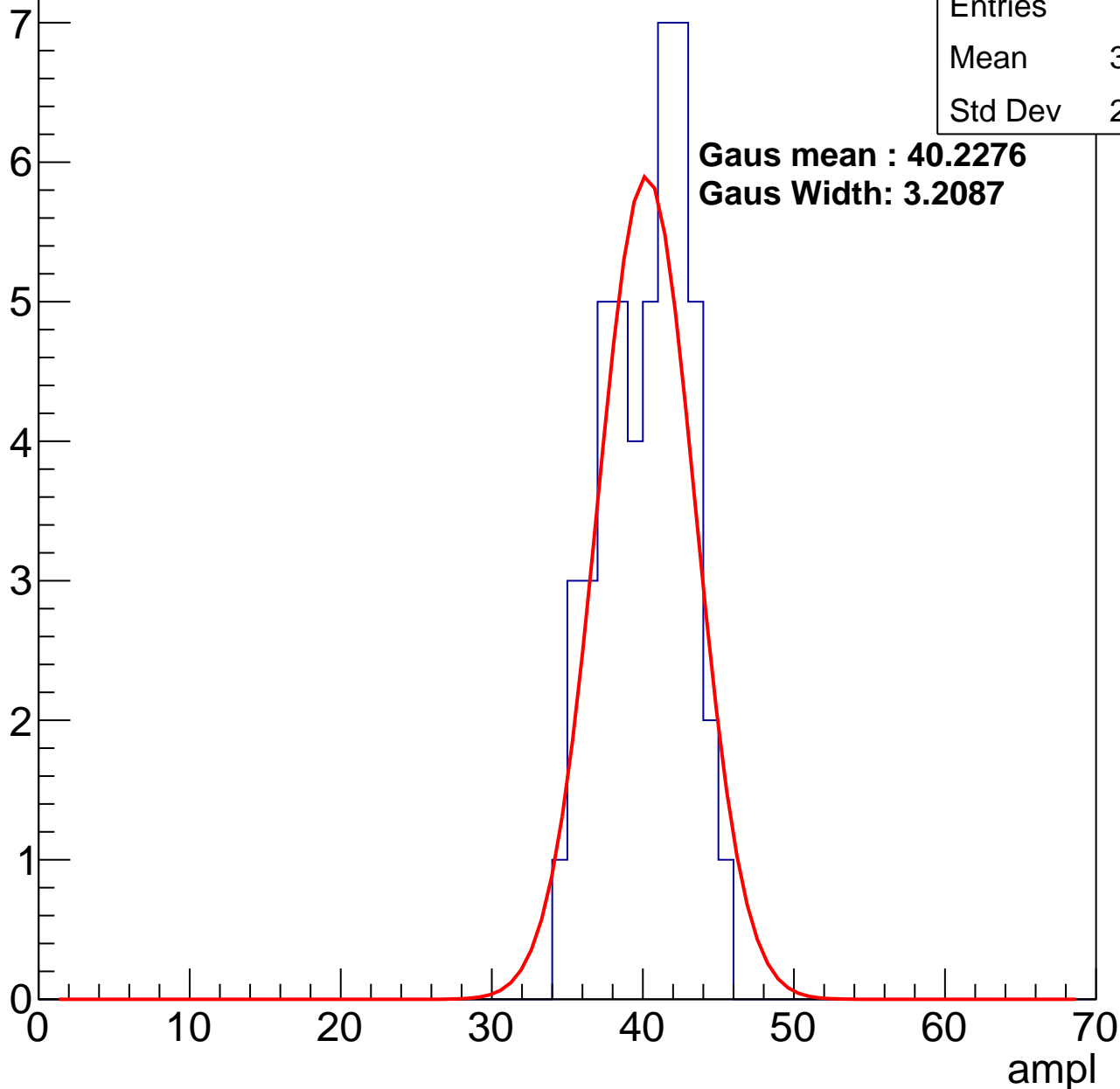
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	39.73
Std Dev	2.744

**Gaus mean : 40.2276**

**Gaus Width: 3.2087**

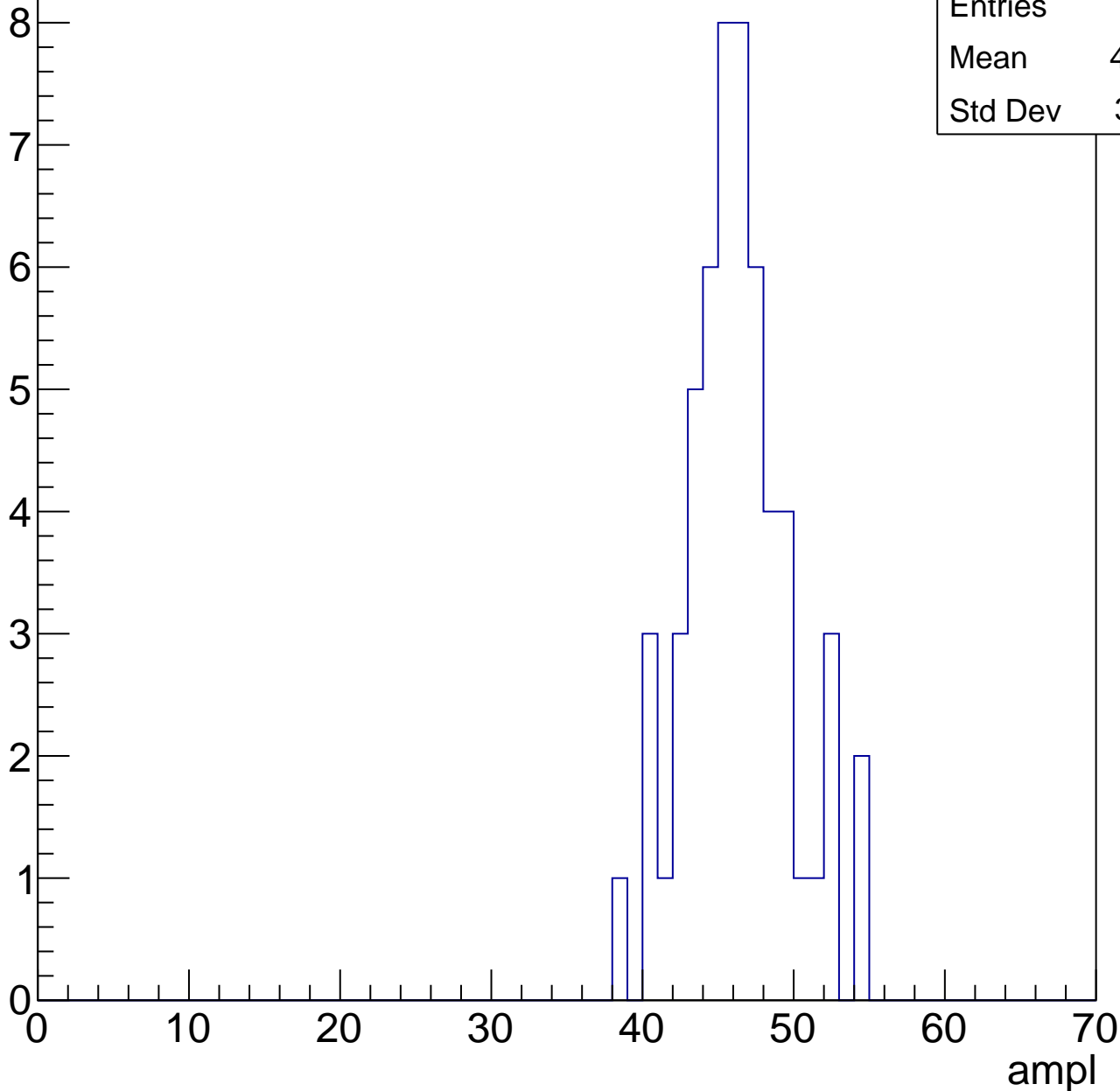


# B1L002S, U3-ch1 10, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	45.84
Std Dev	3.421

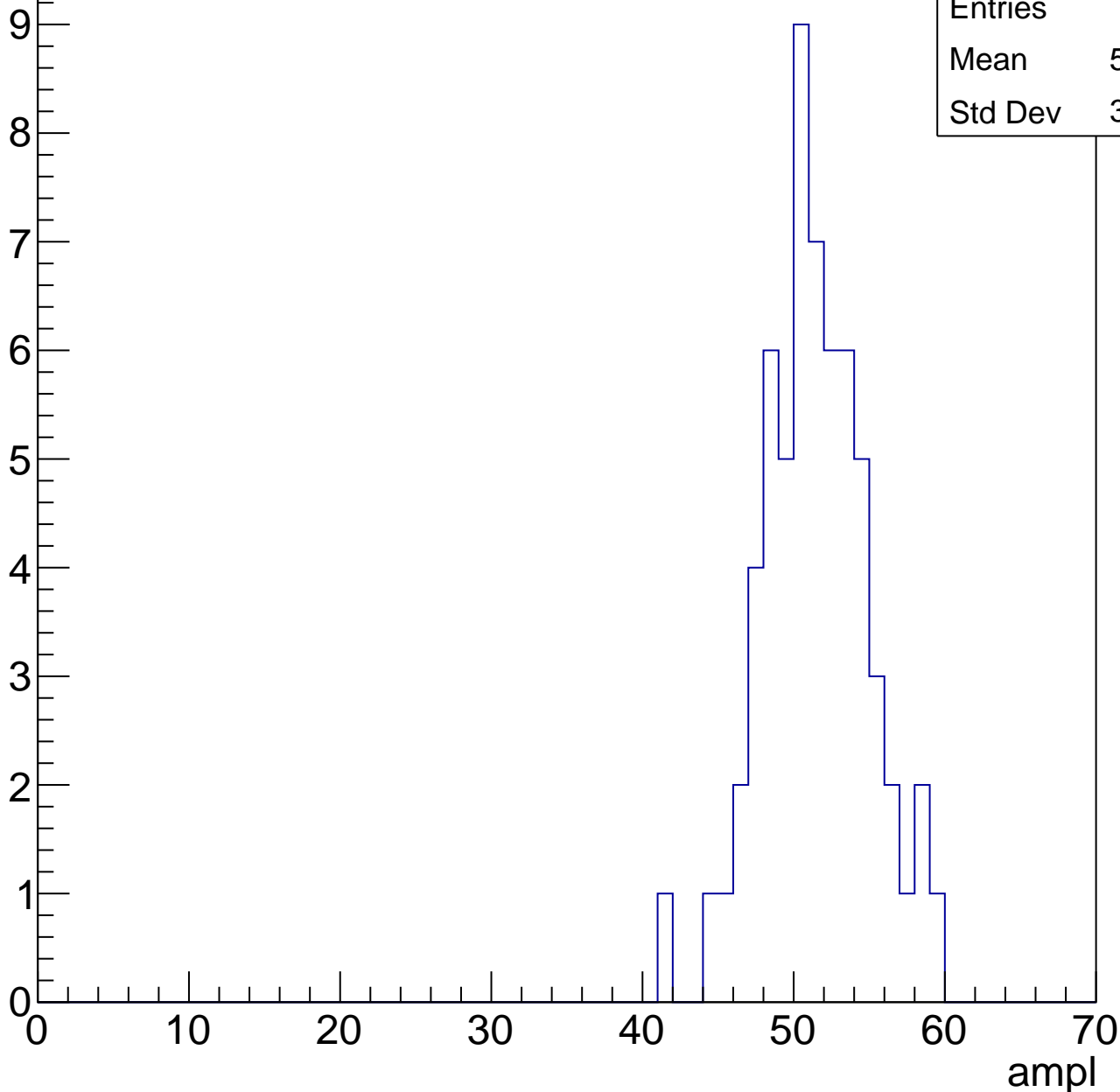


# B1L002S, U3-ch110, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	50.95
Std Dev	3.485

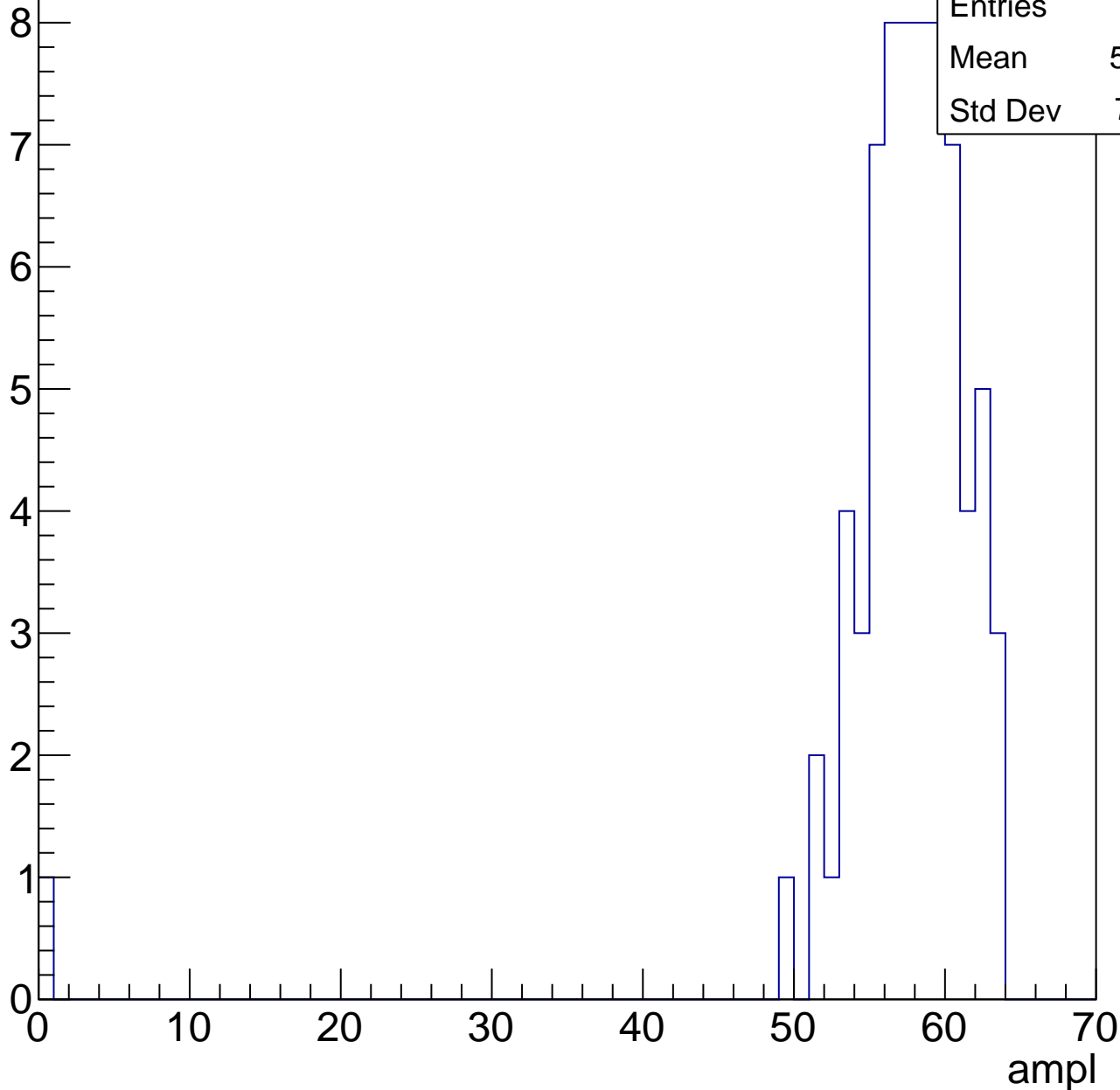


# B1L002S, U3-ch110, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	56.64
Std Dev	7.491

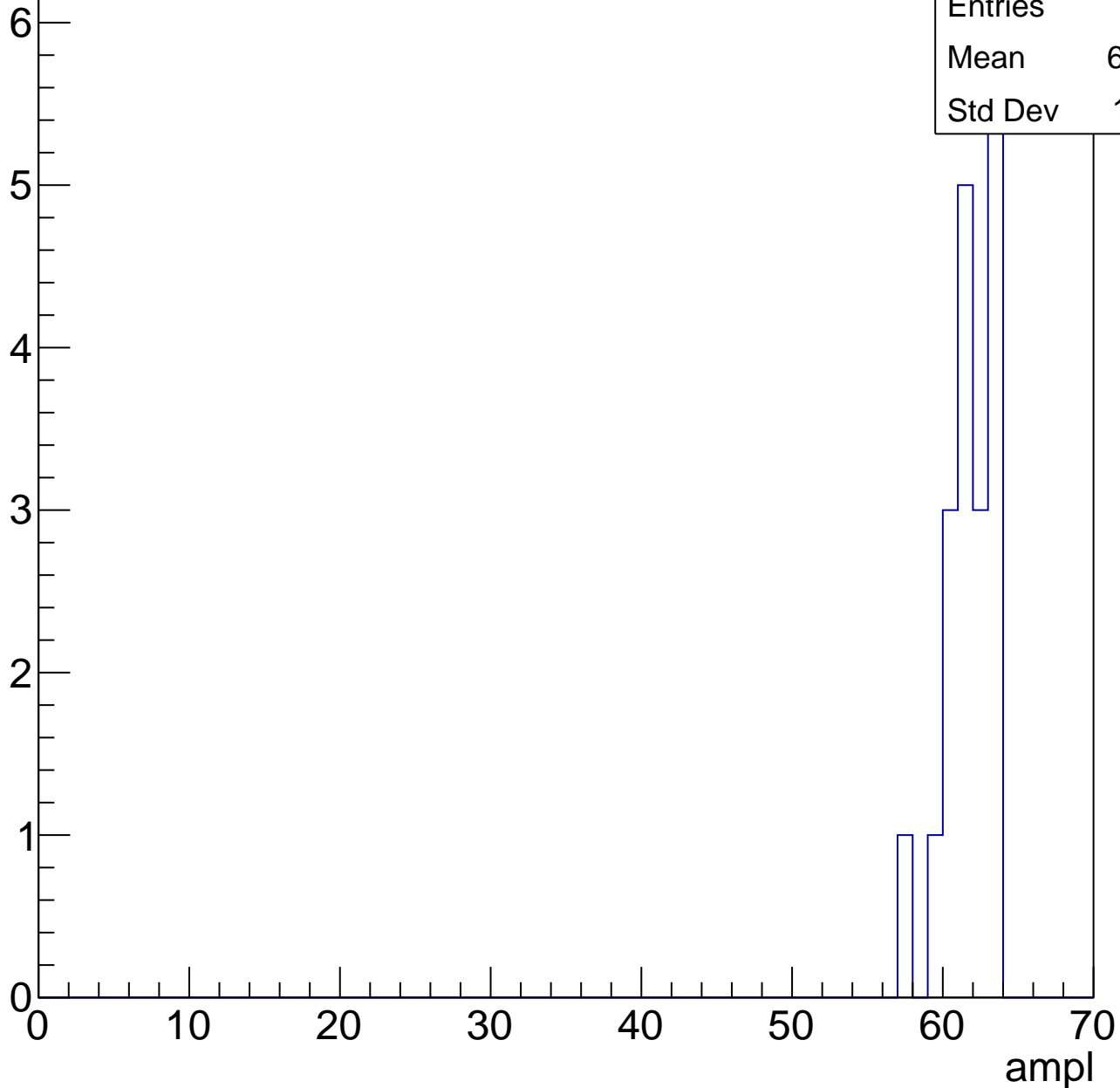


# B1L002S, U3-ch110, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	61.32
Std Dev	1.591

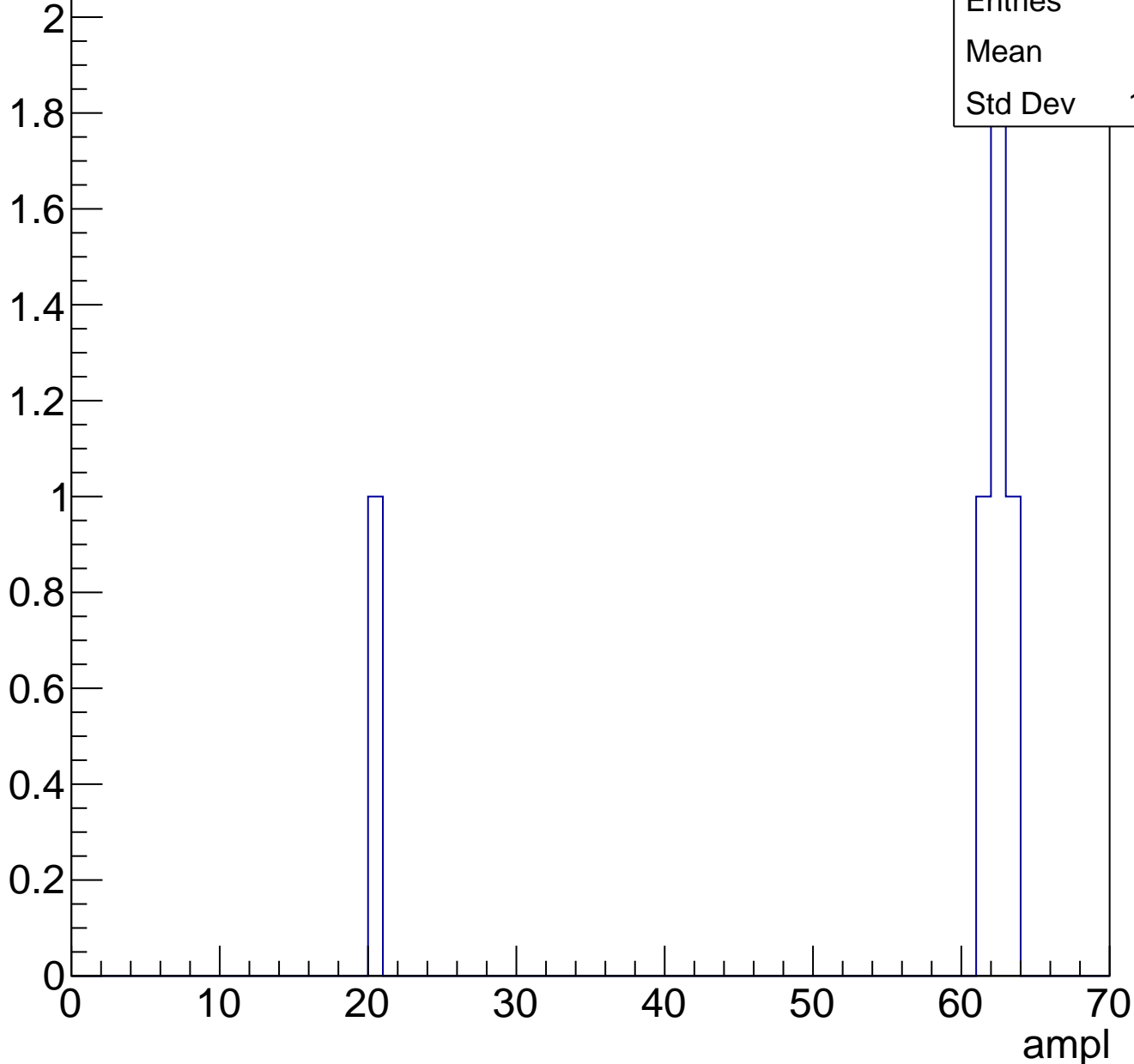




# B1L002S, U3-ch110, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	53.6
Std Dev	16.81

# B1L002S, U3-ch111, adc0

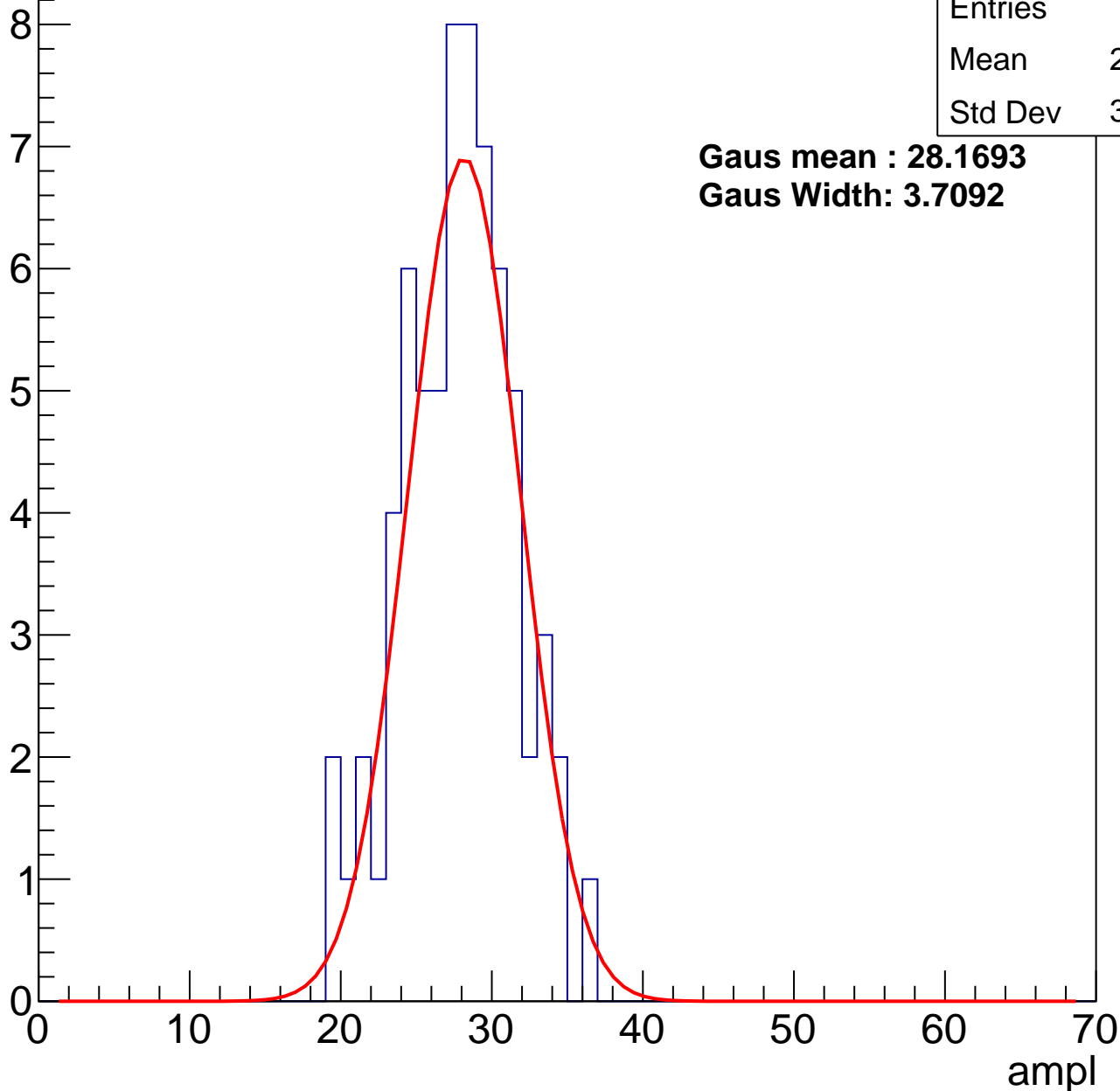
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	27.32
Std Dev	3.672

**Gaus mean : 28.1693**

**Gaus Width: 3.7092**



# B1L002S, U3-ch111, adc1

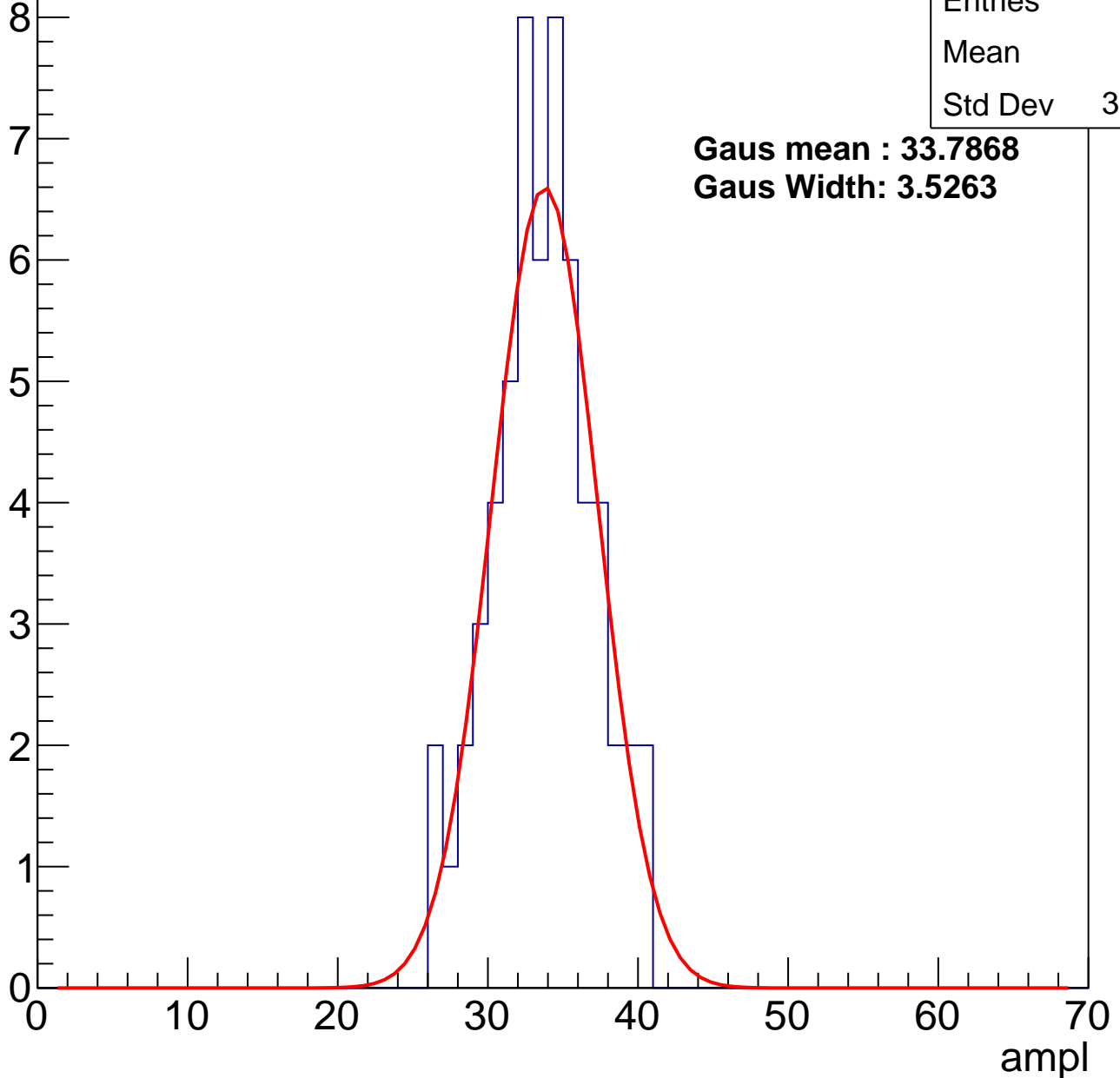
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	33.2
Std Dev	3.308

**Gaus mean : 33.7868**

**Gaus Width: 3.5263**



# B1L002S, U3-ch111, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	73
Mean	40.29
Std Dev	3.394

**Gaus mean : 40.3550**

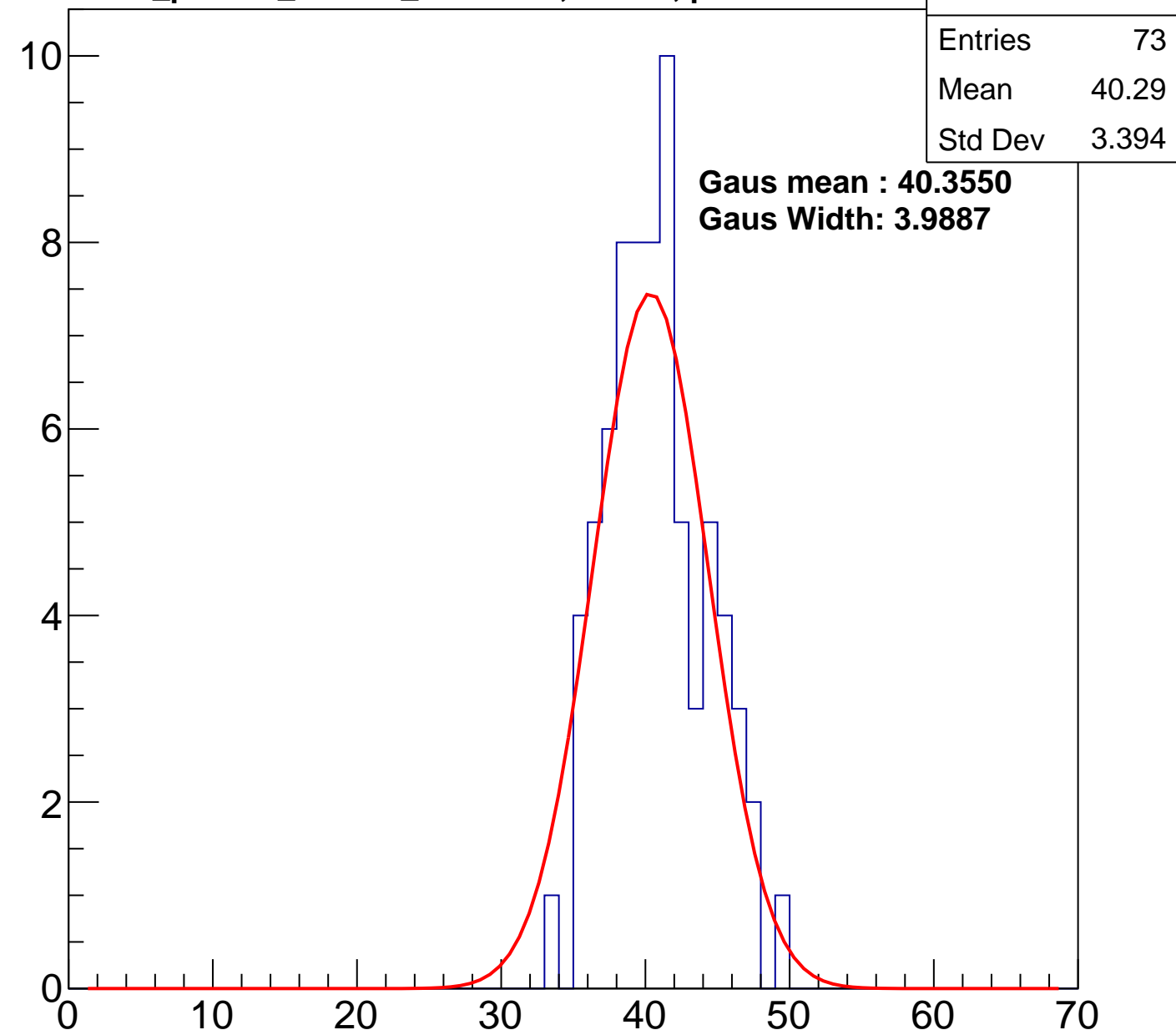
**Gaus Width: 3.9887**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70

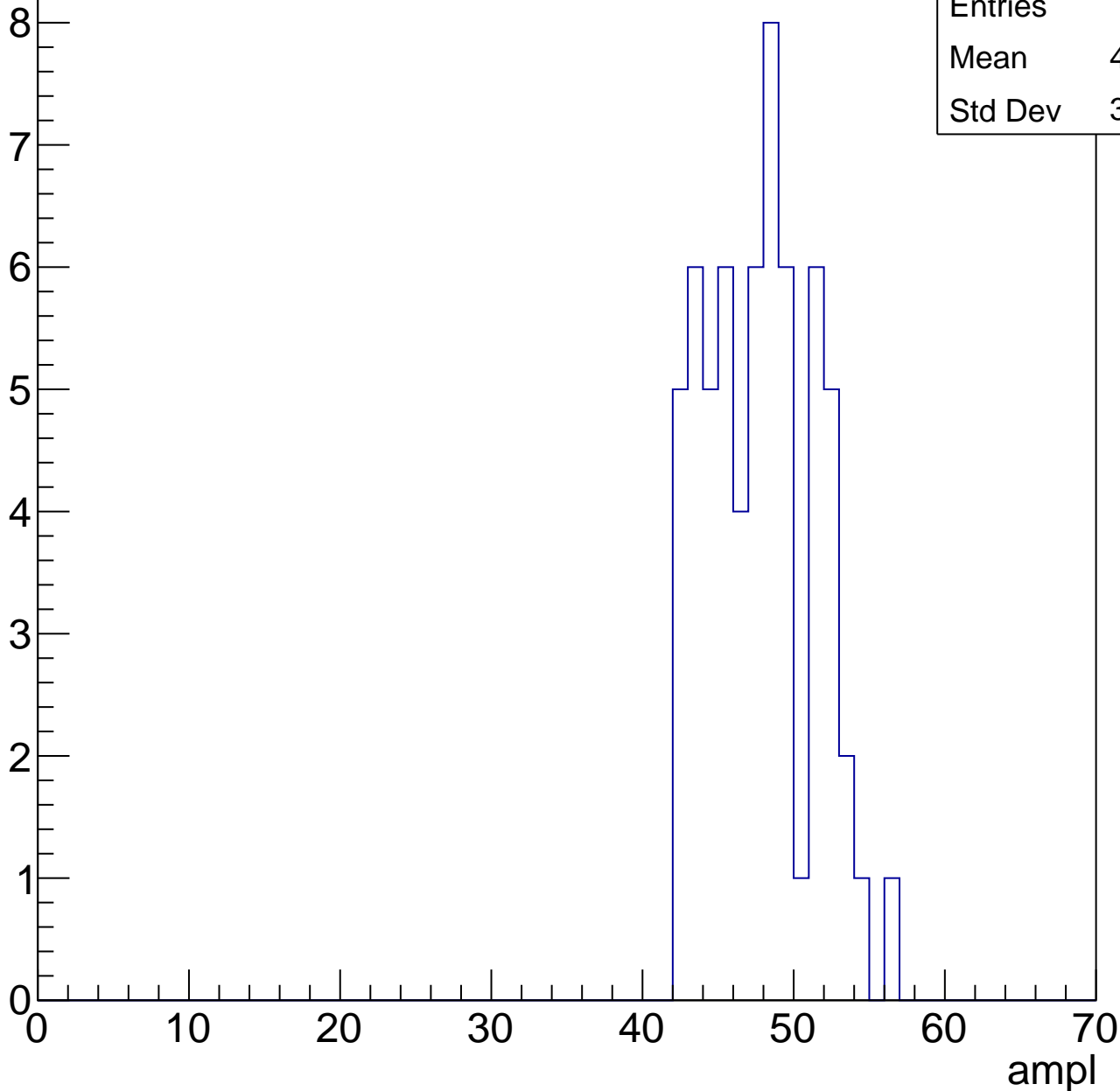


# B1L002S, U3-ch111, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	47.32
Std Dev	3.482

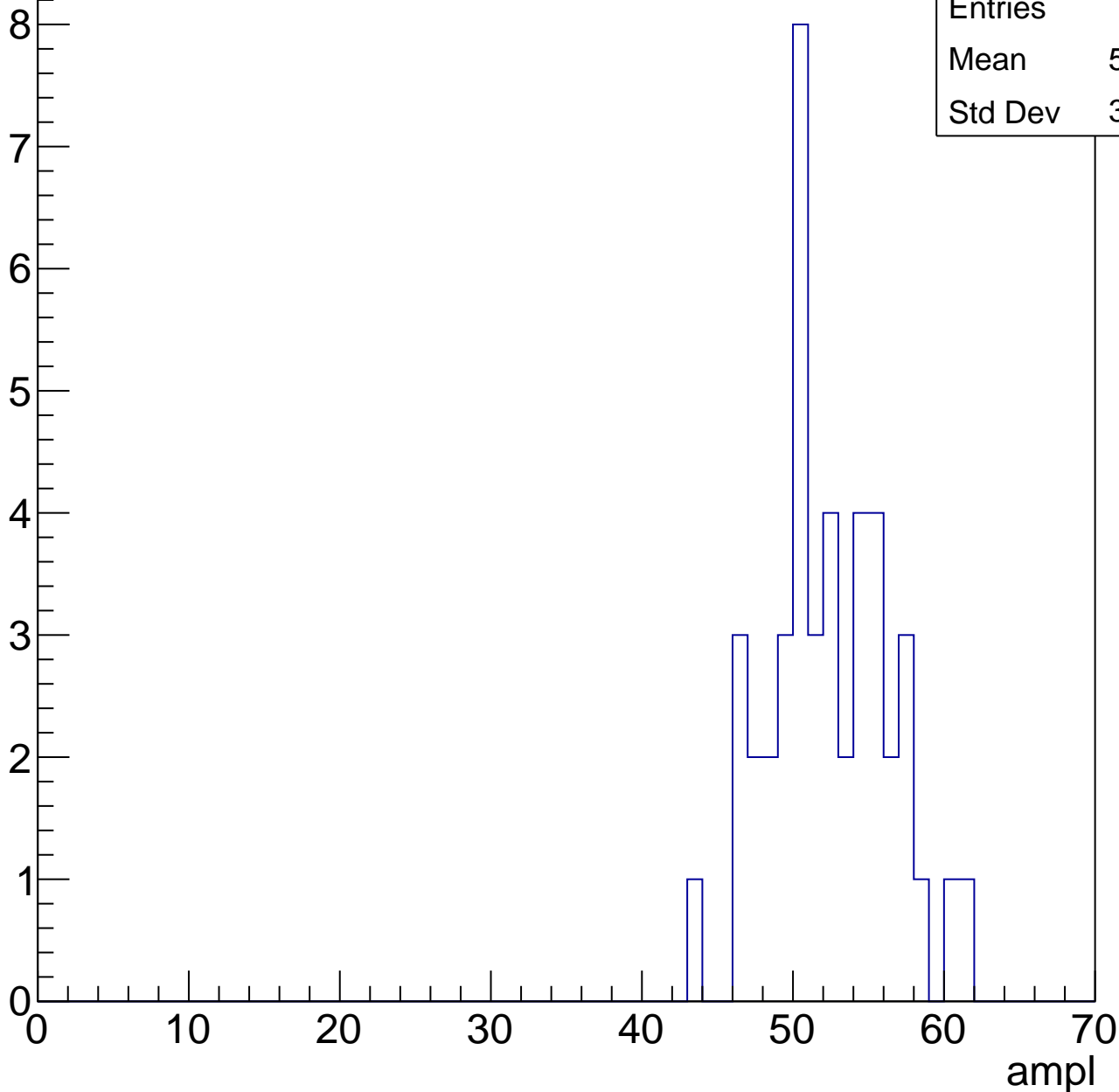


# B1L002S, U3-ch111, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	51.89
Std Dev	3.915



# B1L002S, U3-ch111, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

10

Entries 77

Mean 57.03

Std Dev 7.288

8

6

4

2

0

0

10

20

30

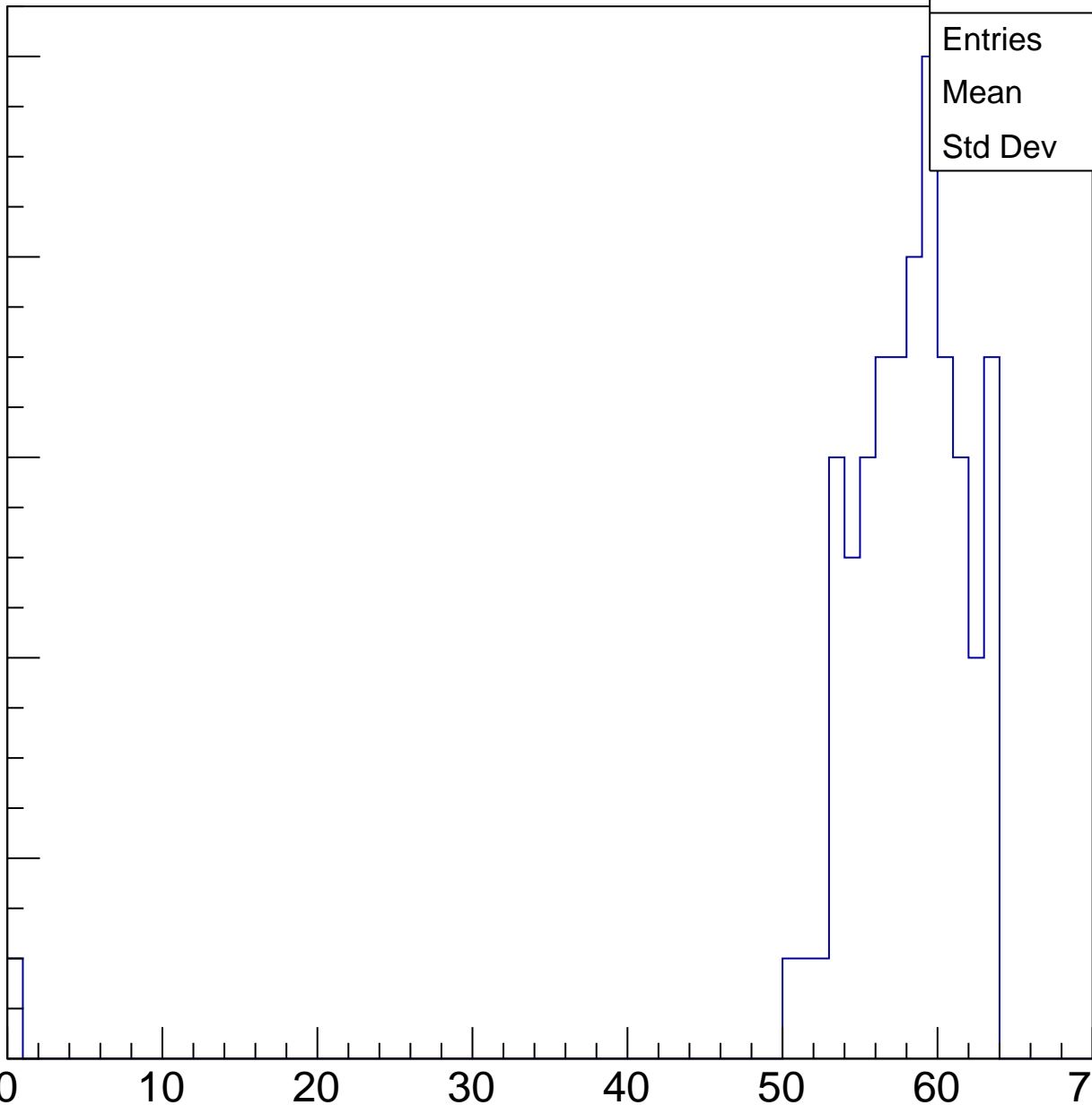
40

50

60

ampl

70

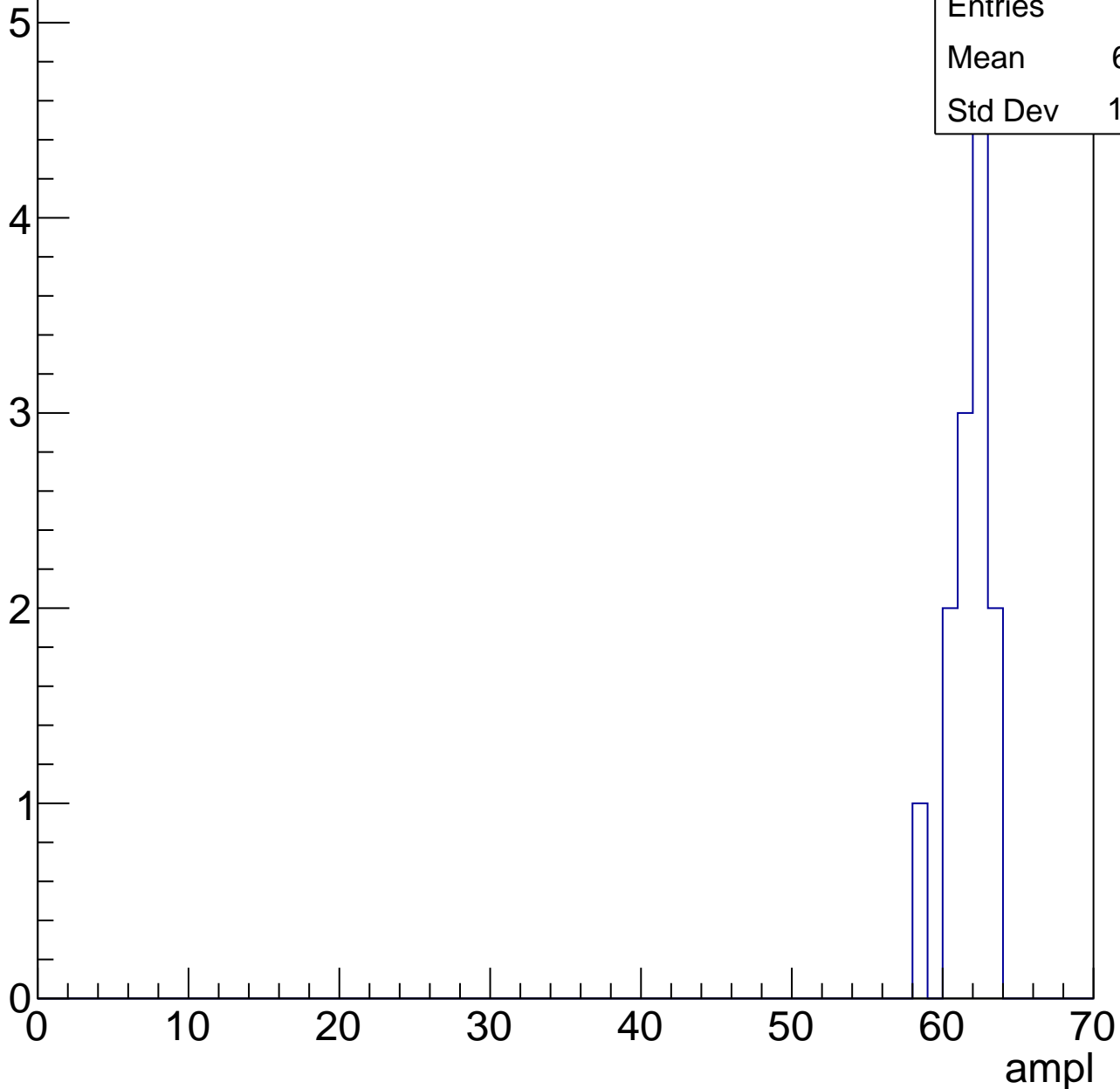


# B1L002S, U3-ch111, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	13
Mean	61.31
Std Dev	1.323

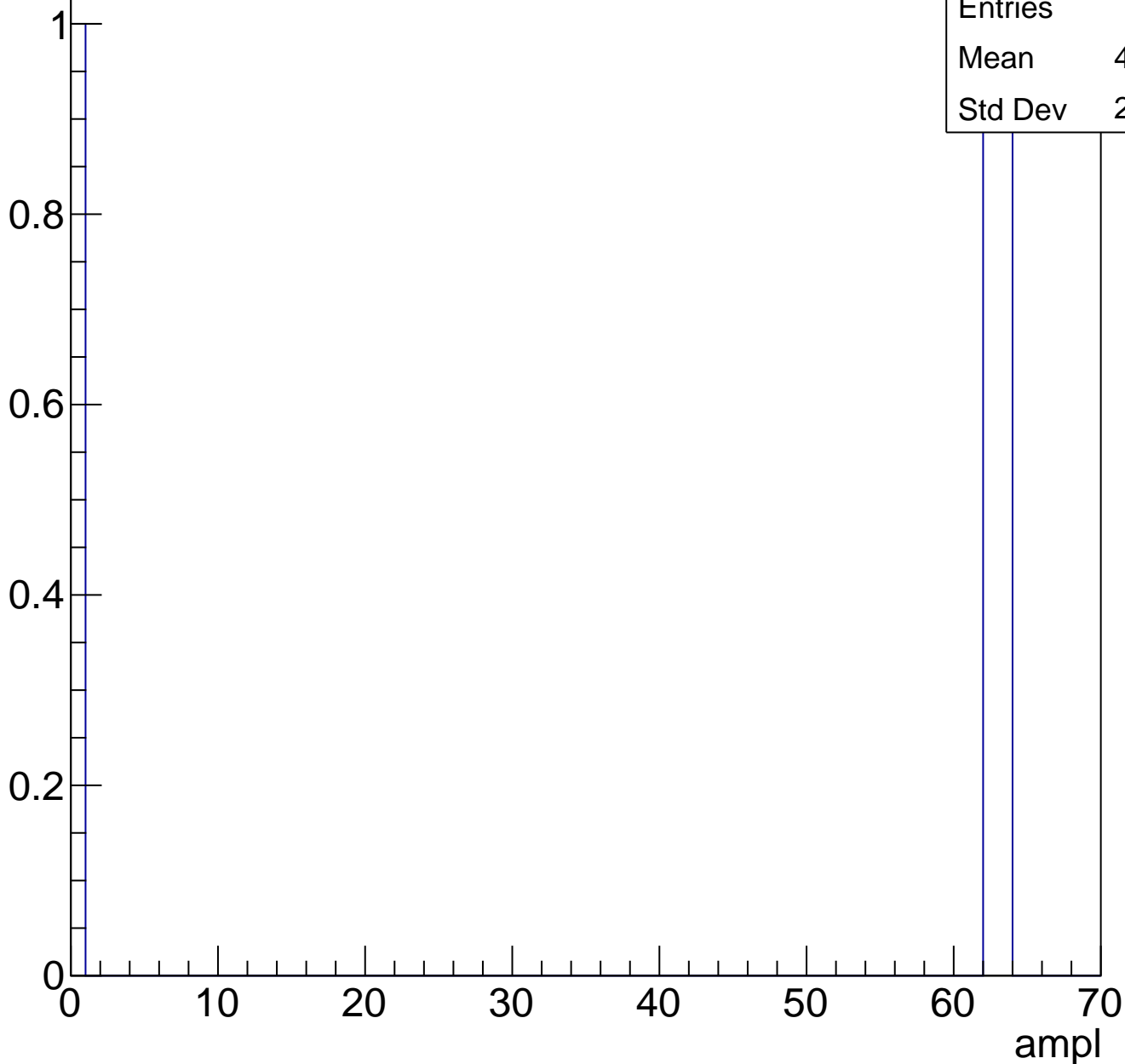




# B1L002S, U3-ch111, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch112, adc0

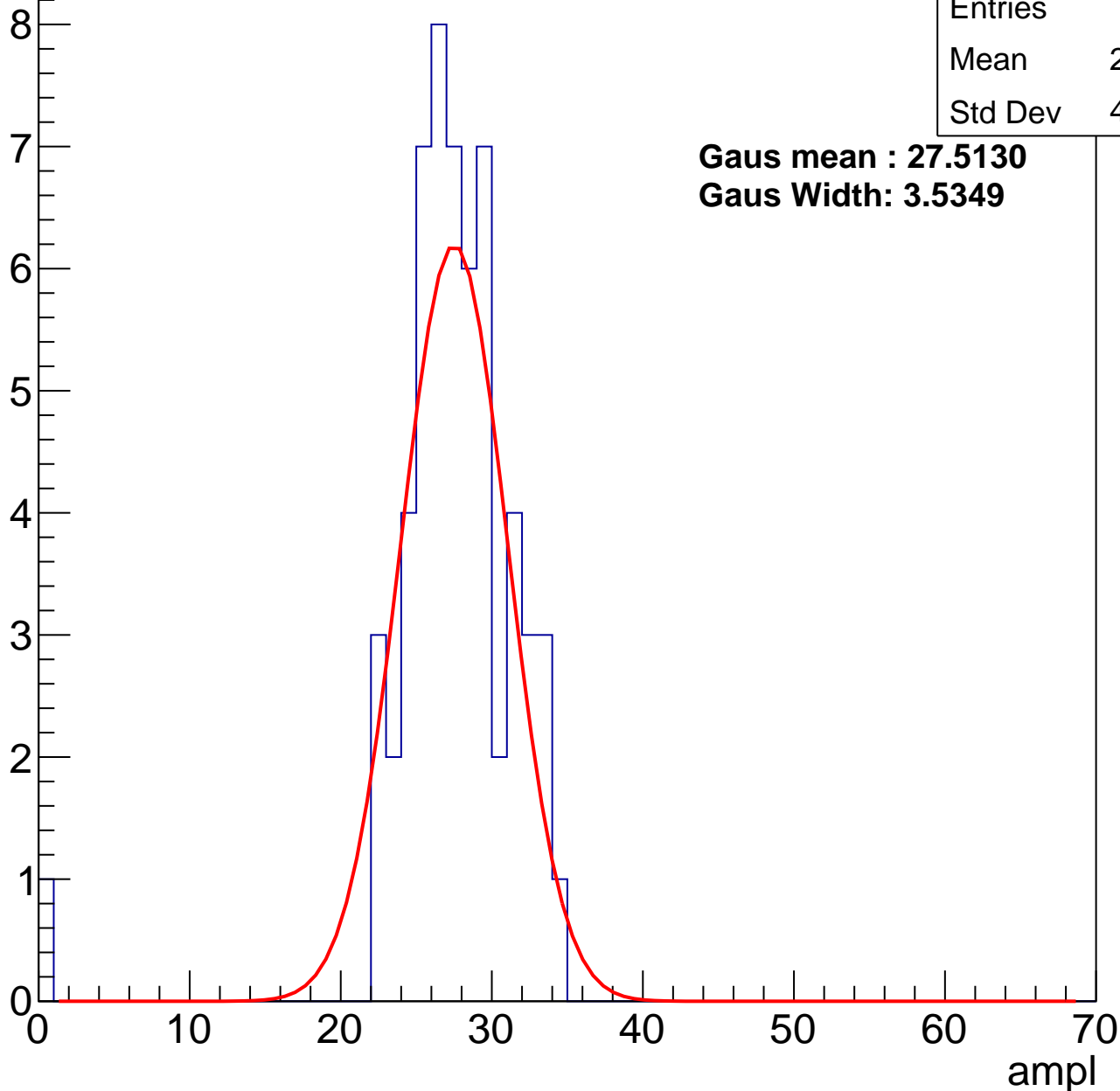
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	26.97
Std Dev	4.653

**Gaus mean : 27.5130**

**Gaus Width: 3.5349**



# B1L002S, U3-ch112, adc1

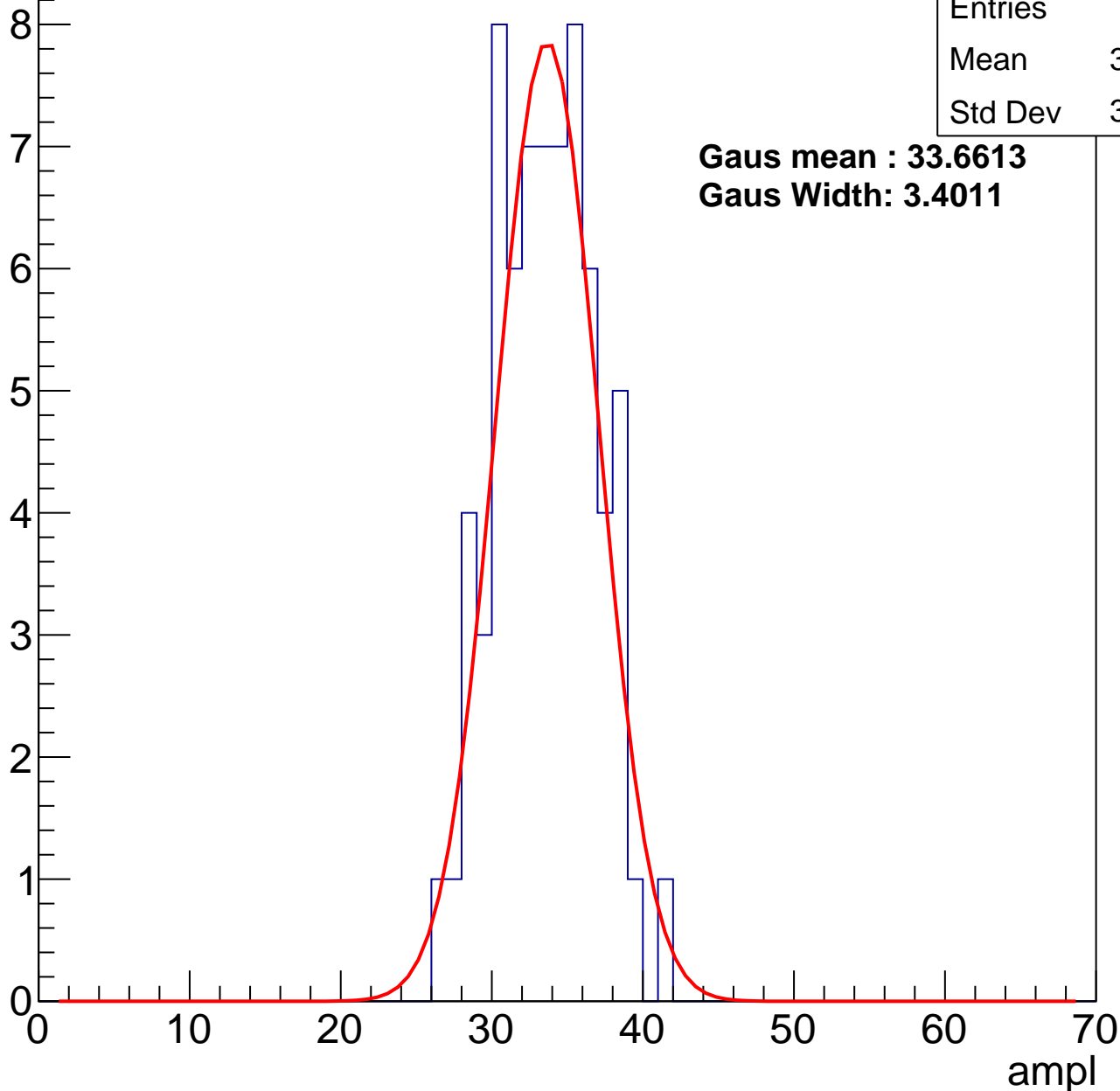
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	33.12
Std Dev	3.224

**Gaus mean : 33.6613**

**Gaus Width: 3.4011**



# B1L002S, U3-ch112, adc2

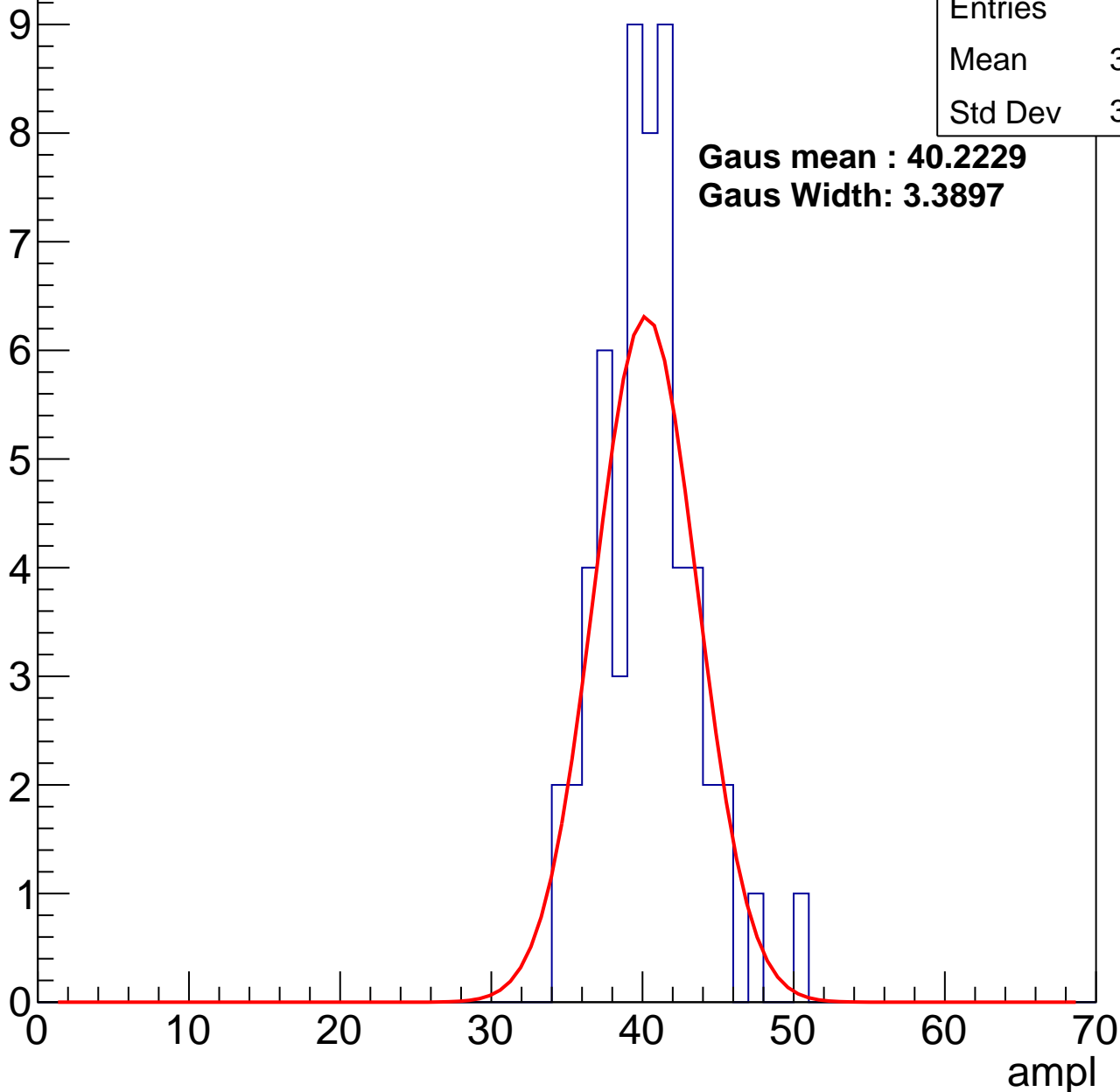
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	39.88
Std Dev	3.112

**Gaus mean : 40.2229**

**Gaus Width: 3.3897**

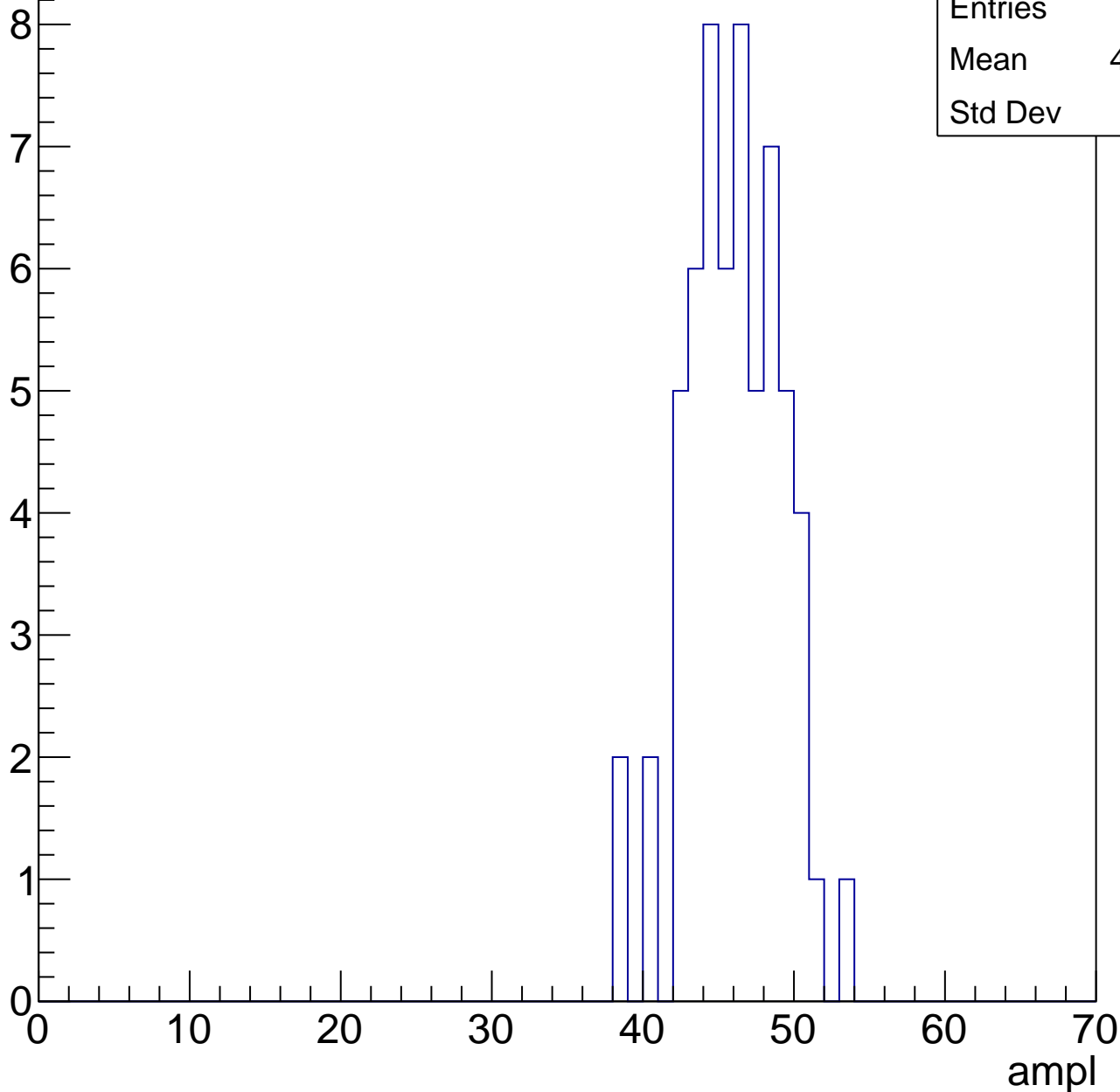


# B1L002S, U3-ch112, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	45.57
Std Dev	3.1

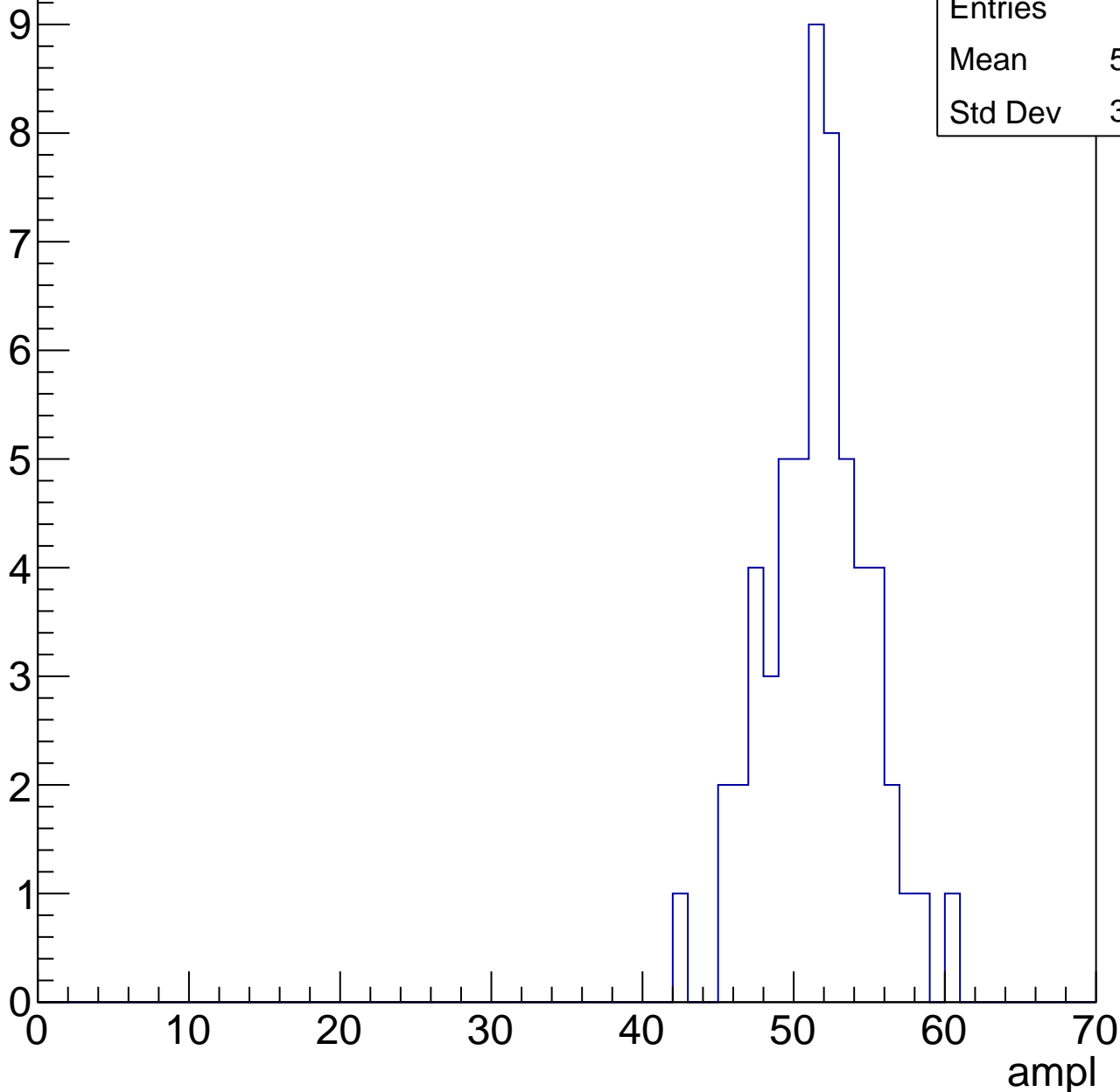


# B1L002S, U3-ch112, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	51.12
Std Dev	3.408



# B1L002S, U3-ch112, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

8  
7  
6  
5  
4  
3  
2  
1  
0

Entries	42
Mean	56.21
Std Dev	2.55

ampl

0 10 20 30 40 50 60 70

0

1

2

3

4

5

6

7

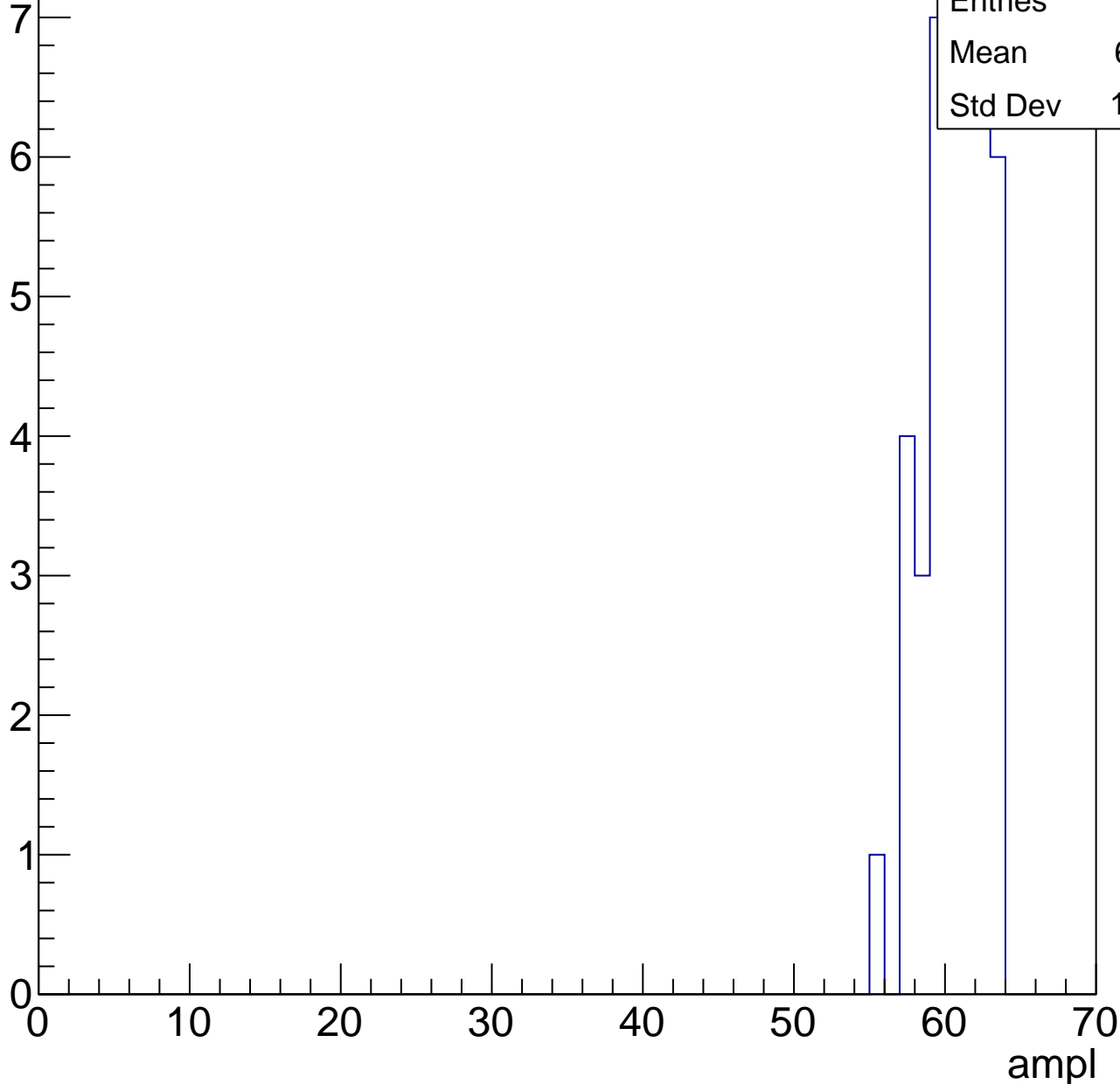
8

# B1L002S, U3-ch112, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	42
Mean	60.21
Std Dev	1.994

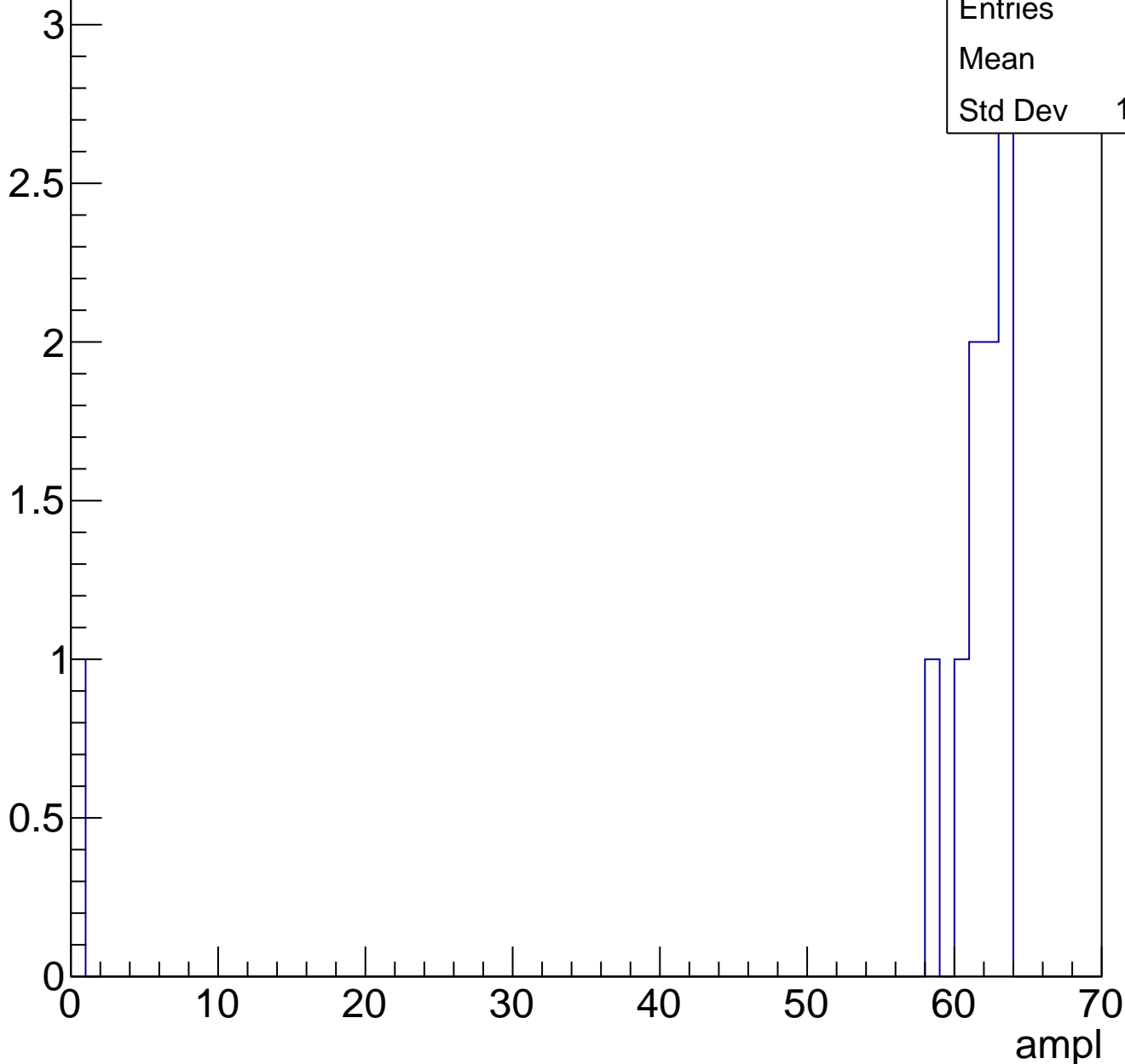




# B1L002S, U3-ch112, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch113, adc0

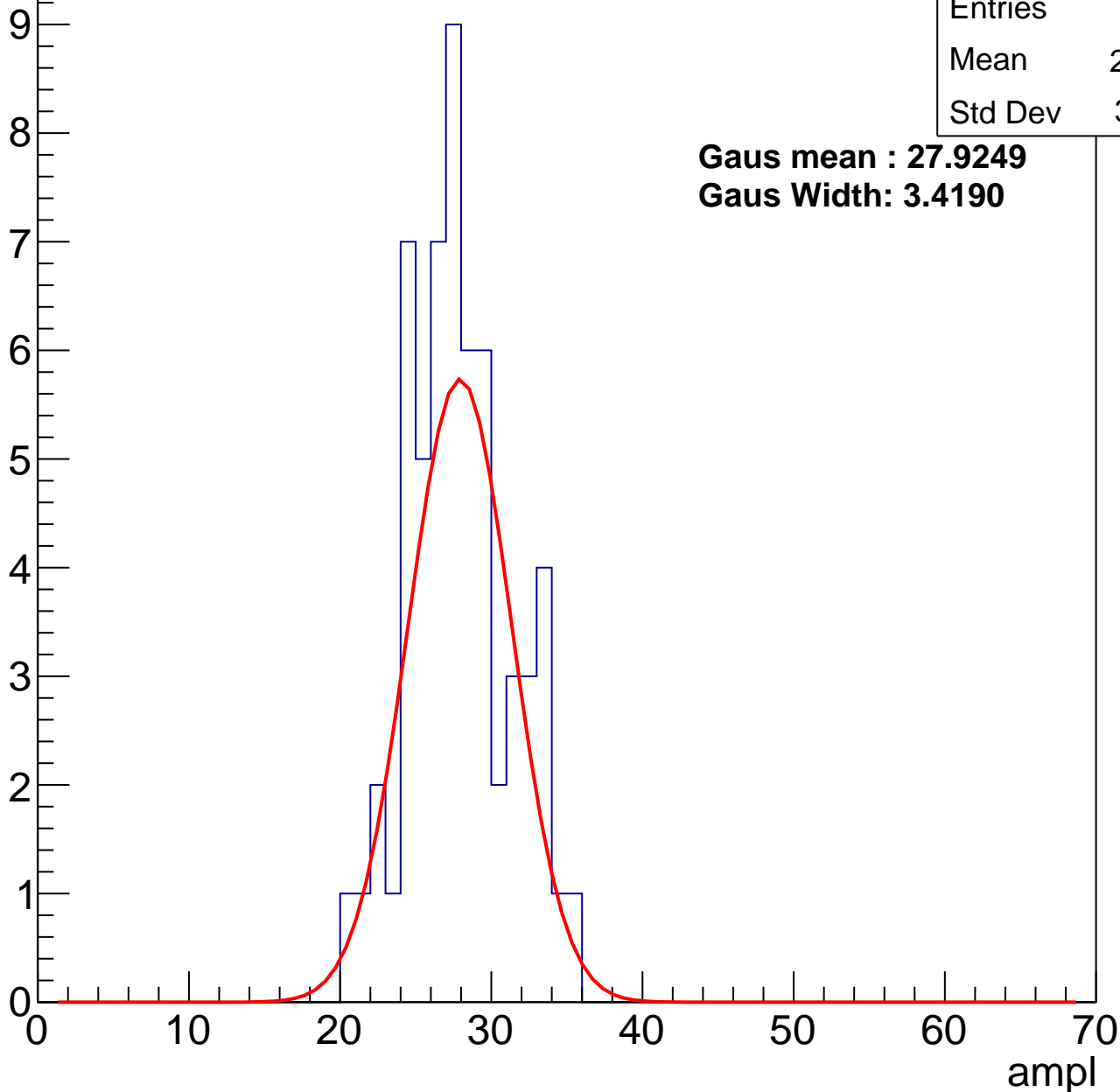
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.42
Std Dev	3.341

**Gaus mean : 27.9249**

**Gaus Width: 3.4190**



# B1L002S, U3-ch113, adc1

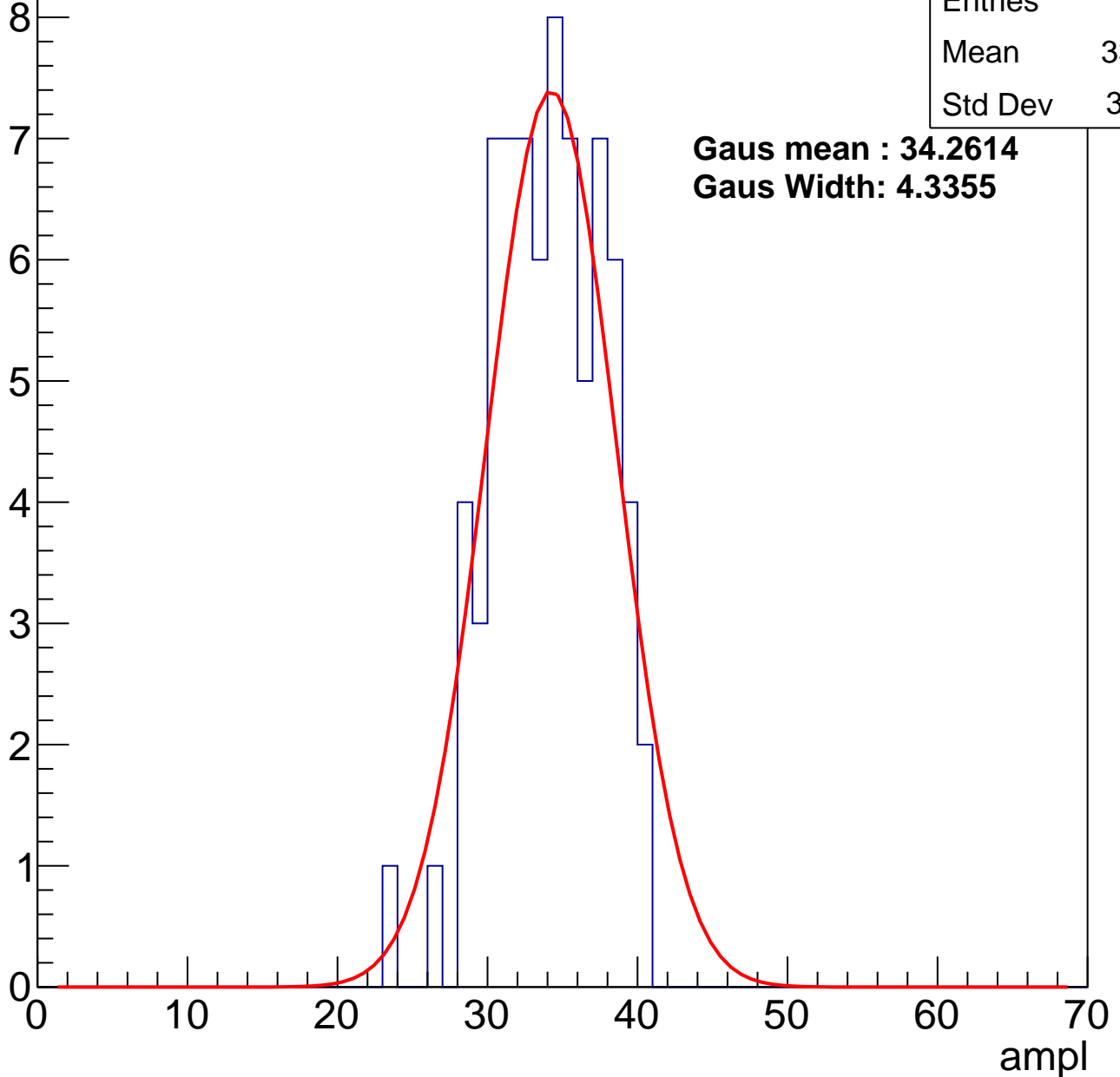
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	75
Mean	33.56
Std Dev	3.571

**Gaus mean : 34.2614**

**Gaus Width: 4.3355**



# B1L002S, U3-ch113, adc2

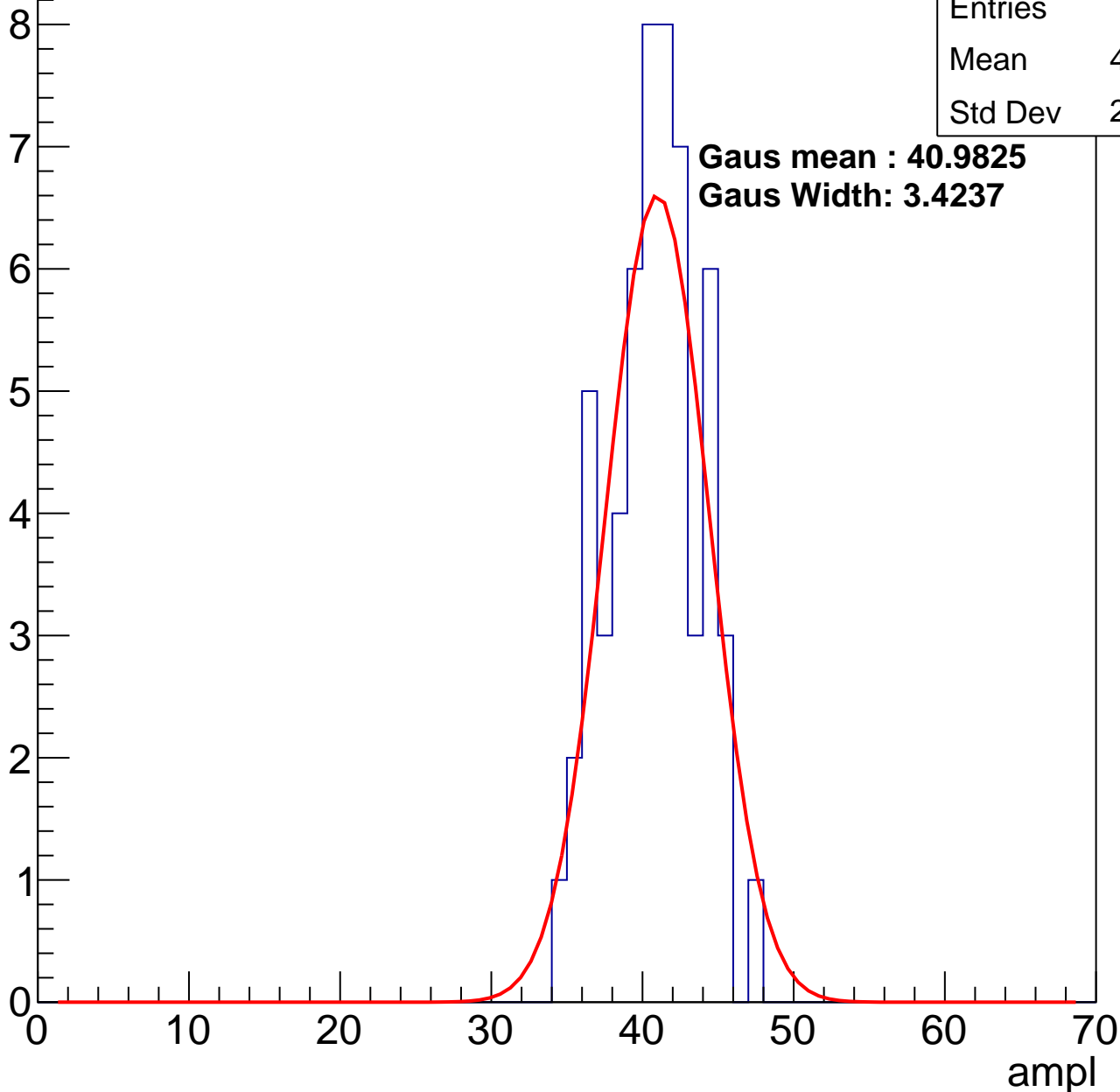
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	40.32
Std Dev	2.939

**Gaus mean : 40.9825**

**Gaus Width: 3.4237**

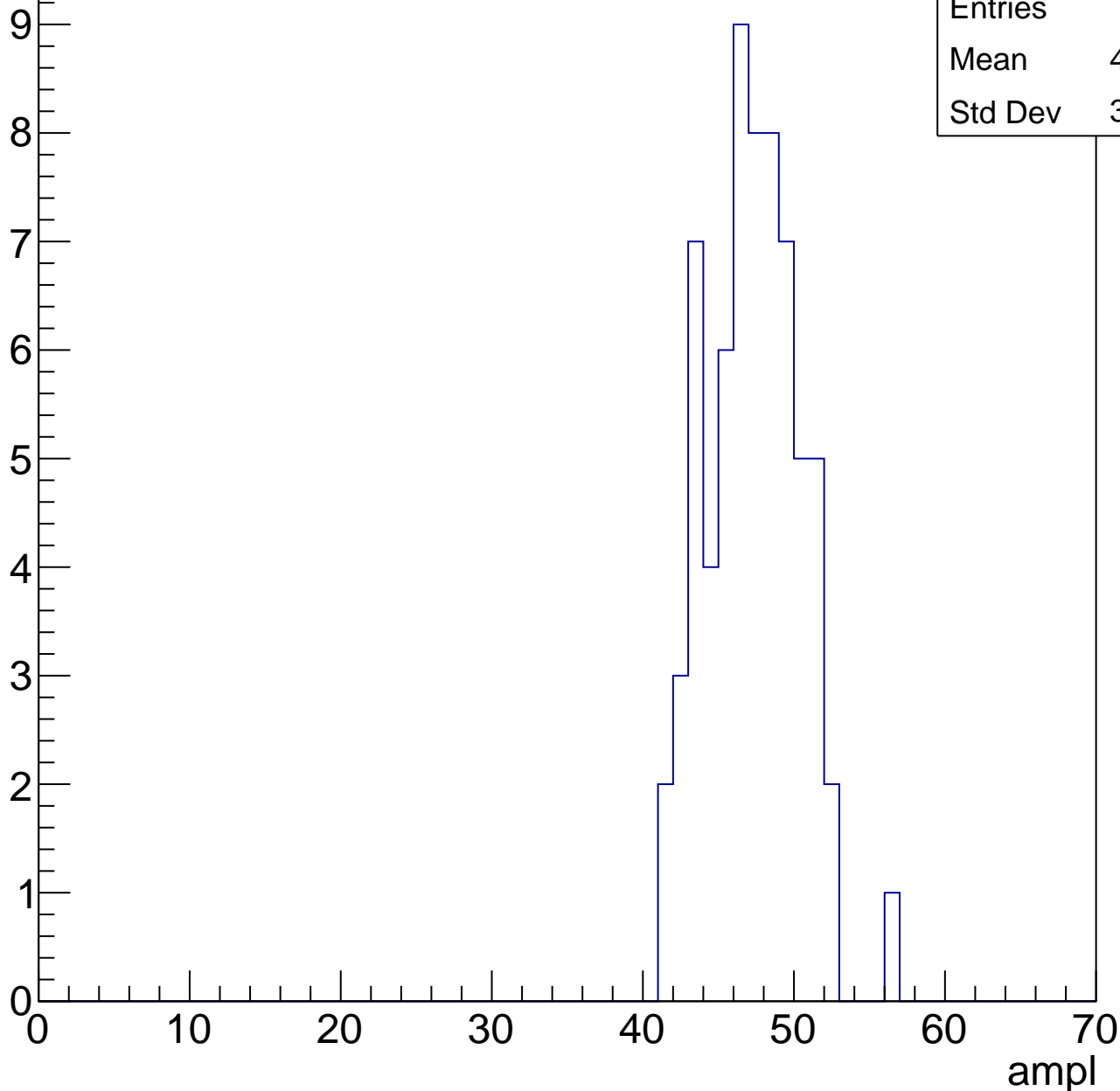


# B1L002S, U3-ch113, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

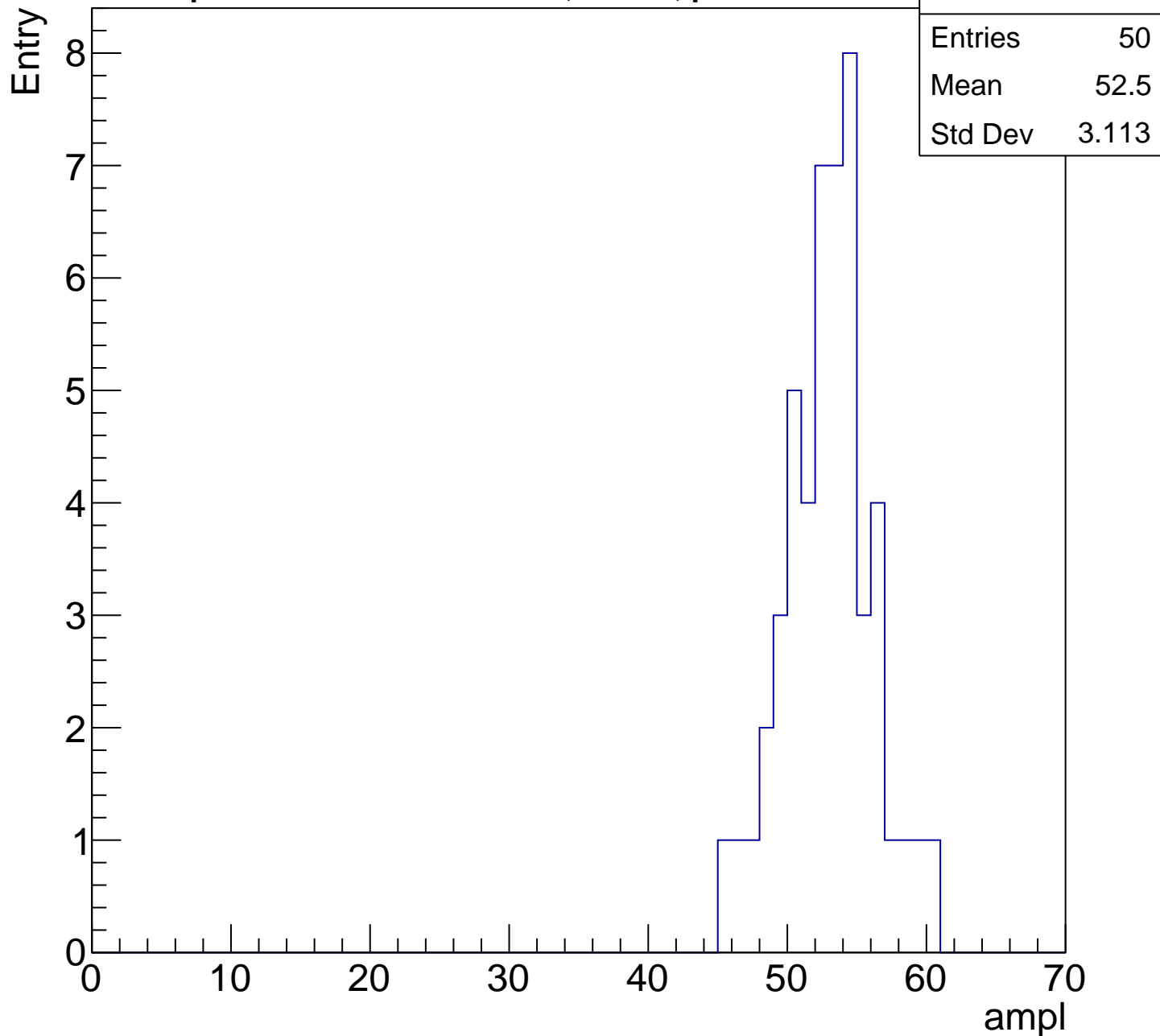
Entry

Entries	67
Mean	46.82
Std Dev	3.037



# B1L002S, U3-ch113, adc4

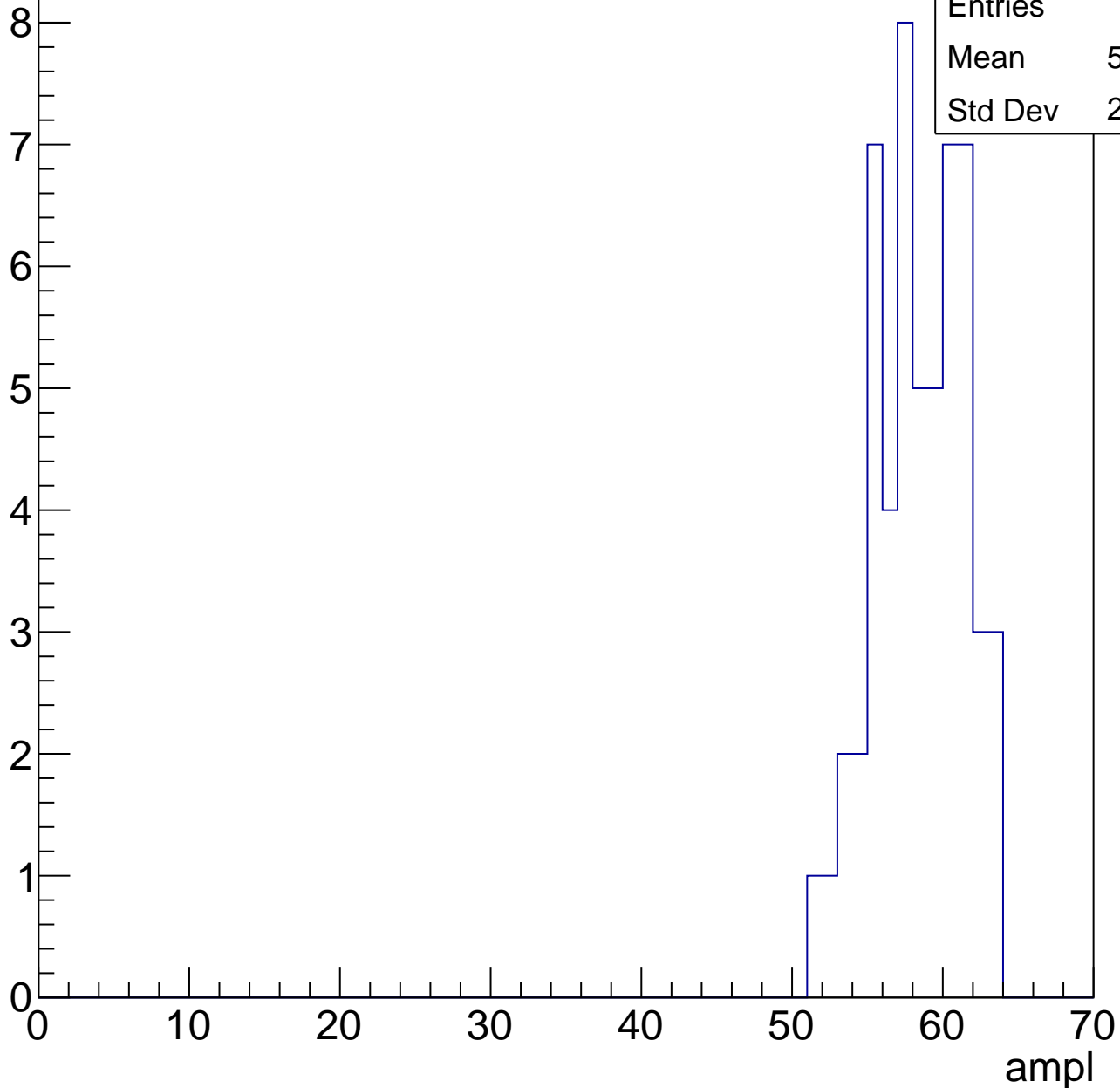
calib\_packv5\_042523\_0143.root, FC#10, port B3



# B1L002S, U3-ch113, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

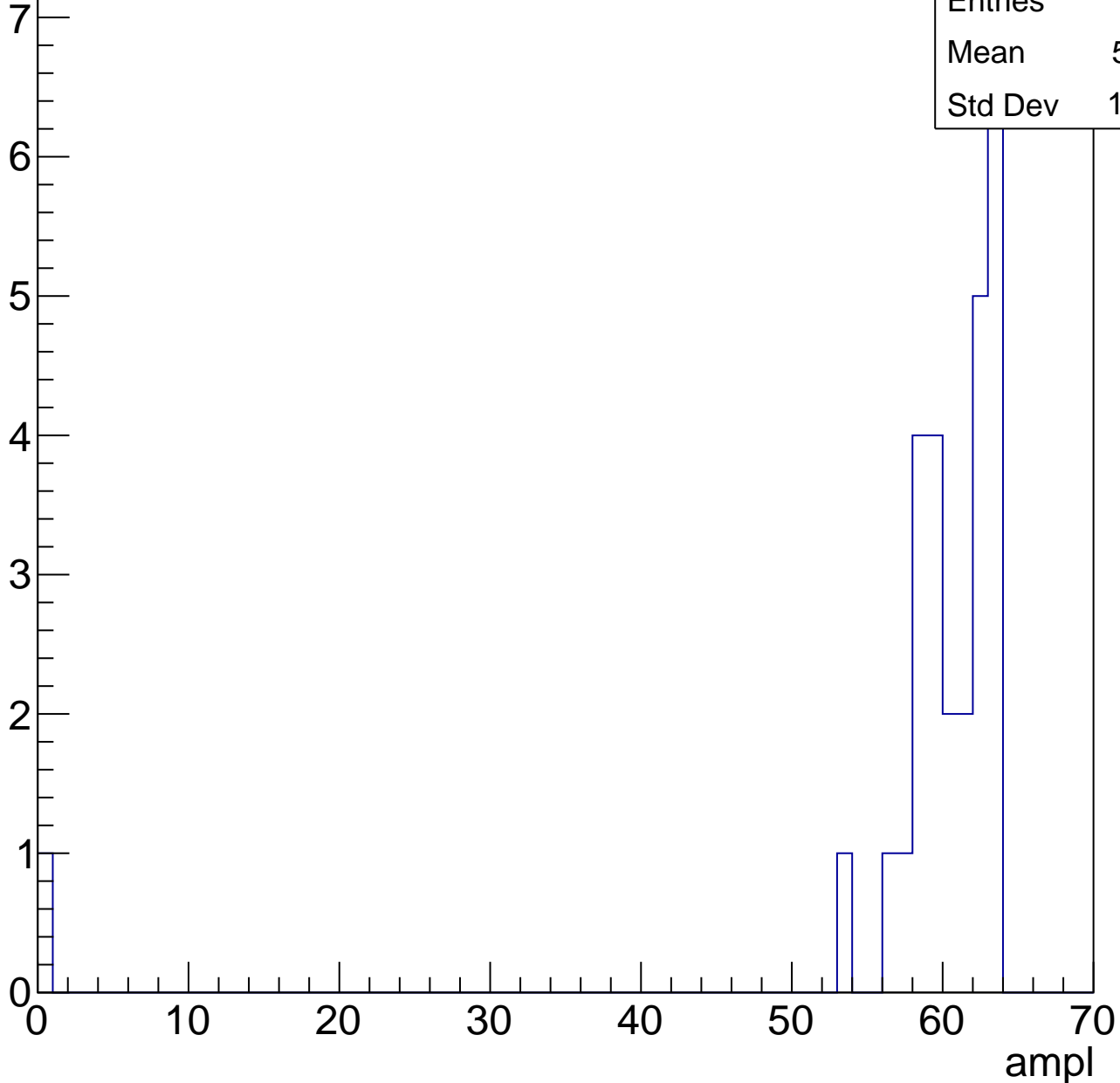


# B1L002S, U3-ch113, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	58.11
Std Dev	11.46

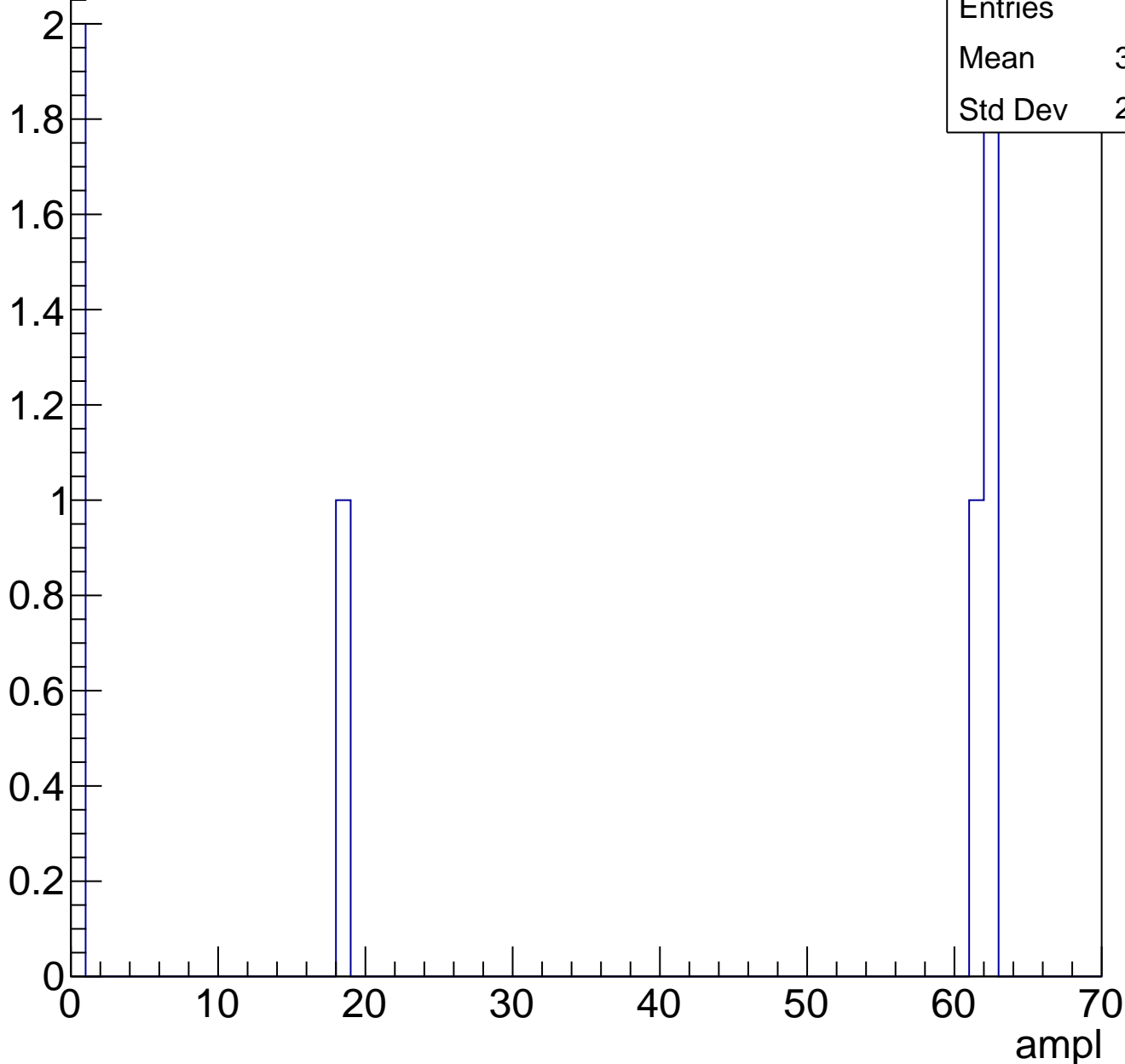




# B1L002S, U3-ch113, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	6
Mean	33.83
Std Dev	28.47

# B1L002S, U3-ch114, adc0

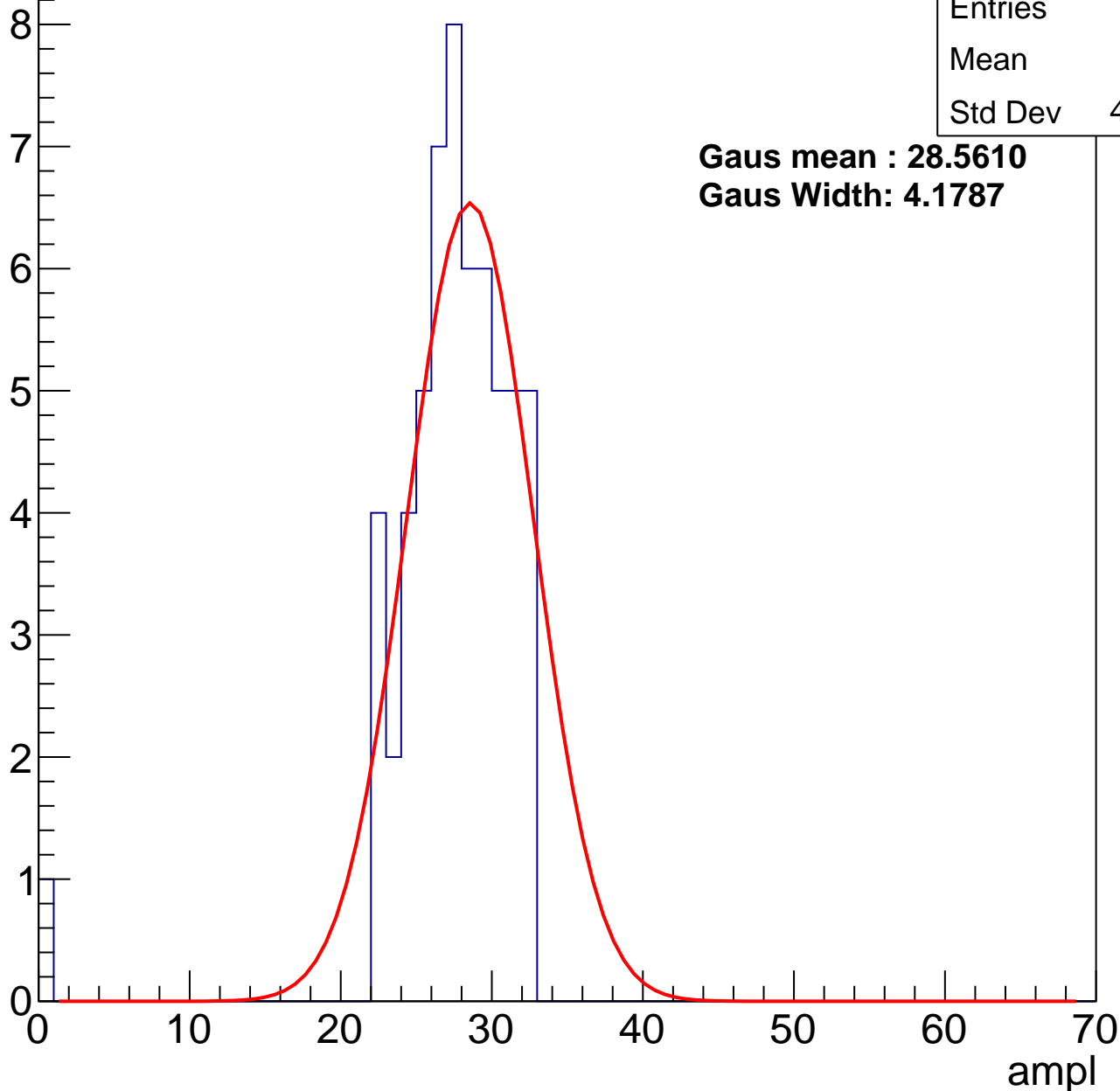
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	26.9
Std Dev	4.555

**Gaus mean : 28.5610**

**Gaus Width: 4.1787**



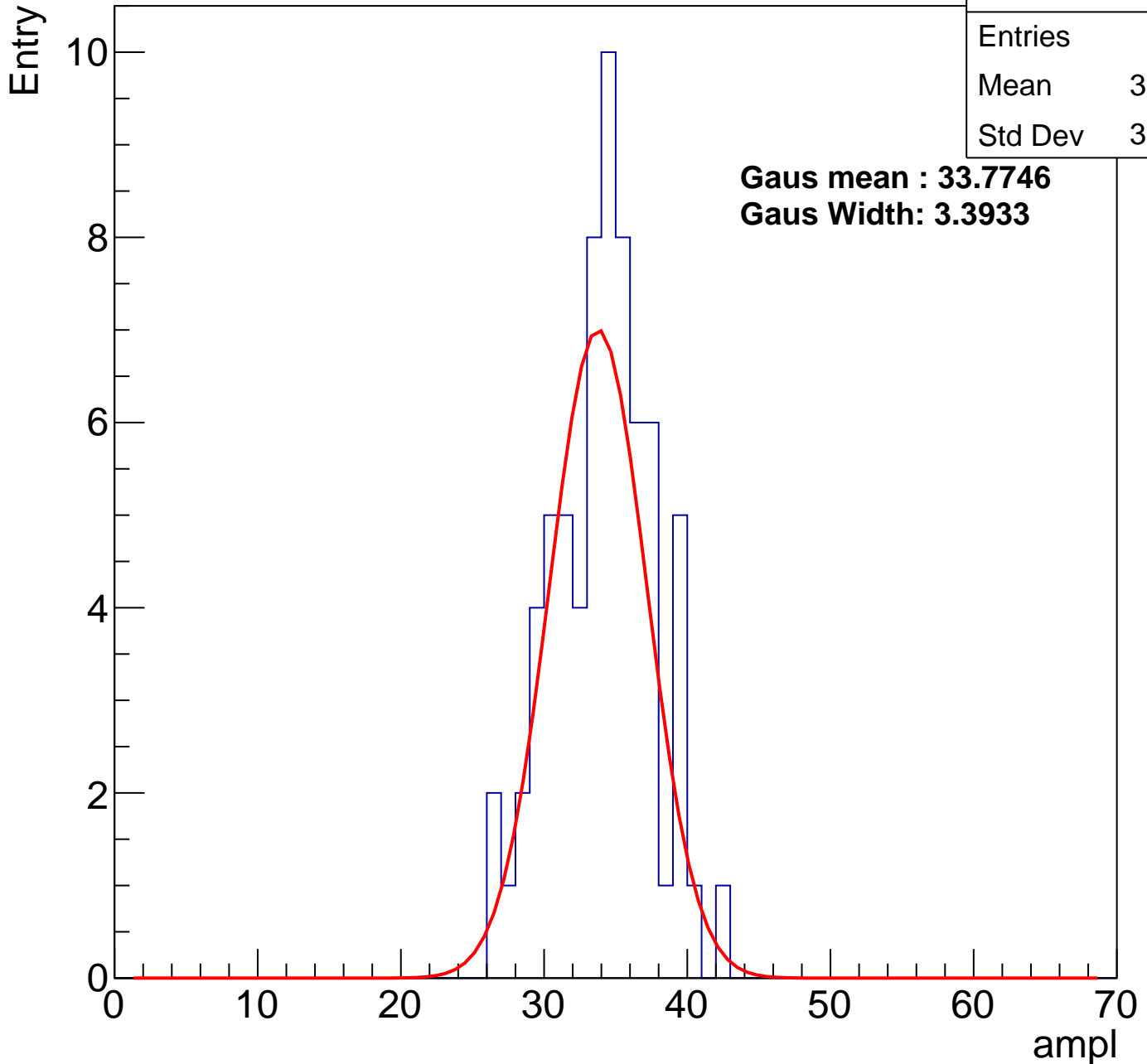
# B1L002S, U3-ch114, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	69
Mean	33.64
Std Dev	3.439

**Gaus mean : 33.7746**

**Gaus Width: 3.3933**



# B1L002S, U3-ch114, adc2

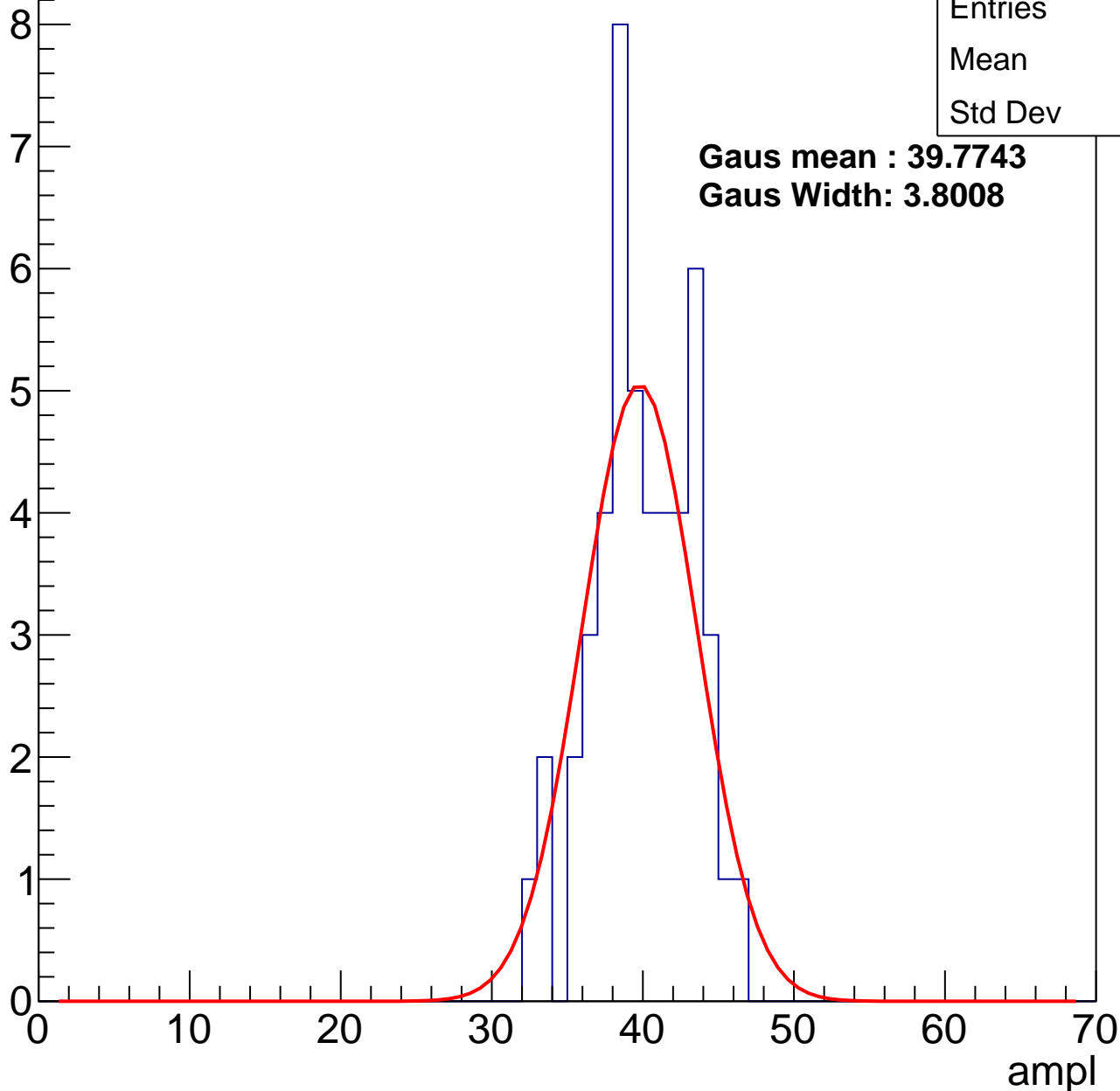
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	48
Mean	39.5
Std Dev	3.24

**Gaus mean : 39.7743**

**Gaus Width: 3.8008**

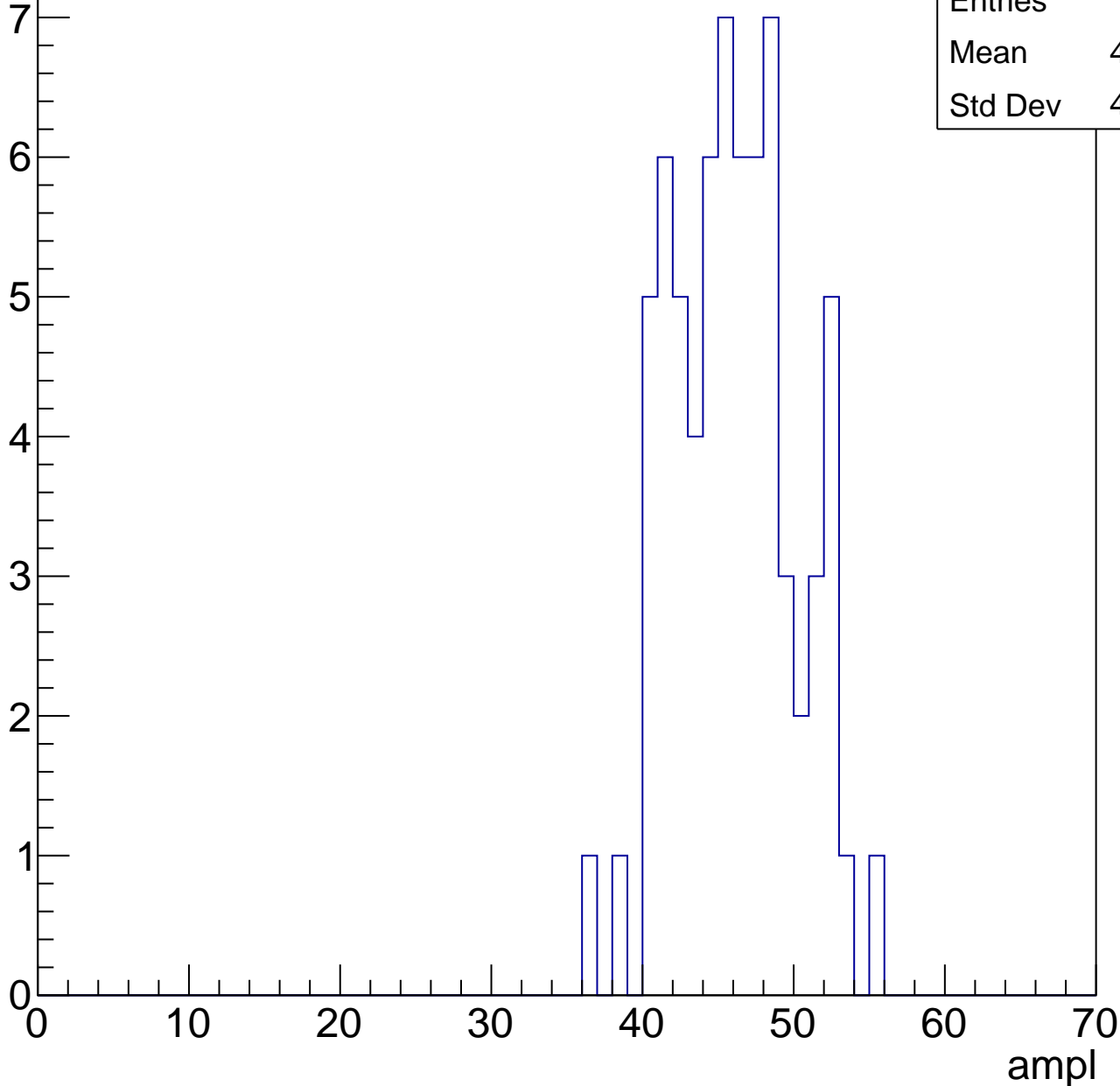


# B1L002S, U3-ch114, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	45.55
Std Dev	4.002

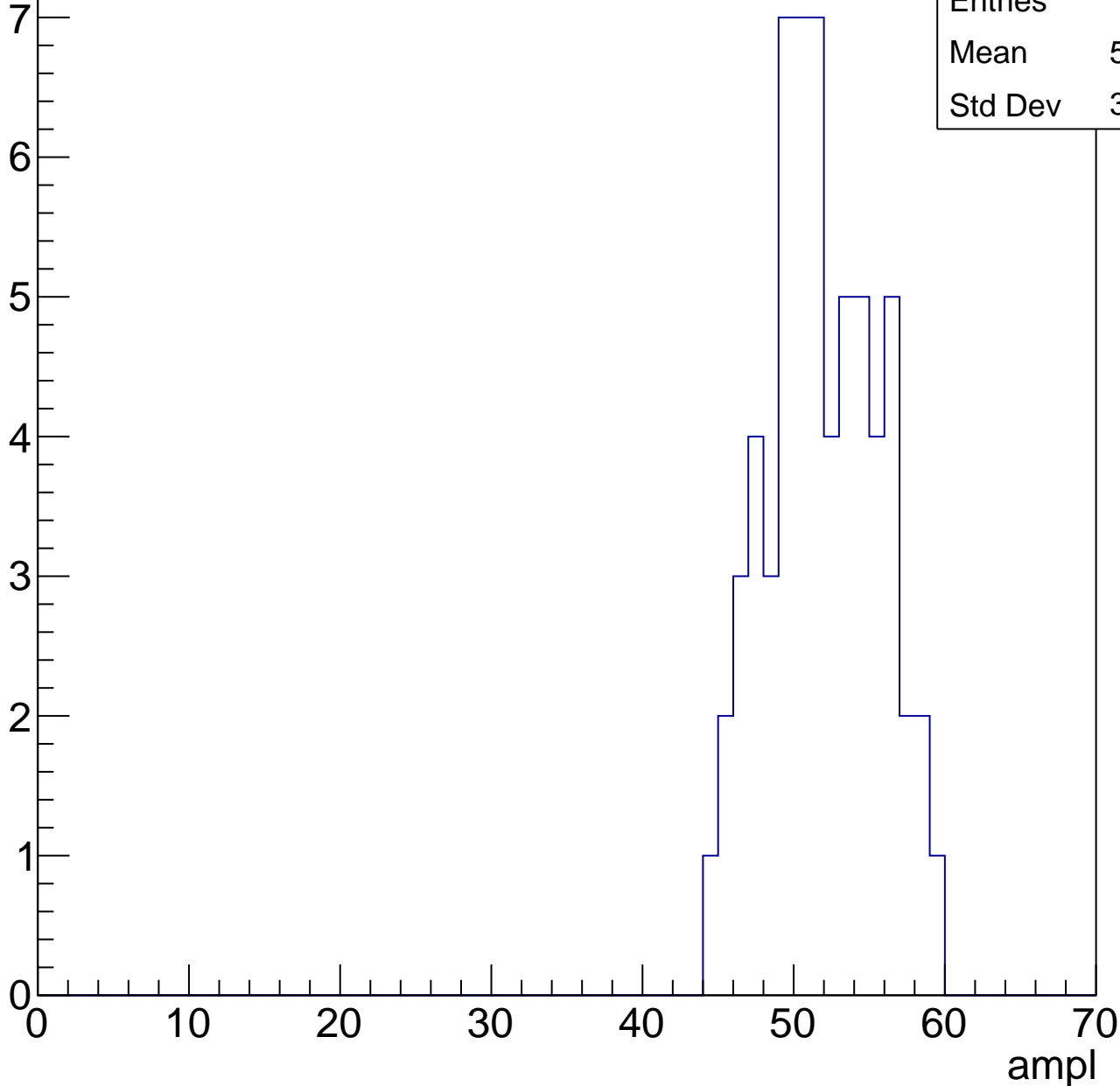


# B1L002S, U3-ch114, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	51.39
Std Dev	3.603

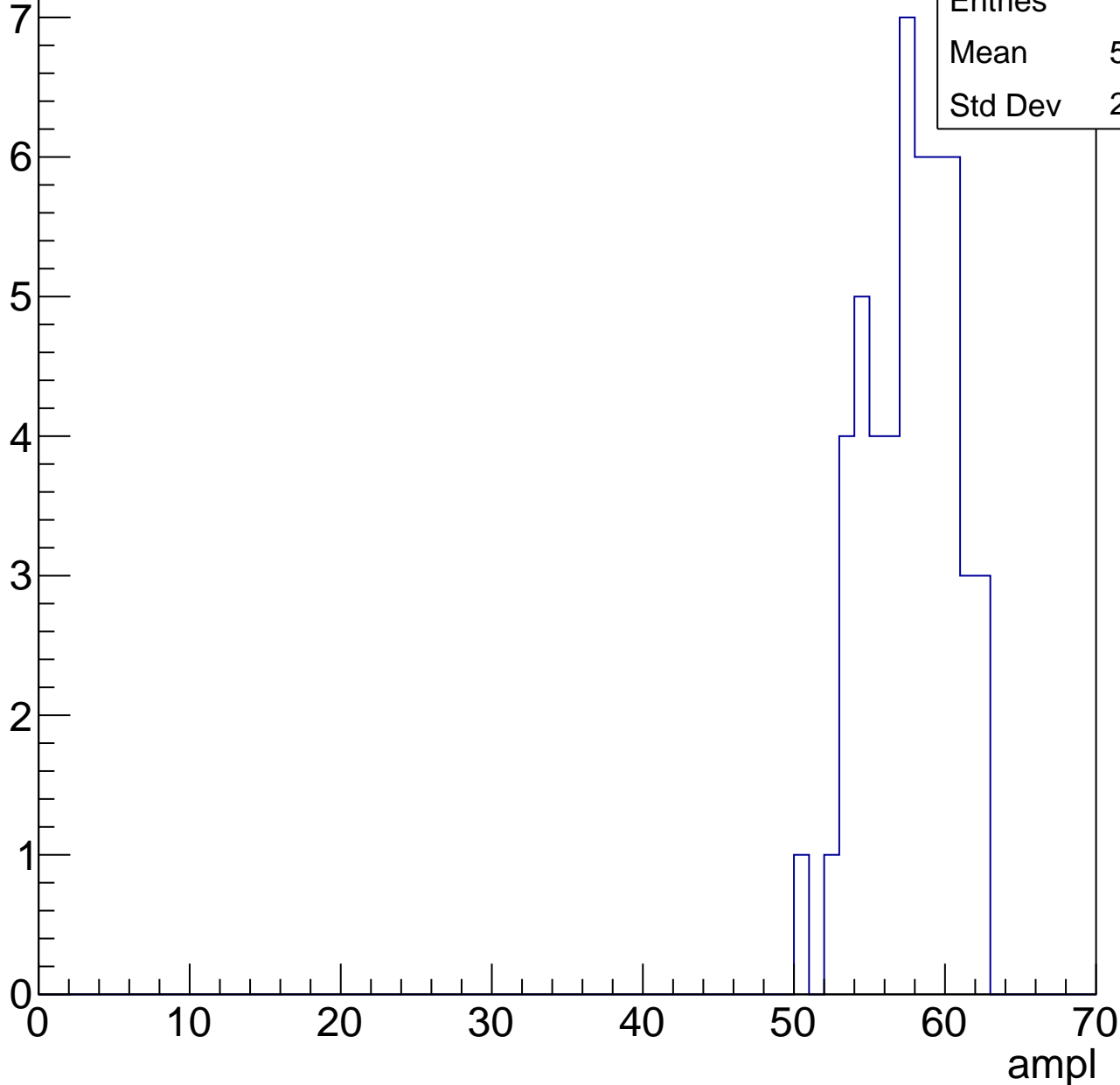


# B1L002S, U3-ch114, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	57.16
Std Dev	2.859

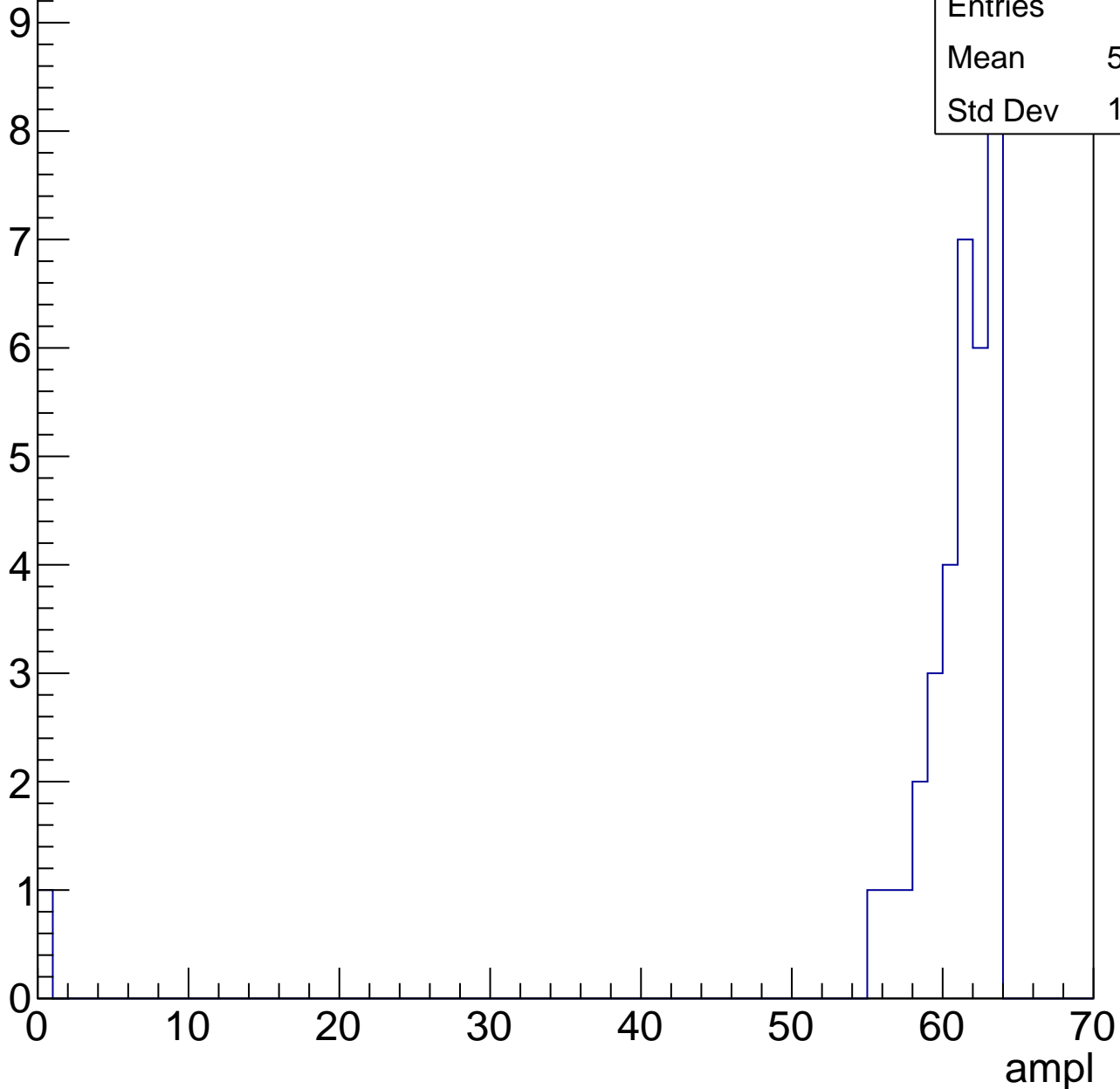


# B1L002S, U3-ch114, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	35
Mean	59.06
Std Dev	10.34





# B1L002S, U3-ch114, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch115, adc0

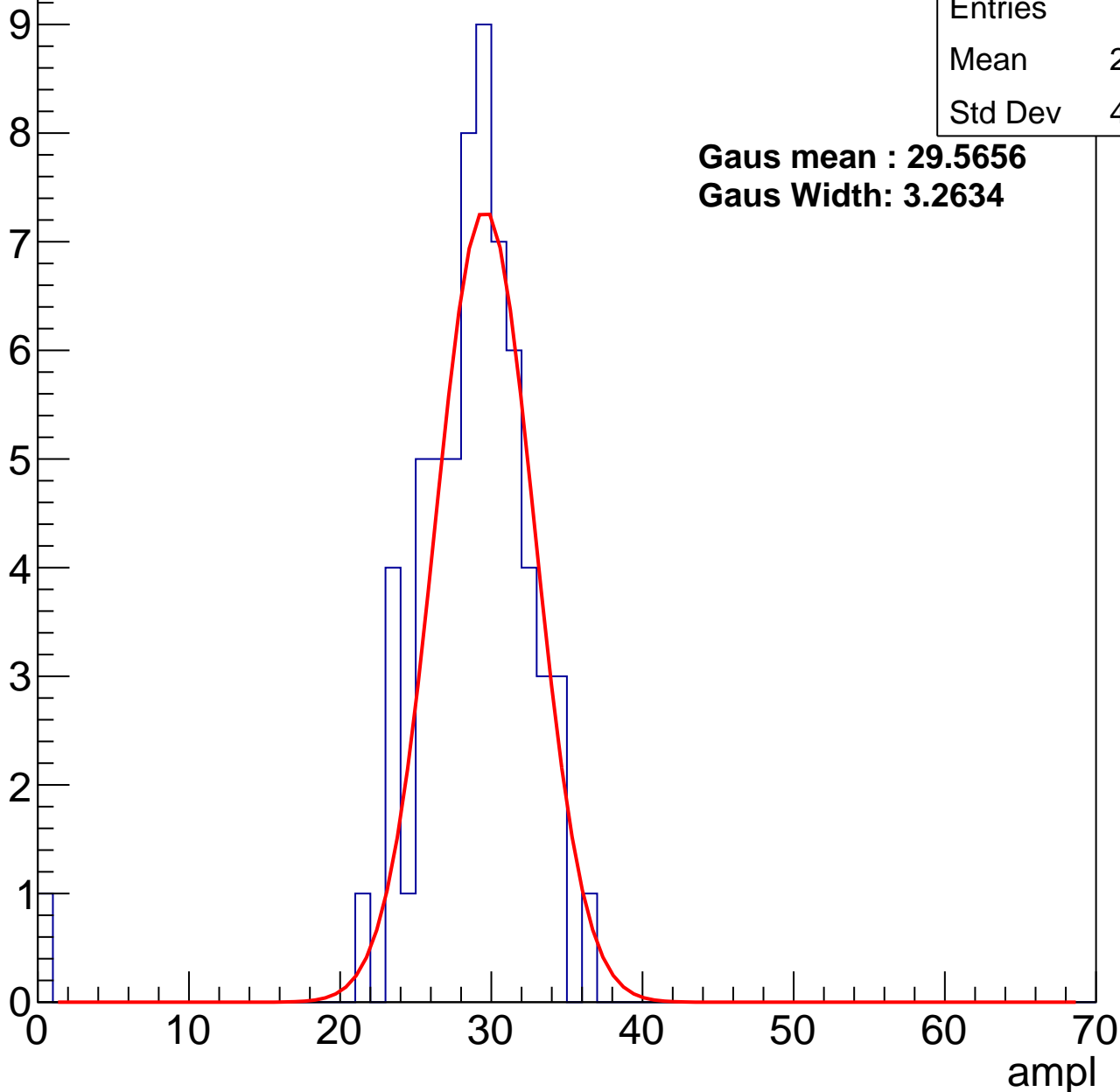
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	28.14
Std Dev	4.747

**Gaus mean : 29.5656**

**Gaus Width: 3.2634**



# B1L002S, U3-ch115, adc1

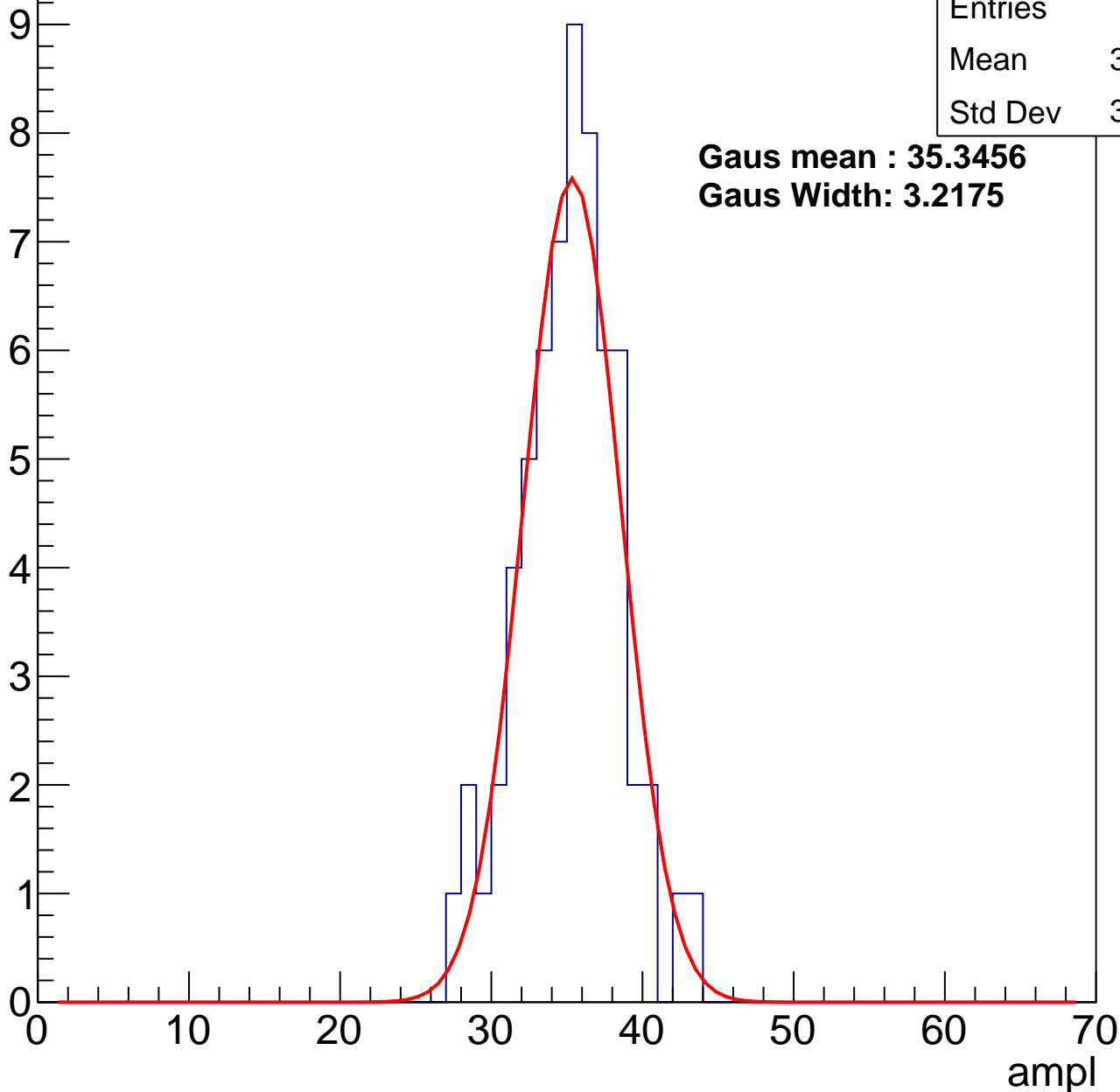
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	34.73
Std Dev	3.247

**Gaus mean : 35.3456**

**Gaus Width: 3.2175**



# B1L002S, U3-ch115, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	41.07
Std Dev	3.378

**Gaus mean : 41.8780**

**Gaus Width: 2.9140**

10

8

6

4

2

0

0

10

20

30

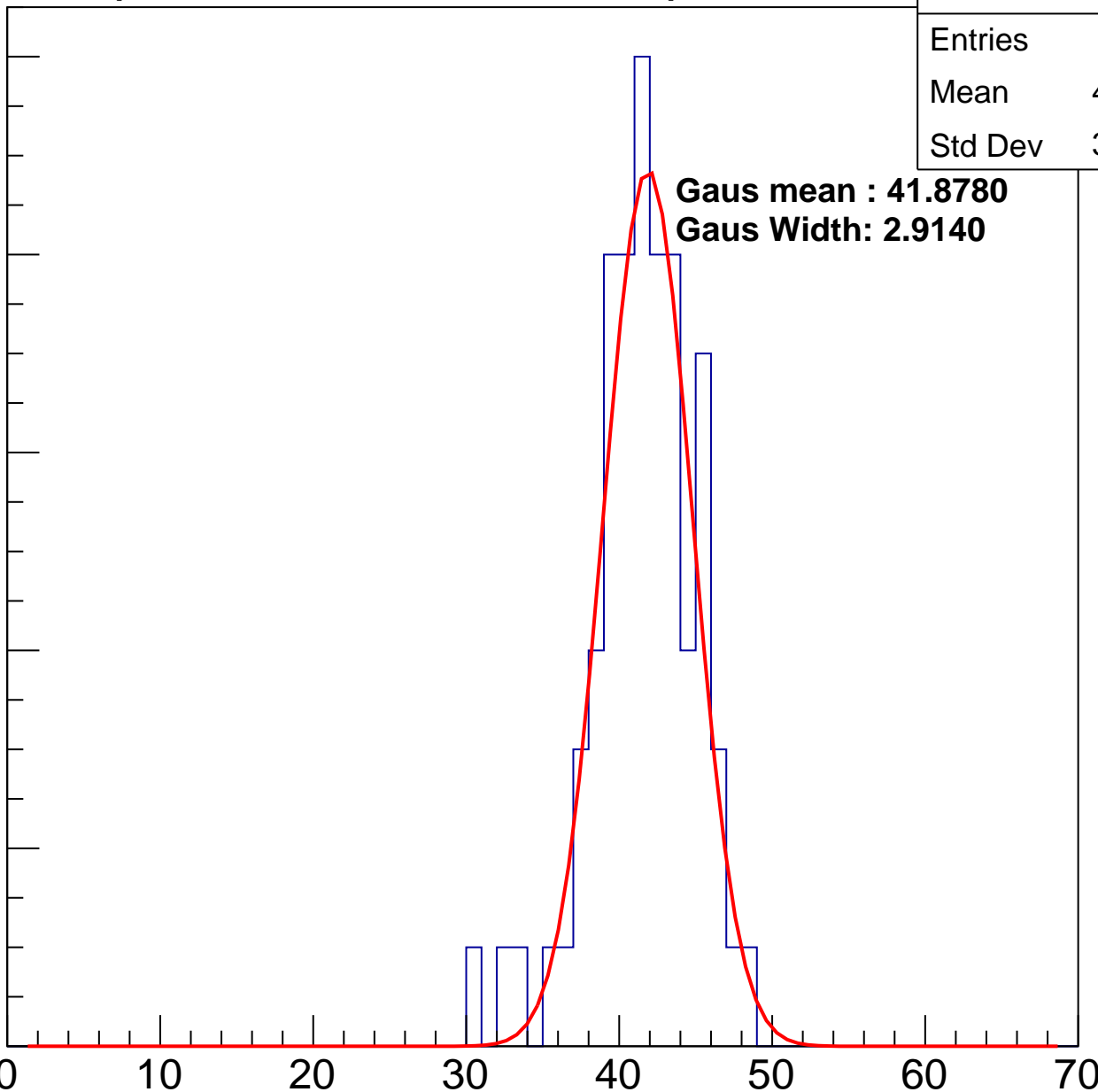
40

50

60

70

ampl

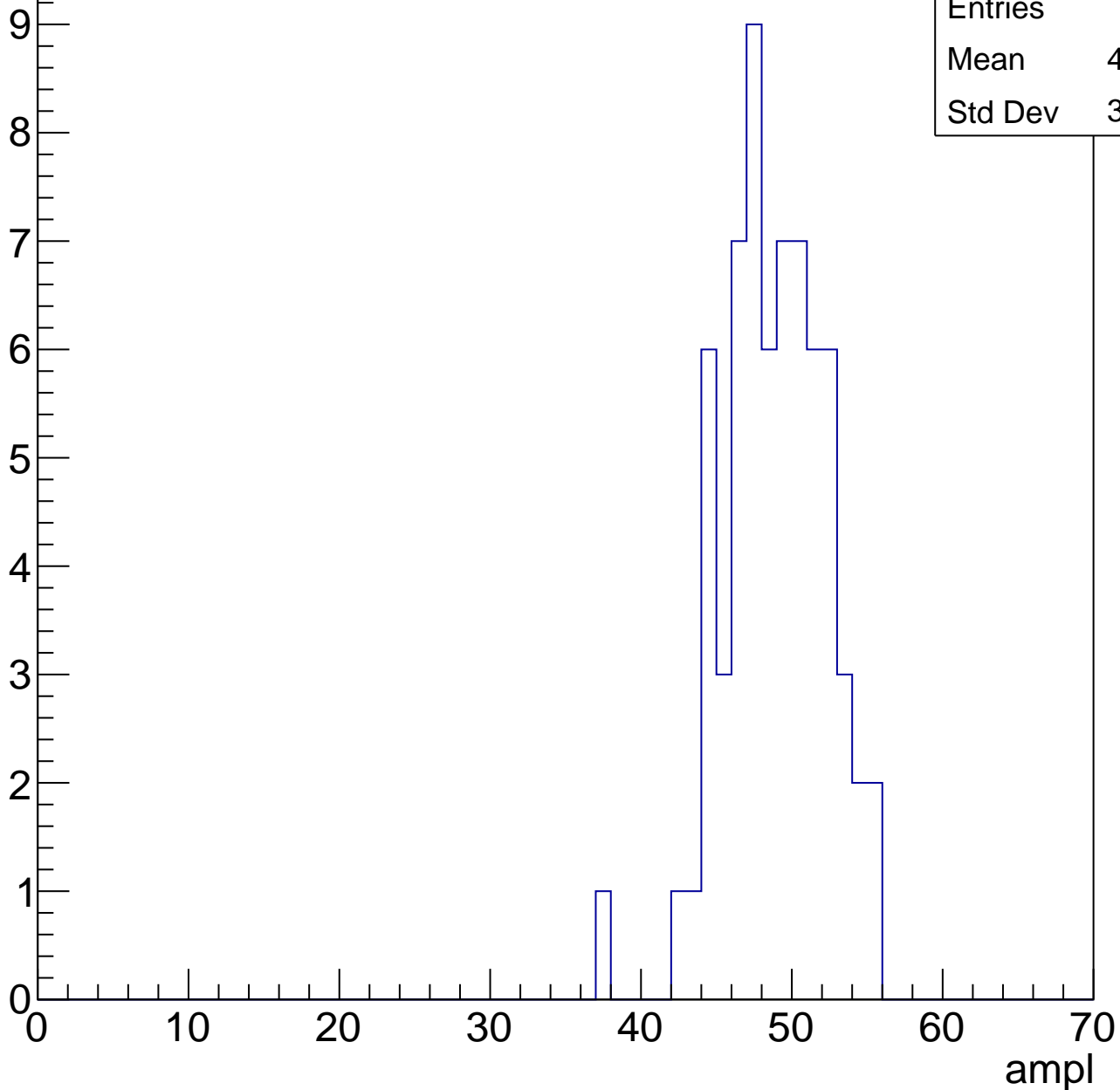


# B1L002S, U3-ch115, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	67
Mean	48.39
Std Dev	3.372

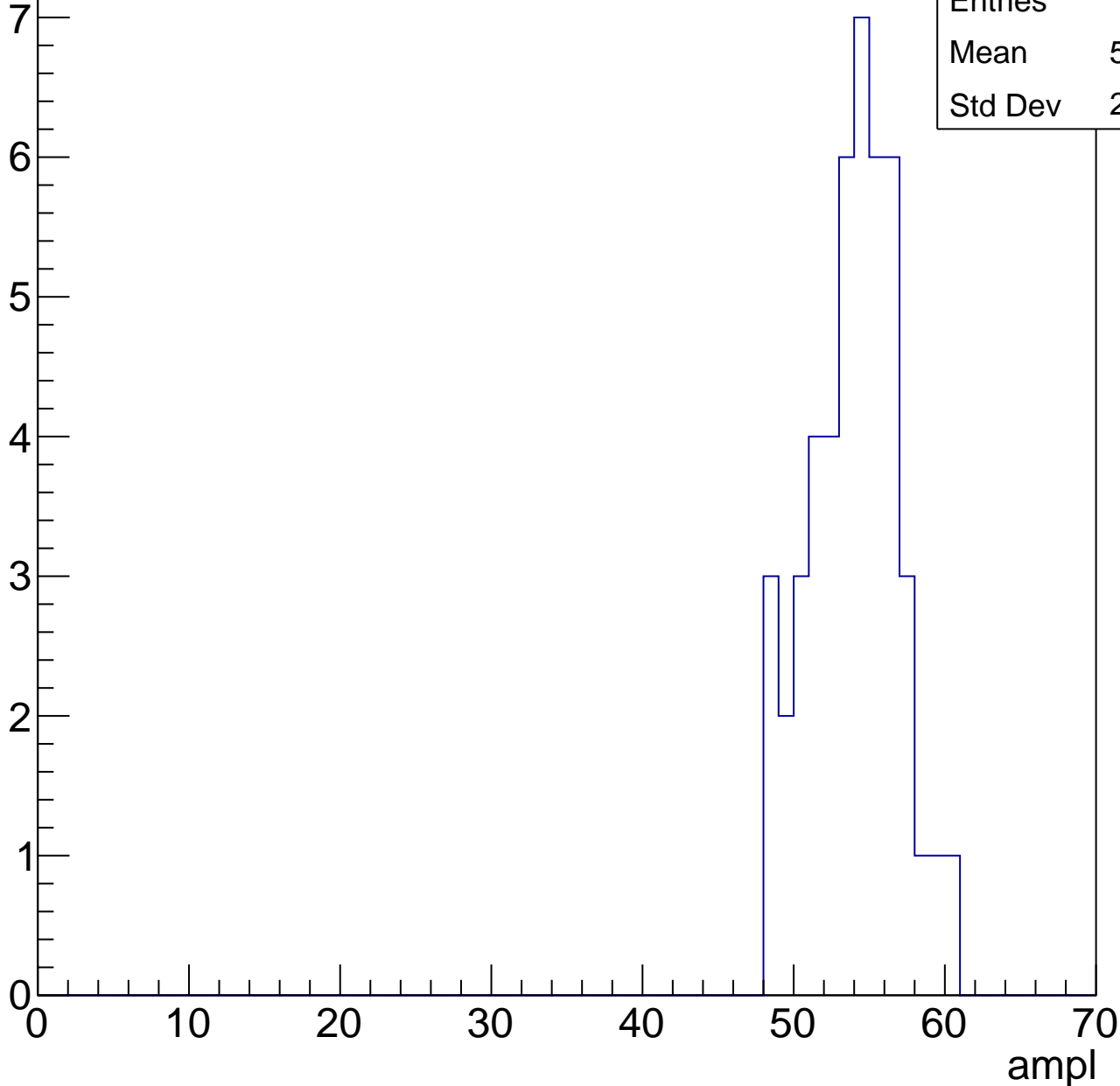


# B1L002S, U3-ch115, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

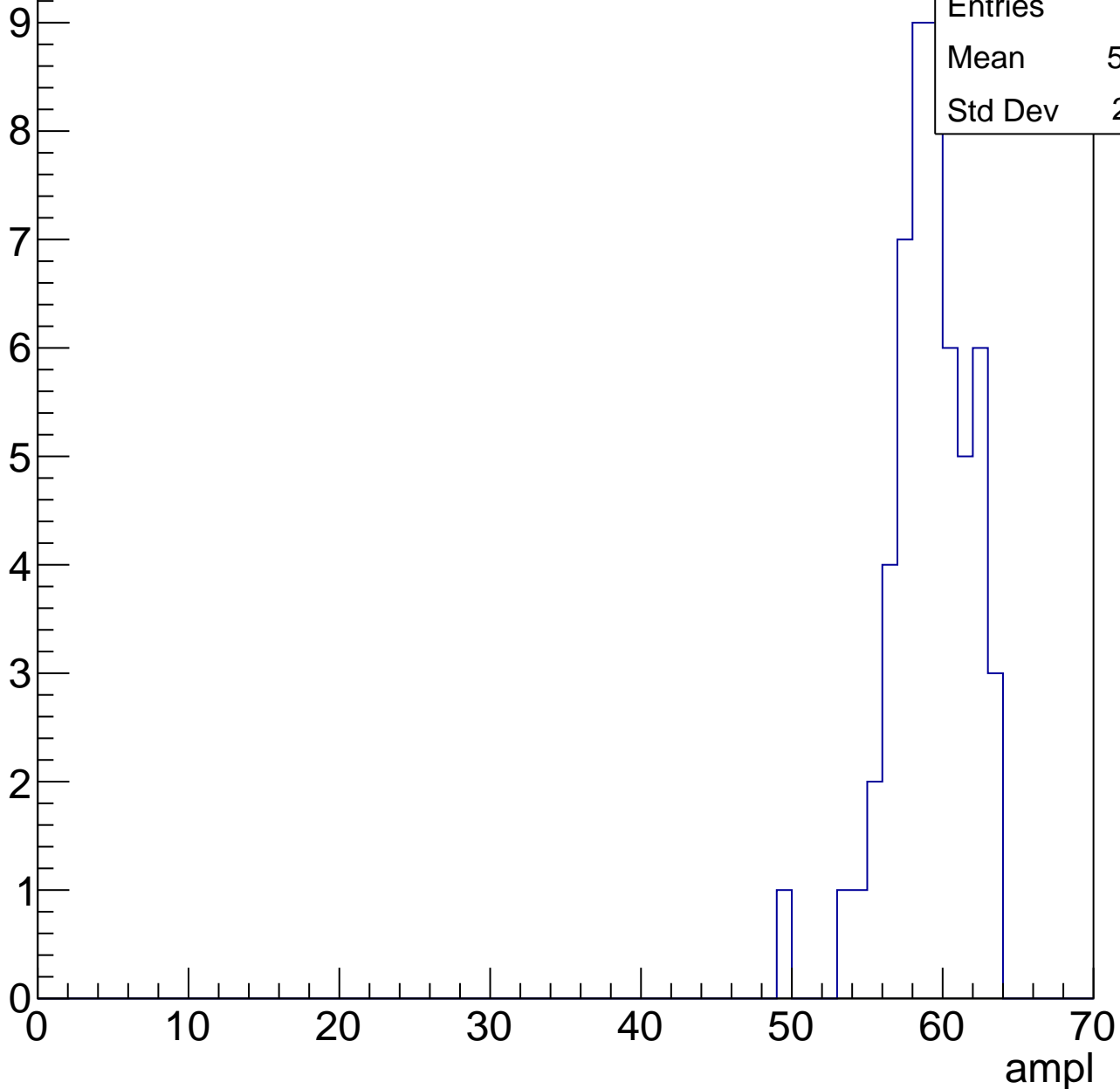
Entries	47
Mean	53.49
Std Dev	2.865



# B1L002S, U3-ch115, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

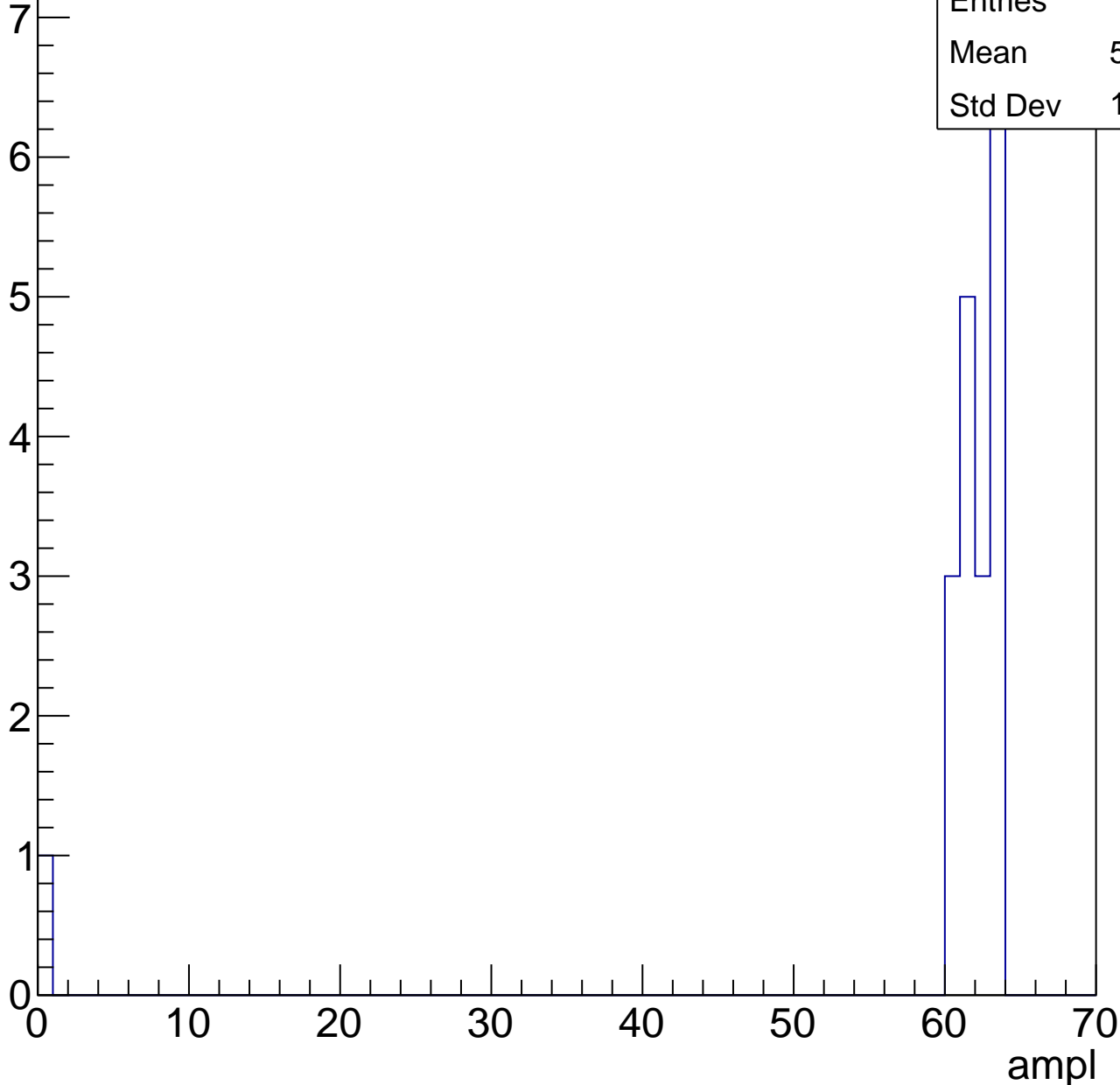


# B1L002S, U3-ch115, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	58.53
Std Dev	13.84





# B1L002S, U3-ch115, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch116, adc0

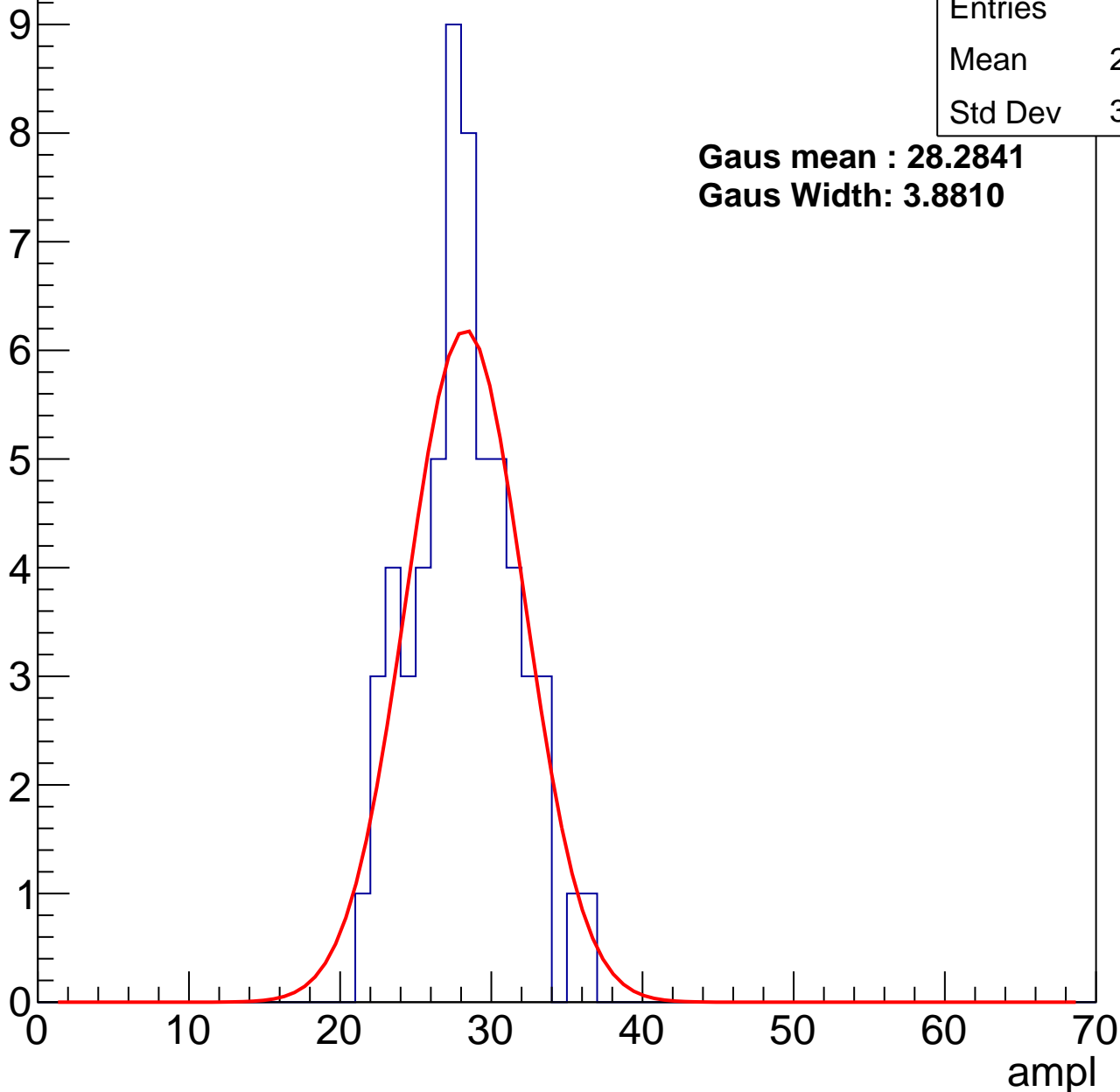
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	27.68
Std Dev	3.347

**Gaus mean : 28.2841**

**Gaus Width: 3.8810**



# B1L002S, U3-ch116, adc1

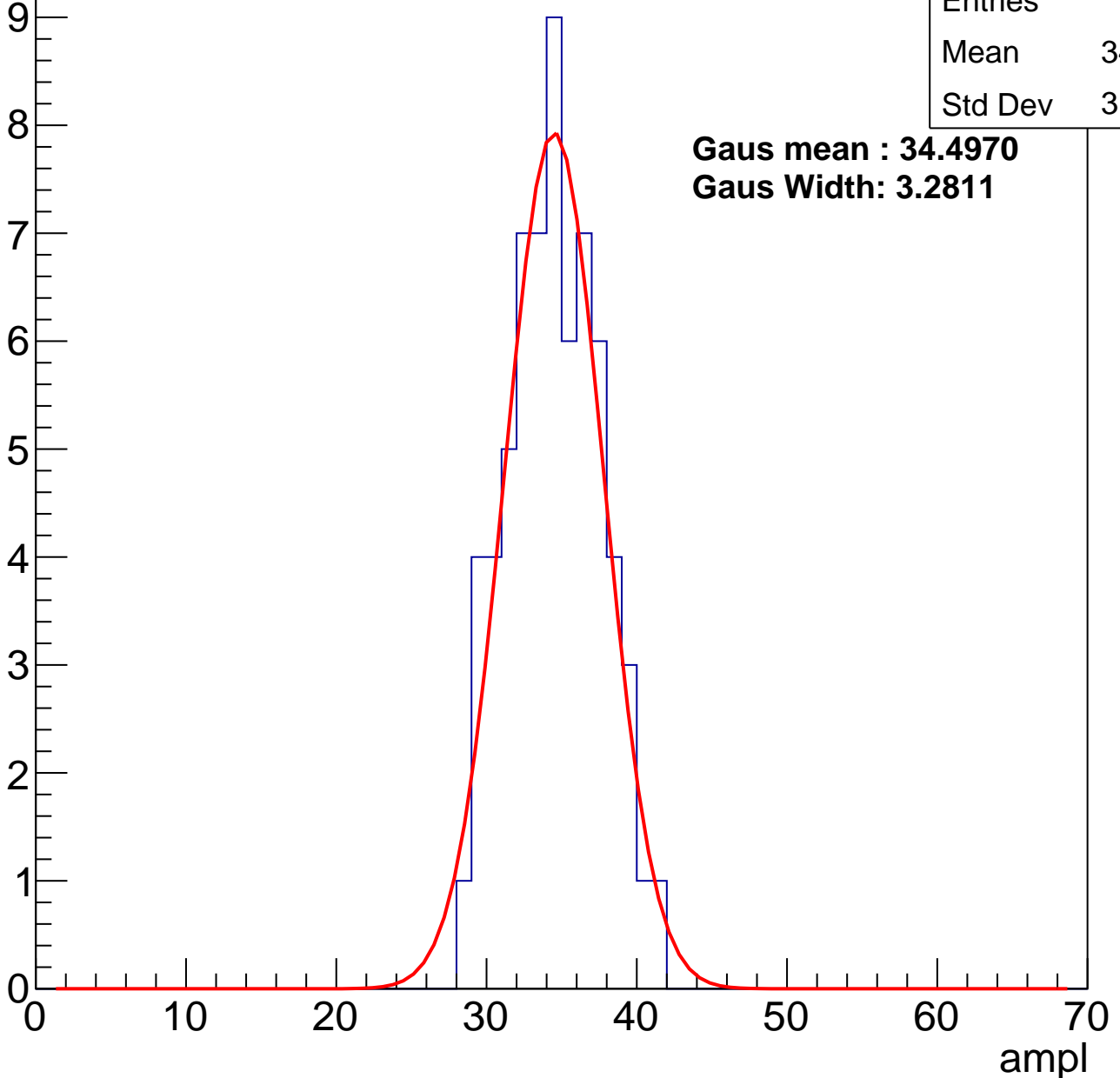
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.06
Std Dev	3.017

**Gaus mean : 34.4970**

**Gaus Width: 3.2811**



# B1L002S, U3-ch116, adc2

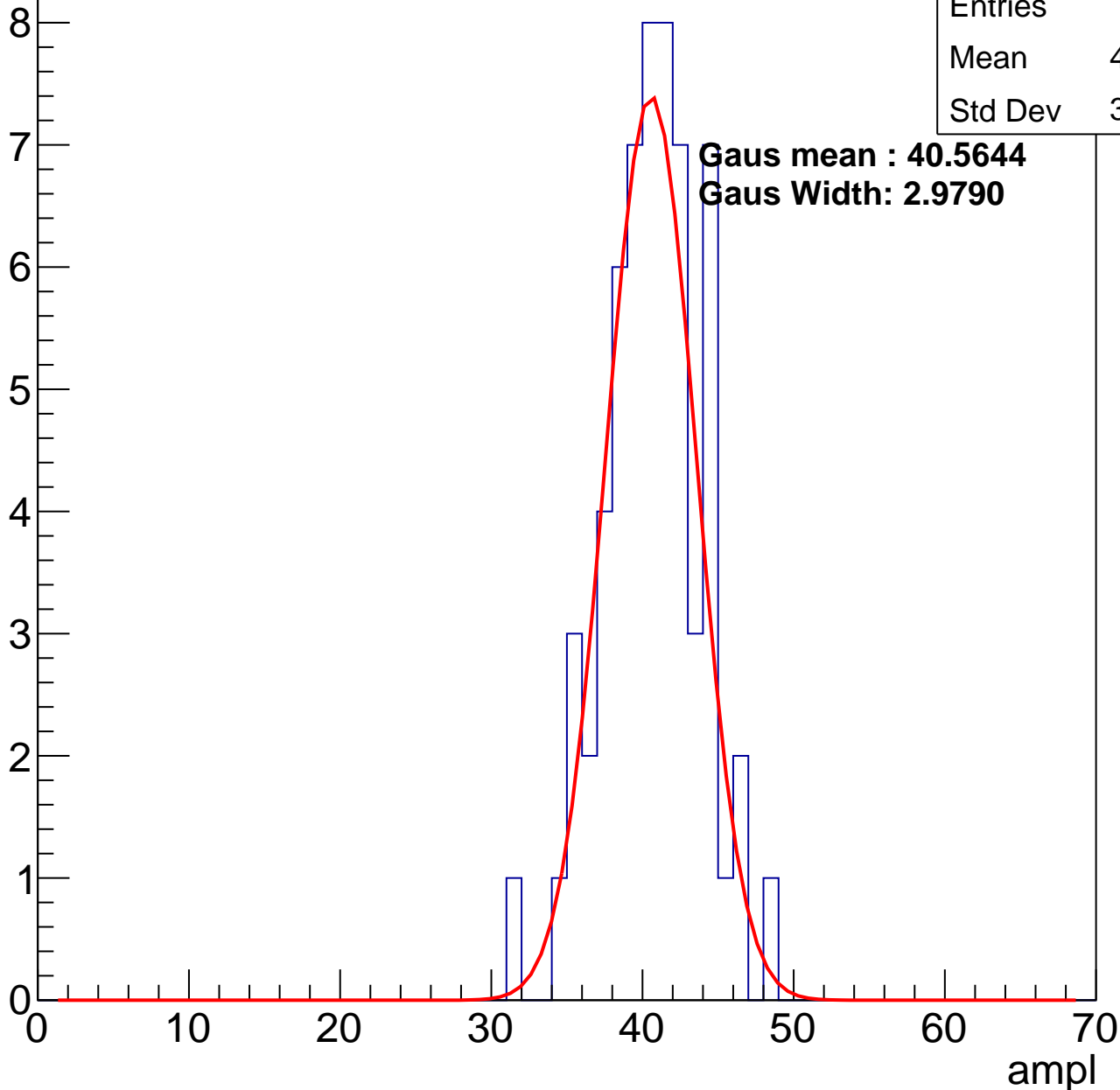
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	40.25
Std Dev	3.197

**Gaus mean : 40.5644**

**Gaus Width: 2.9790**



# B1L002S, U3-ch116, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	46.82
Std Dev	3.051

Entry

10

8

6

4

2

0

0

10

20

30

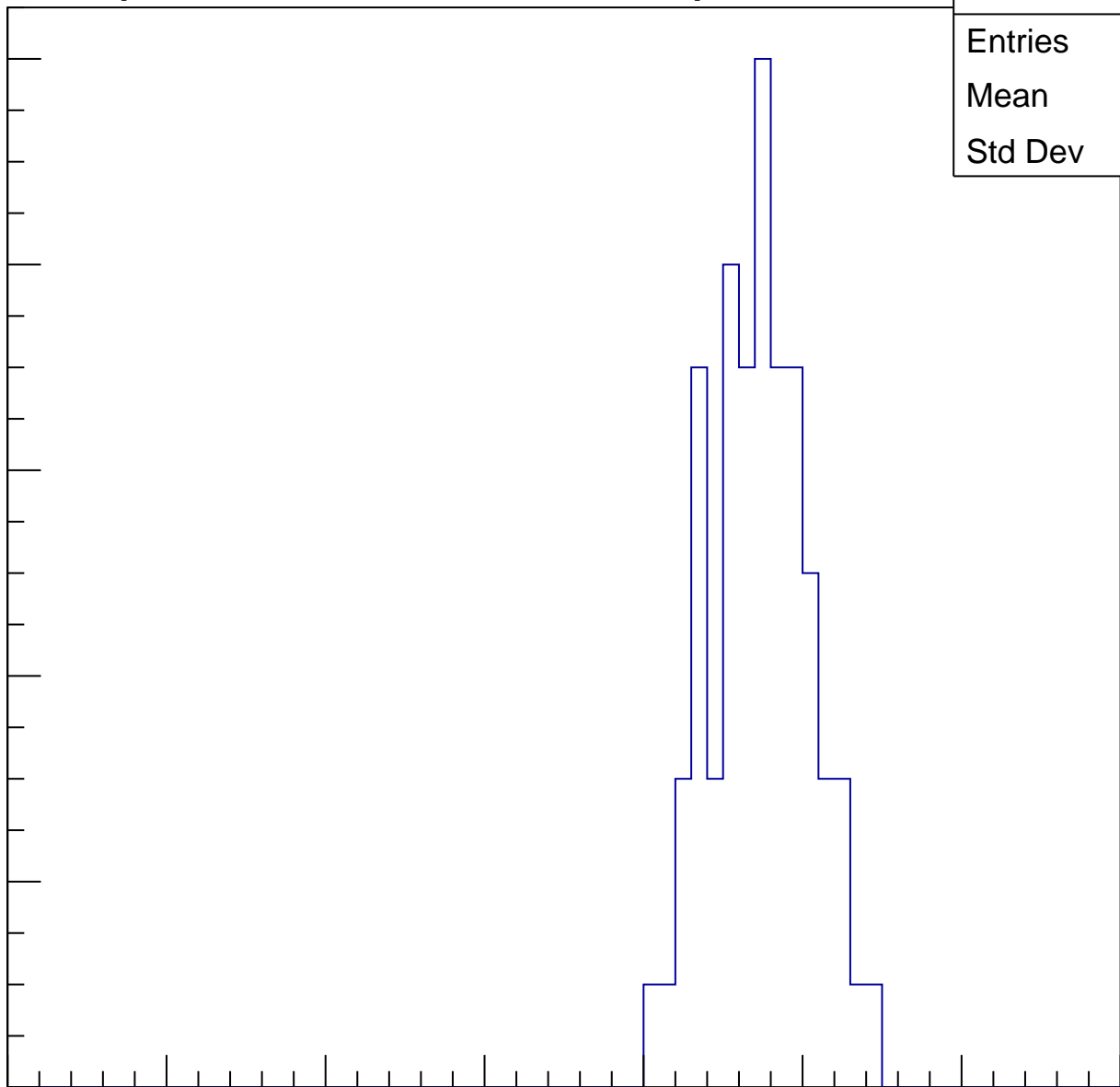
40

50

60

70

ampl

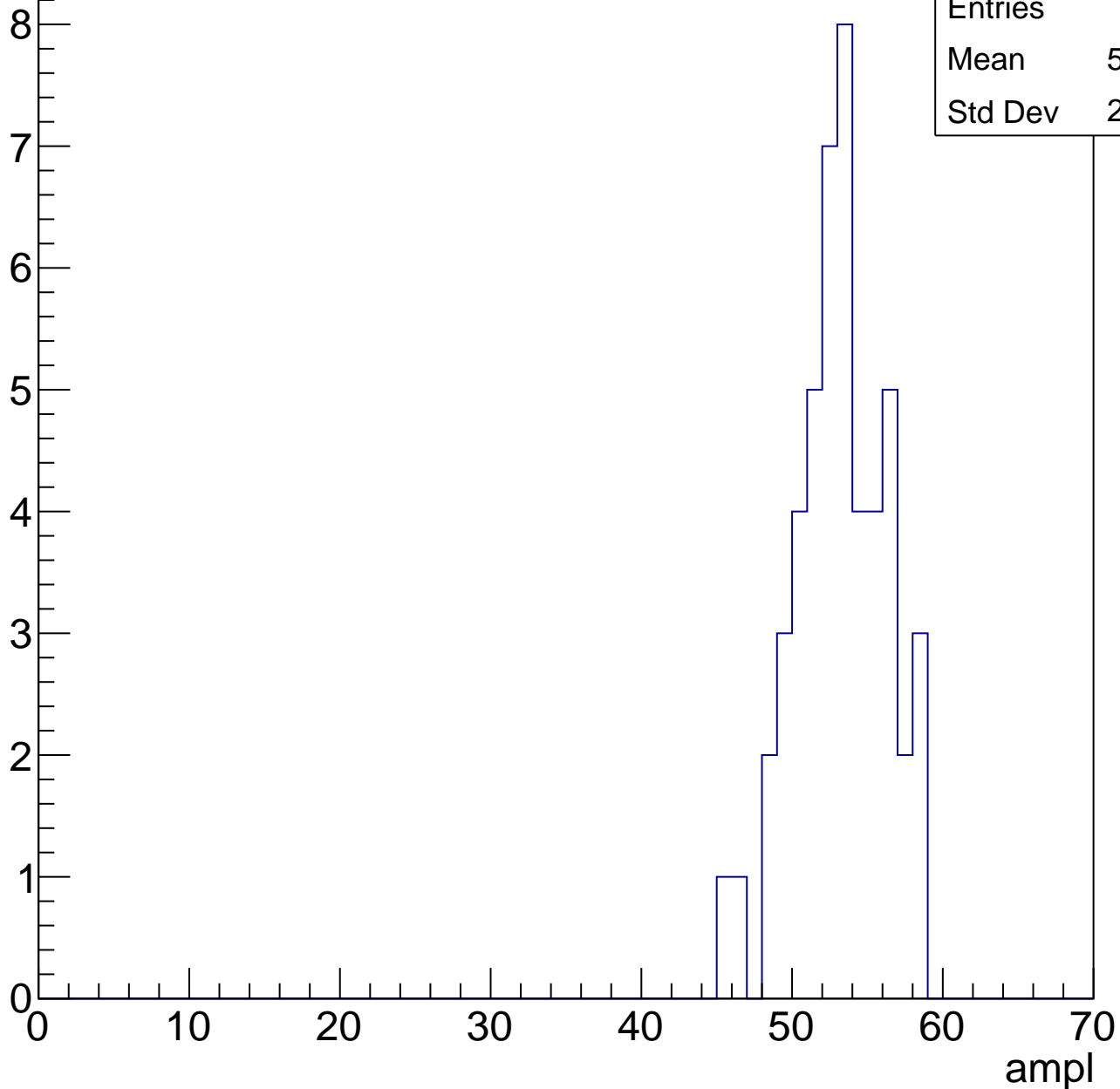


# B1L002S, U3-ch116, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	49
Mean	52.67
Std Dev	2.999

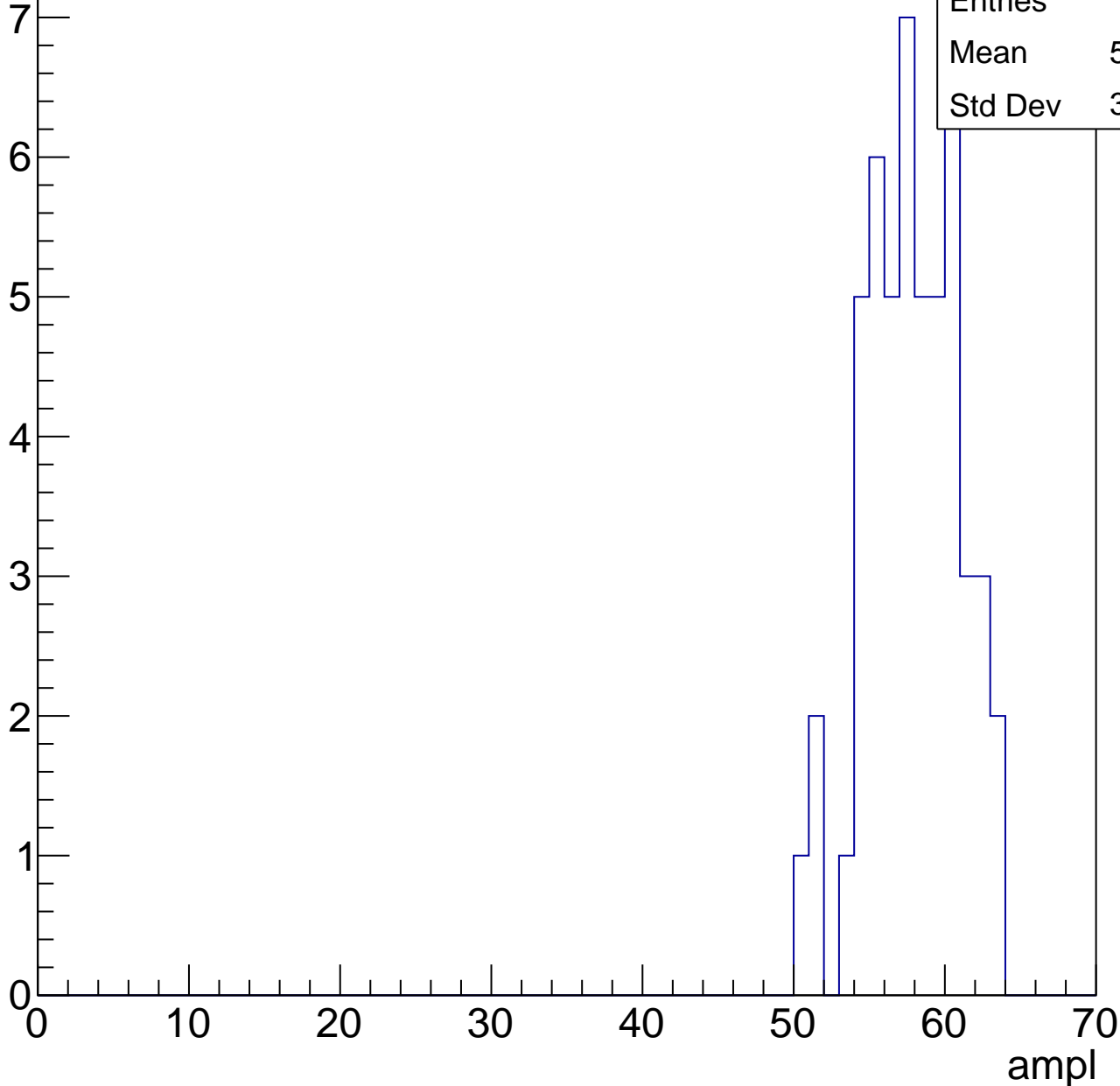


# B1L002S, U3-ch116, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	57.38
Std Dev	3.058

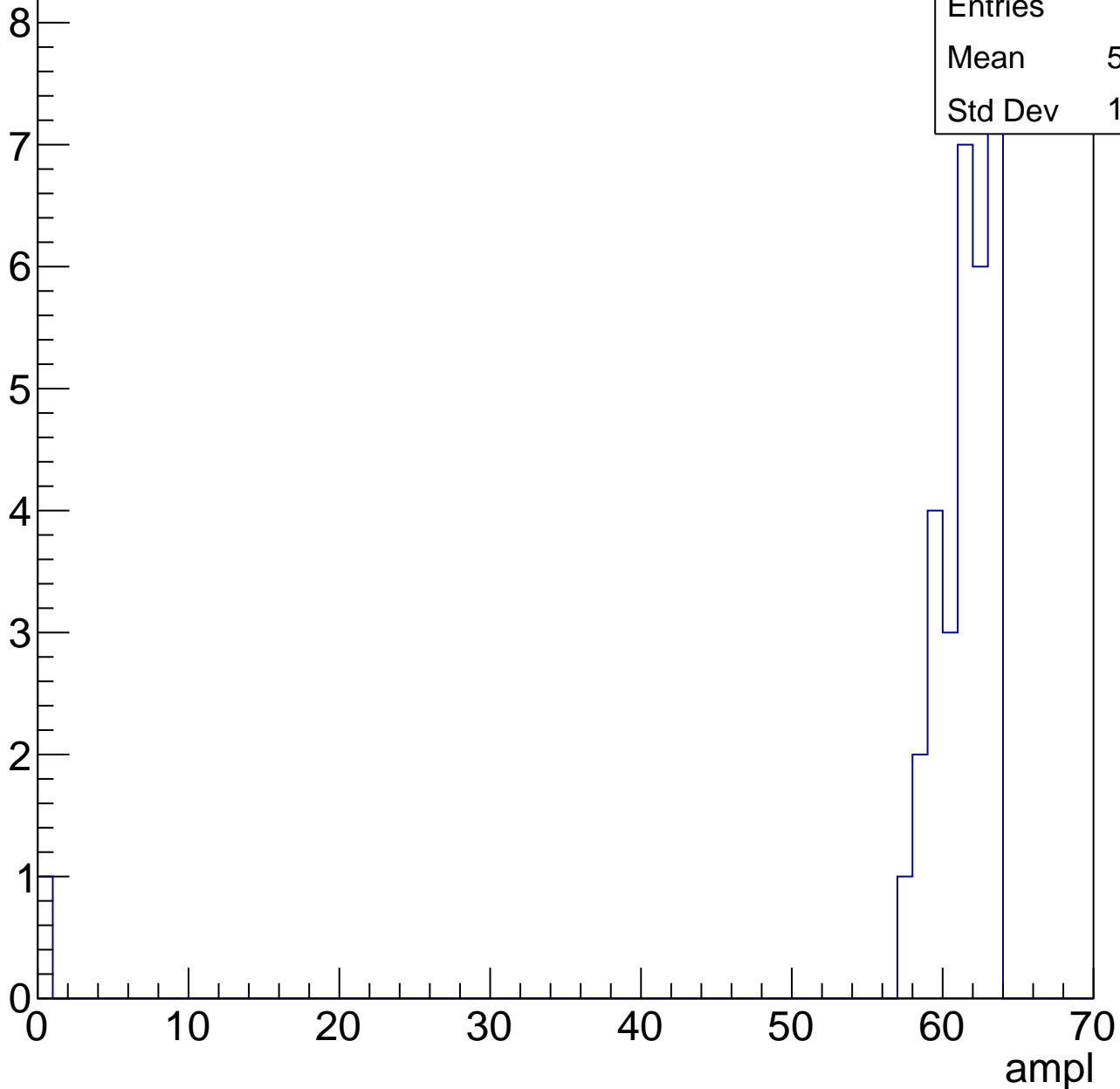


# B1L002S, U3-ch116, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	32
Mean	59.12
Std Dev	10.75

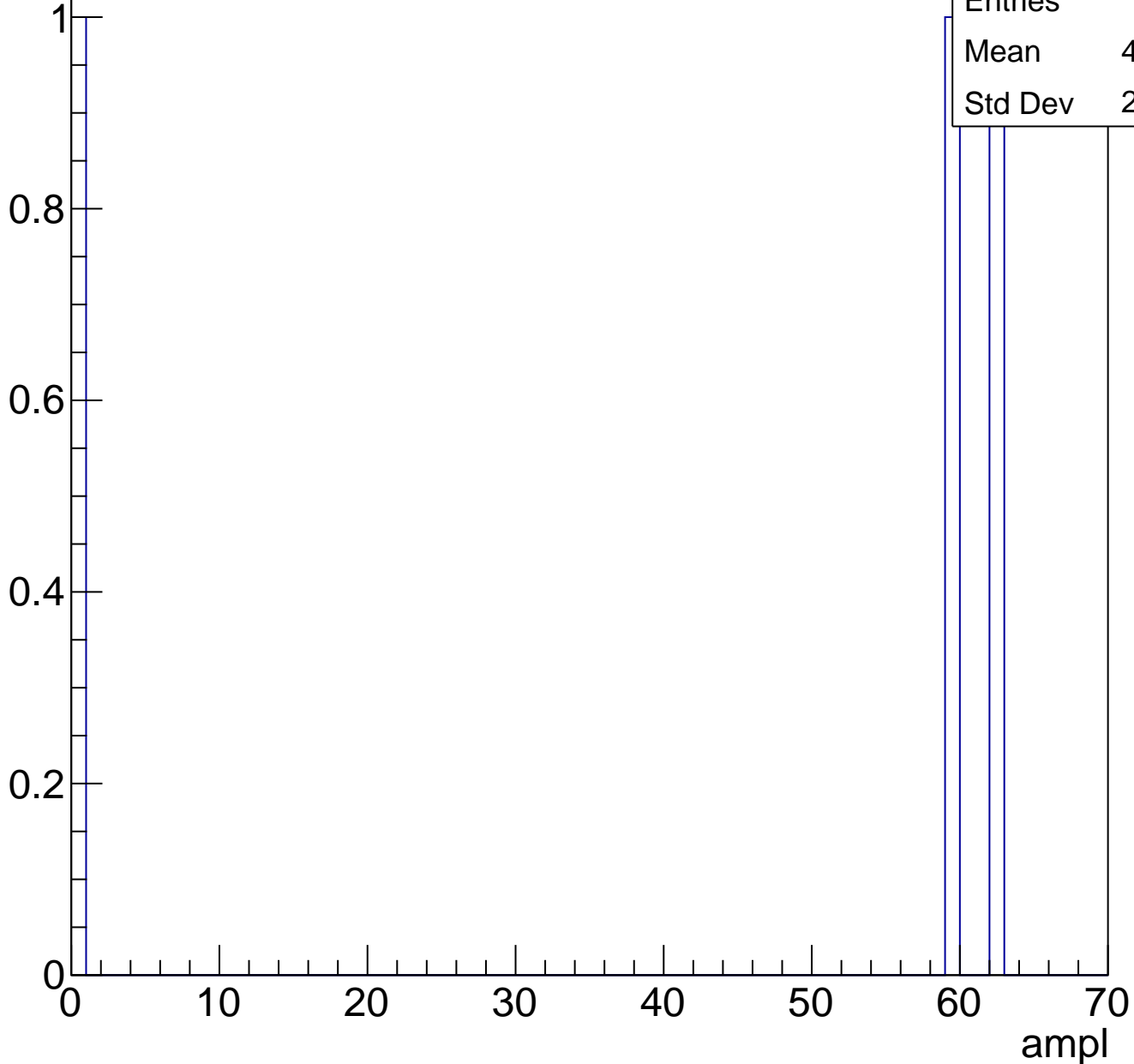




# B1L002S, U3-ch116, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	40.33
Std Dev	28.55

# B1L002S, U3-ch117, adc0

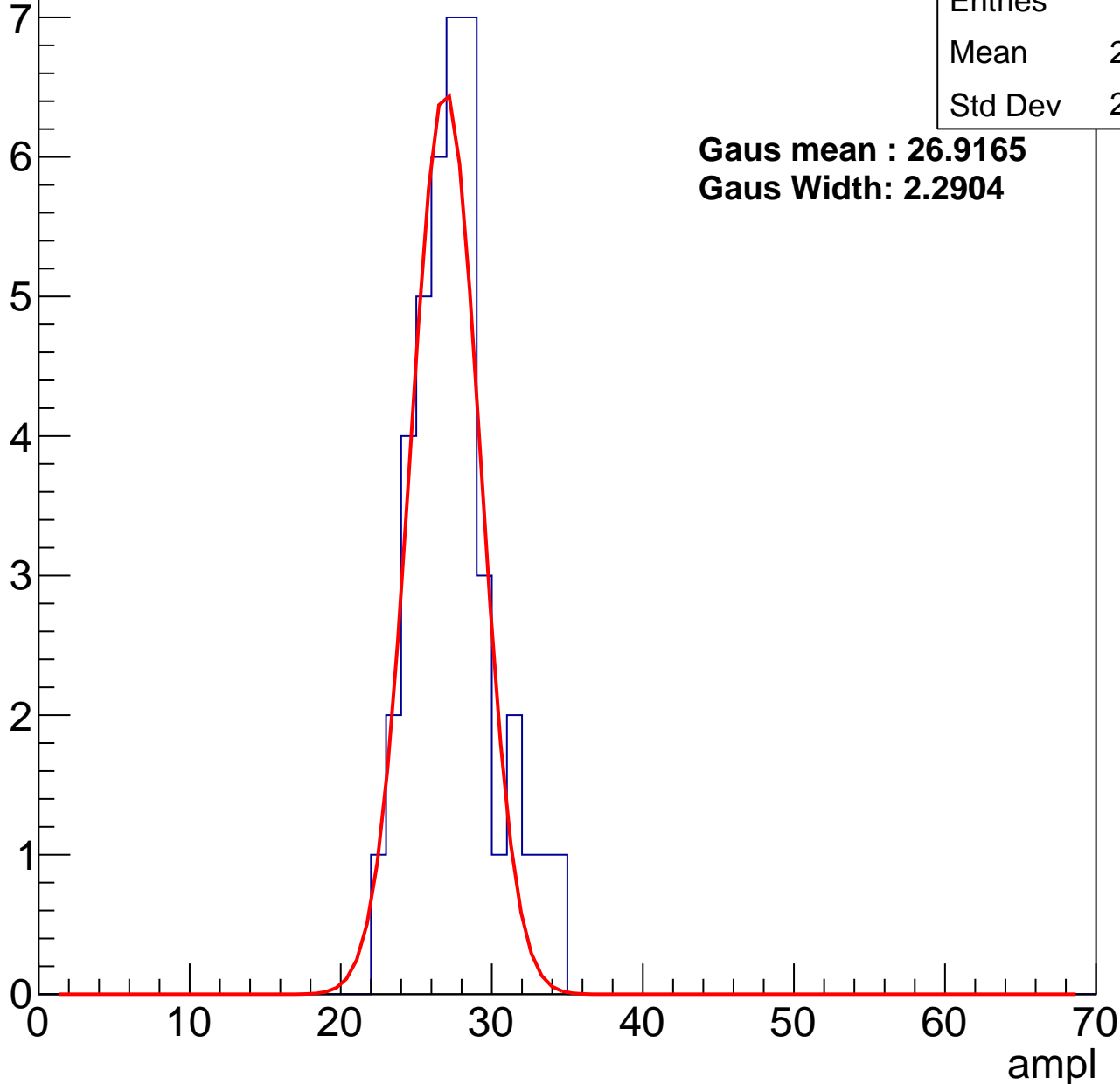
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	41
Mean	27.02
Std Dev	2.655

**Gaus mean : 26.9165**

**Gaus Width: 2.2904**



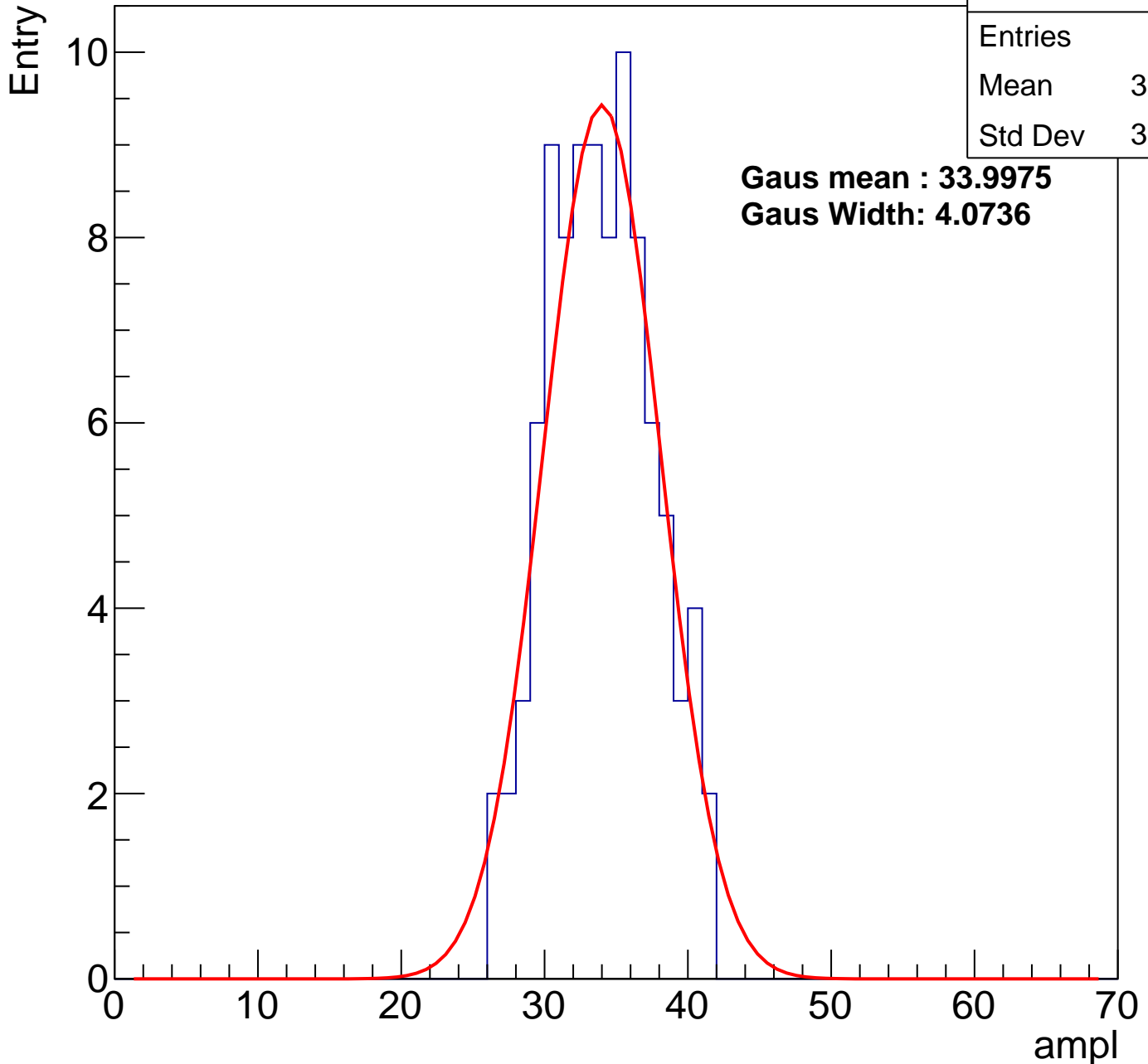
# B1L002S, U3-ch117, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	94
Mean	33.49
Std Dev	3.593

**Gaus mean : 33.9975**

**Gaus Width: 4.0736**



# B1L002S, U3-ch117, adc2

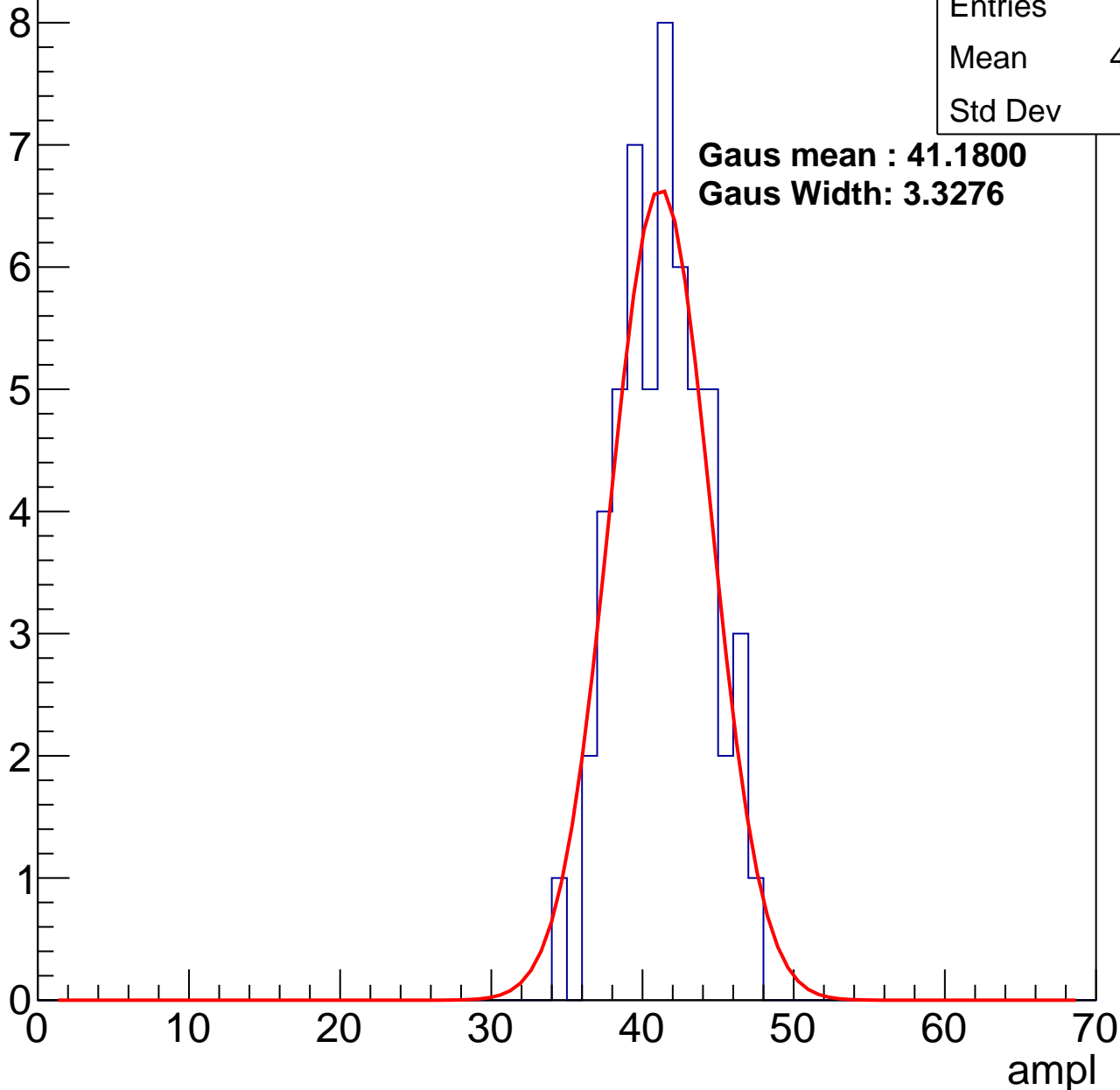
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	40.87
Std Dev	2.9

**Gaus mean : 41.1800**

**Gaus Width: 3.3276**

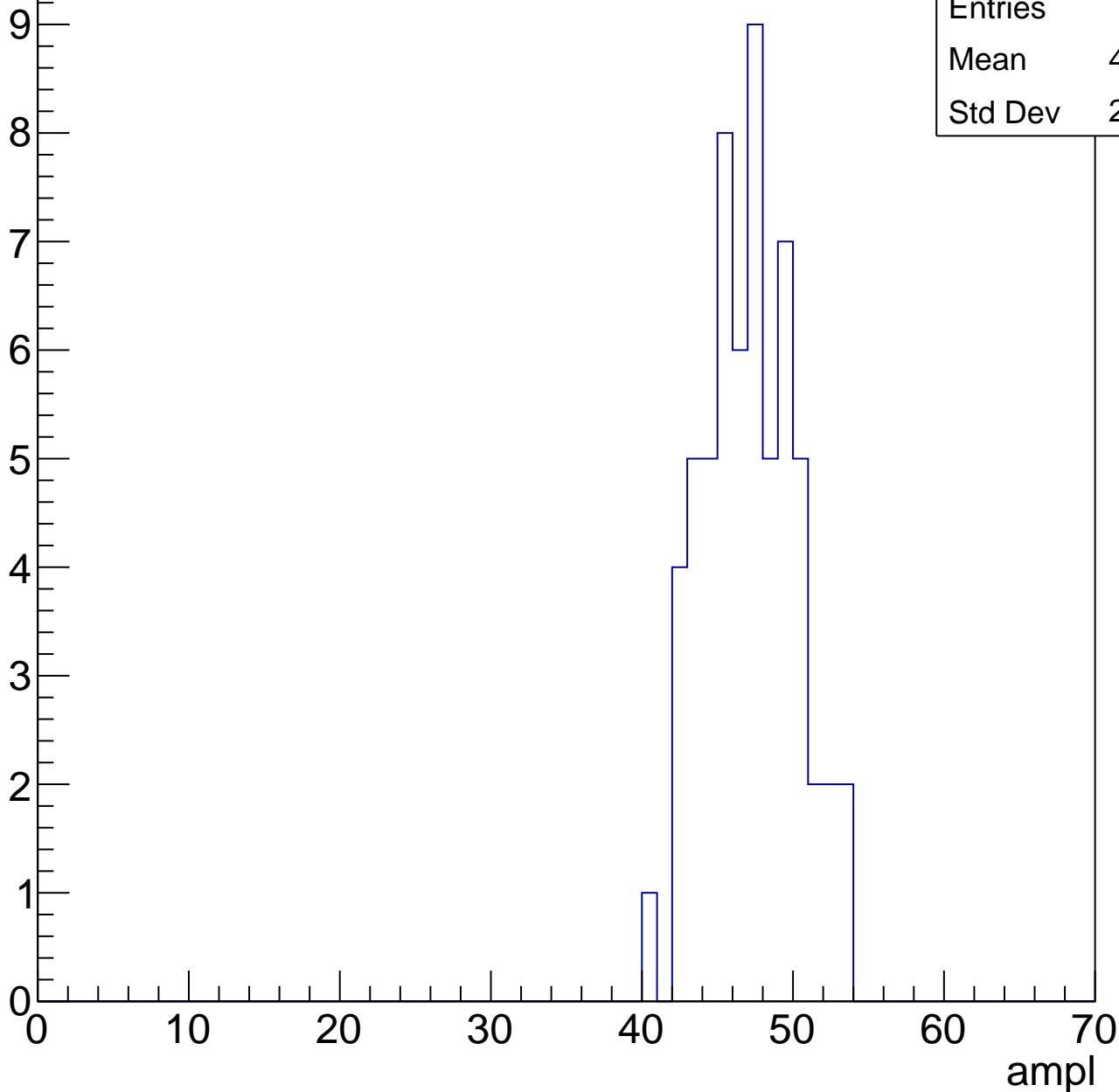


# B1L002S, U3-ch117, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	46.67
Std Dev	2.968

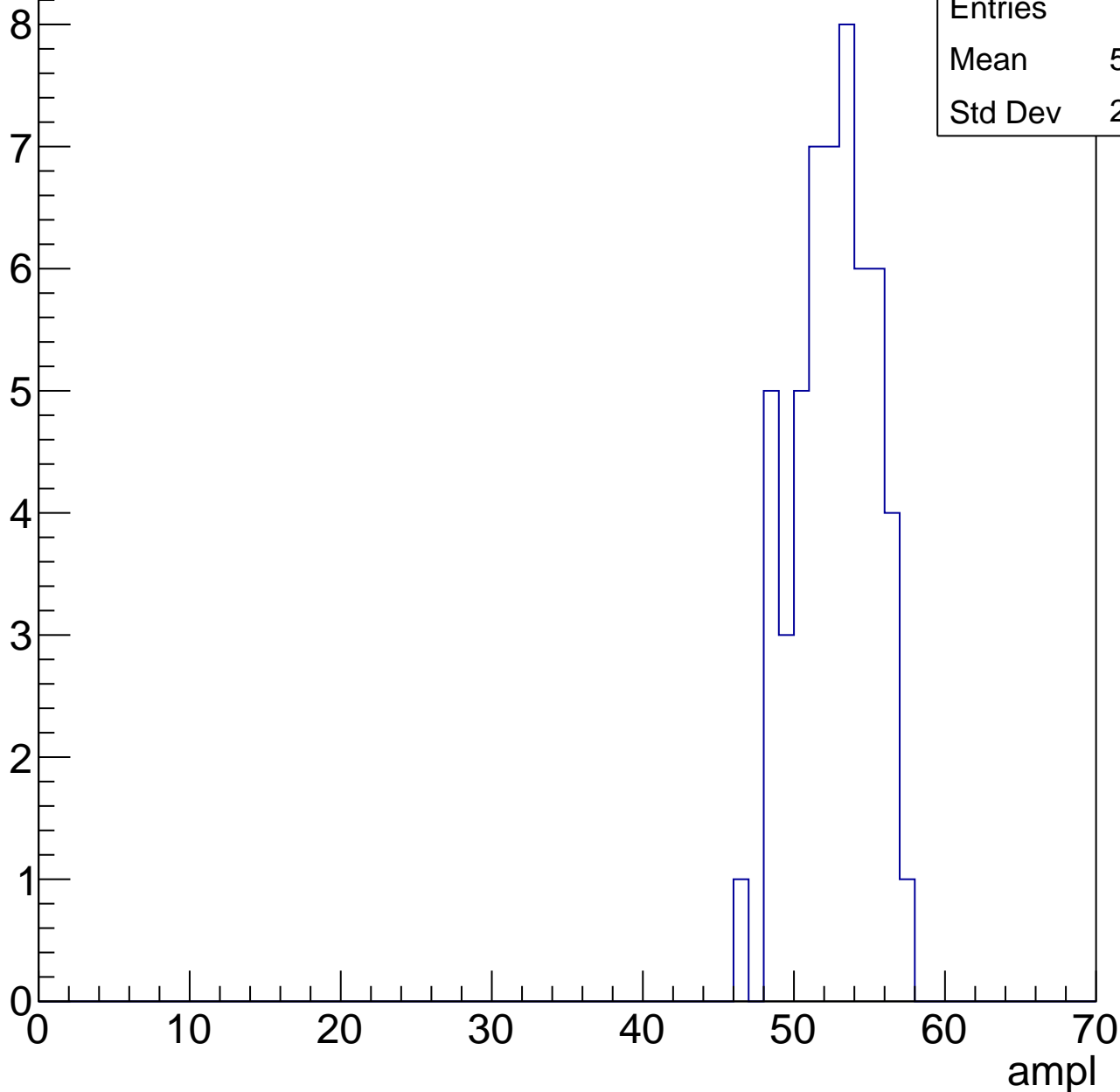


# B1L002S, U3-ch117, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.13
Std Dev	2.548

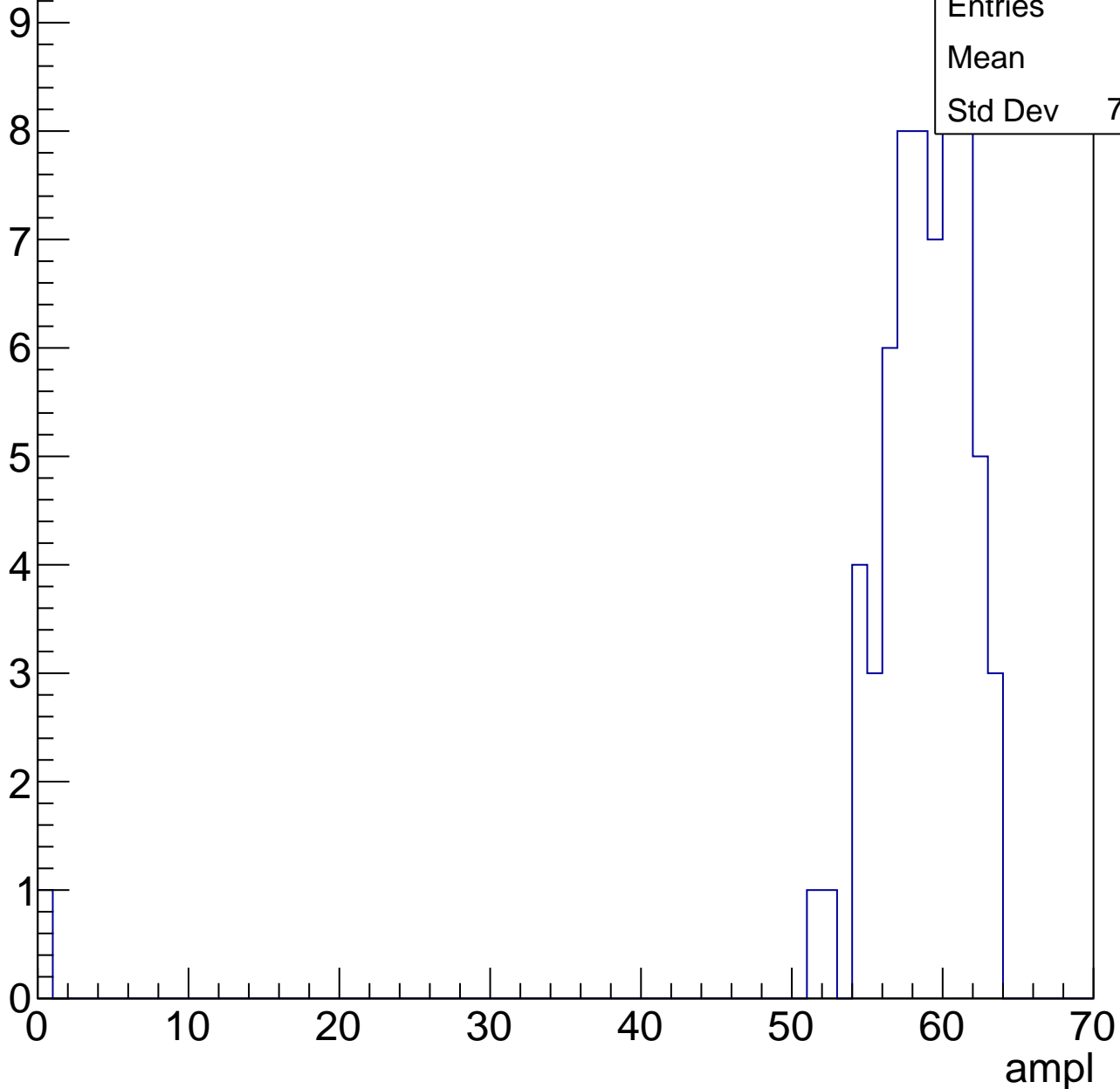


# B1L002S, U3-ch117, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	57.5
Std Dev	7.732

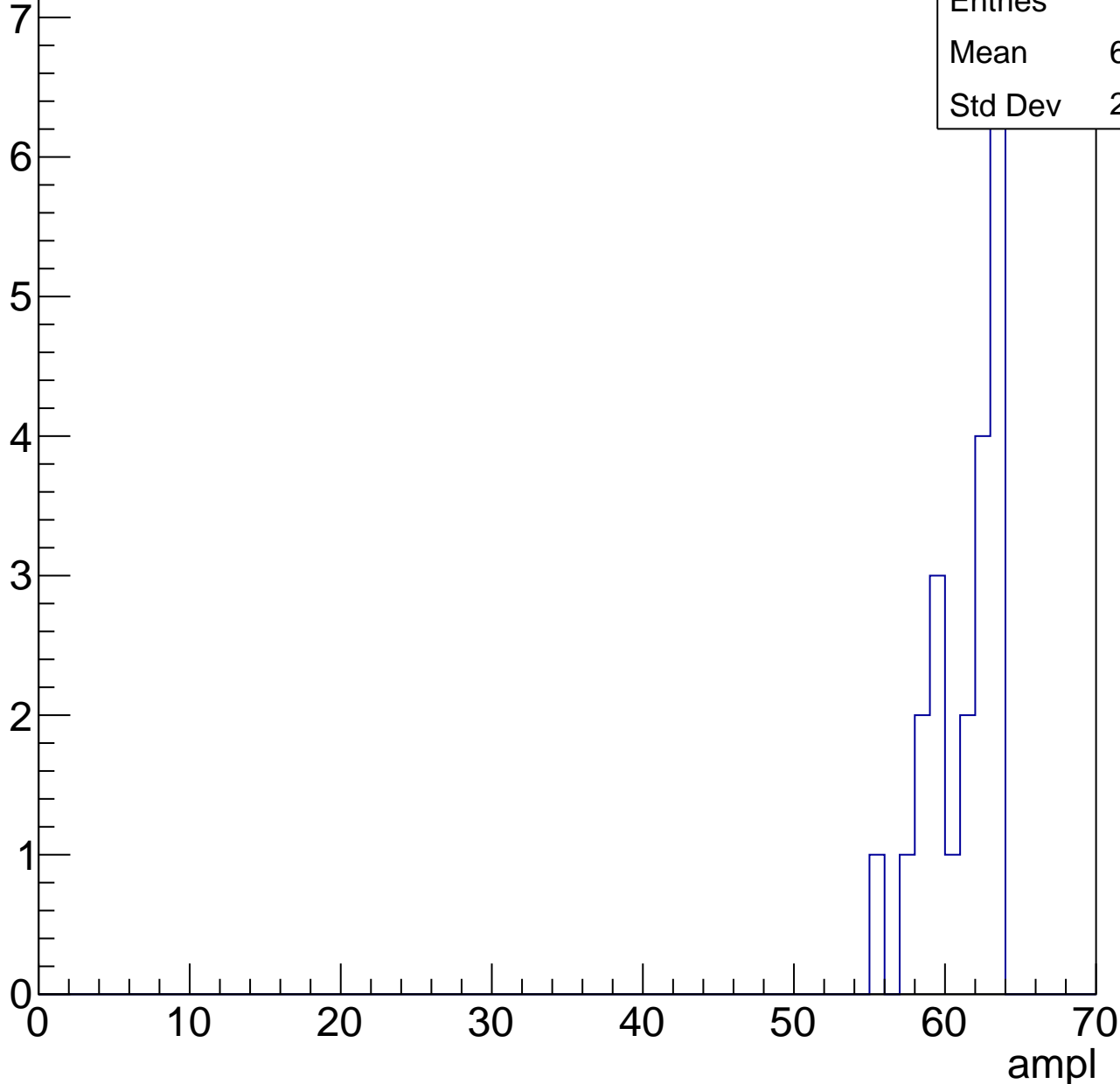


# B1L002S, U3-ch117, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	60.76
Std Dev	2.328

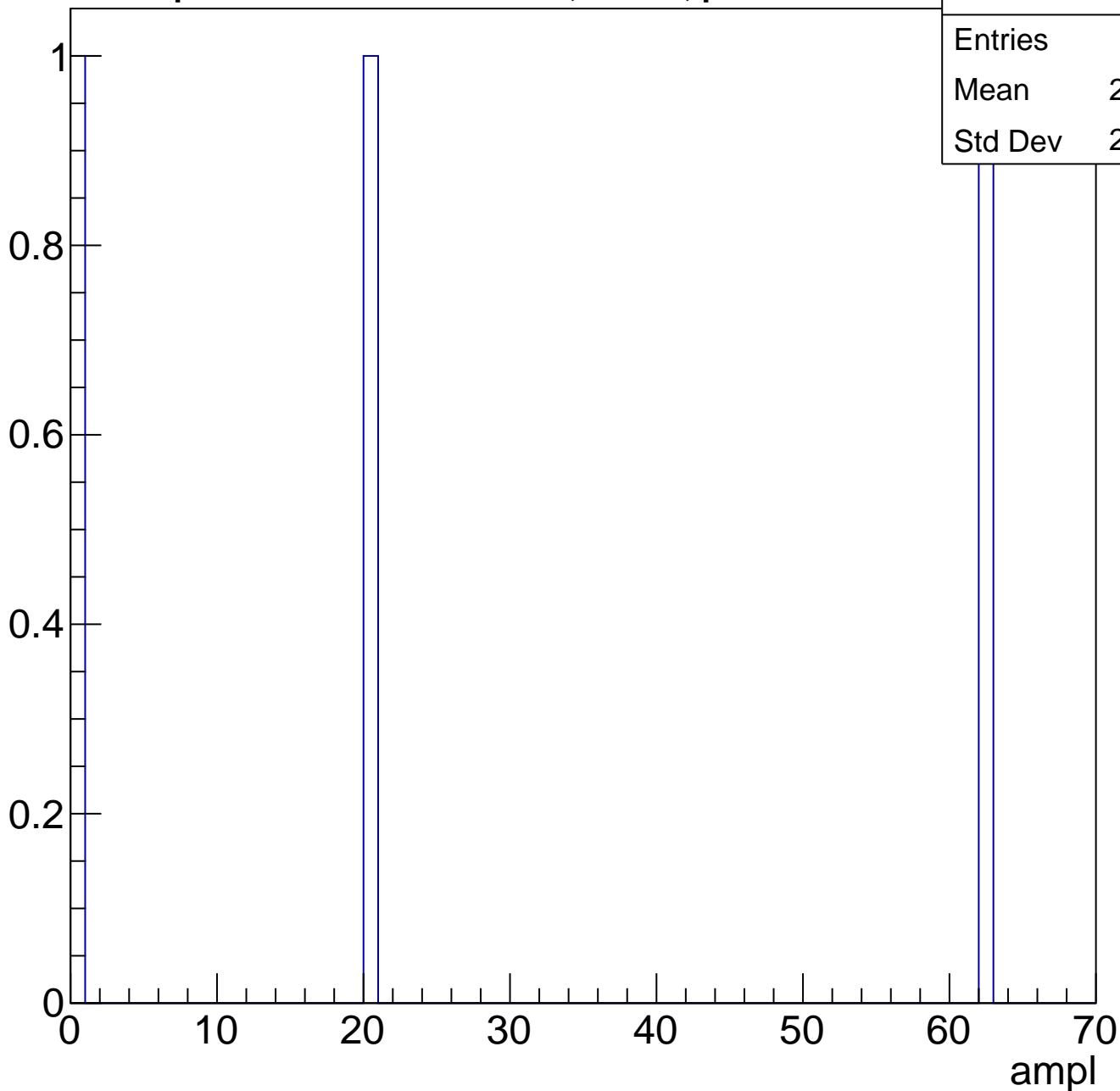




# B1L002S, U3-ch117, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	3
Mean	27.33
Std Dev	25.84

# B1L002S, U3-ch118, adc0

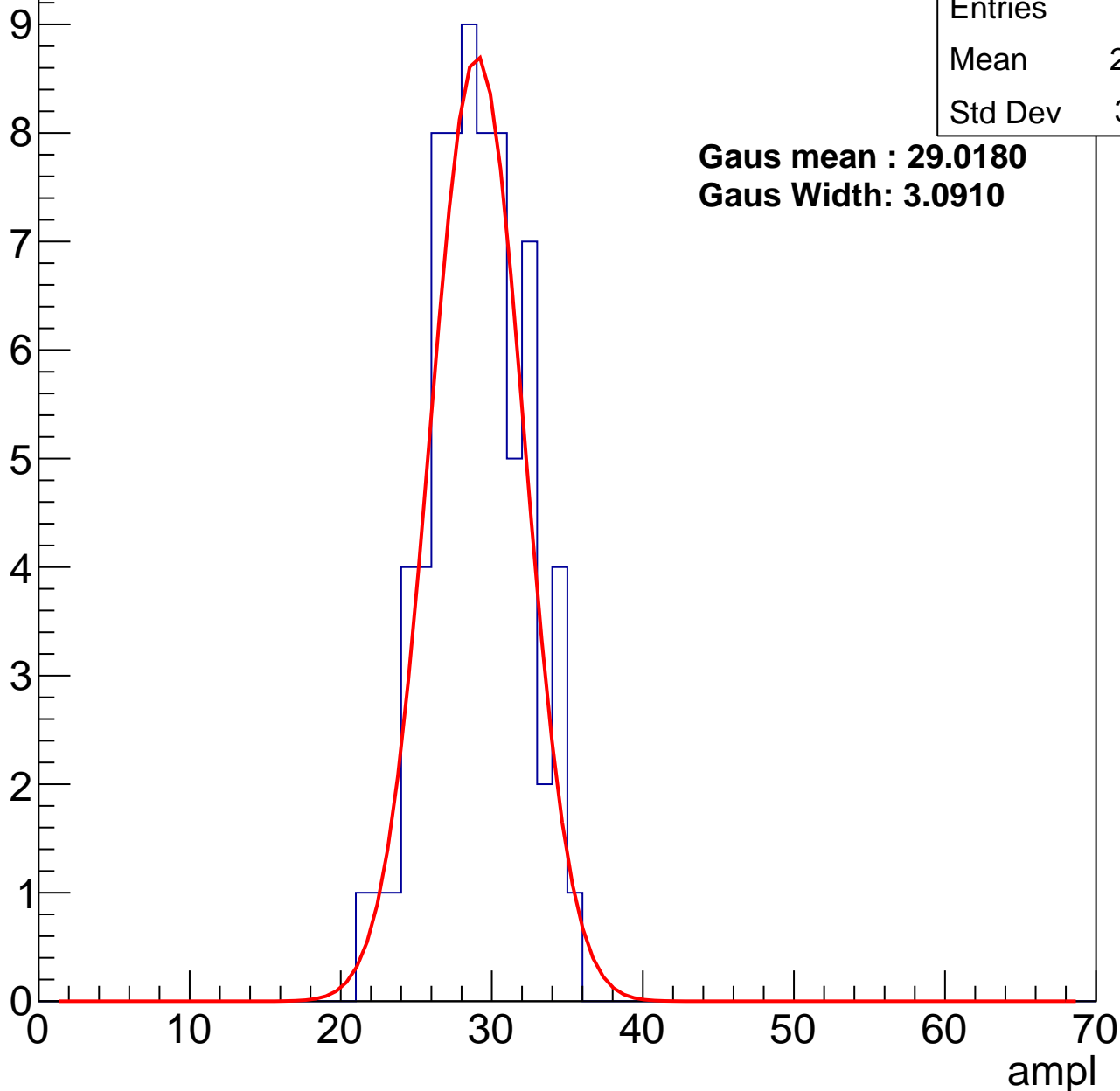
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	28.54
Std Dev	3.071

**Gaus mean : 29.0180**

**Gaus Width: 3.0910**



# B1L002S, U3-ch118, adc1

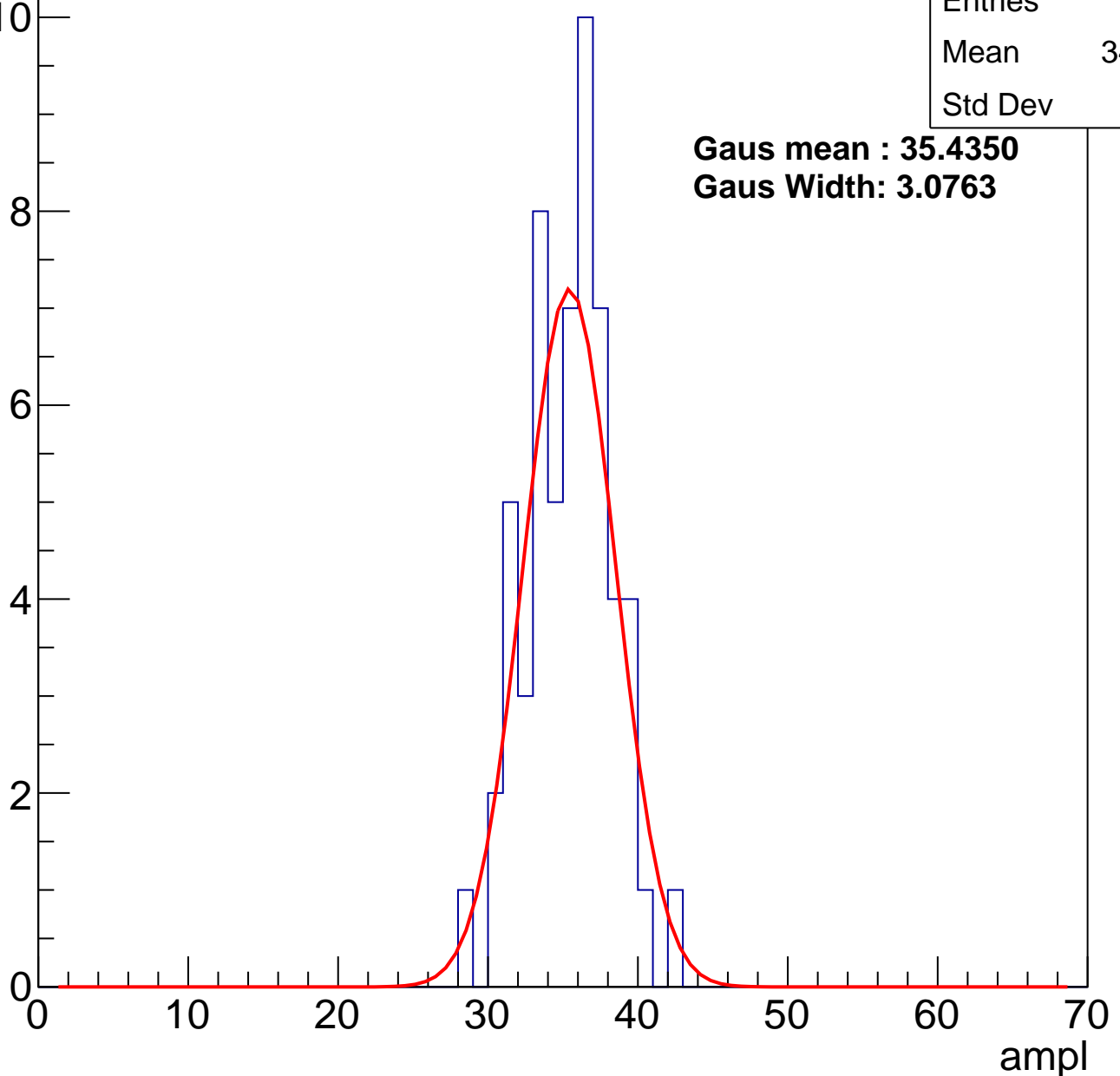
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	34.95
Std Dev	2.8

**Gaus mean : 35.4350**

**Gaus Width: 3.0763**



# B1L002S, U3-ch118, adc2

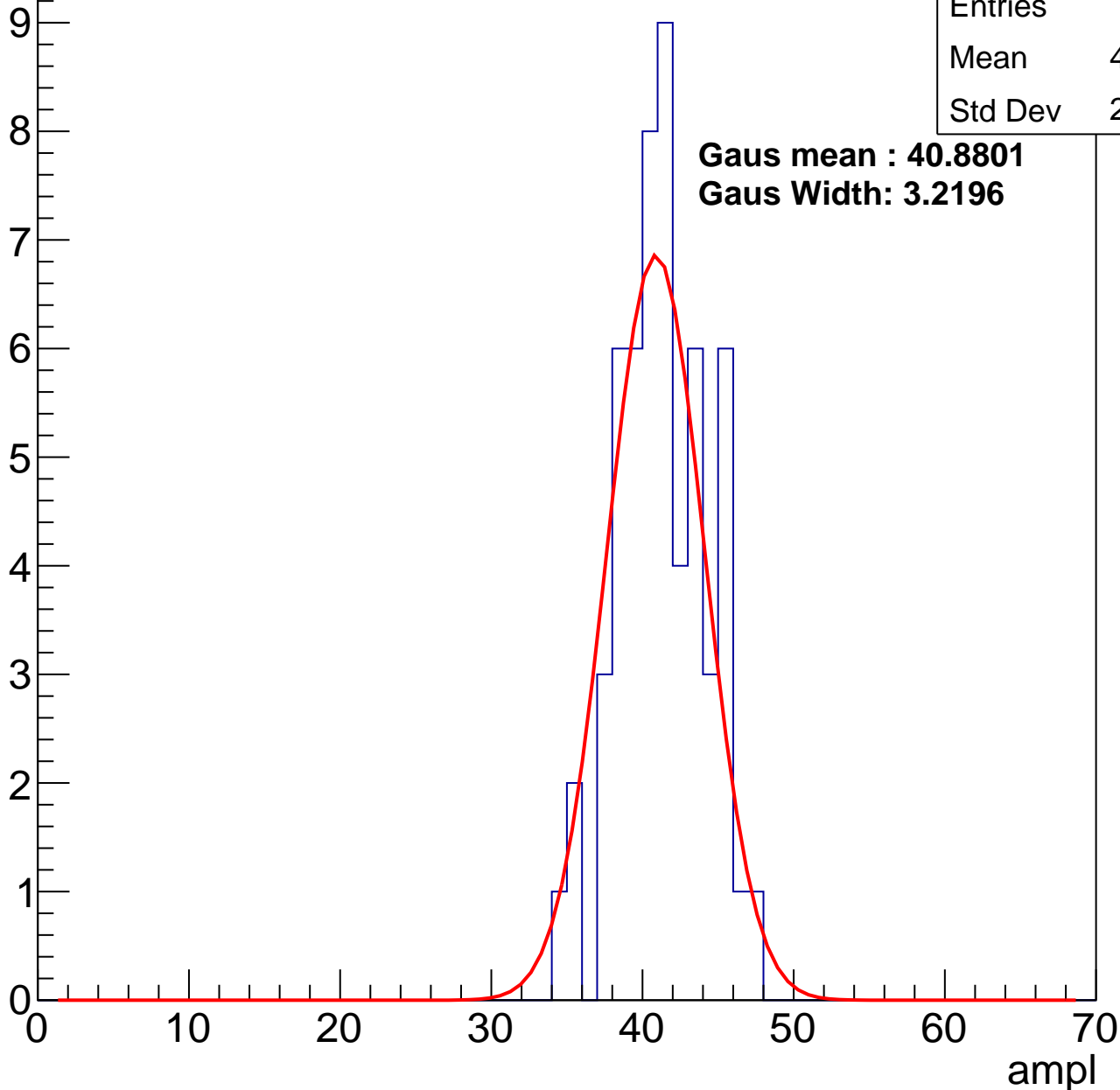
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	40.84
Std Dev	2.883

**Gaus mean : 40.8801**

**Gaus Width: 3.2196**

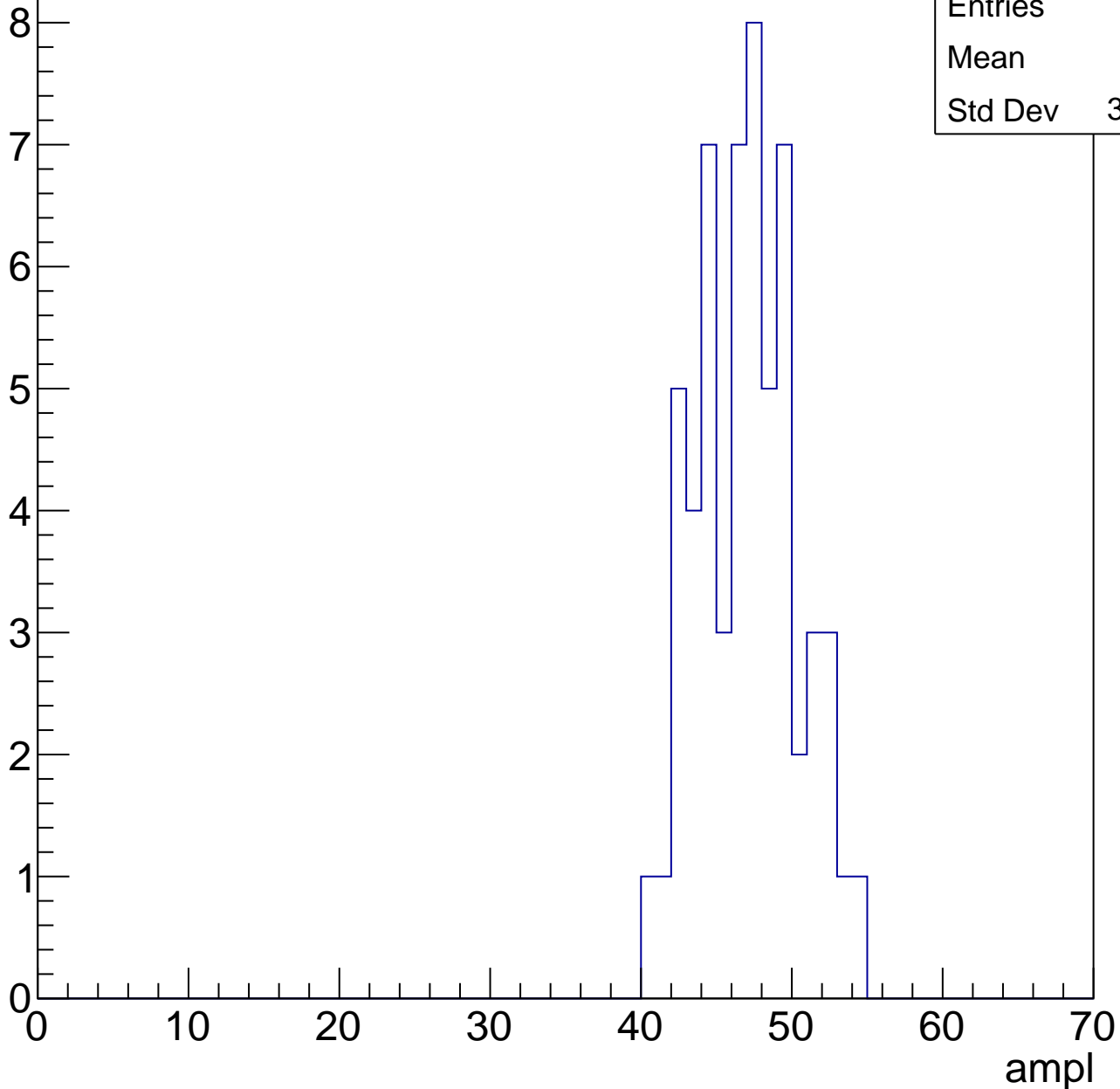


# B1L002S, U3-ch118, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	46.6
Std Dev	3.232

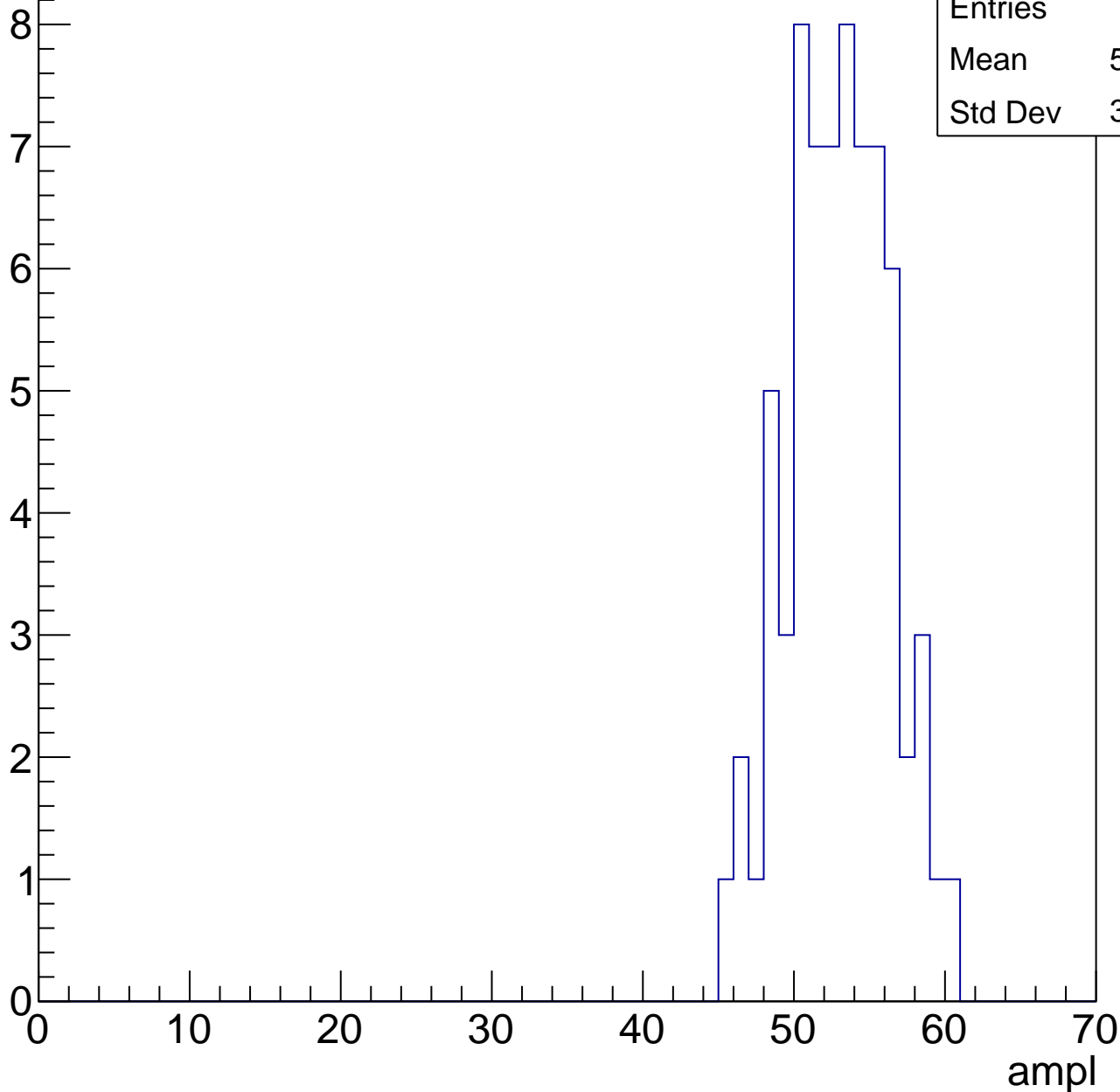


# B1L002S, U3-ch118, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	52.49
Std Dev	3.278

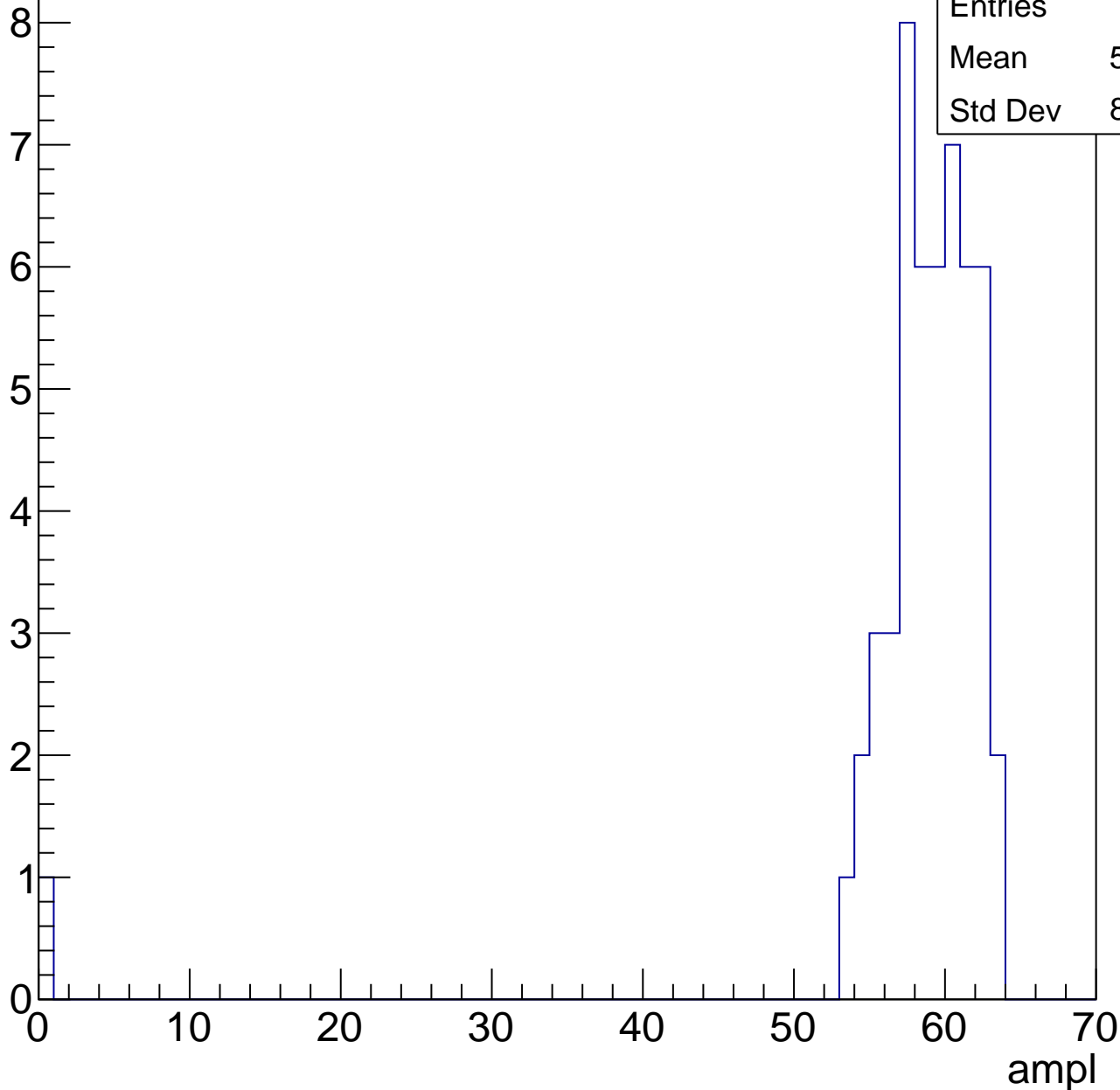


# B1L002S, U3-ch118, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	57.57
Std Dev	8.509

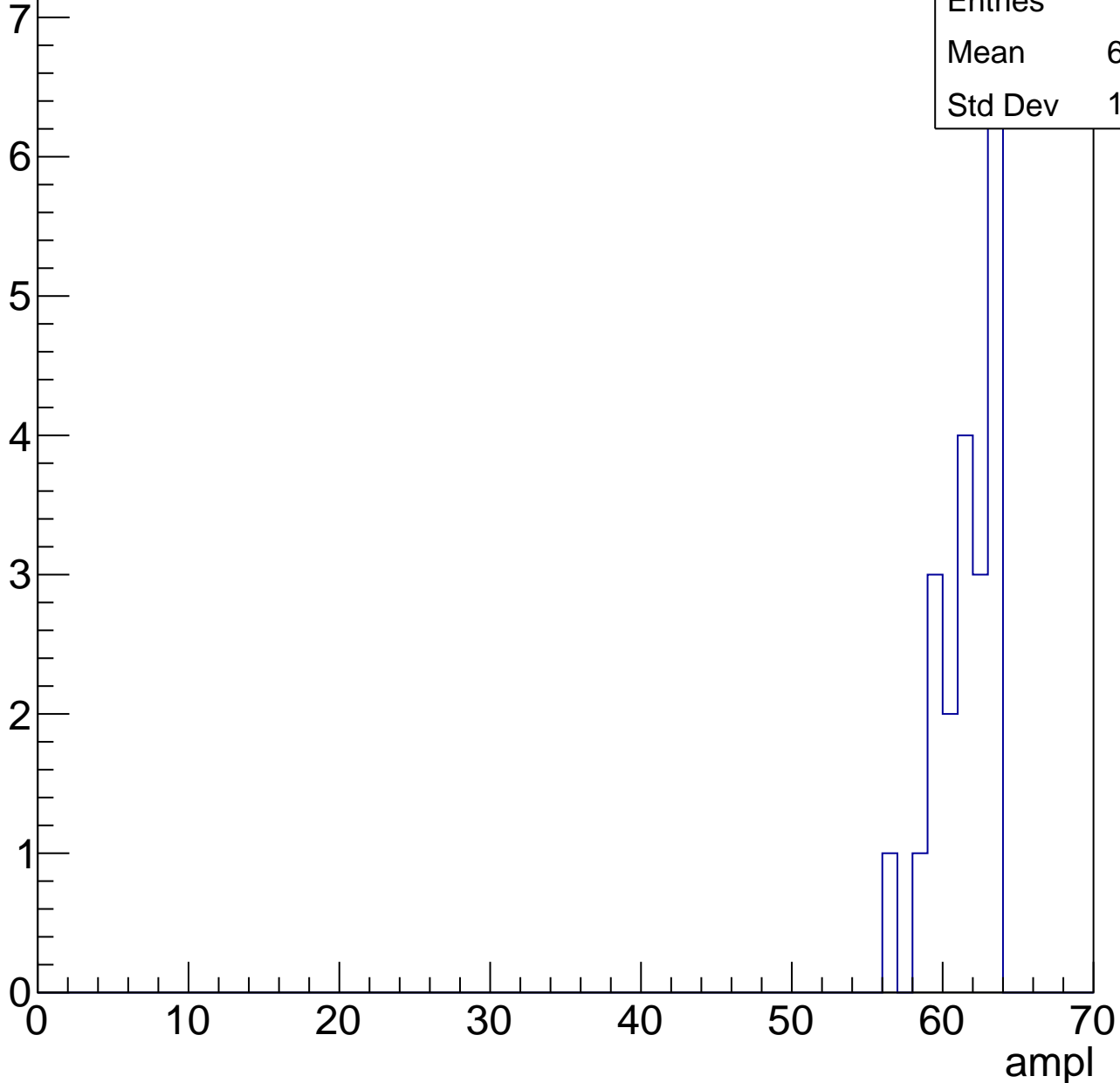


# B1L002S, U3-ch118, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	21
Mean	61.05
Std Dev	1.939





# B1L002S, U3-ch118, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch119, adc0

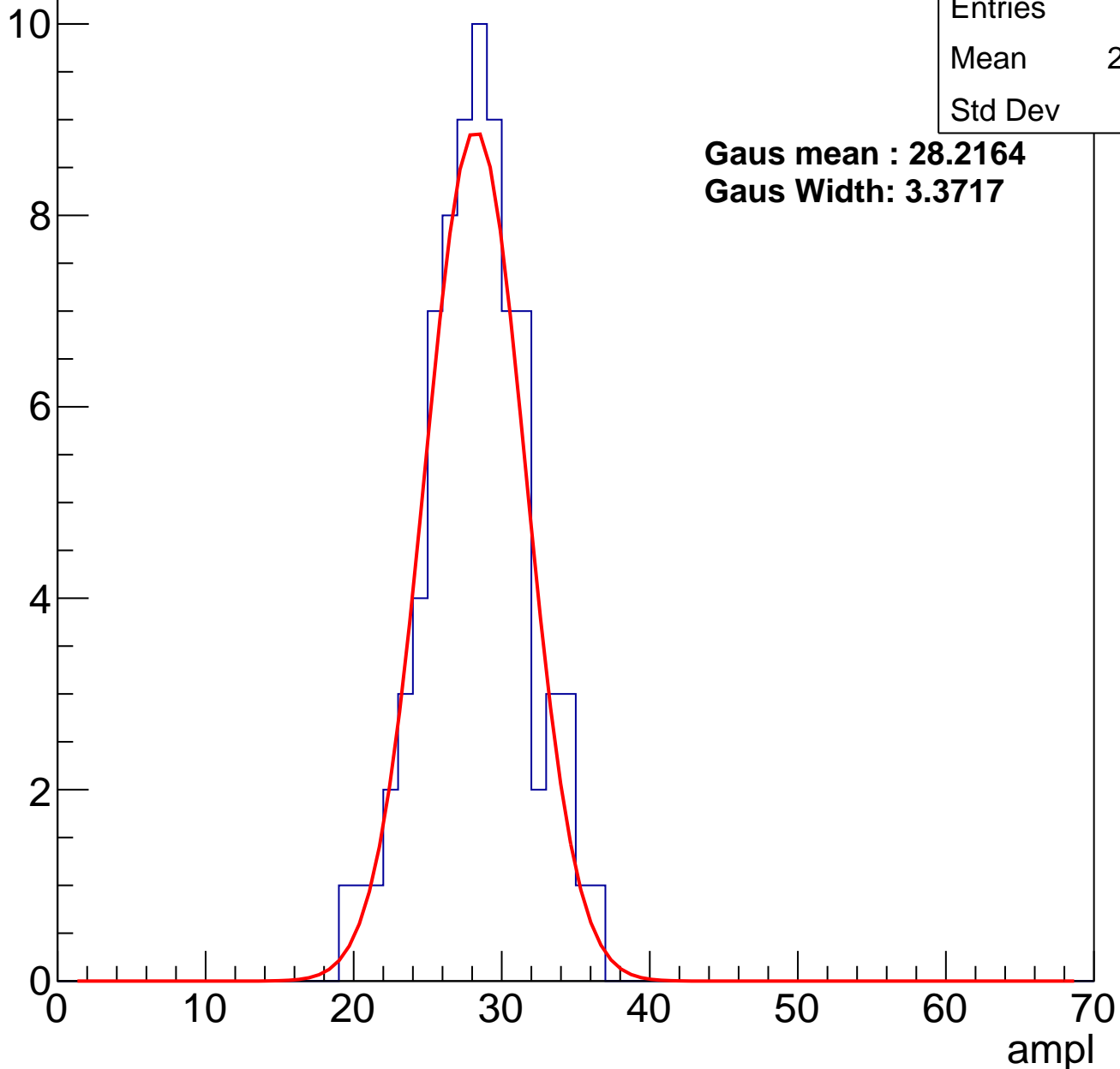
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	79
Mean	27.84
Std Dev	3.44

**Gaus mean : 28.2164**

**Gaus Width: 3.3717**

Entry



# B1L002S, U3-ch119, adc1

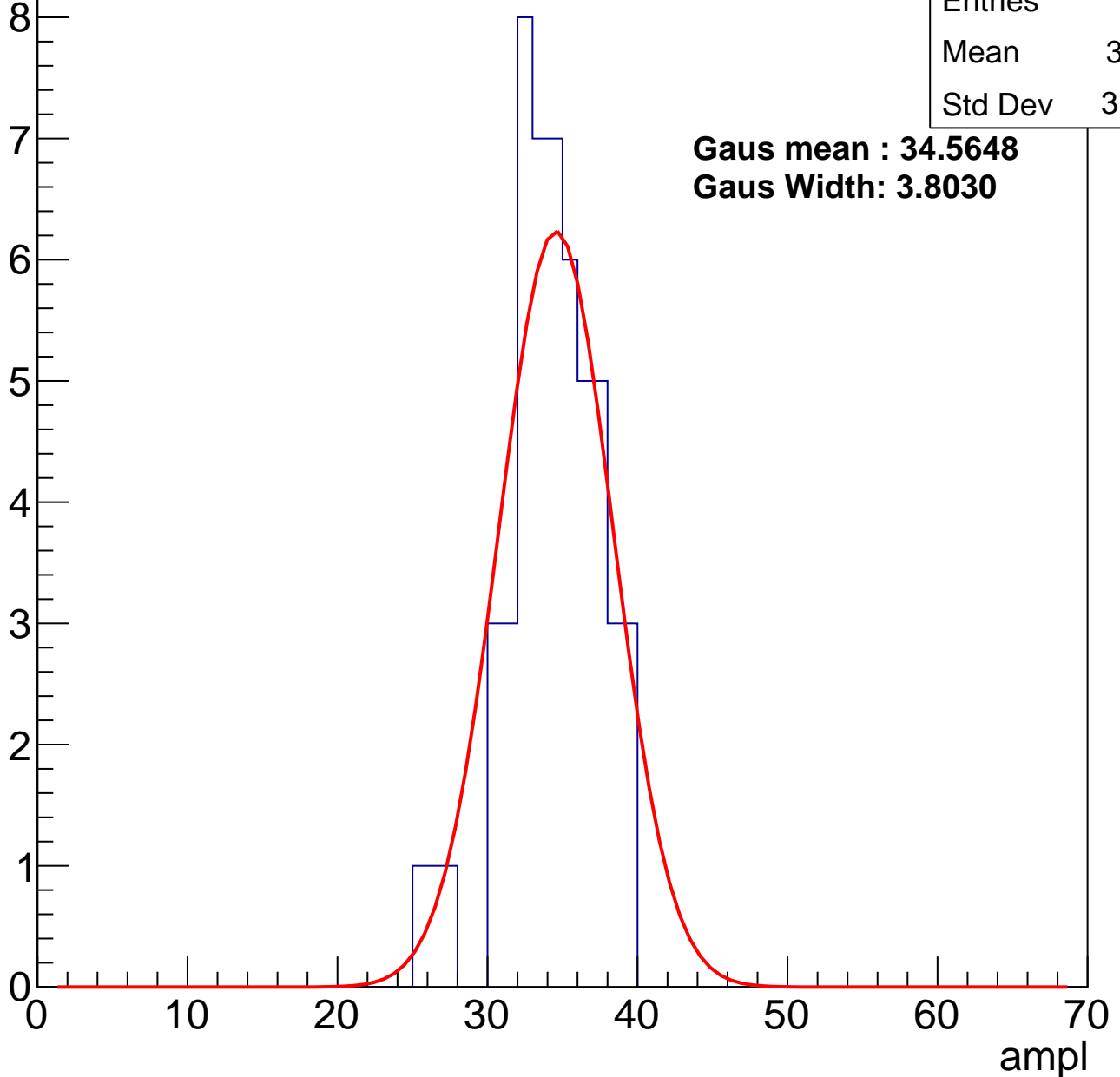
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	33.81
Std Dev	3.072

**Gaus mean : 34.5648**

**Gaus Width: 3.8030**



# B1L002S, U3-ch119, adc2

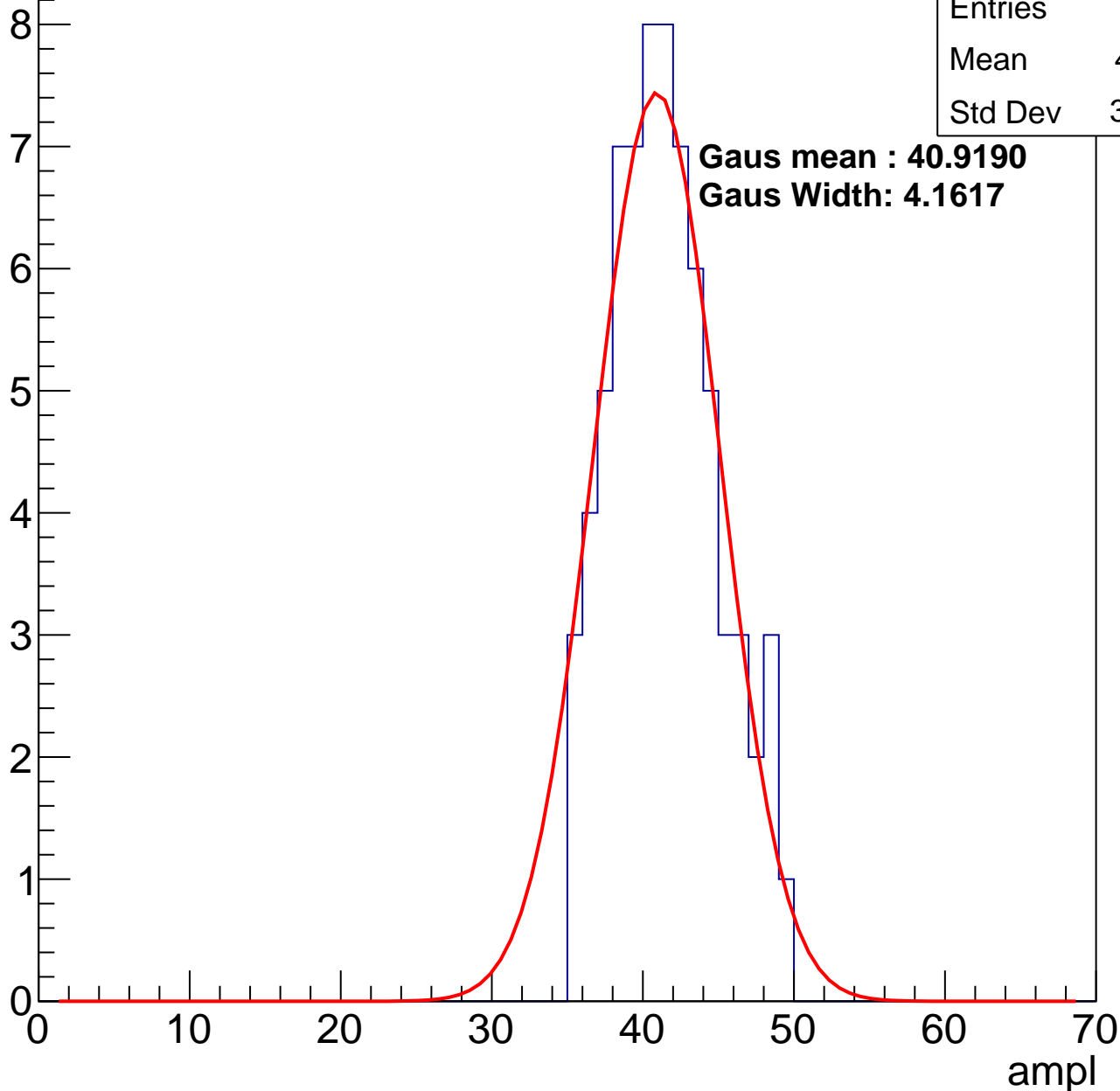
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	41.01
Std Dev	3.474

**Gaus mean : 40.9190**

**Gaus Width: 4.1617**

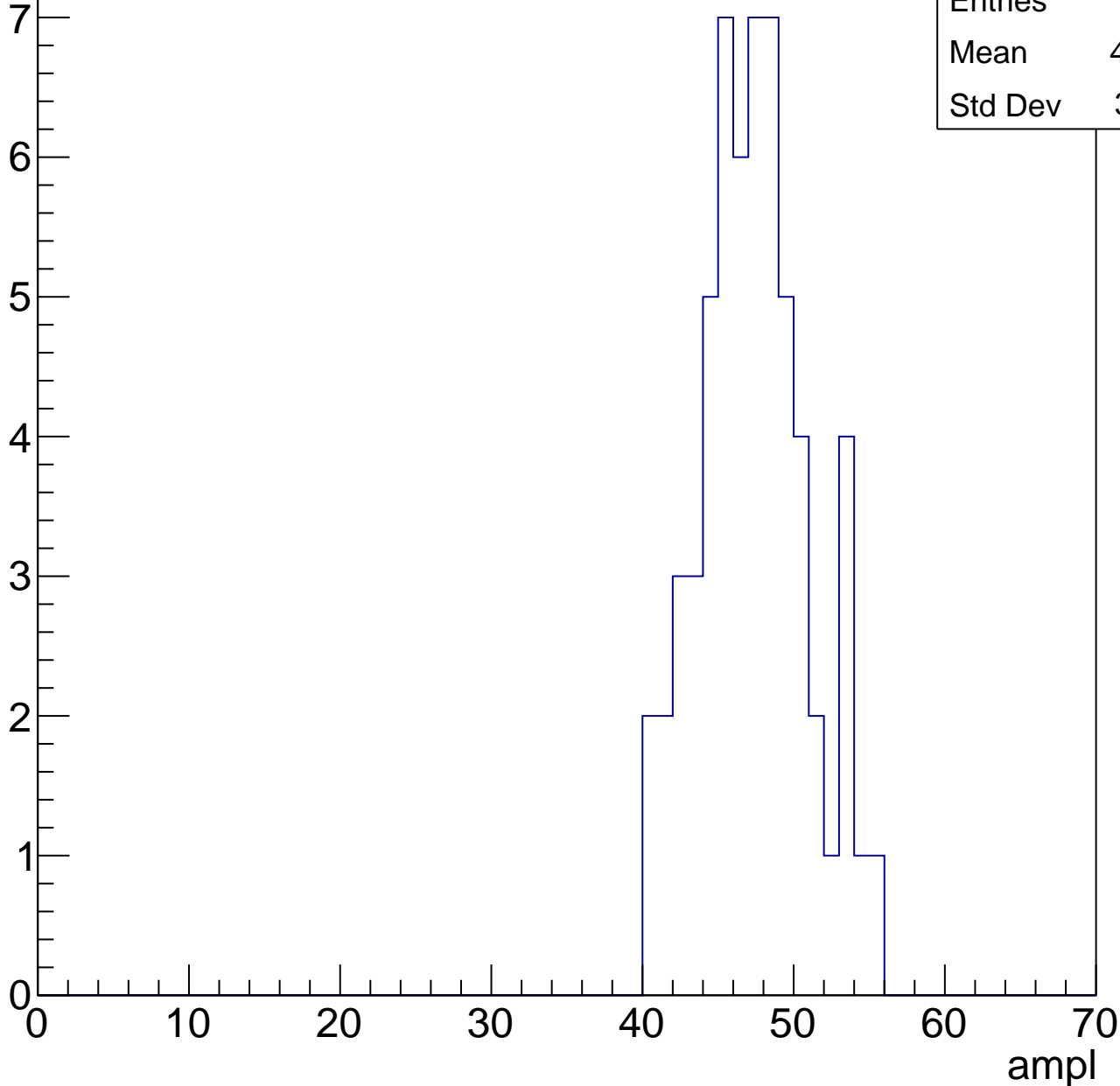


# B1L002S, U3-ch119, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	46.88
Std Dev	3.531

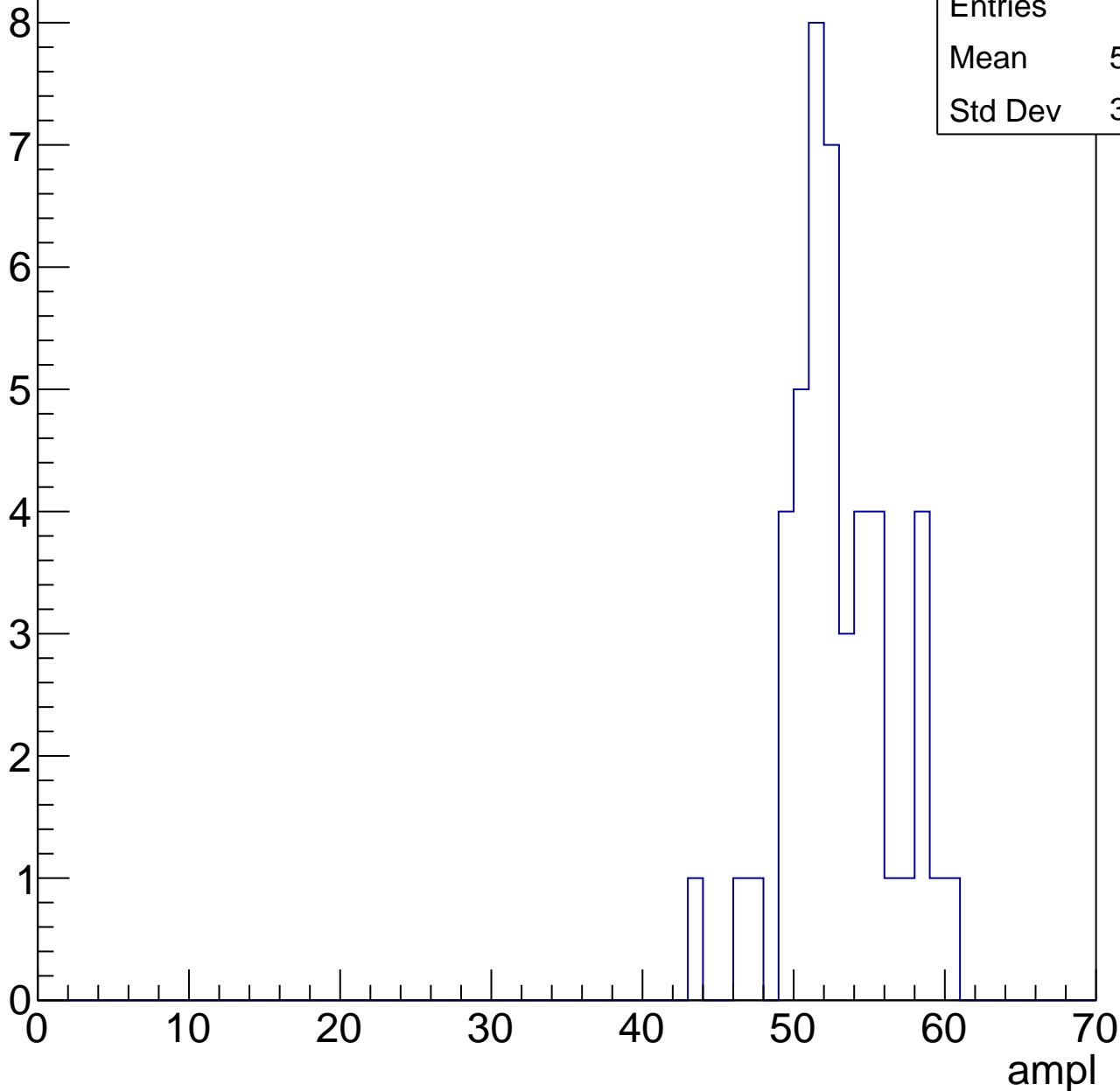


# B1L002S, U3-ch119, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	46
Mean	52.46
Std Dev	3.456

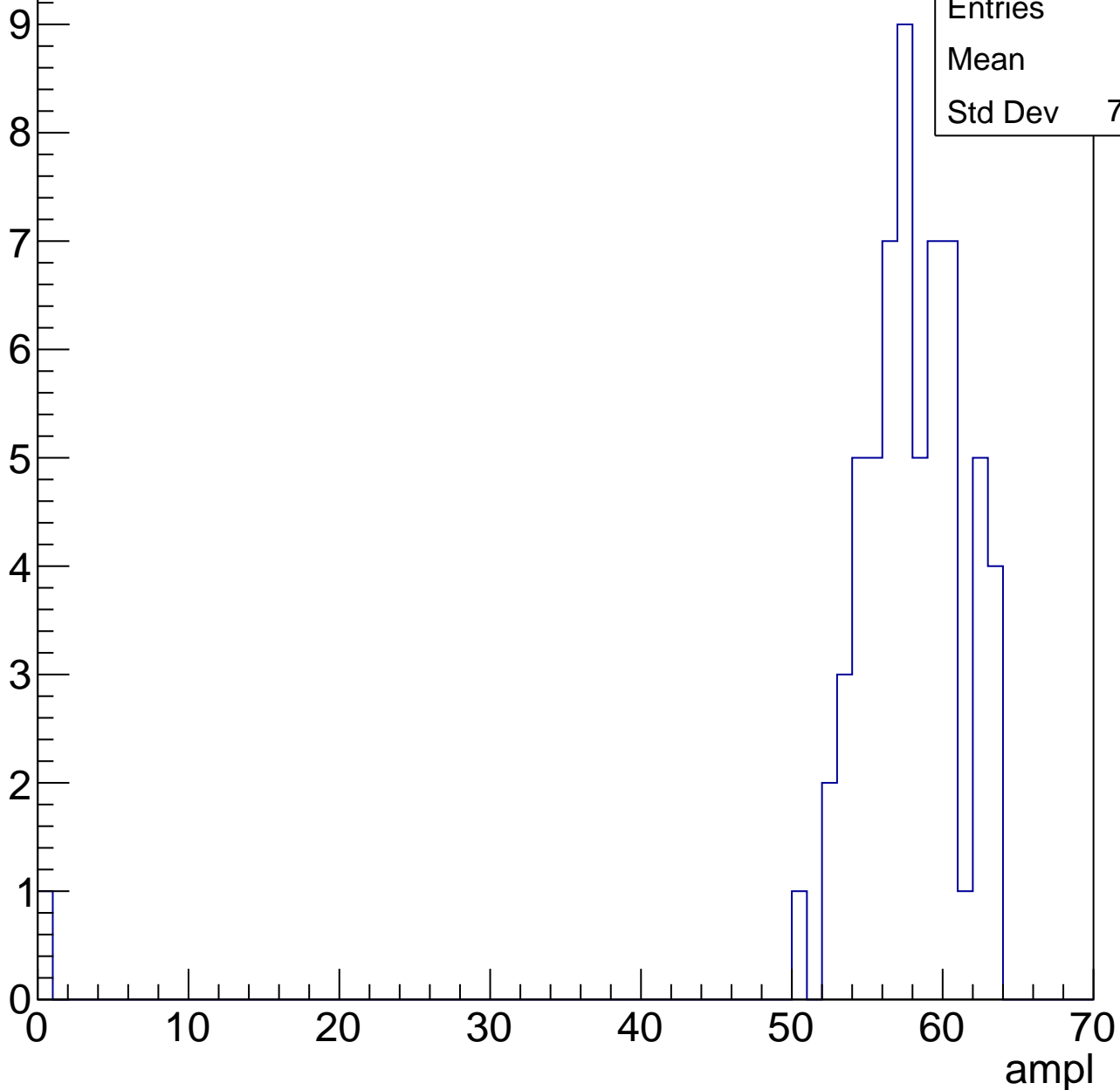


# B1L002S, U3-ch119, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	56.6
Std Dev	7.869

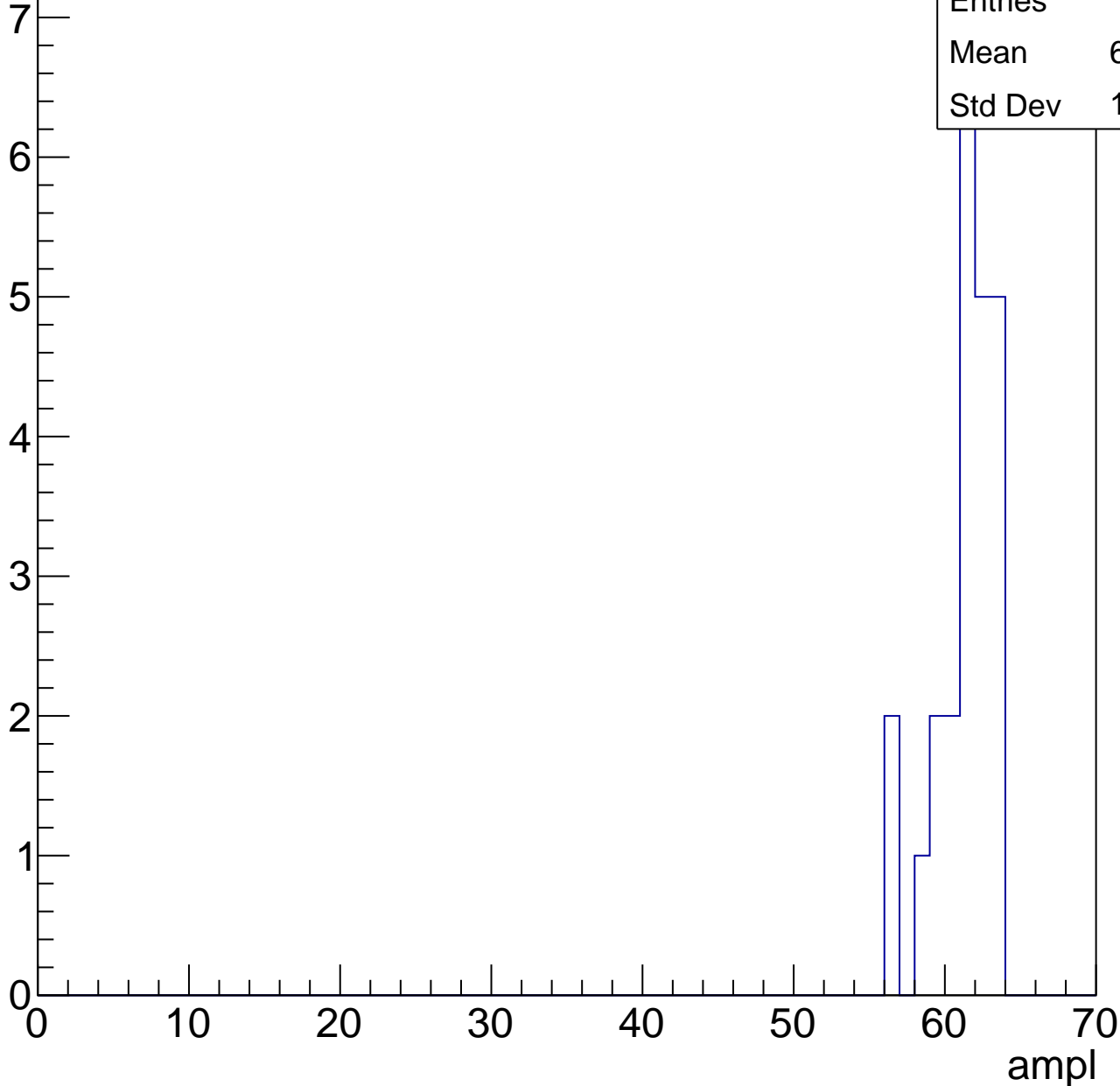


# B1L002S, U3-ch119, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	24
Mean	60.83
Std Dev	1.972

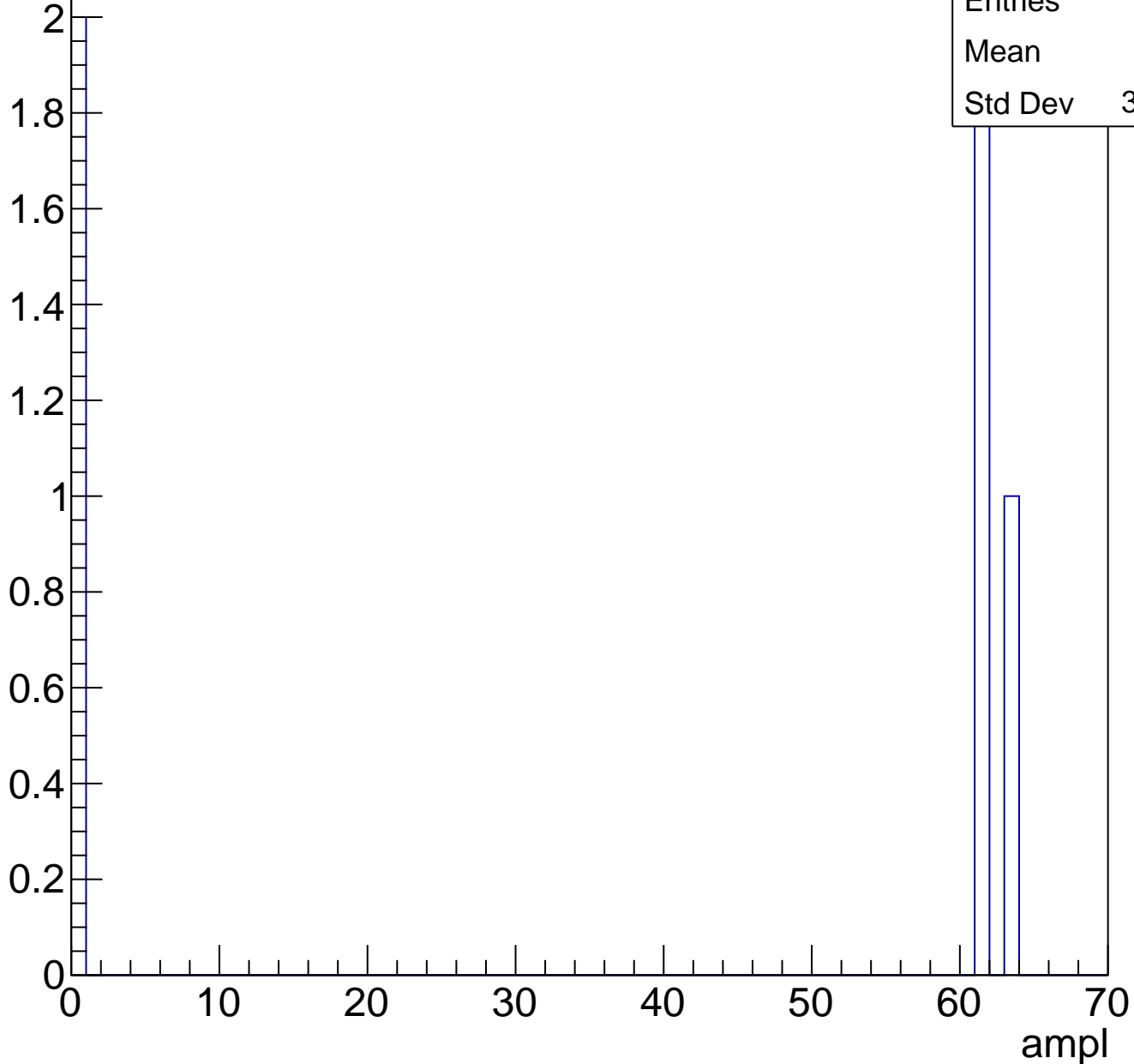




# B1L002S, U3-ch119, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch120, adc0

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	86
Mean	26.26
Std Dev	6.667

**Gaus mean : 27.9987**

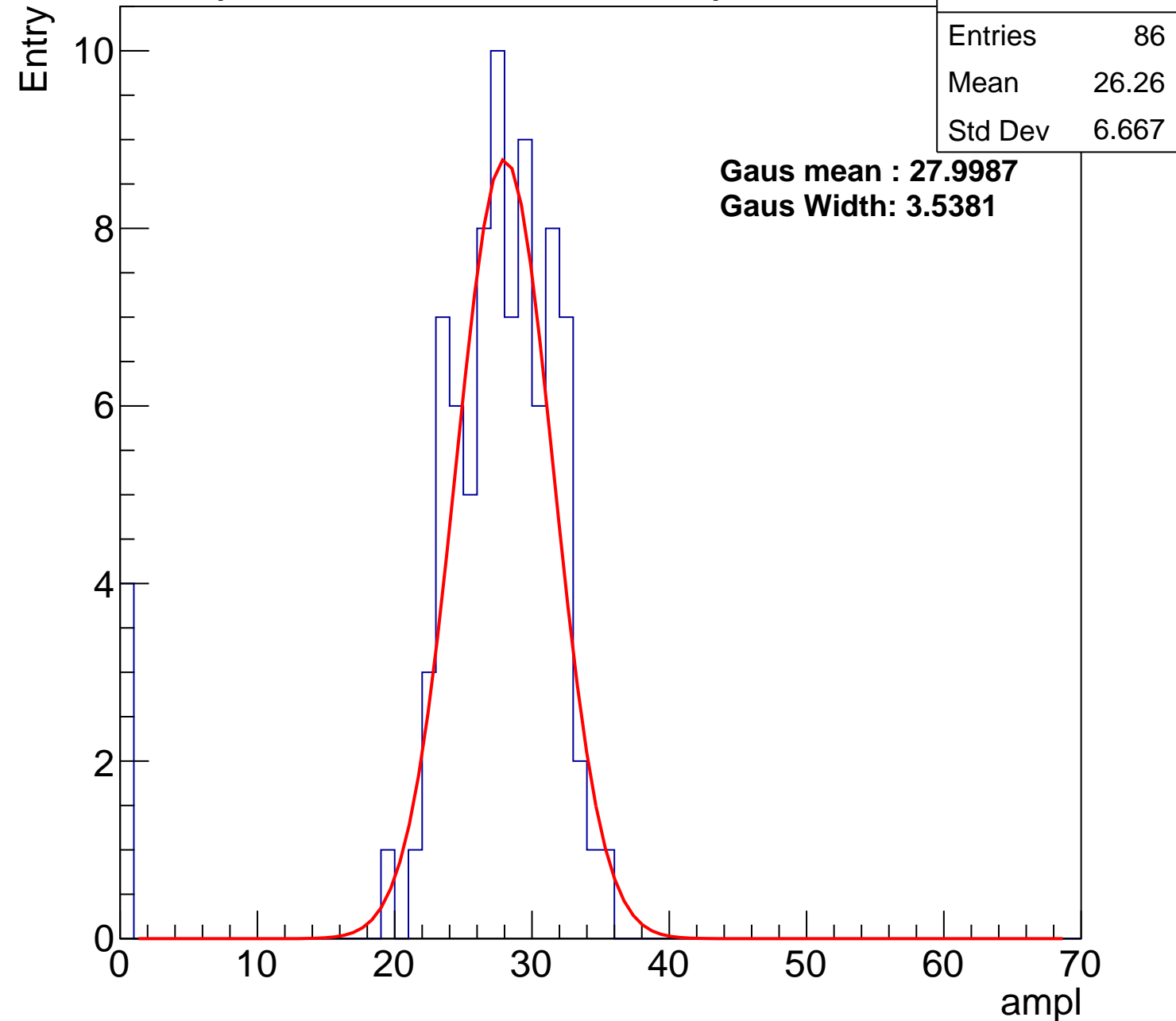
**Gaus Width: 3.5381**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch120, adc1

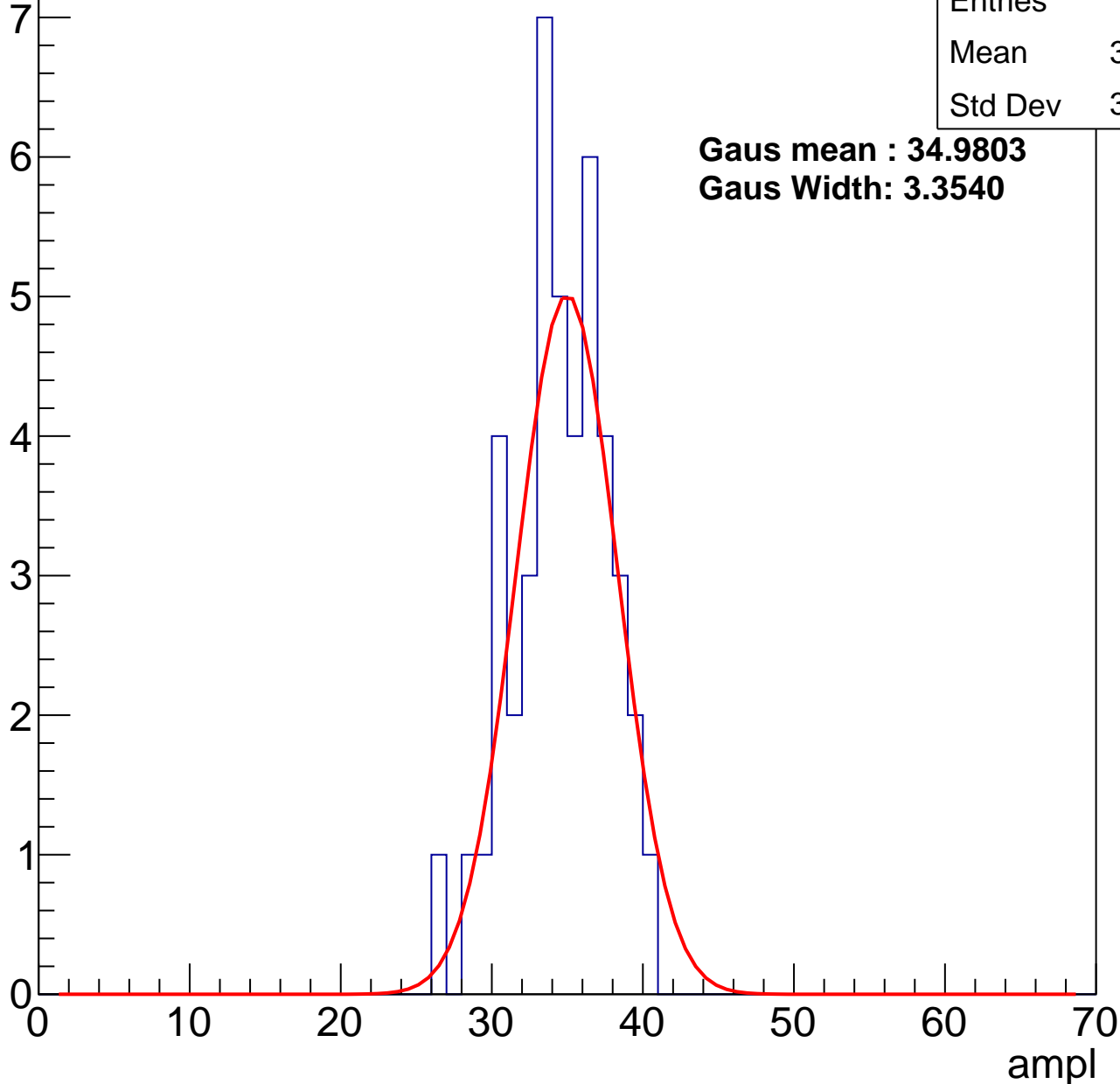
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	34.05
Std Dev	3.104

**Gaus mean : 34.9803**

**Gaus Width: 3.3540**



# B1L002S, U3-ch120, adc2

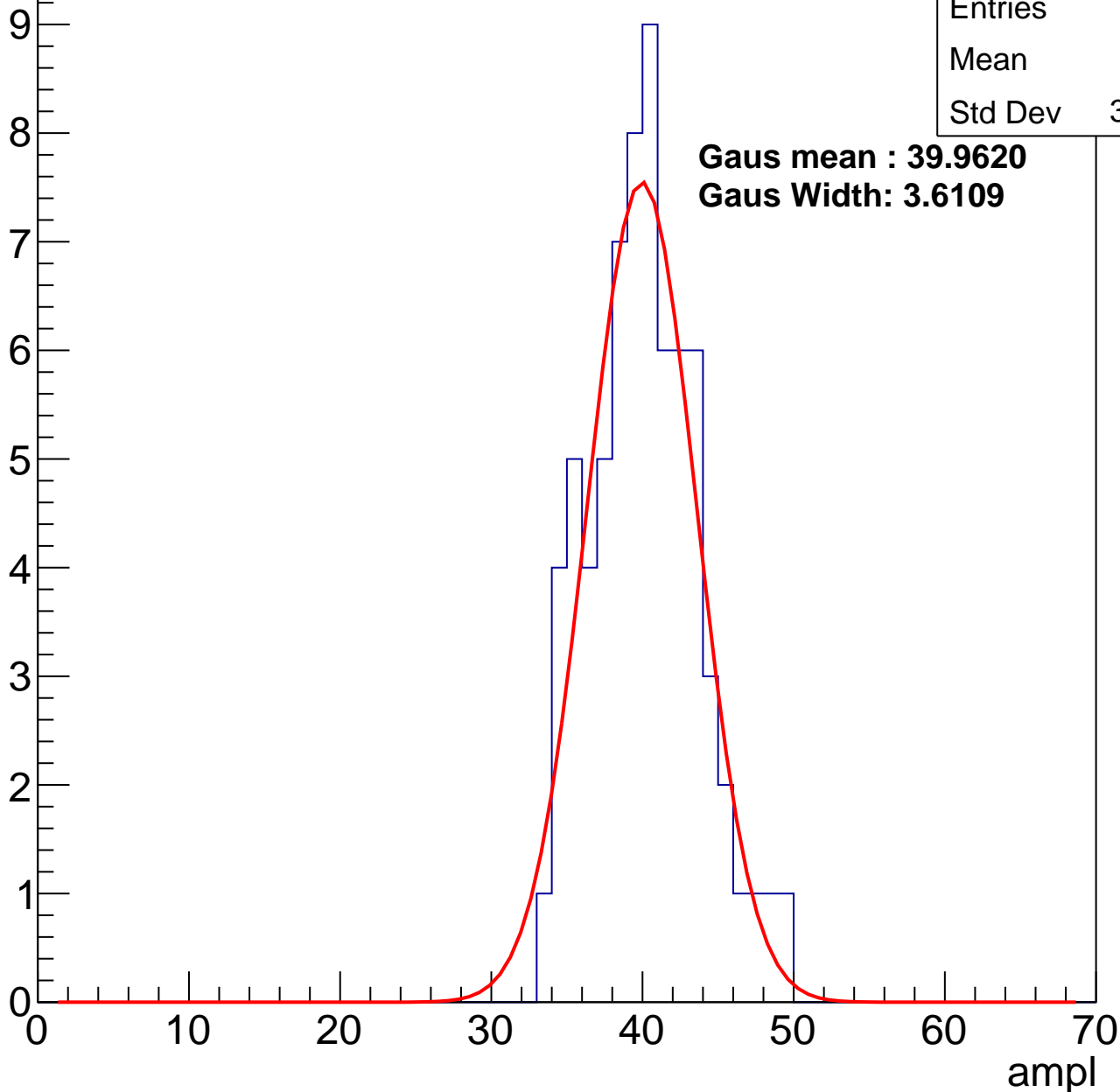
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	70
Mean	39.7
Std Dev	3.523

**Gaus mean : 39.9620**

**Gaus Width: 3.6109**

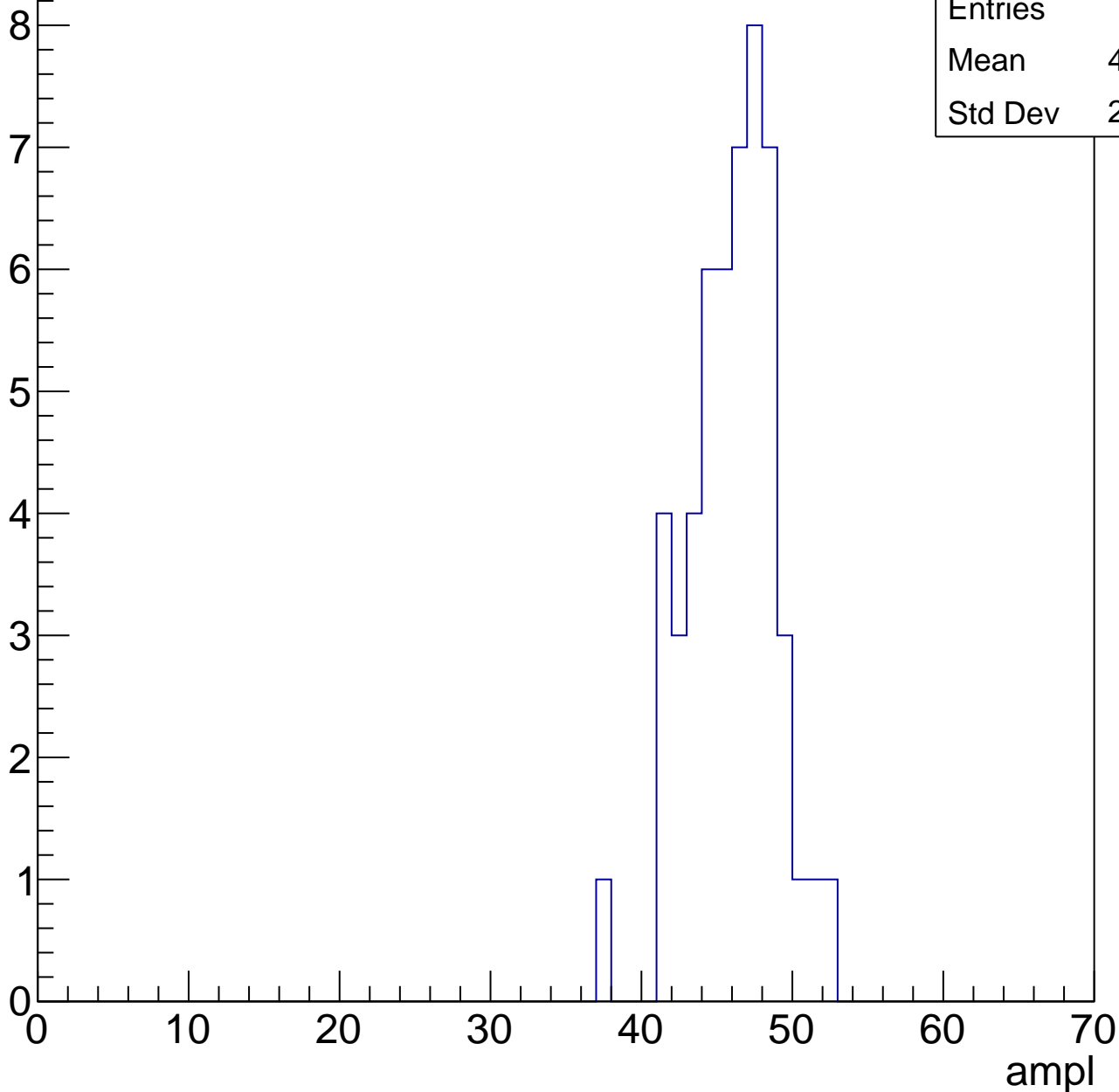


# B1L002S, U3-ch120, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	45.52
Std Dev	2.852

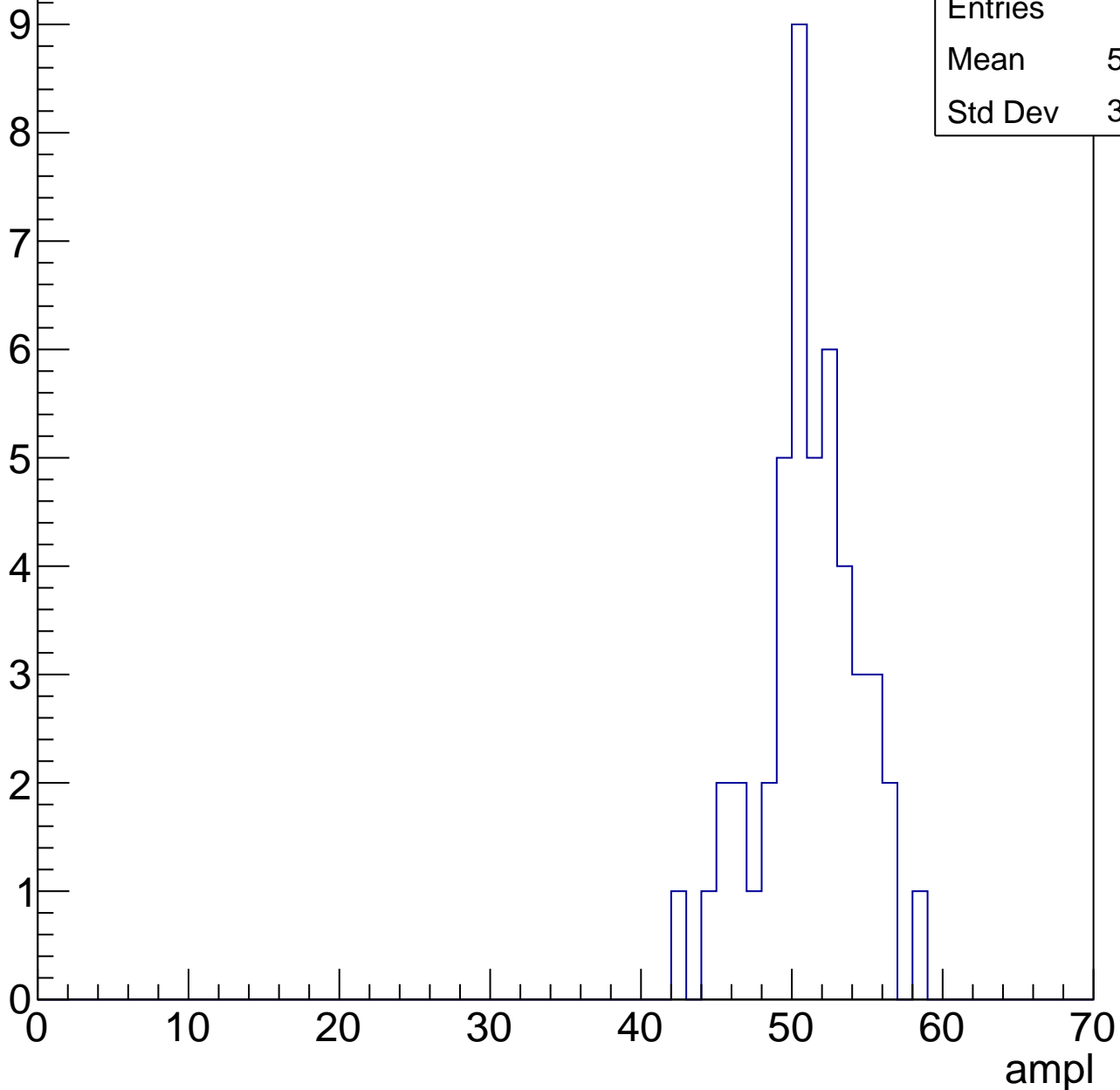


# B1L002S, U3-ch120, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	47
Mean	50.68
Std Dev	3.282

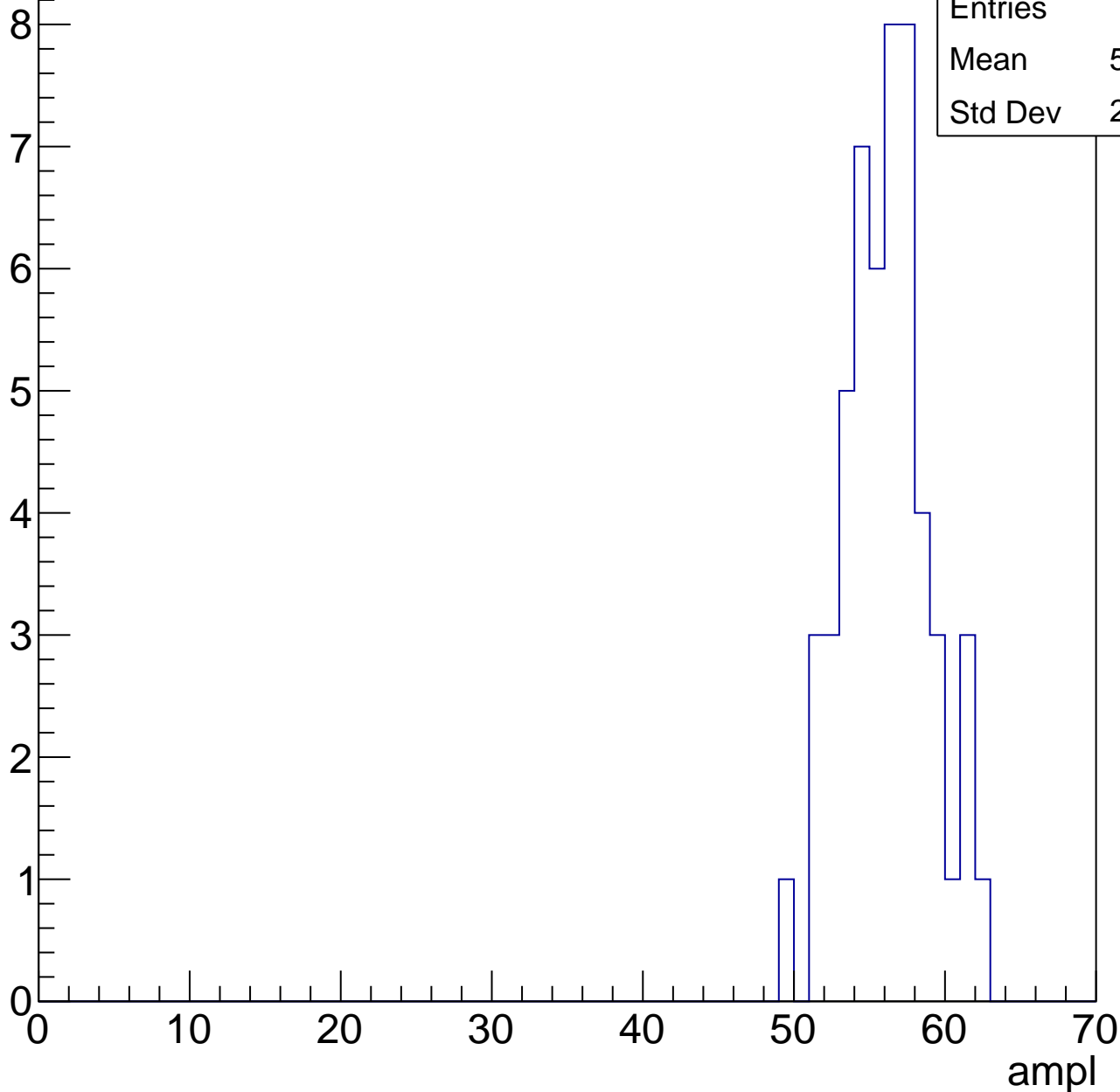


# B1L002S, U3-ch120, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	55.64
Std Dev	2.822

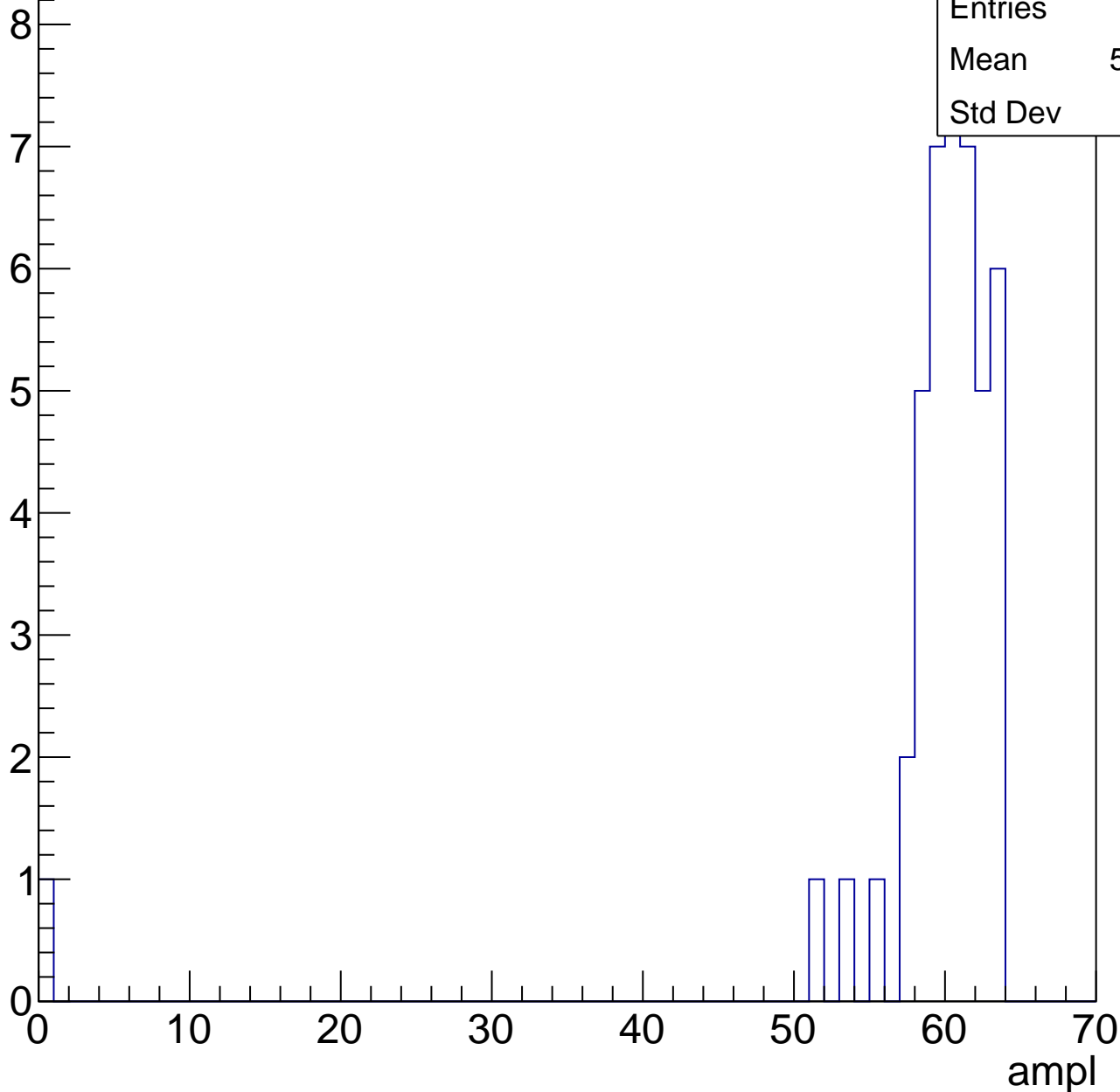


# B1L002S, U3-ch120, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	44
Mean	58.43
Std Dev	9.26

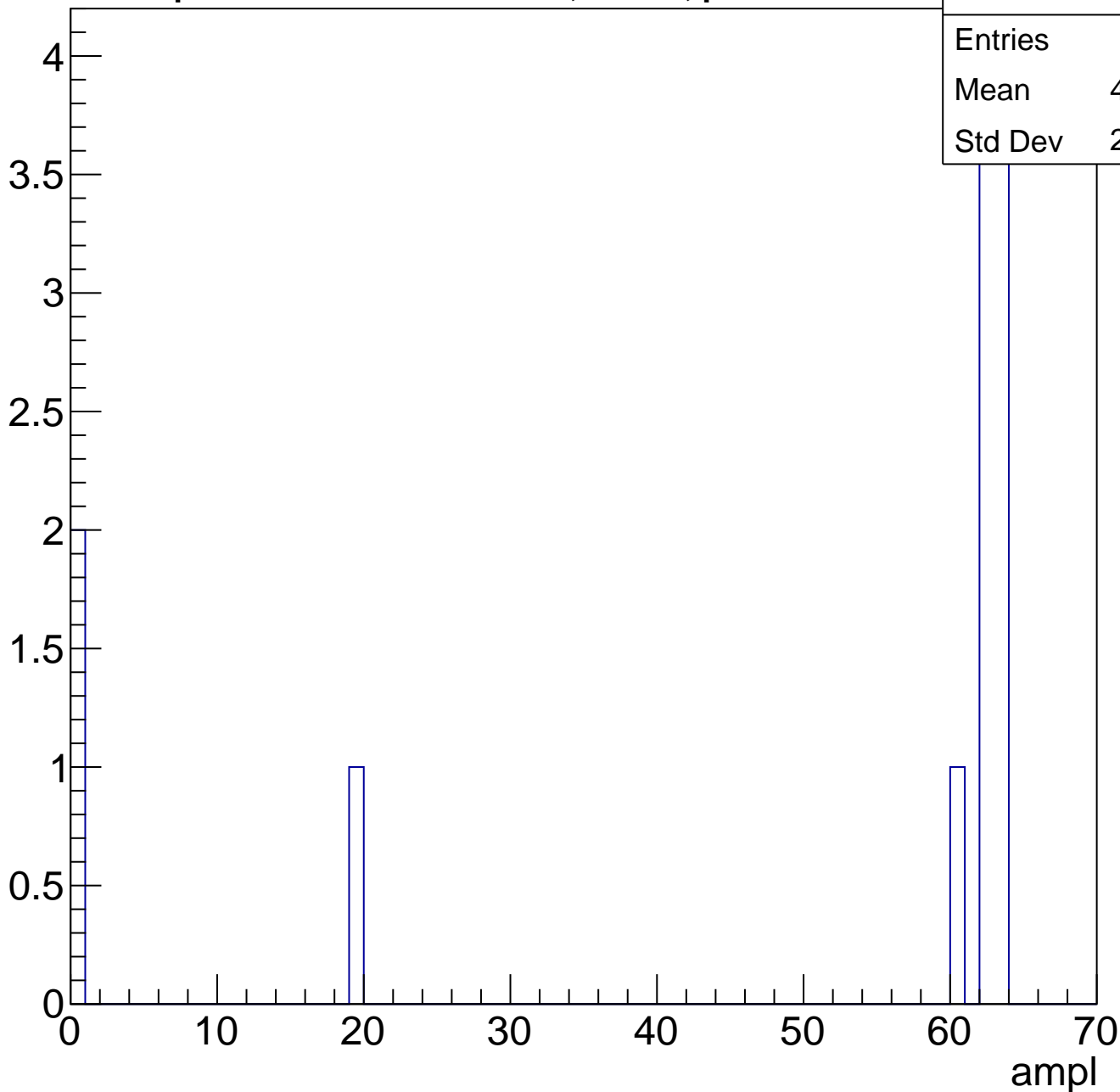




# B1L002S, U3-ch120, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	12
Mean	48.25
Std Dev	24.62

# B1L002S, U3-ch121, adc0

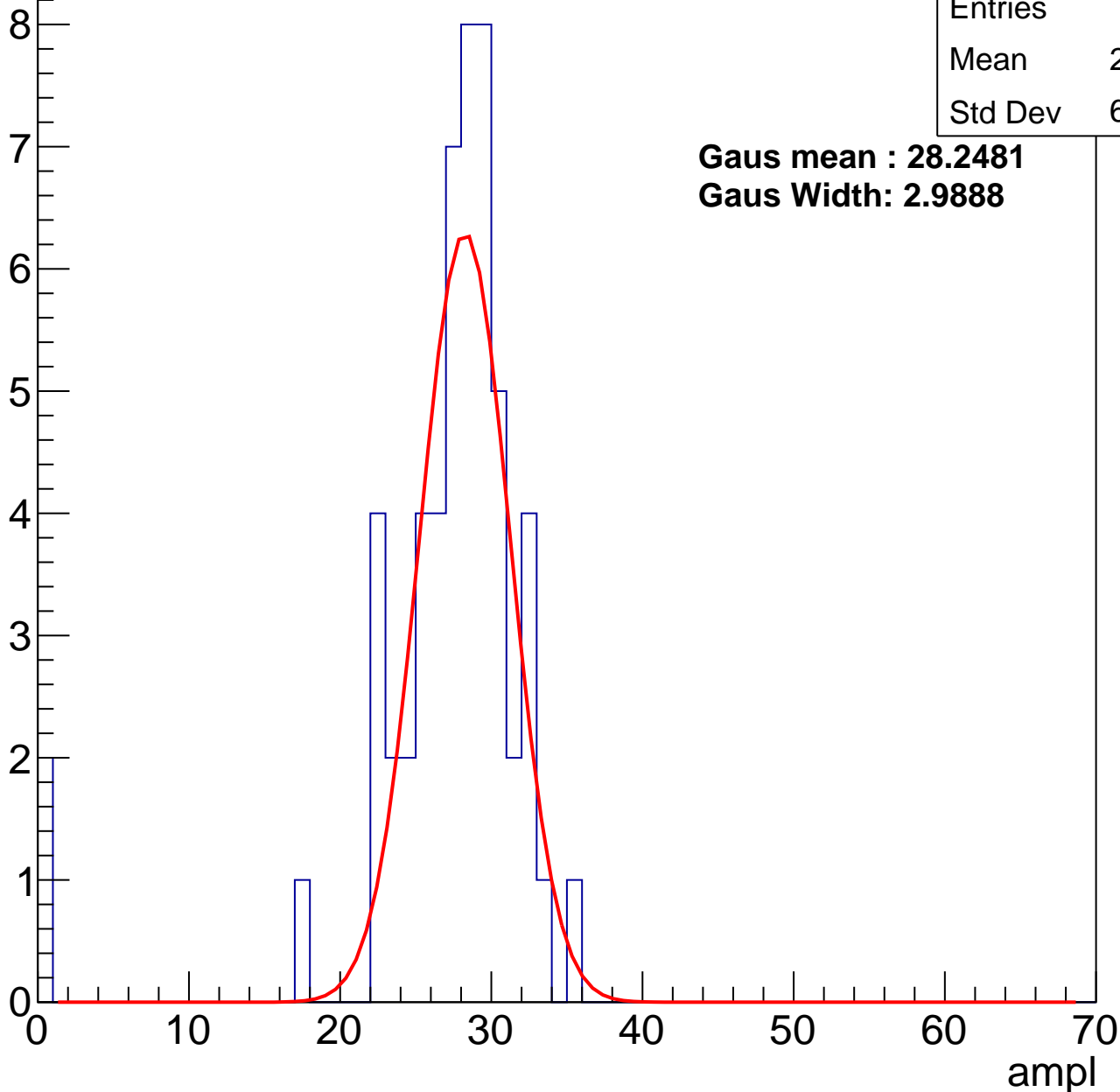
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	55
Mean	26.47
Std Dev	6.075

**Gaus mean : 28.2481**

**Gaus Width: 2.9888**



# B1L002S, U3-ch121, adc1

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	76
Mean	34.13
Std Dev	3.548

**Gaus mean : 34.4876**

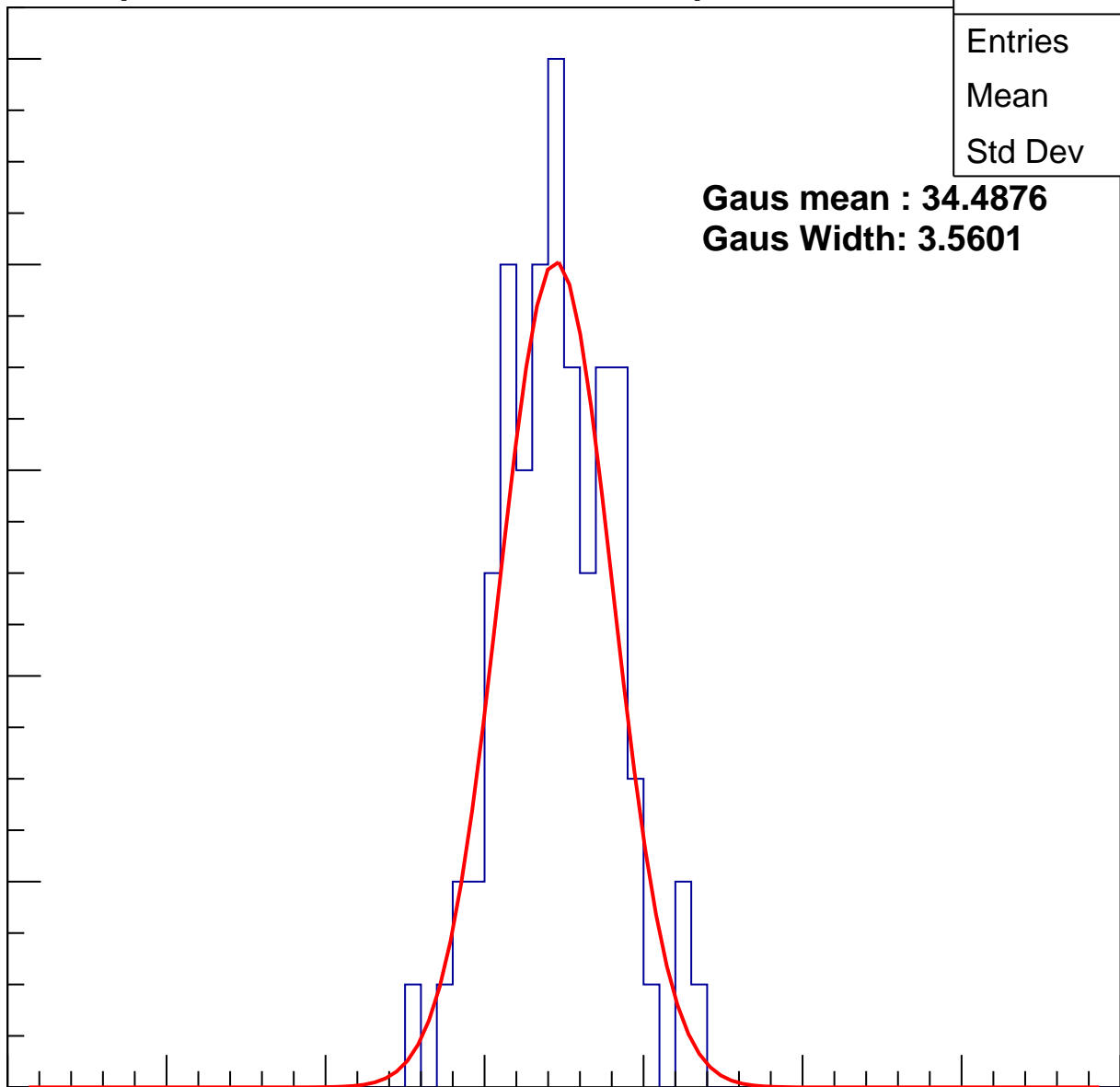
**Gaus Width: 3.5601**

Entry

10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B1L002S, U3-ch121, adc2

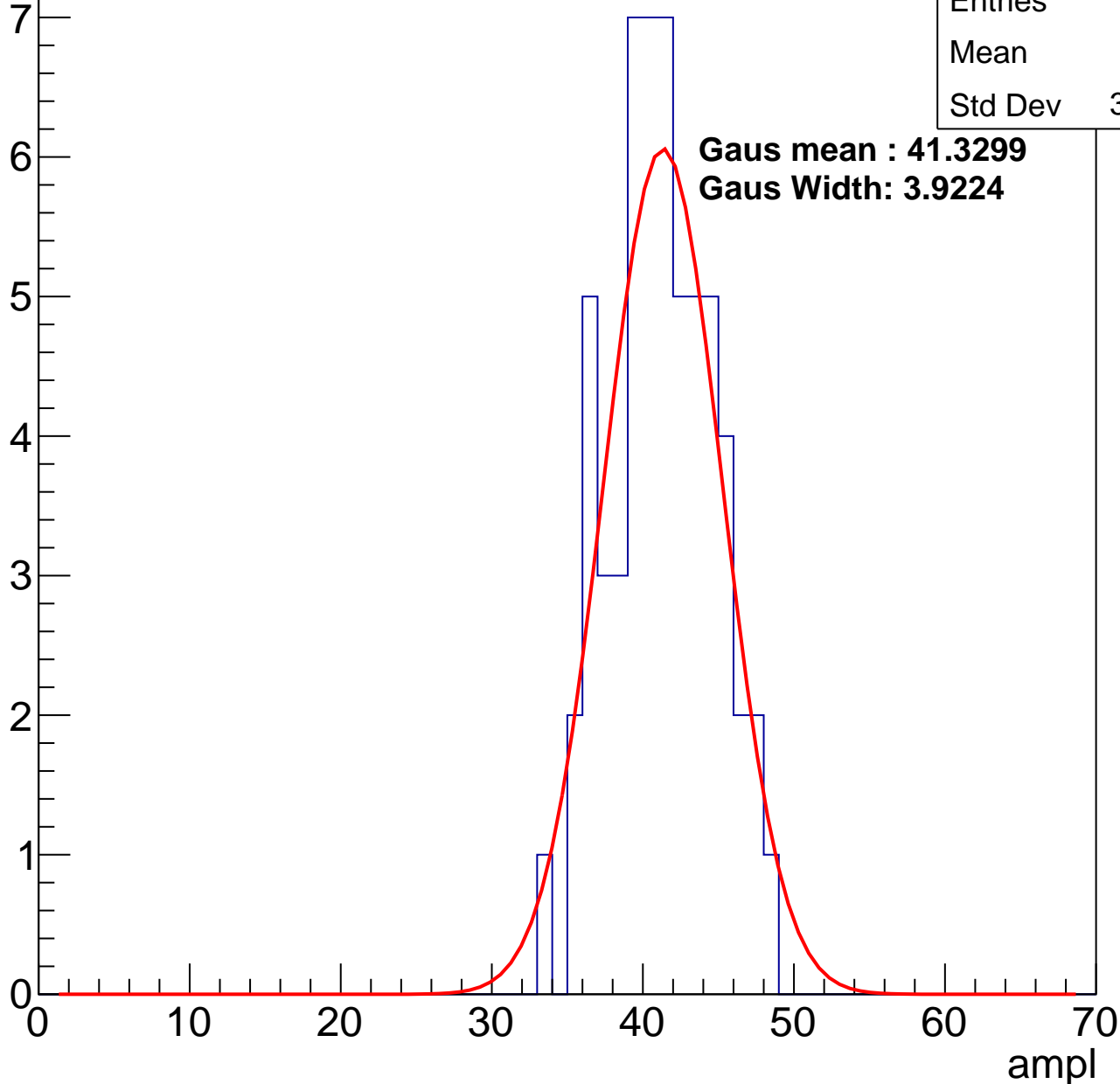
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	59
Mean	40.8
Std Dev	3.379

**Gaus mean : 41.3299**

**Gaus Width: 3.9224**

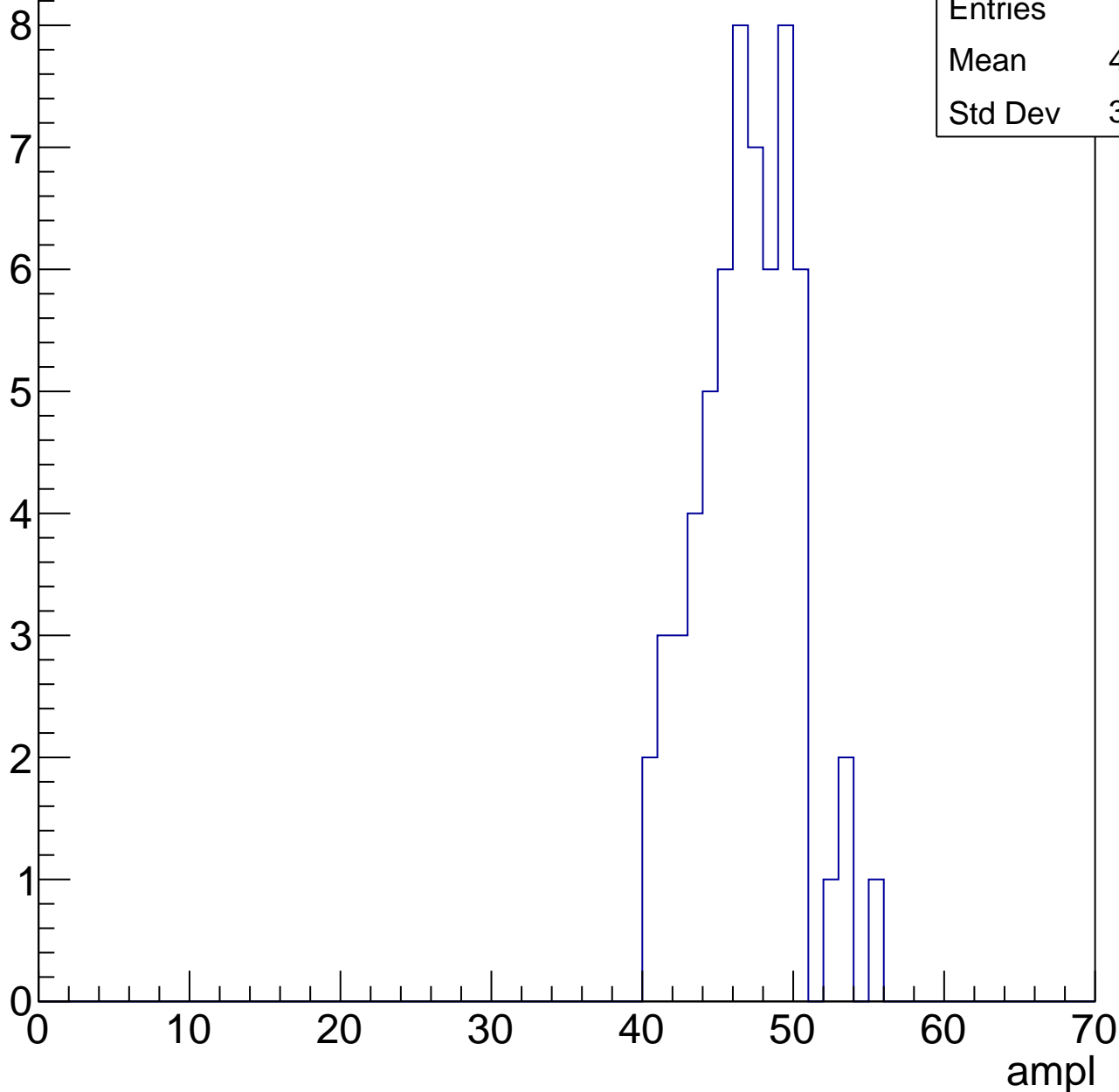


# B1L002S, U3-ch121, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	62
Mean	46.47
Std Dev	3.256

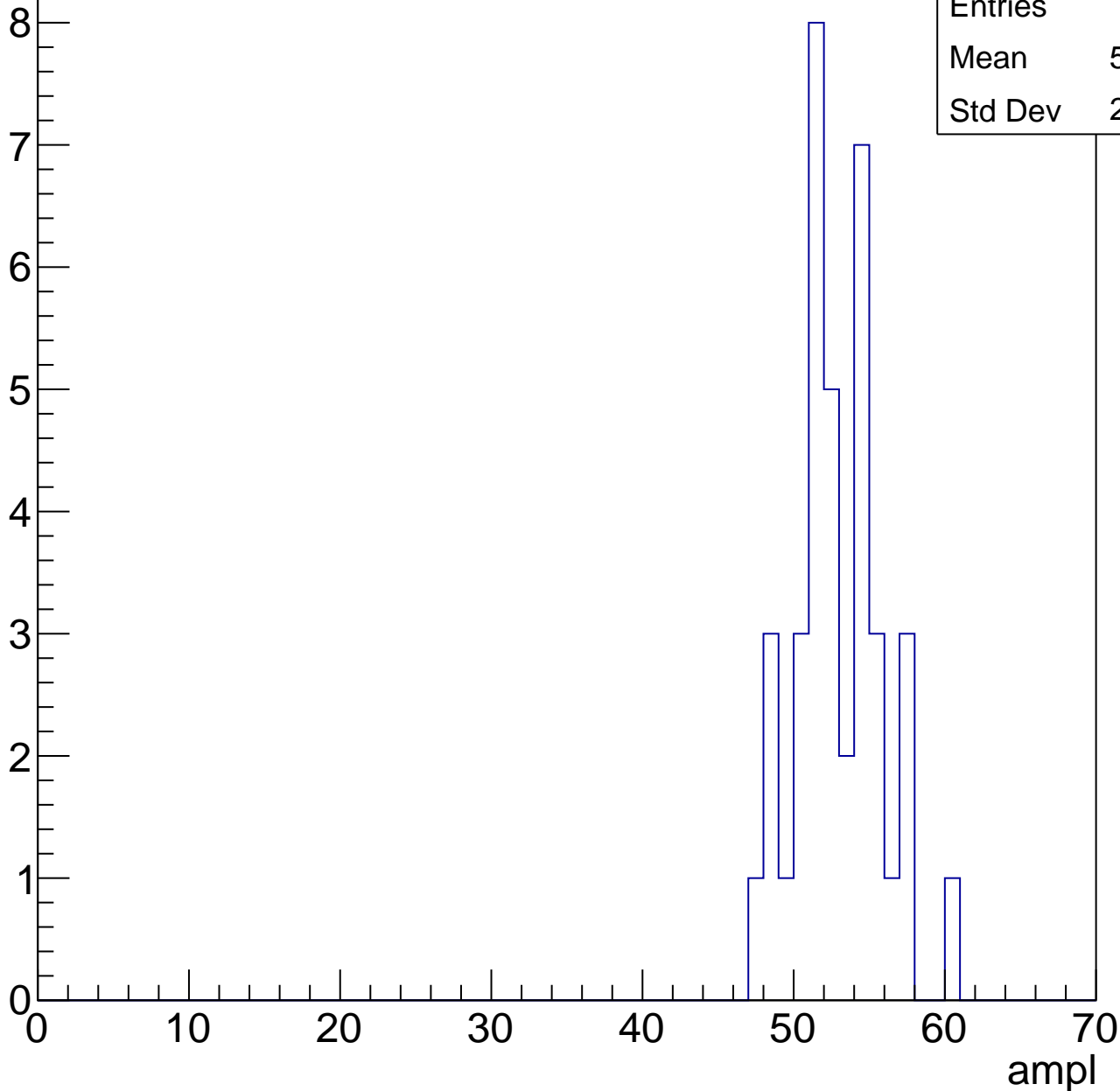


# B1L002S, U3-ch121, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

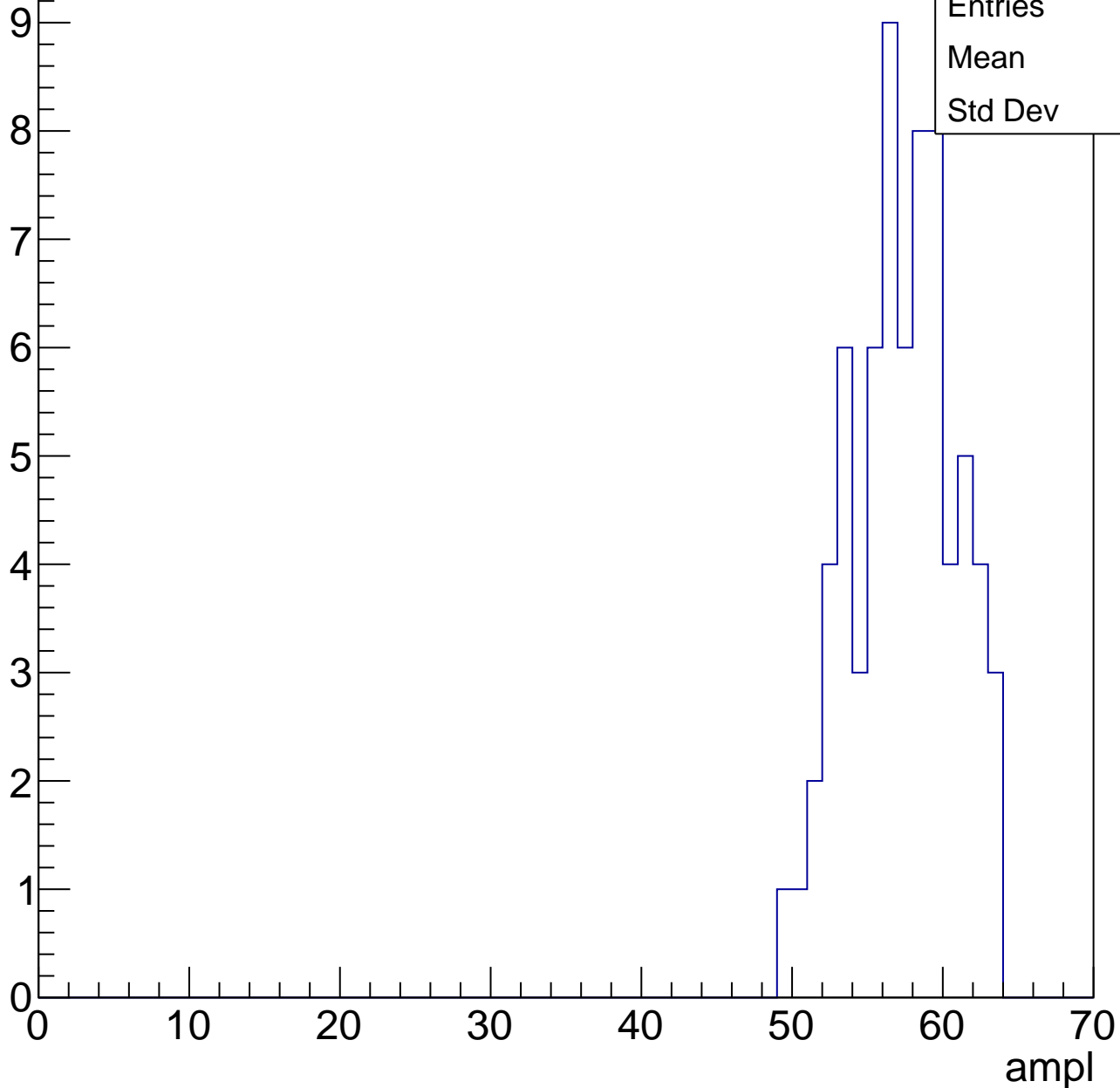
Entries	38
Mean	52.47
Std Dev	2.835



# B1L002S, U3-ch121, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

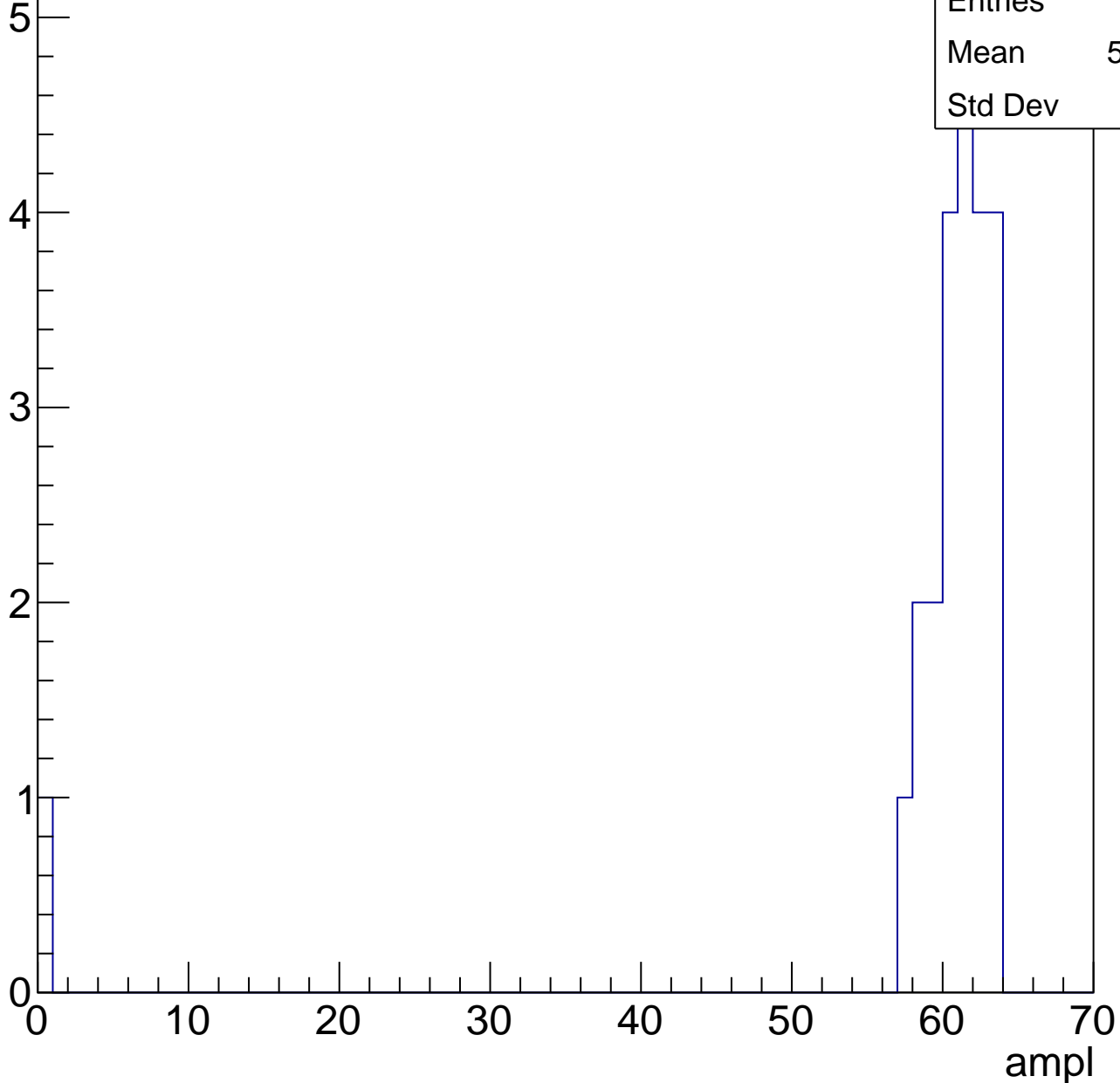


# B1L002S, U3-ch121, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.09
Std Dev	12.5

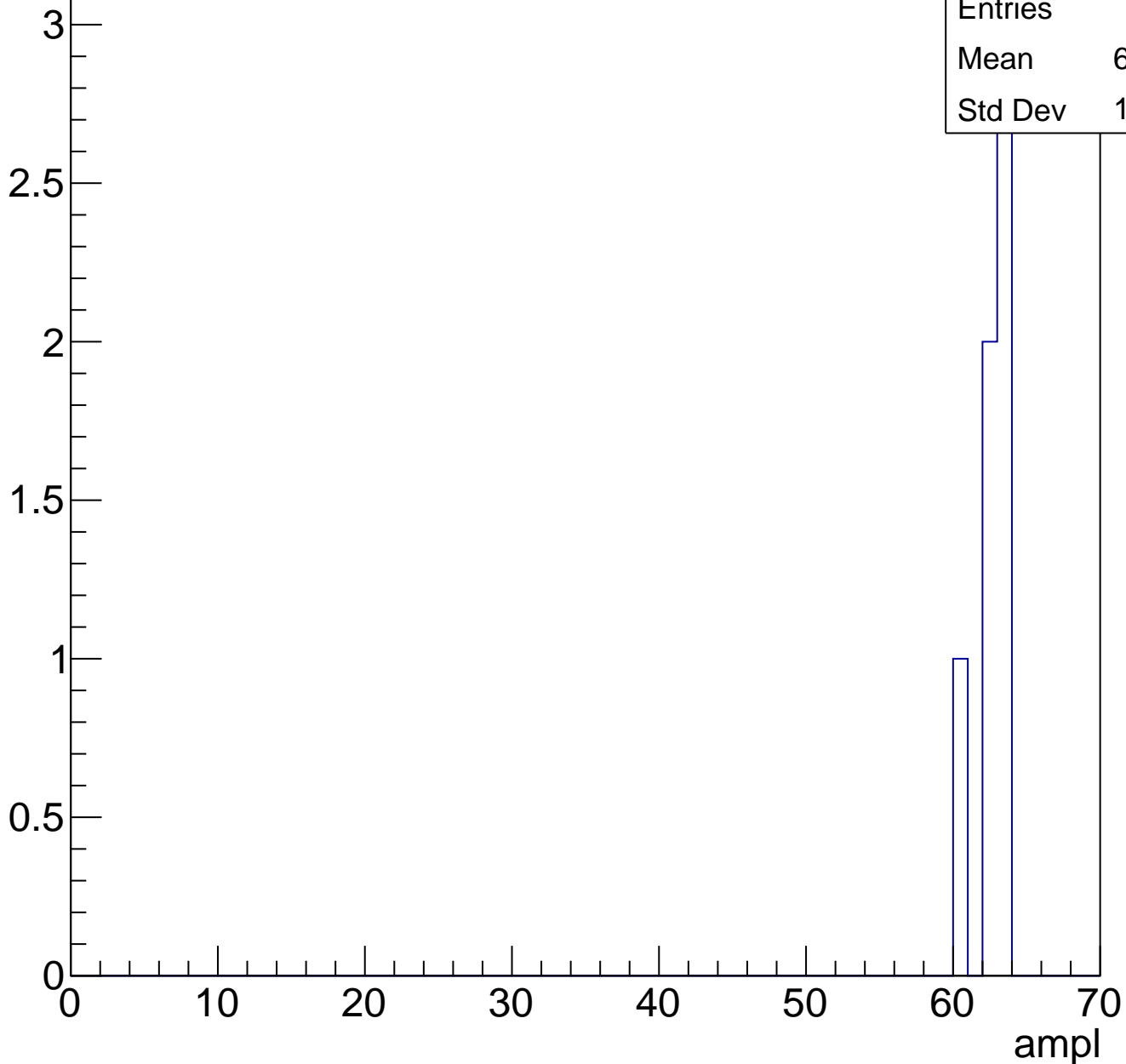




# B1L002S, U3-ch121, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch122, adc0

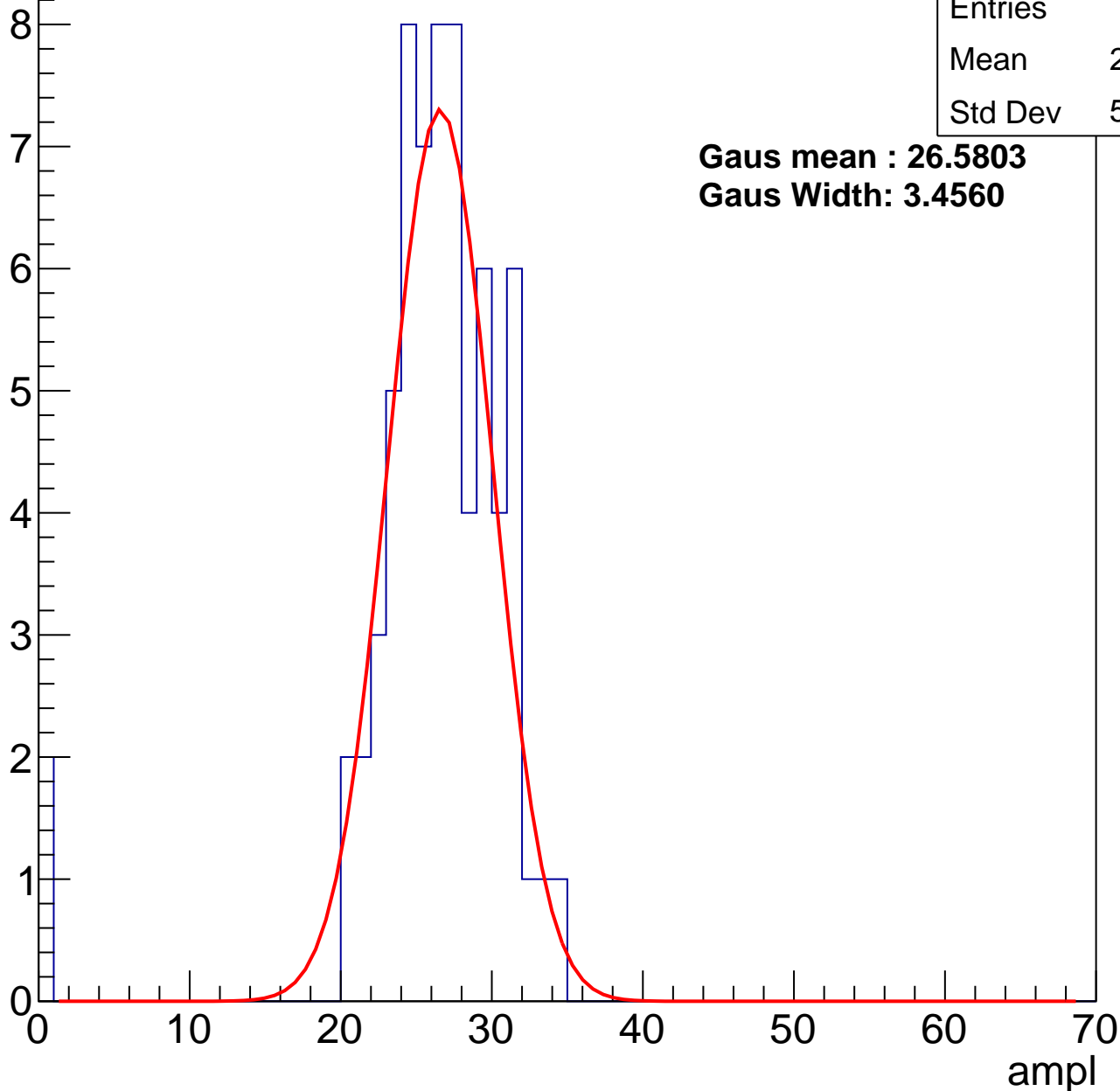
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	25.66
Std Dev	5.476

**Gaus mean : 26.5803**

**Gaus Width: 3.4560**



# B1L002S, U3-ch122, adc1

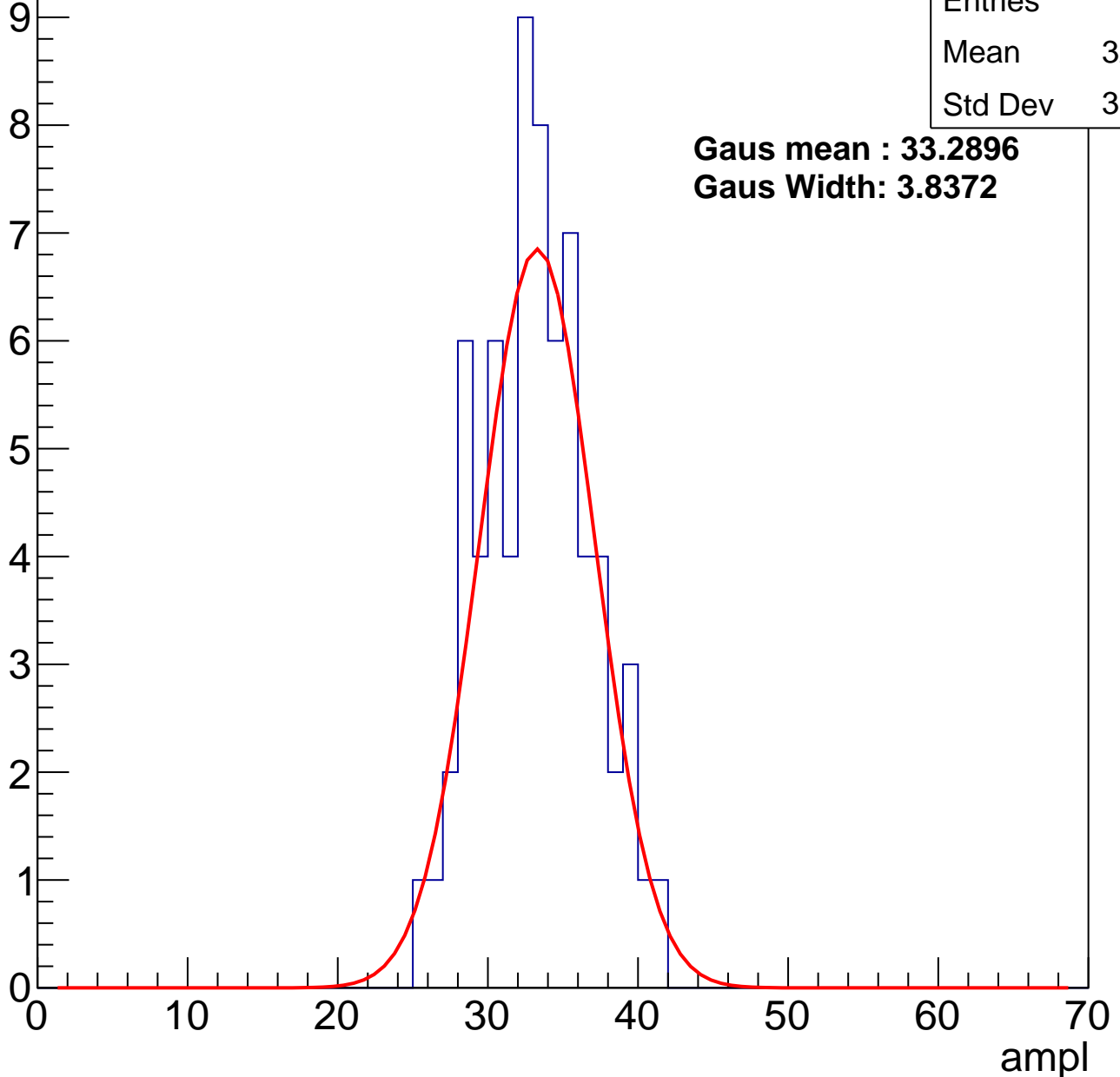
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	69
Mean	32.75
Std Dev	3.569

**Gaus mean : 33.2896**

**Gaus Width: 3.8372**



# B1L002S, U3-ch122, adc2

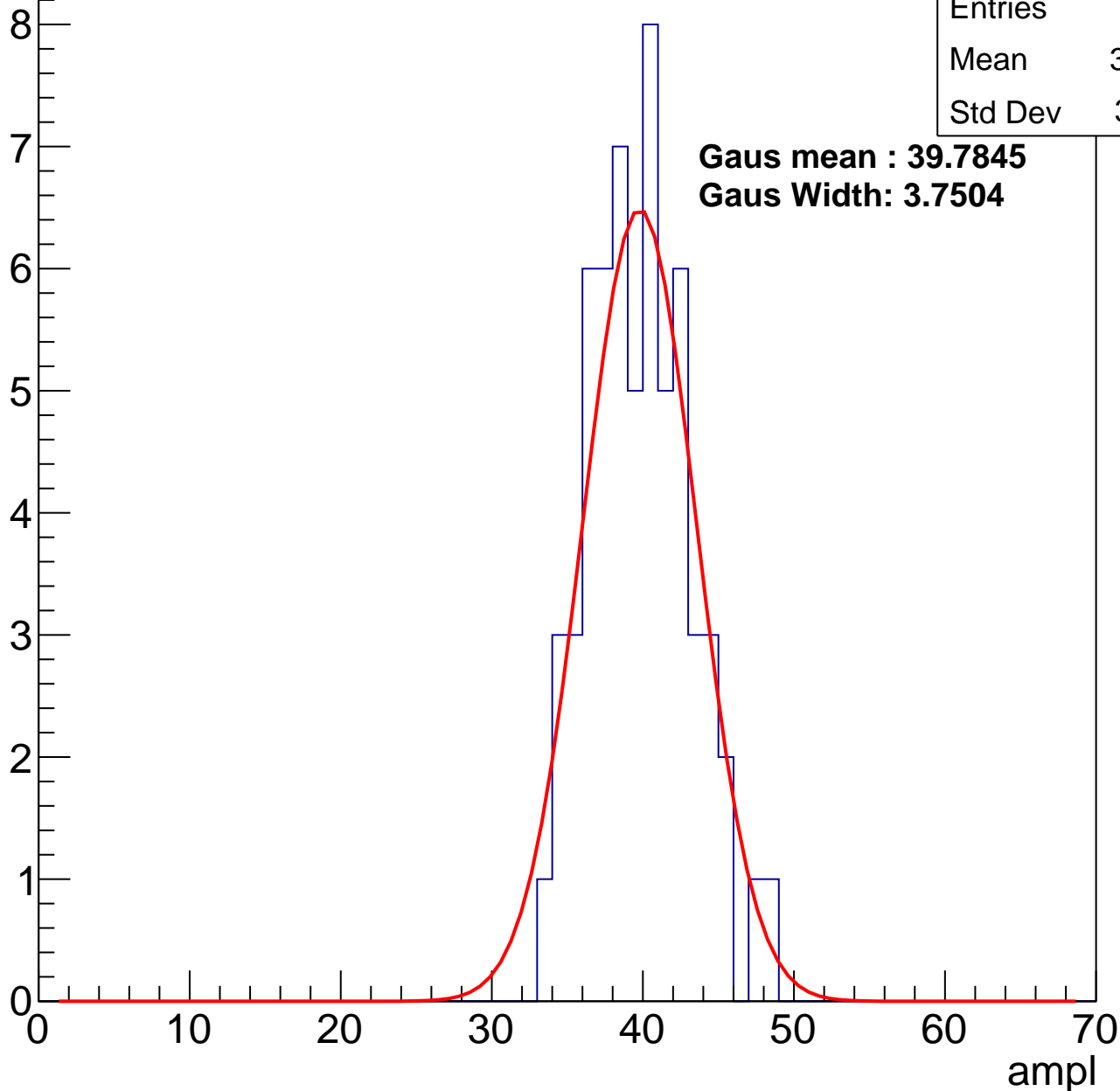
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	60
Mean	39.37
Std Dev	3.311

**Gaus mean : 39.7845**

**Gaus Width: 3.7504**

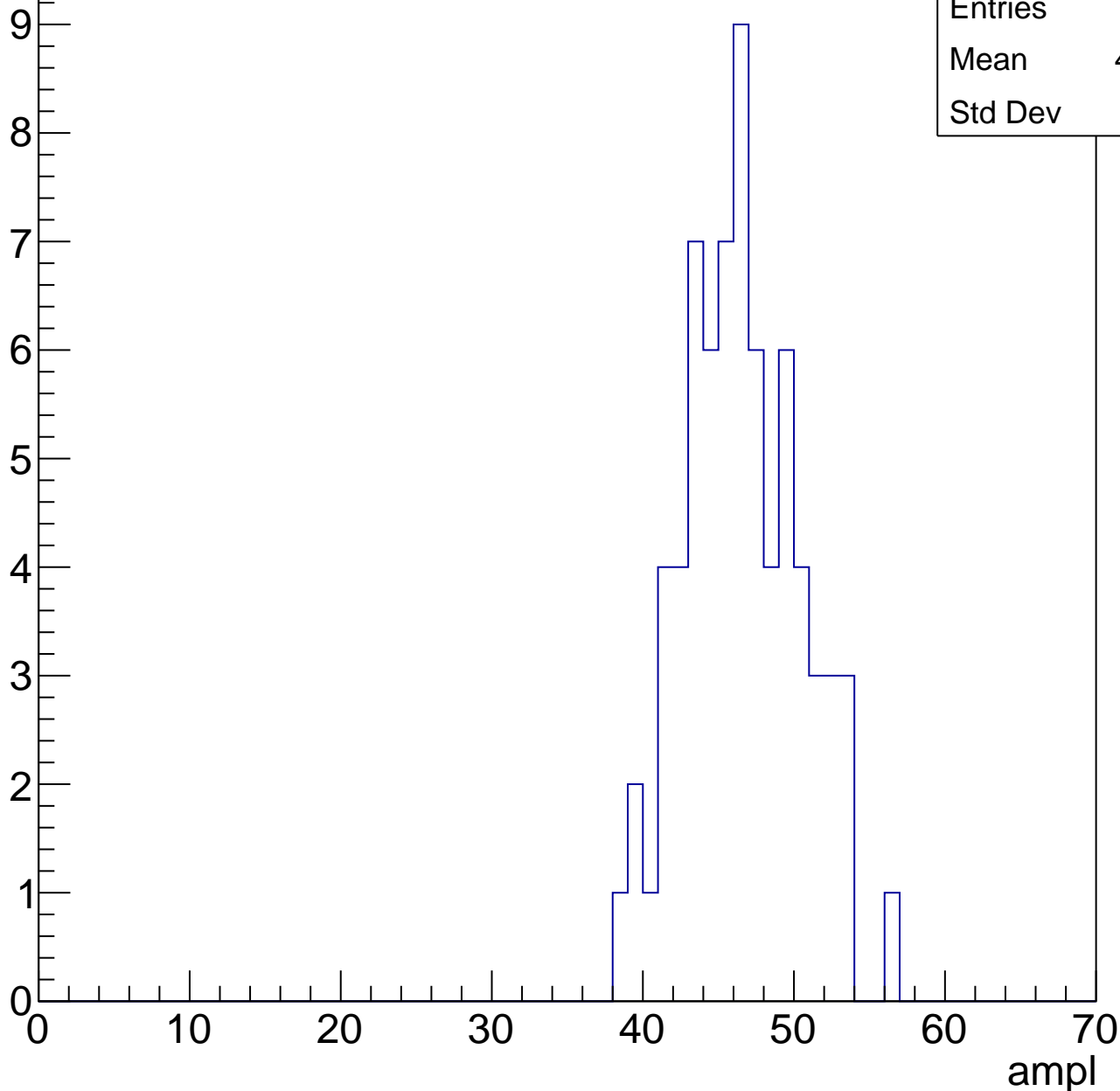


# B1L002S, U3-ch122, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	46.11
Std Dev	3.8

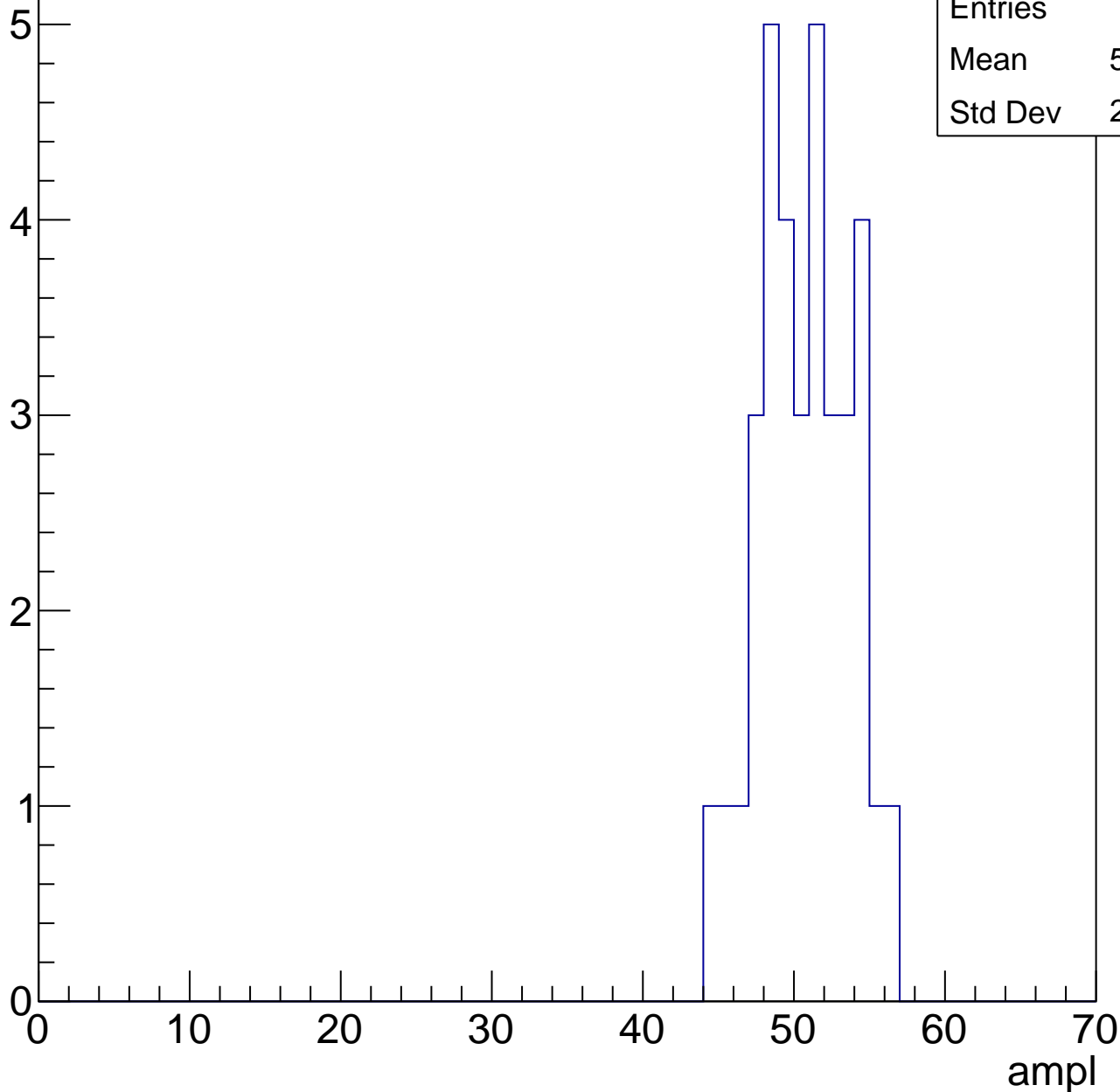


# B1L002S, U3-ch122, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

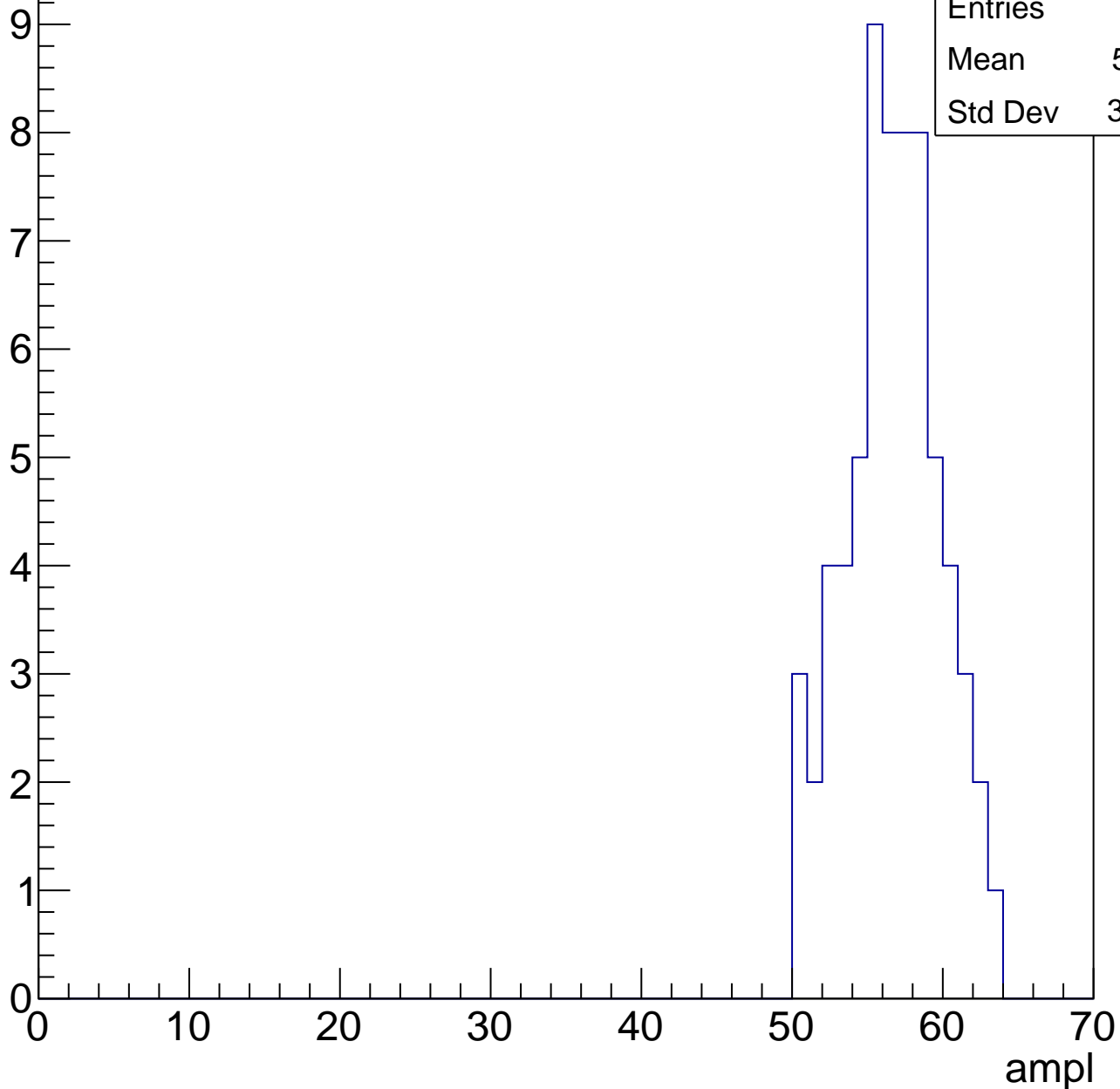
Entries	35
Mean	50.26
Std Dev	2.902



# B1L002S, U3-ch122, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



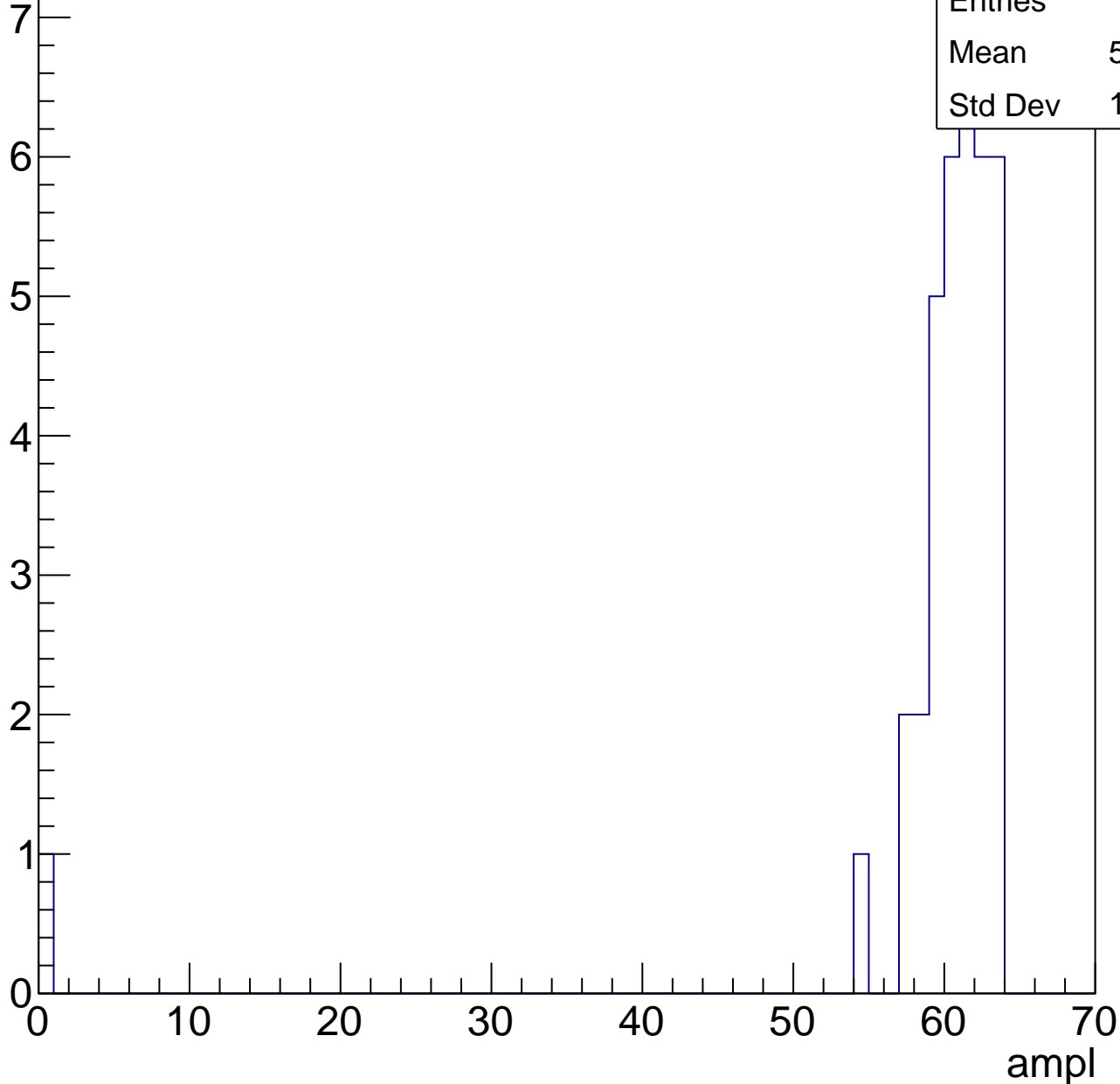
Entries	66
Mean	56.21
Std Dev	3.087

# B1L002S, U3-ch122, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	36
Mean	58.78
Std Dev	10.14

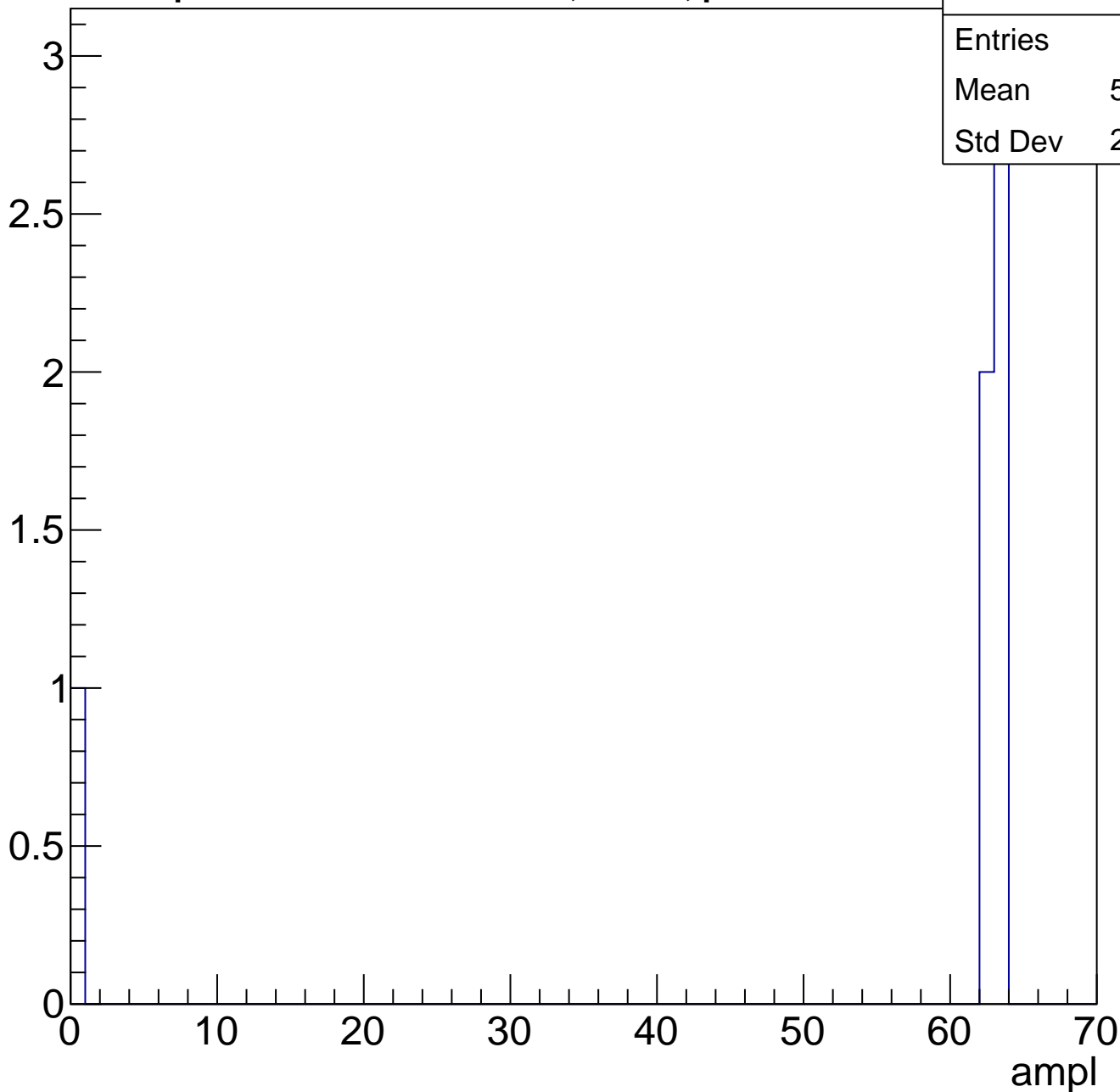




# B1L002S, U3-ch122, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch123, adc0

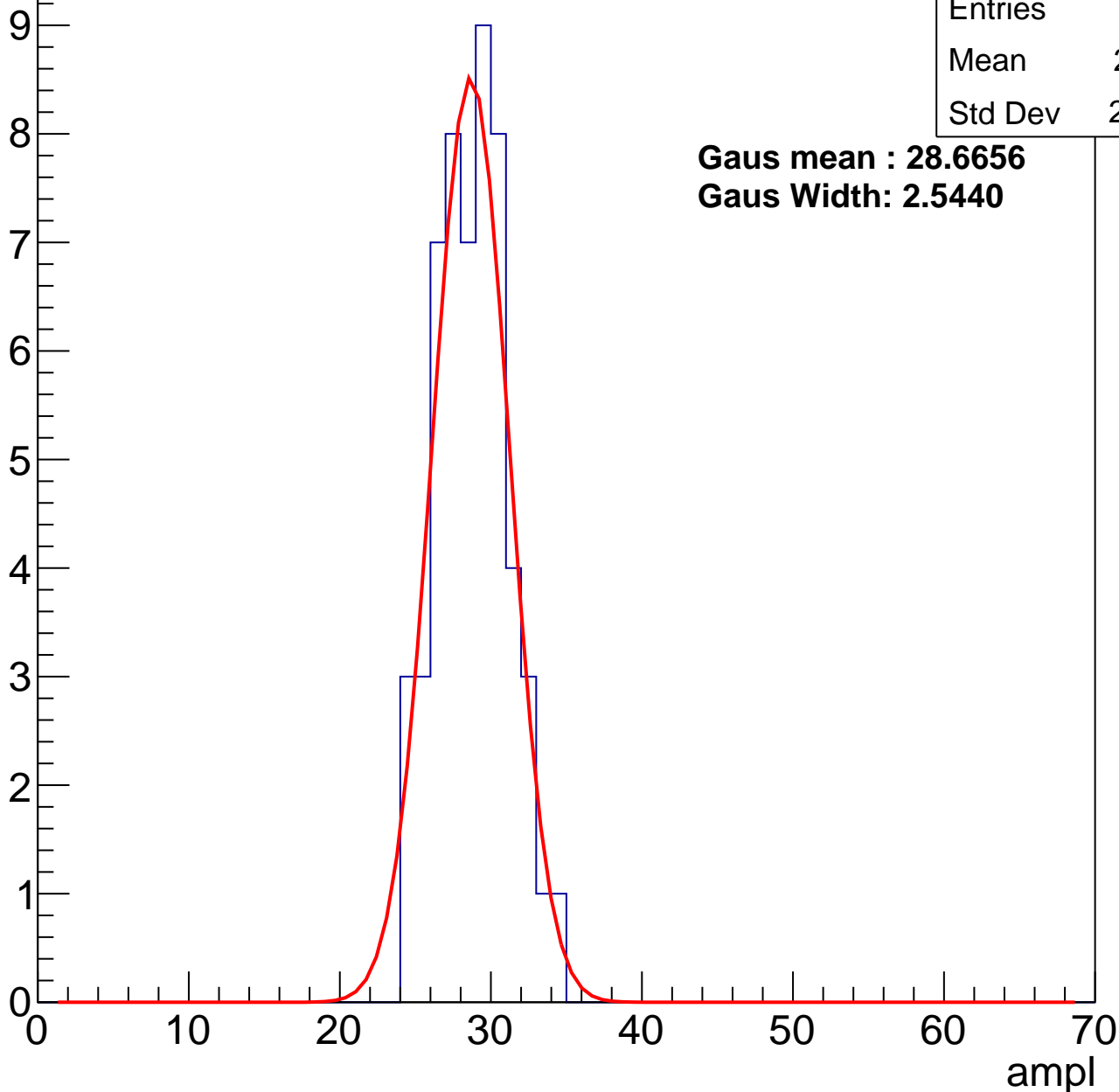
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	54
Mean	28.31
Std Dev	2.324

**Gaus mean : 28.6656**

**Gaus Width: 2.5440**



# B1L002S, U3-ch123, adc1

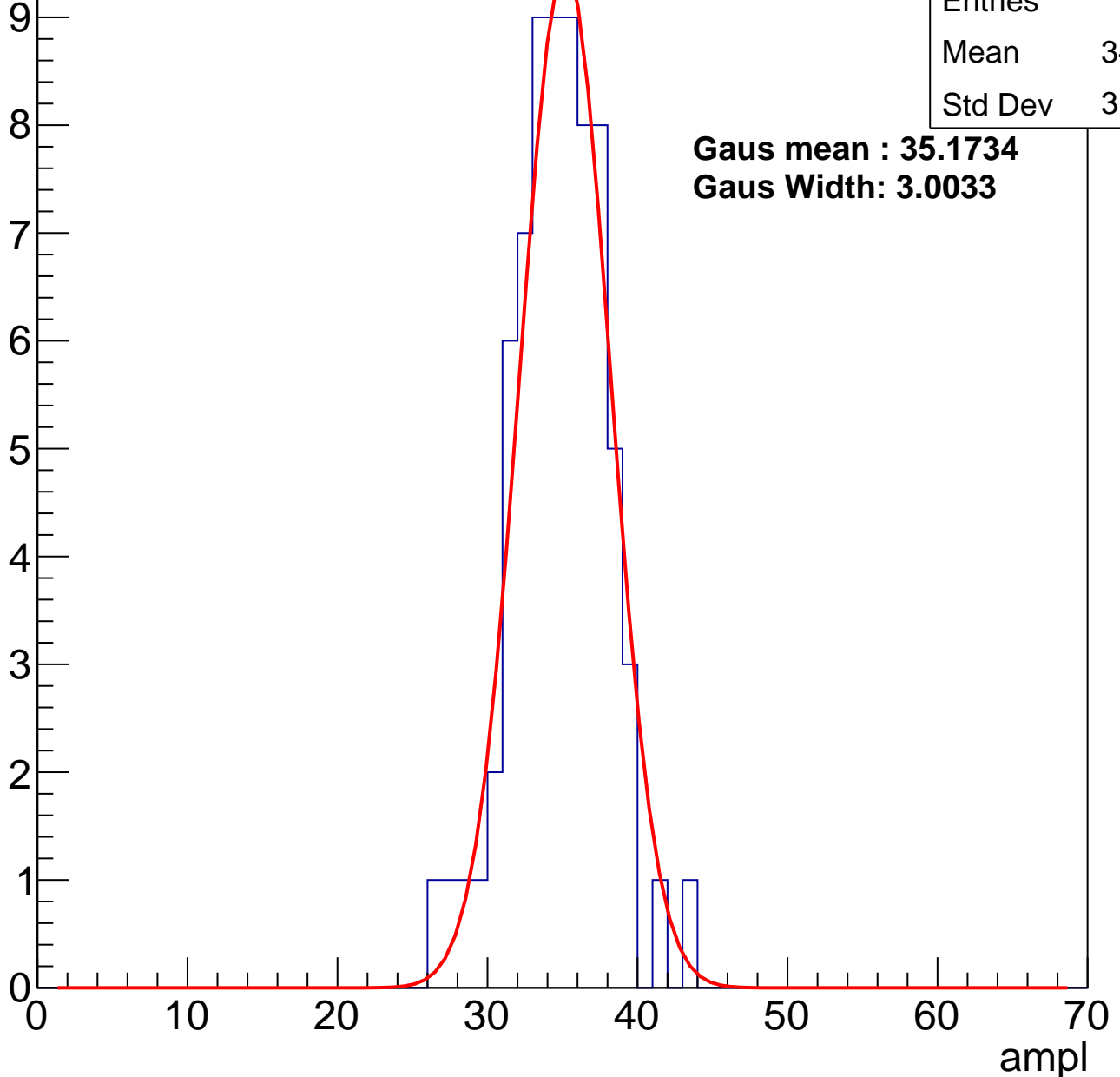
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	34.35
Std Dev	3.087

**Gaus mean : 35.1734**

**Gaus Width: 3.0033**



# B1L002S, U3-ch123, adc2

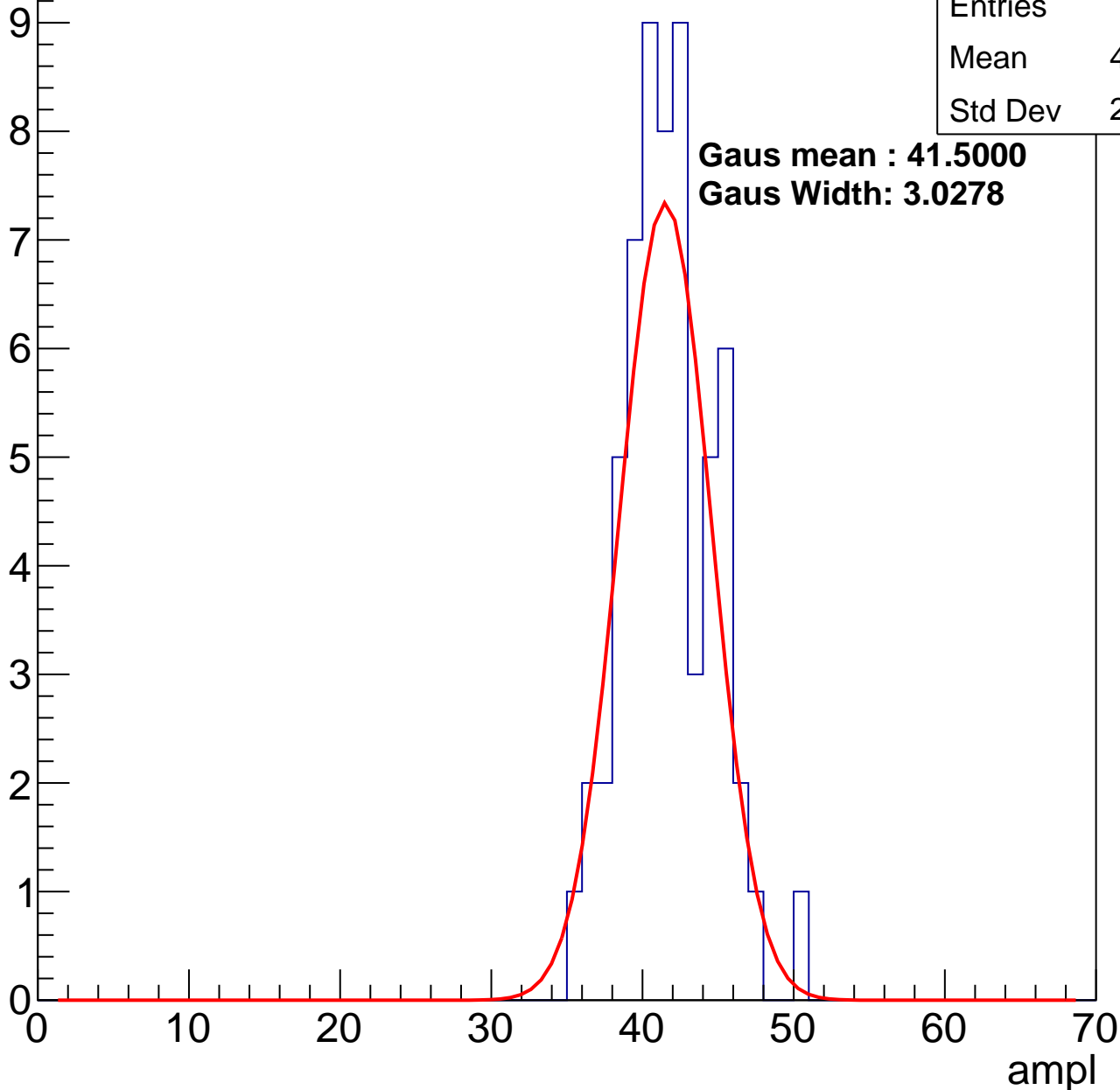
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	41.28
Std Dev	2.932

**Gaus mean : 41.5000**

**Gaus Width: 3.0278**

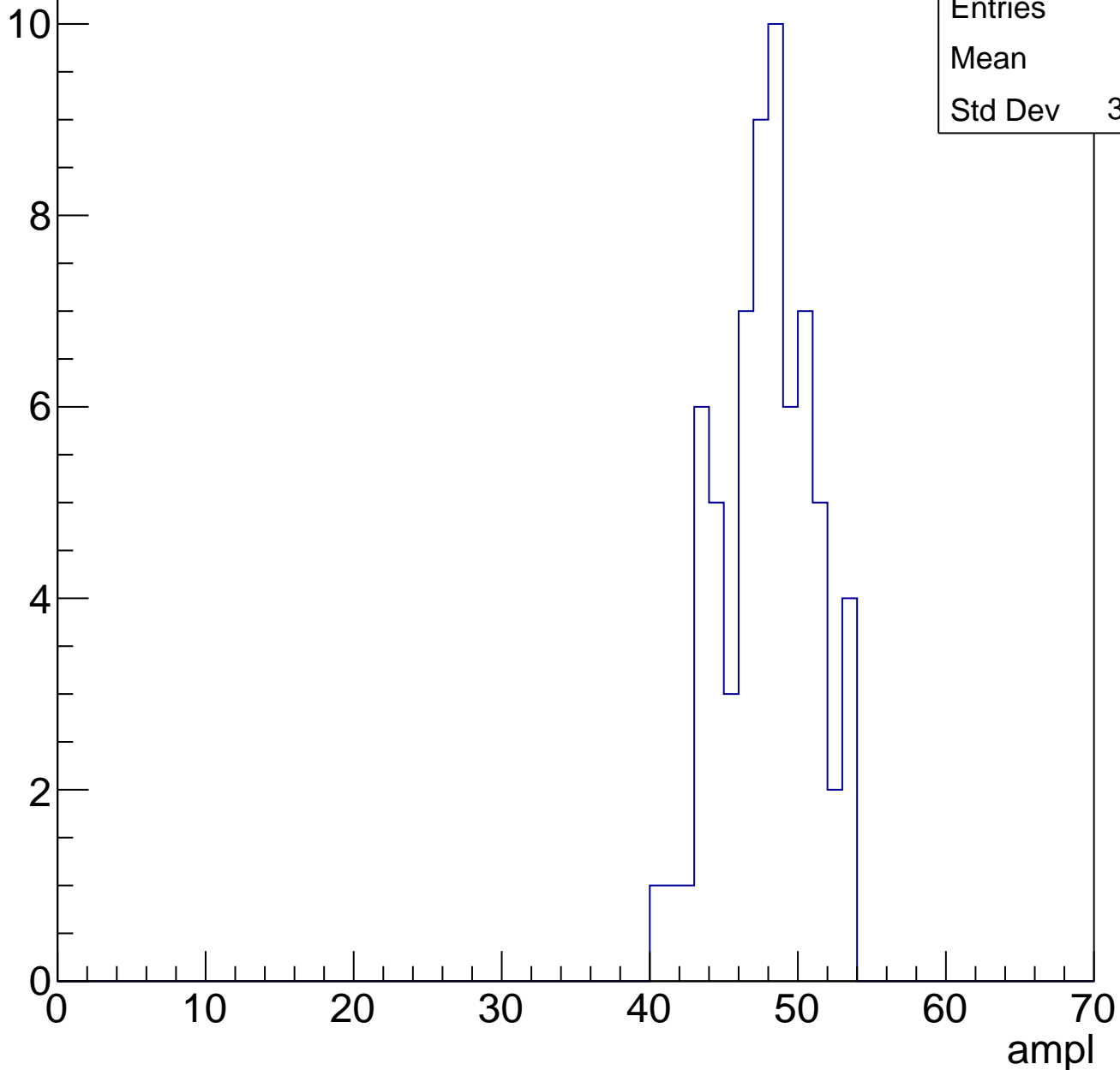


# B1L002S, U3-ch123, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entries	67
Mean	47.4
Std Dev	3.067

Entry

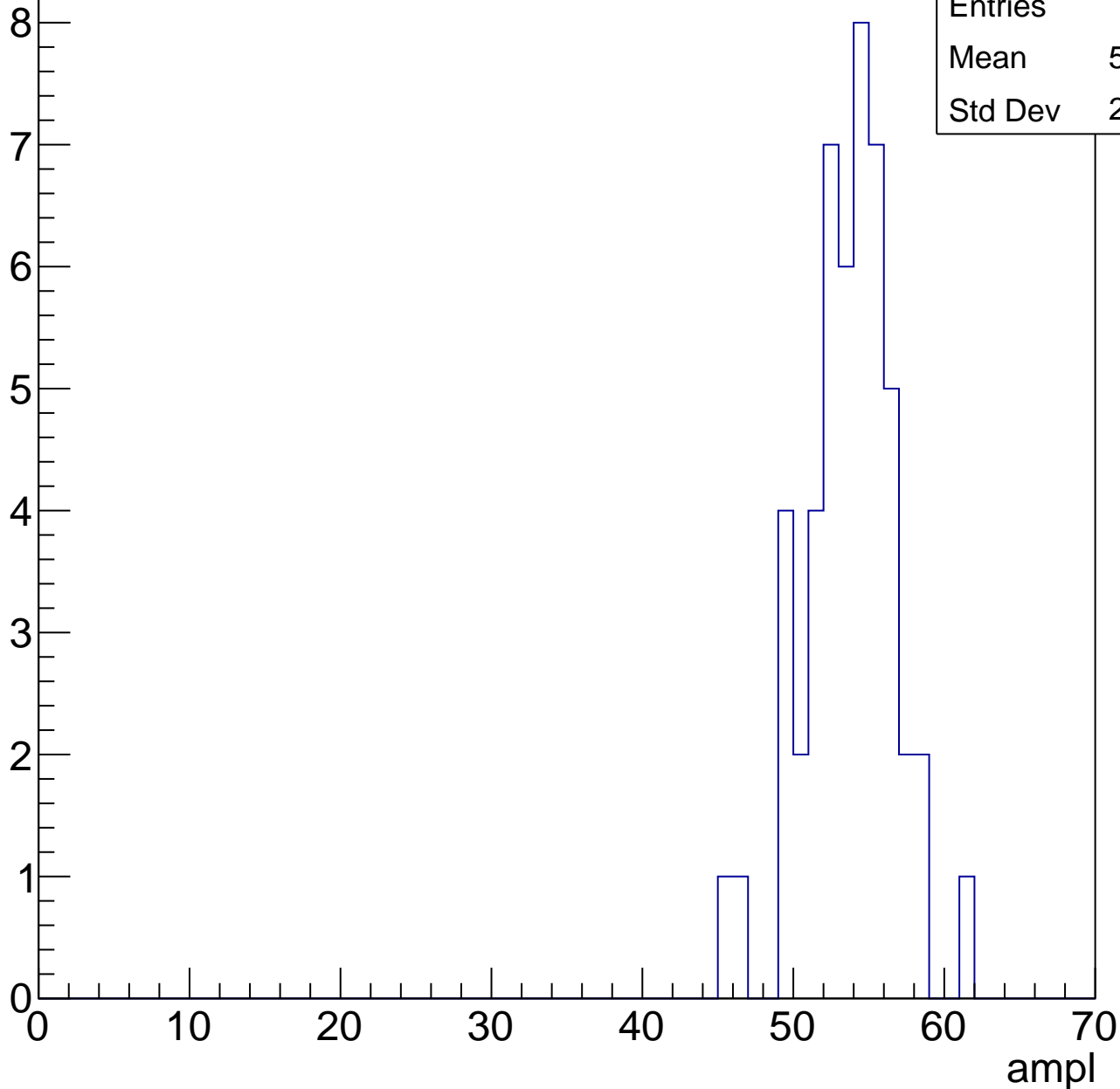


# B1L002S, U3-ch123, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

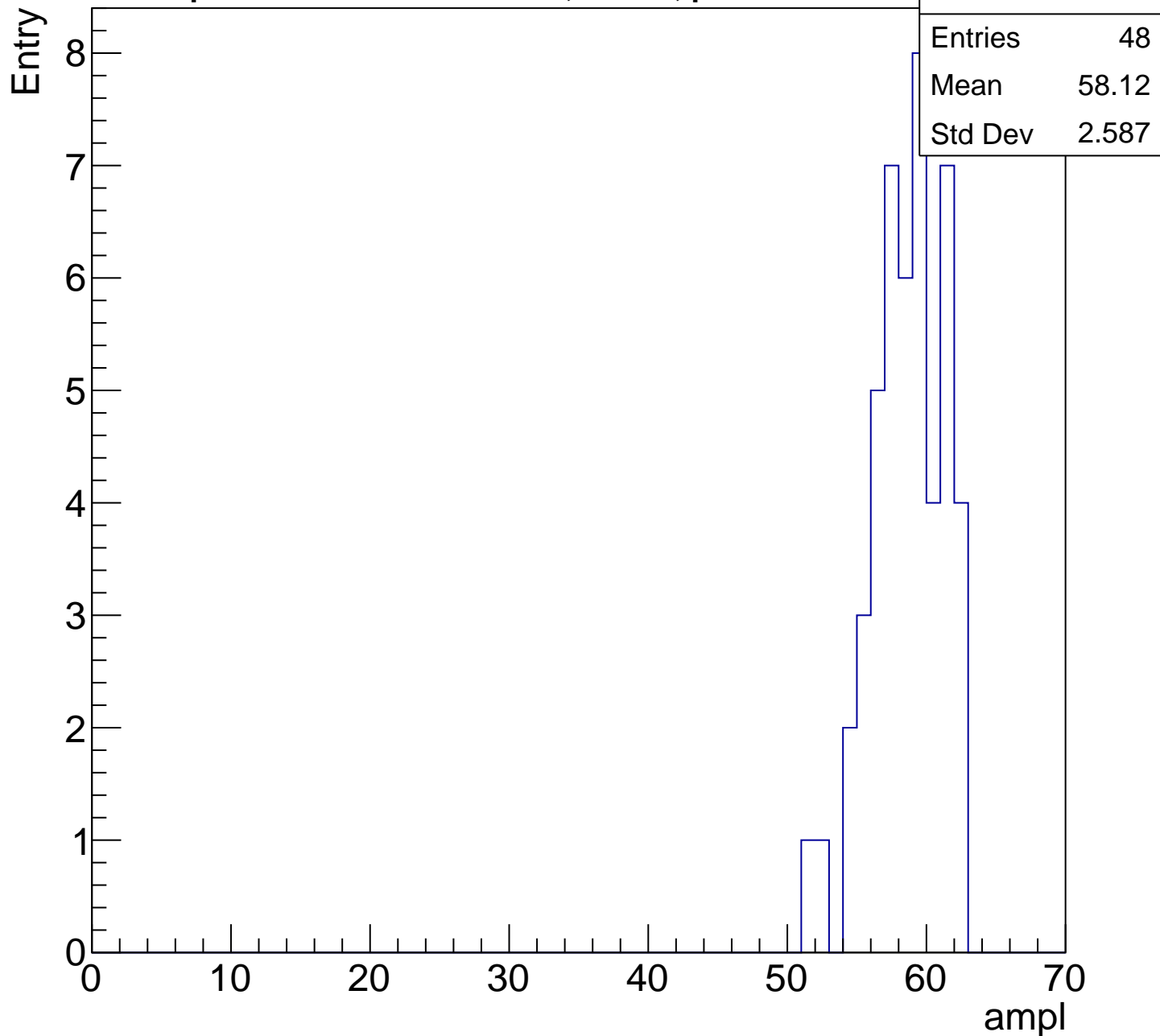
Entry

Entries	50
Mean	53.22
Std Dev	2.975



# B1L002S, U3-ch123, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

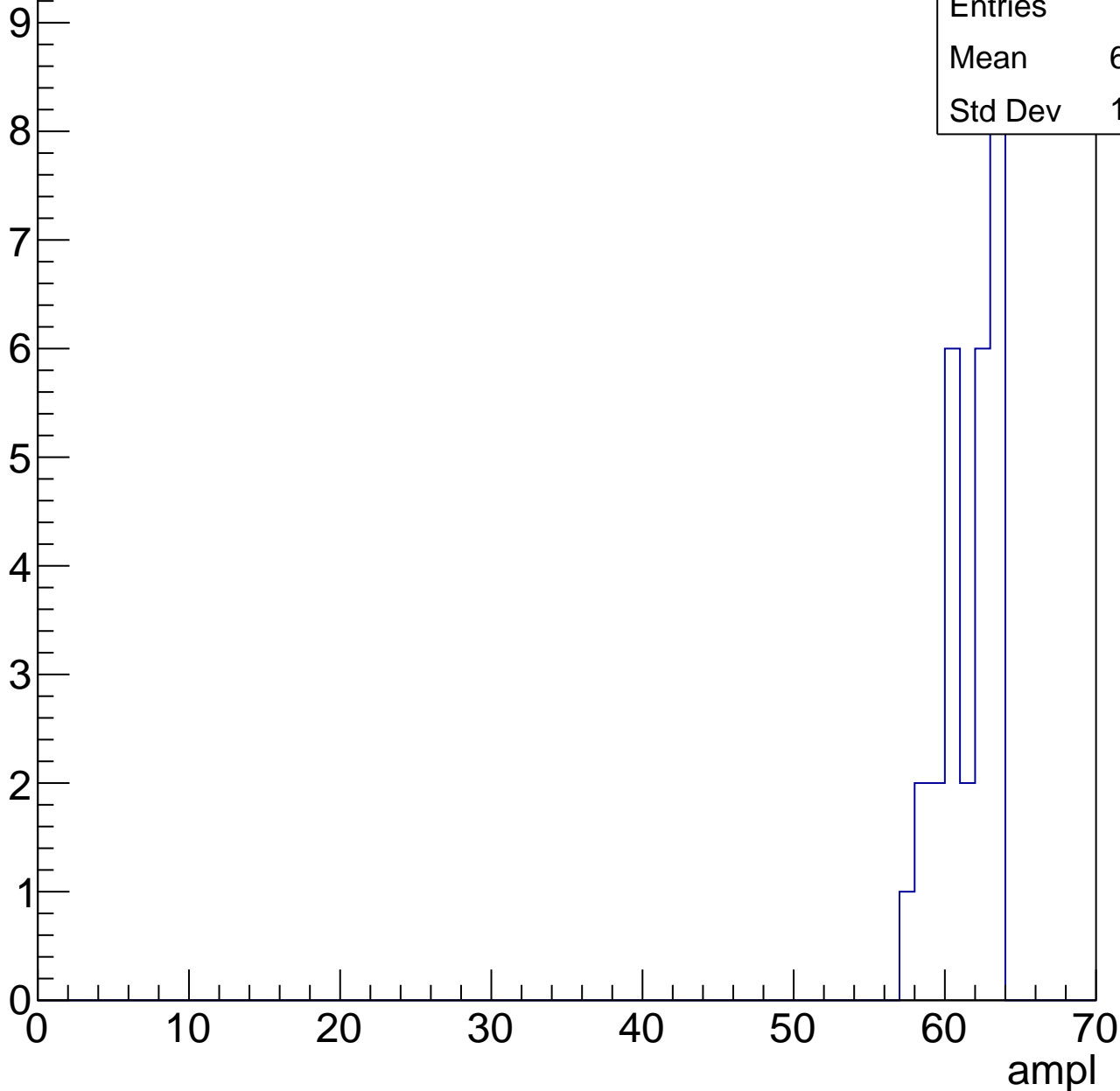


# B1L002S, U3-ch123, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	28
Mean	61.14
Std Dev	1.787

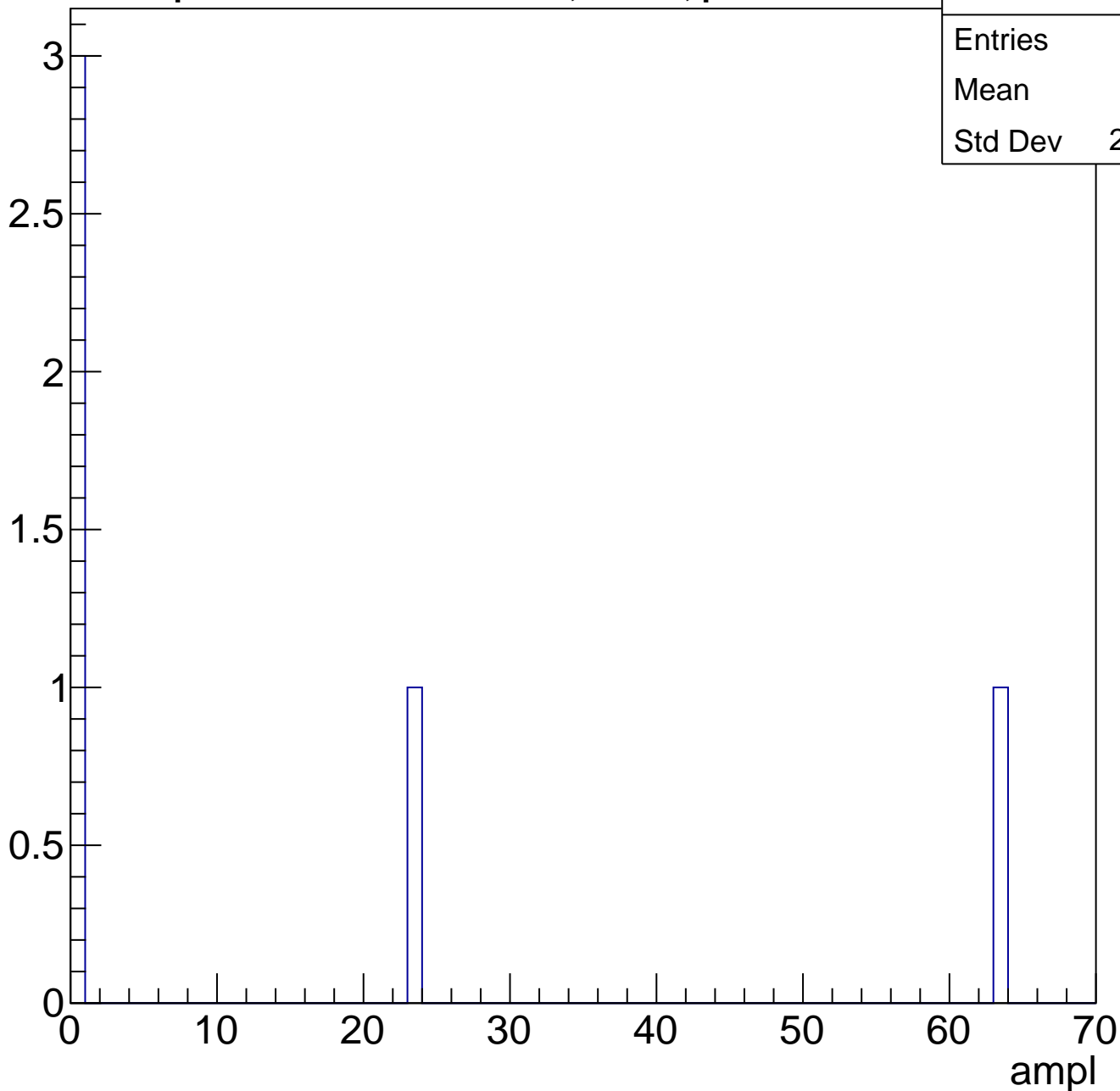




# B1L002S, U3-ch123, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



Entries	5
Mean	17.2
Std Dev	24.57

# B1L002S, U3-ch124, adc0

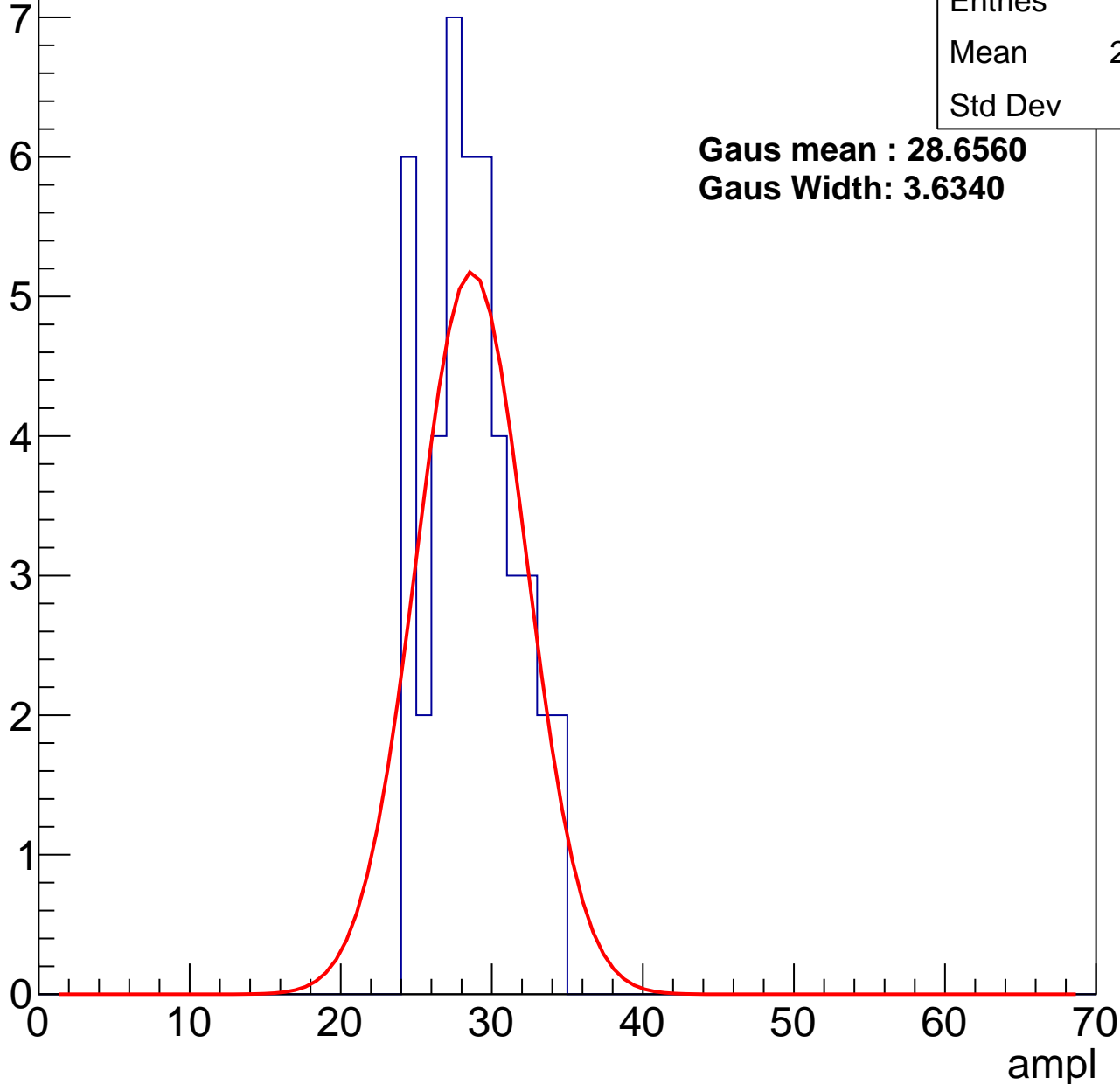
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	45
Mean	28.27
Std Dev	2.8

**Gaus mean : 28.6560**

**Gaus Width: 3.6340**



# B1L002S, U3-ch124, adc1

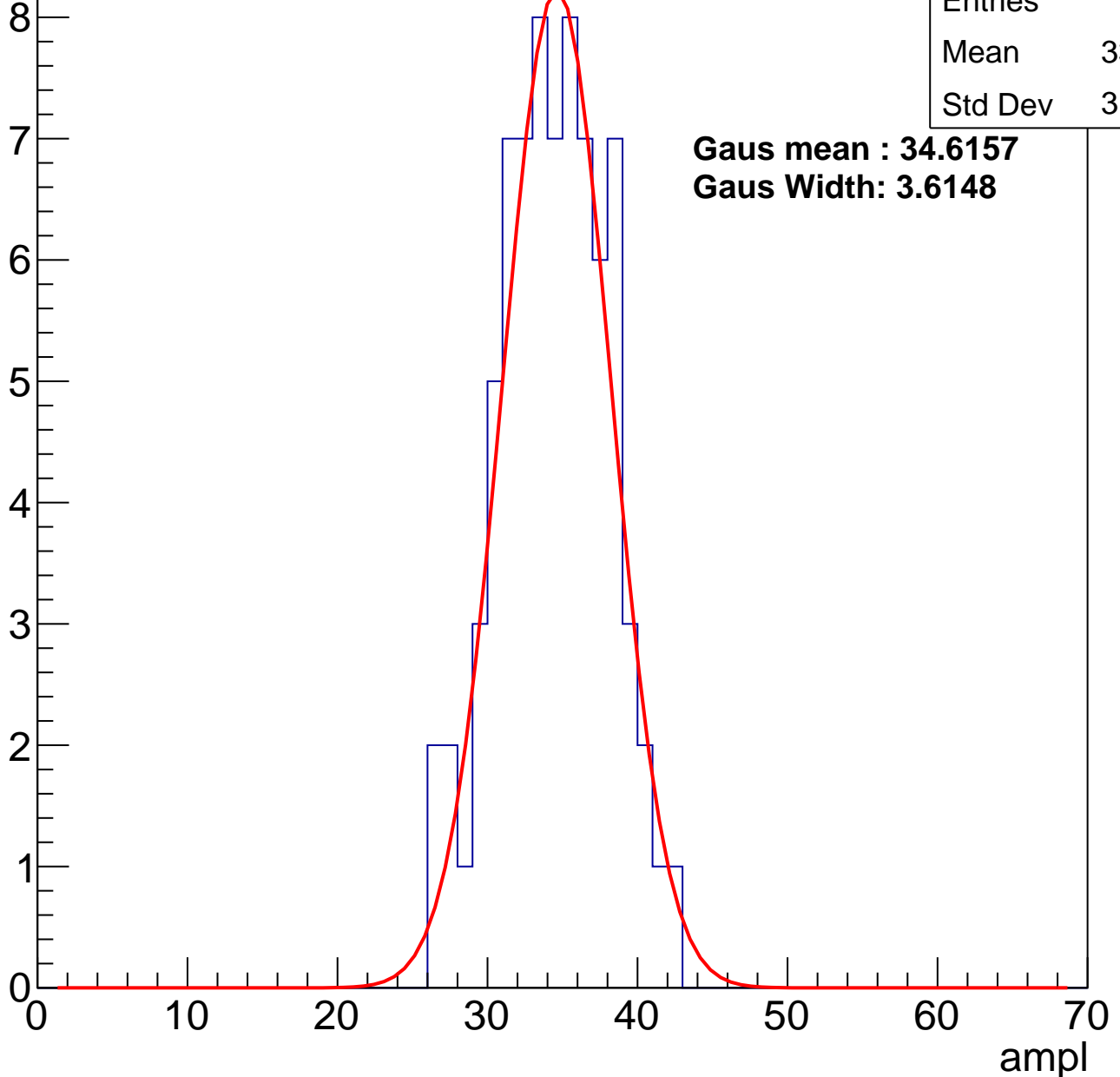
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	77
Mean	33.95
Std Dev	3.564

**Gaus mean : 34.6157**

**Gaus Width: 3.6148**



# B1L002S, U3-ch124, adc2

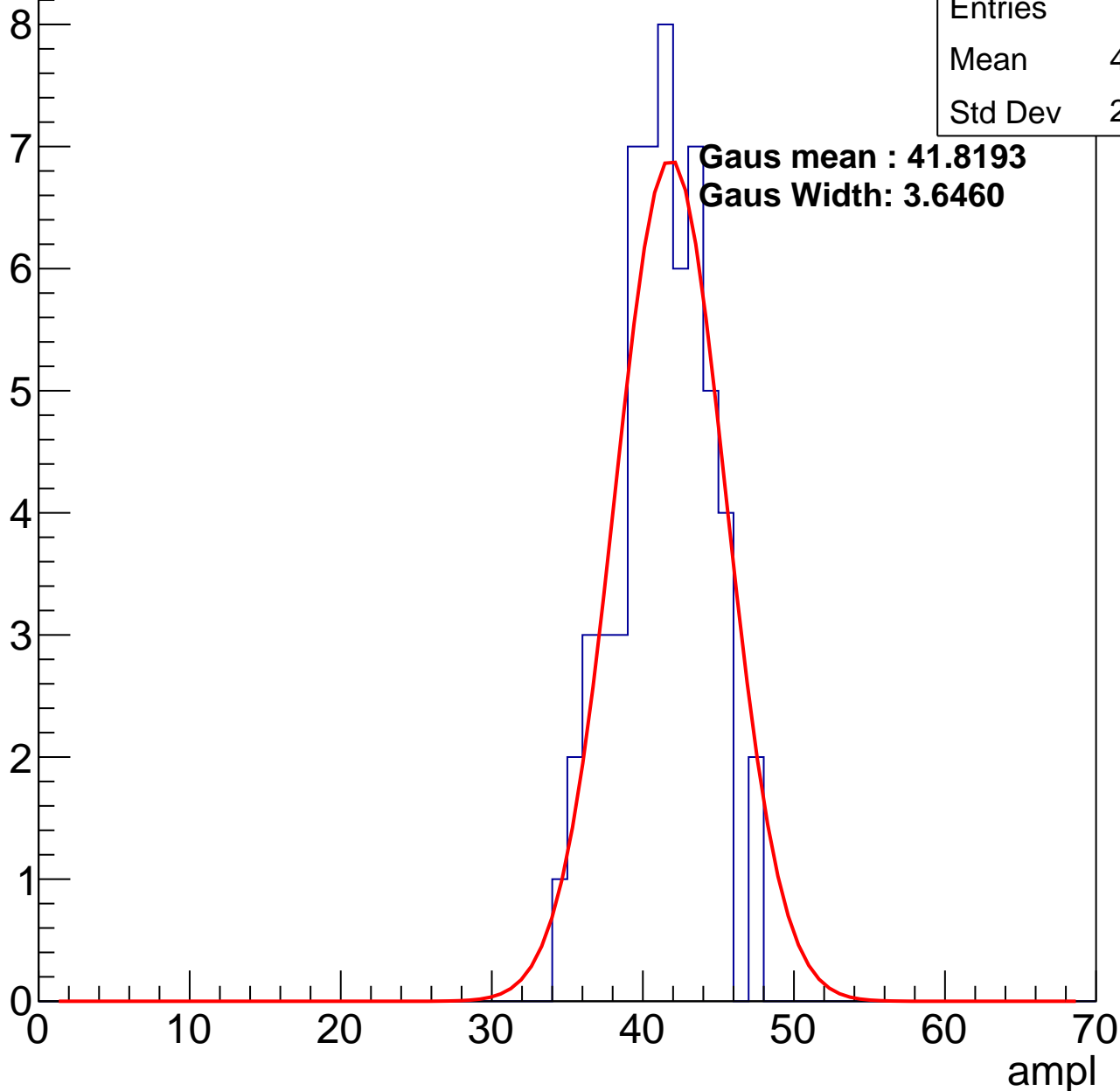
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	40.78
Std Dev	2.989

**Gaus mean : 41.8193**

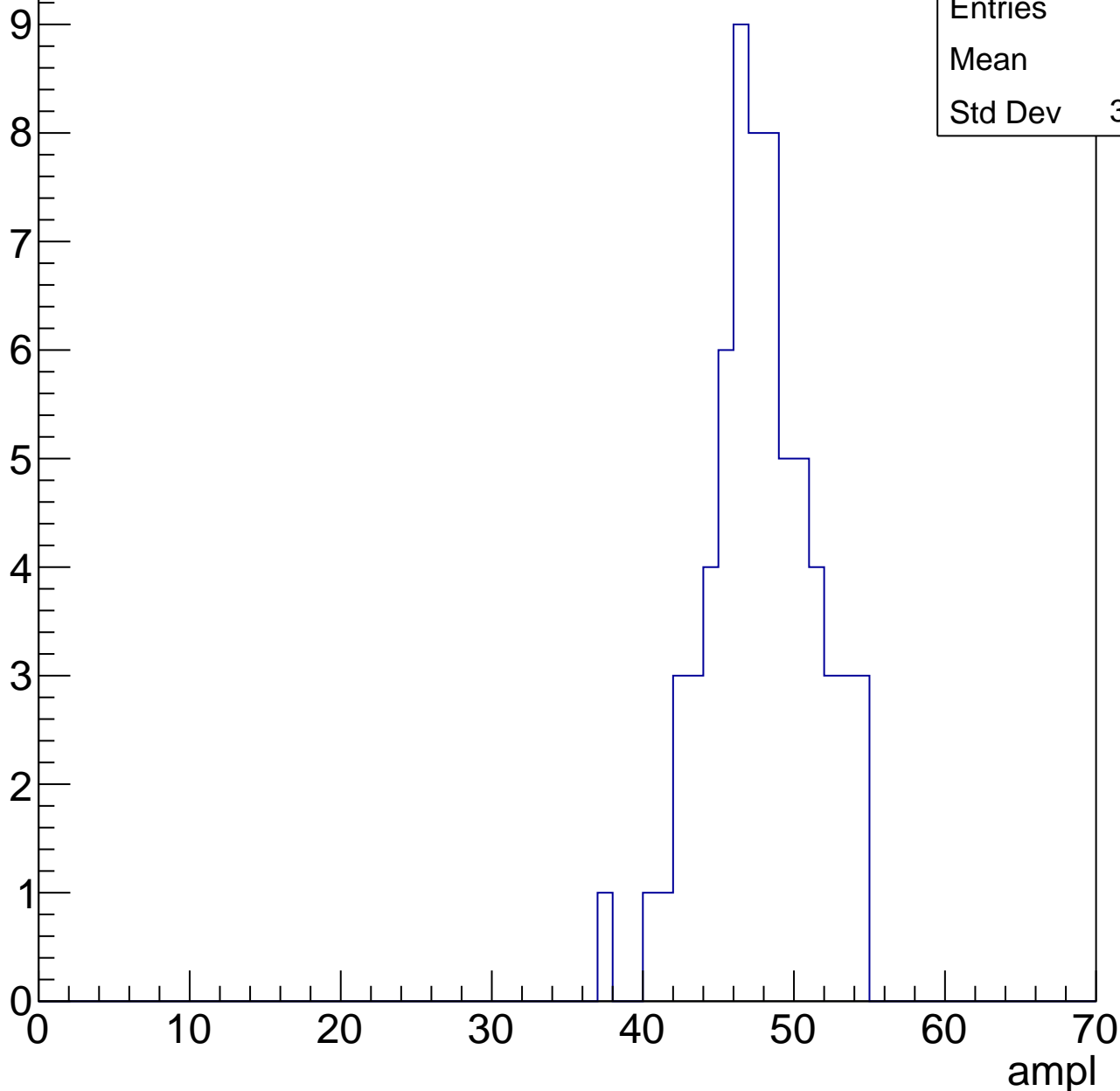
**Gaus Width: 3.6460**



# B1L002S, U3-ch124, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



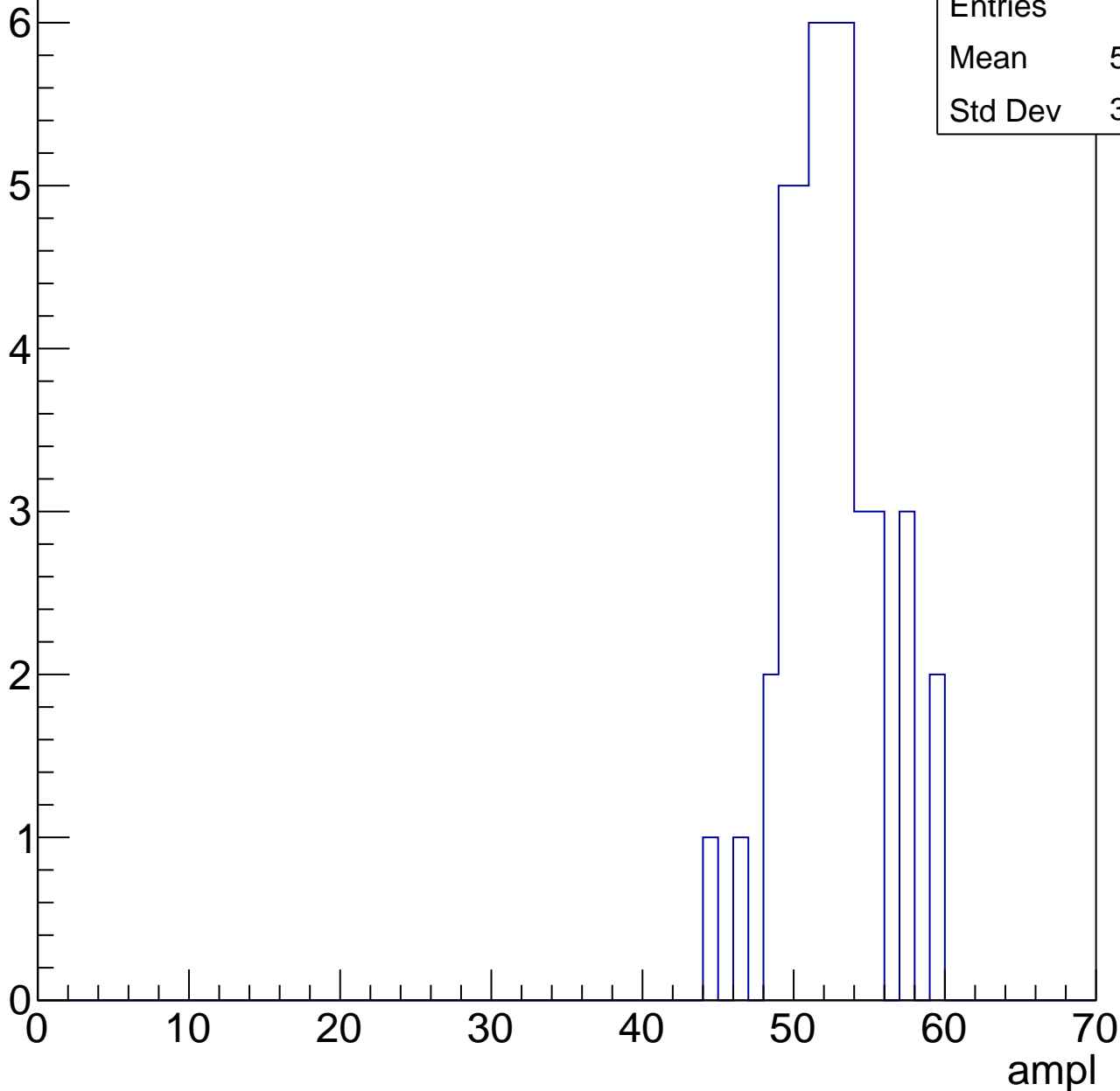
Entries	67
Mean	47.3
Std Dev	3.537

# B1L002S, U3-ch124, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	43
Mean	51.93
Std Dev	3.128

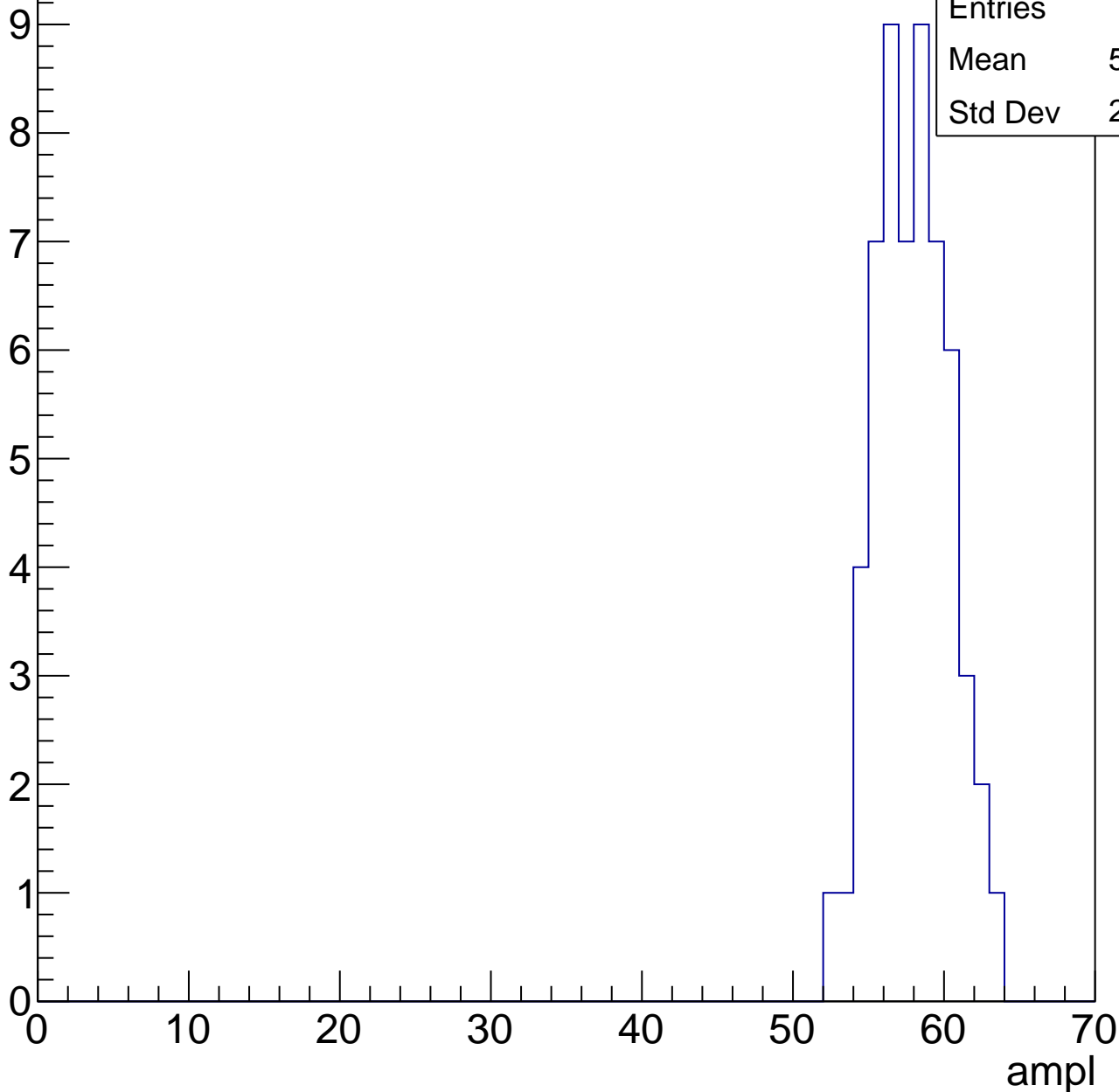


# B1L002S, U3-ch124, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	57
Mean	57.44
Std Dev	2.399

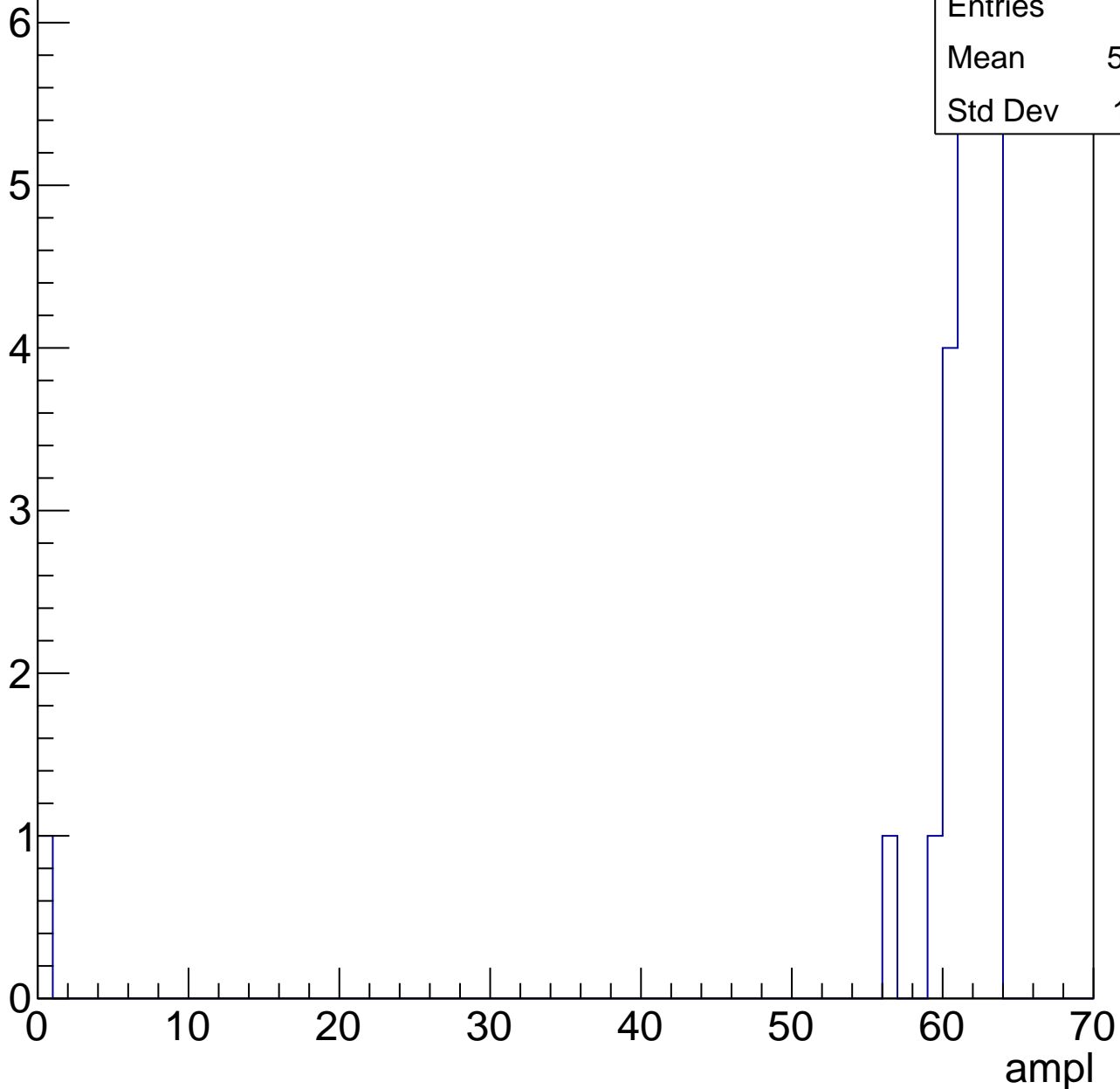


# B1L002S, U3-ch124, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	25
Mean	58.84
Std Dev	12.11

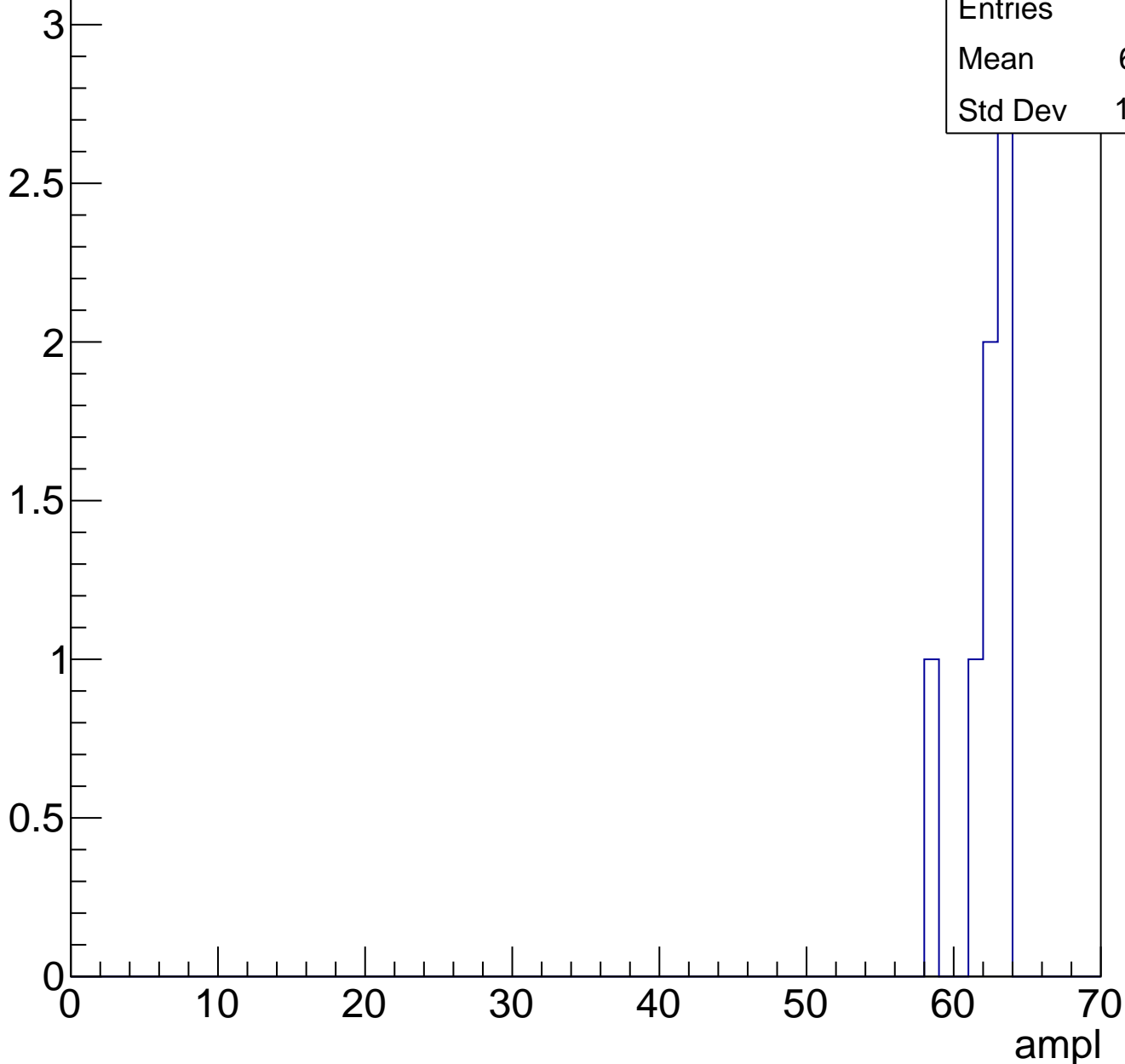




# B1L002S, U3-ch124, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch125, adc0

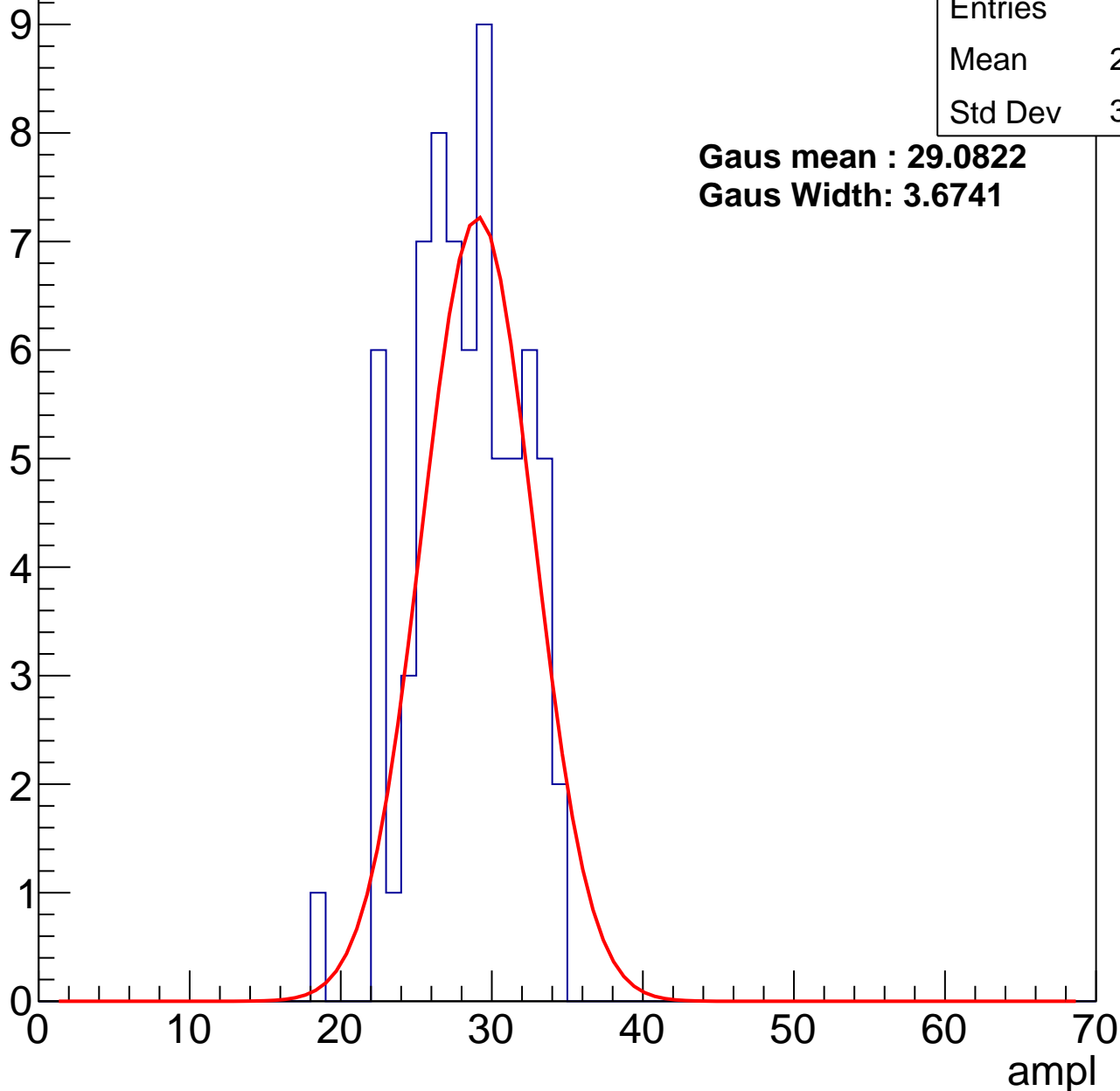
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	27.83
Std Dev	3.472

**Gaus mean : 29.0822**

**Gaus Width: 3.6741**



# B1L002S, U3-ch125, adc1

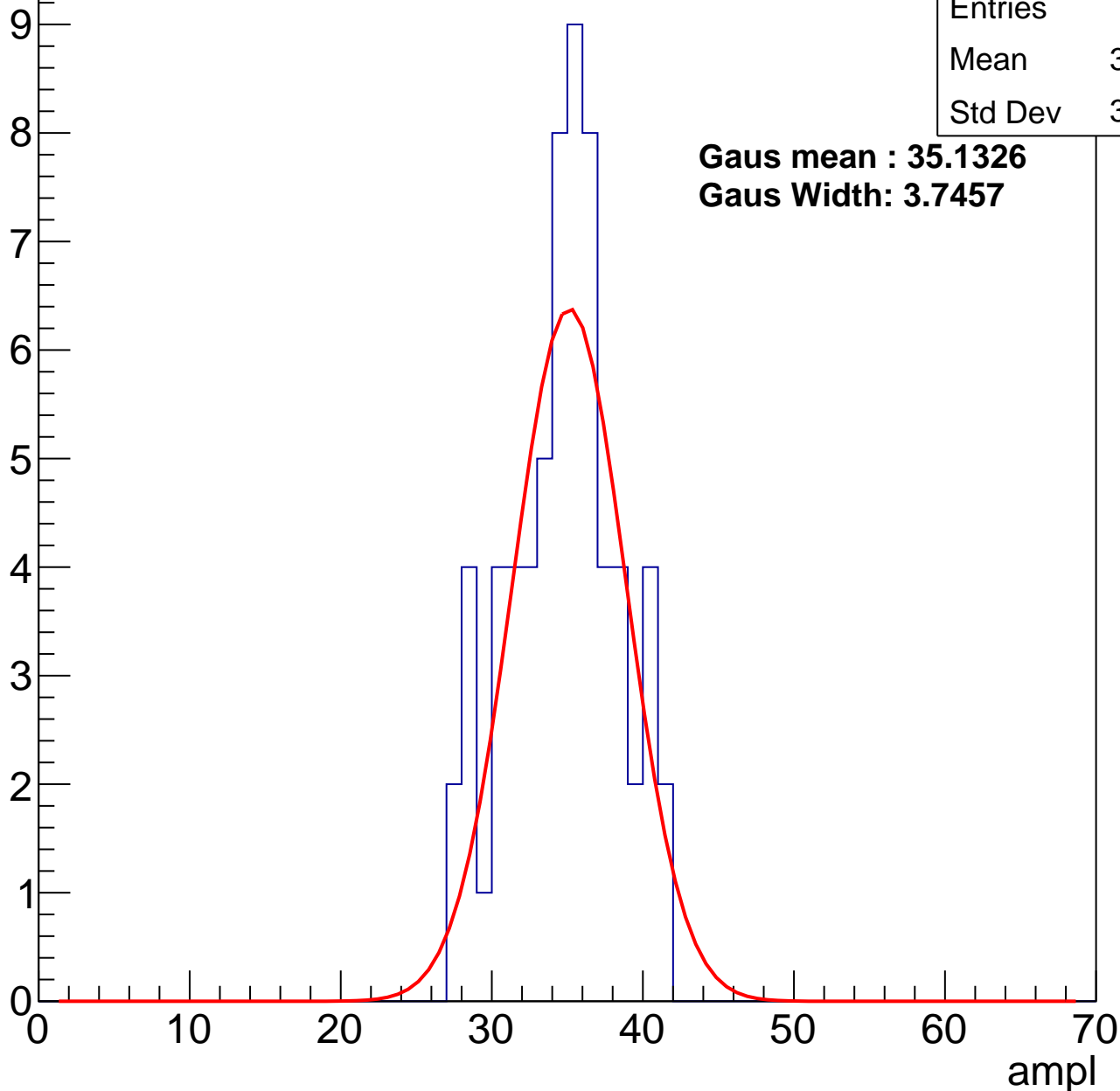
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	34.26
Std Dev	3.544

**Gaus mean : 35.1326**

**Gaus Width: 3.7457**



# B1L002S, U3-ch125, adc2

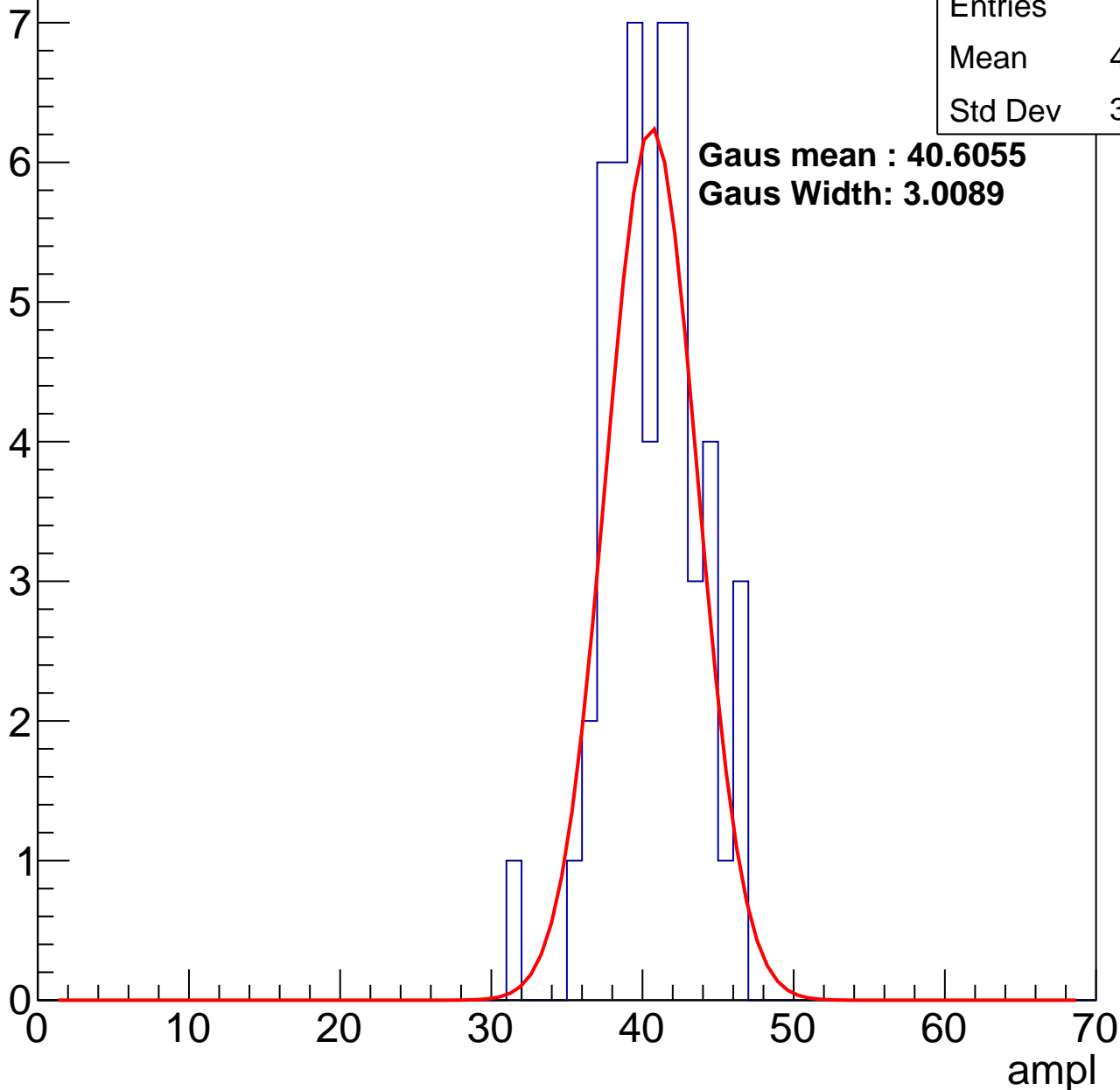
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	52
Mean	40.19
Std Dev	3.038

**Gaus mean : 40.6055**

**Gaus Width: 3.0089**

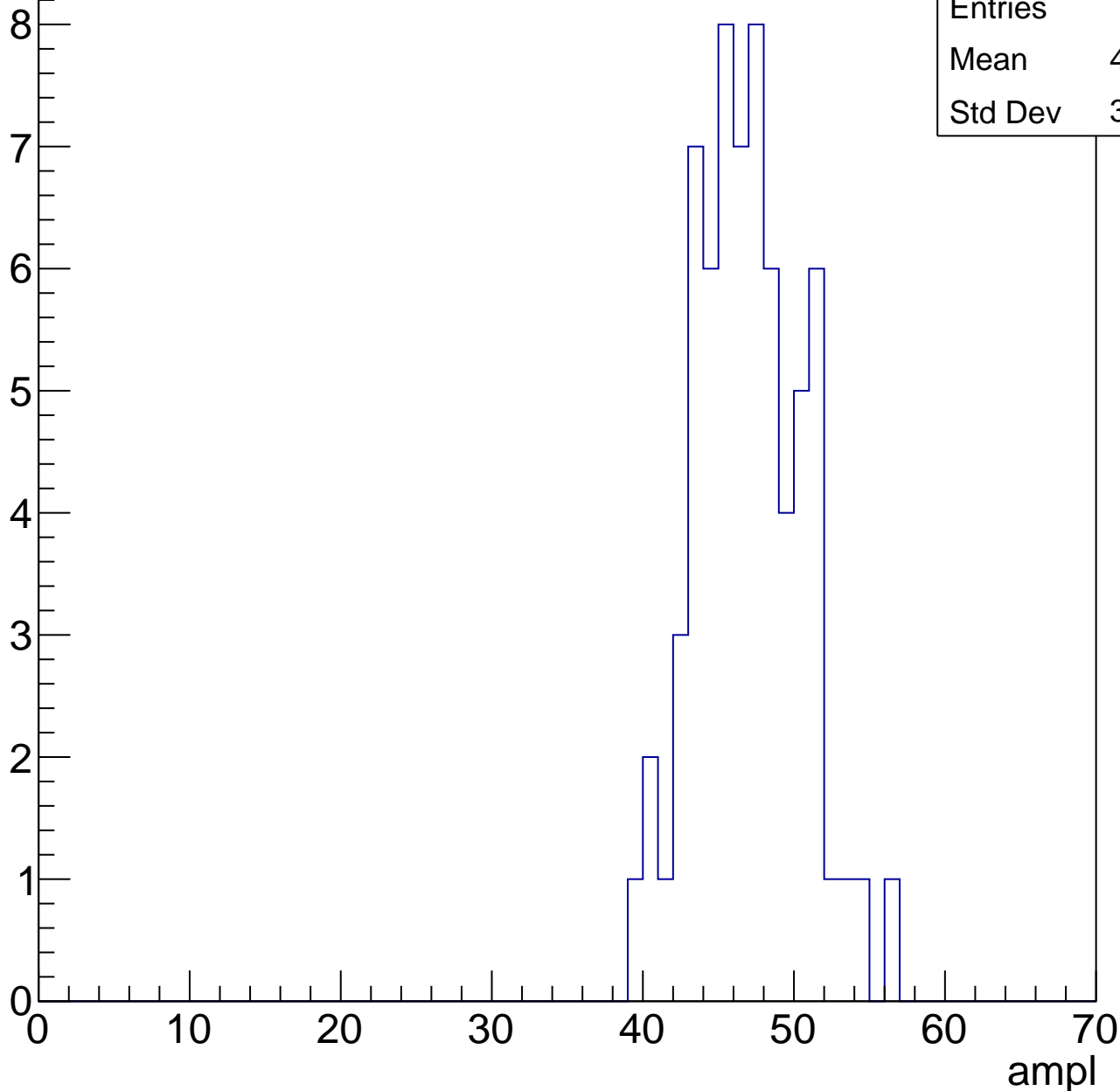


# B1L002S, U3-ch125, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	46.53
Std Dev	3.466

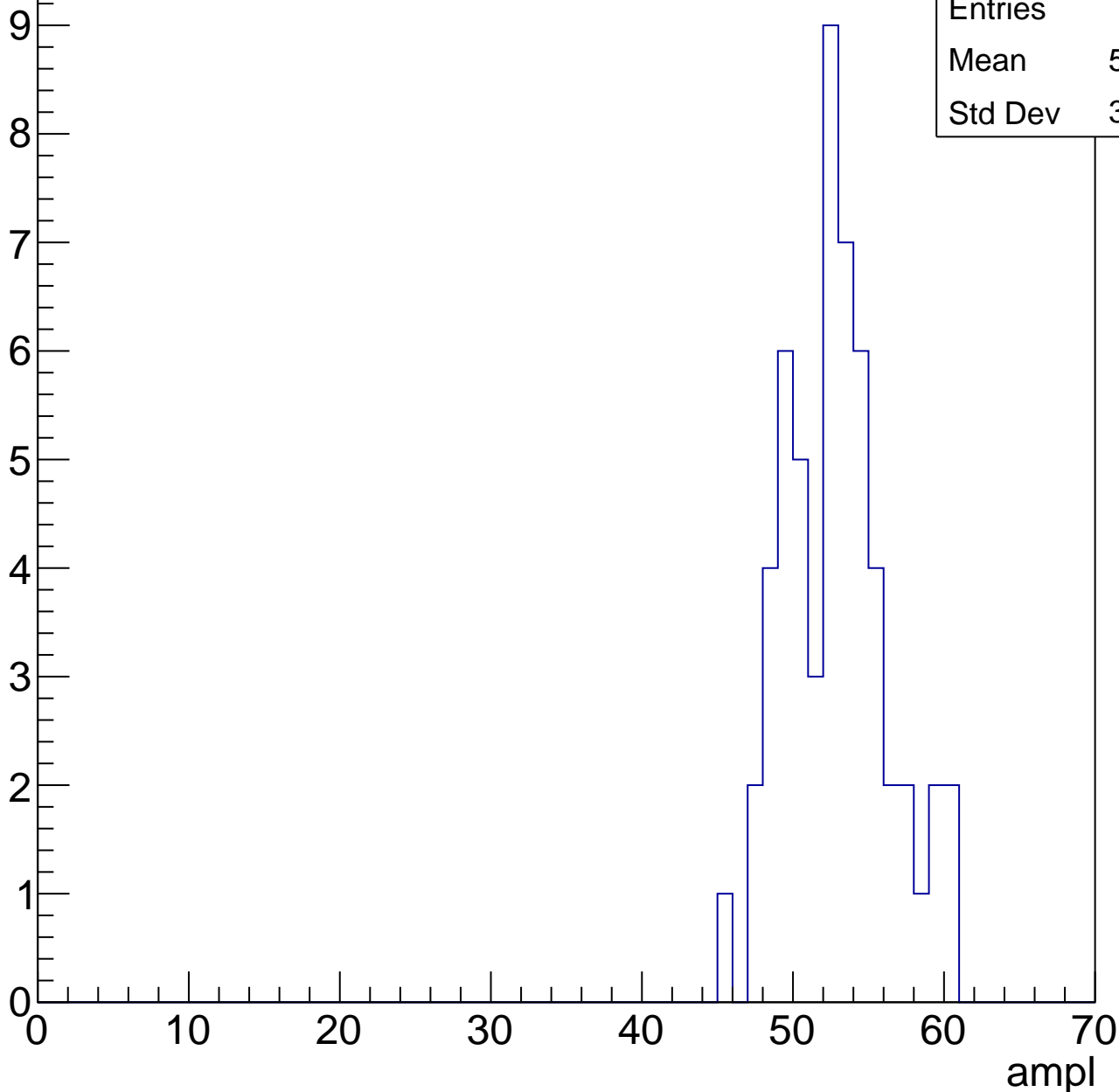


# B1L002S, U3-ch125, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	52.38
Std Dev	3.389

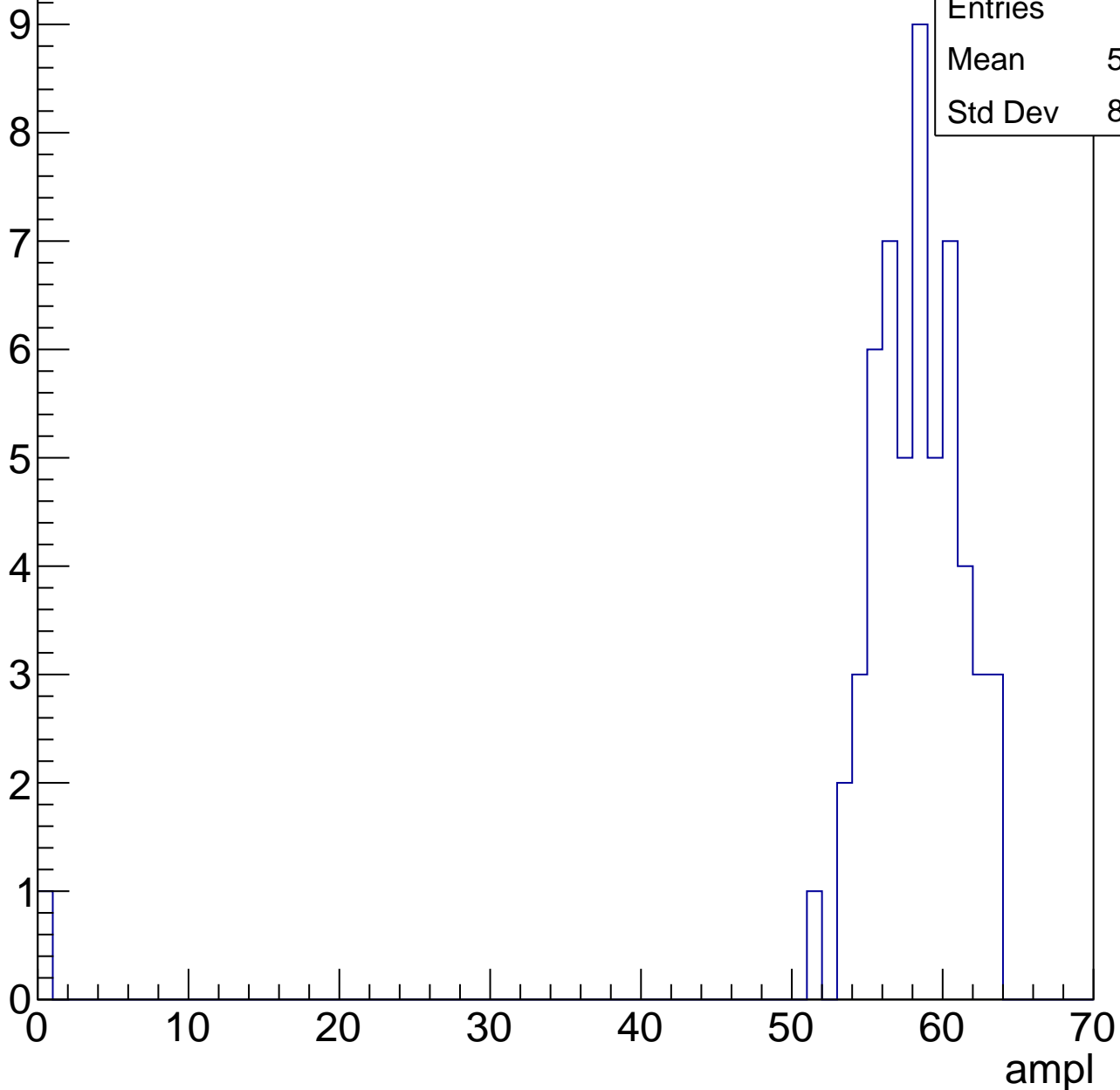


# B1L002S, U3-ch125, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	56
Mean	56.82
Std Dev	8.142

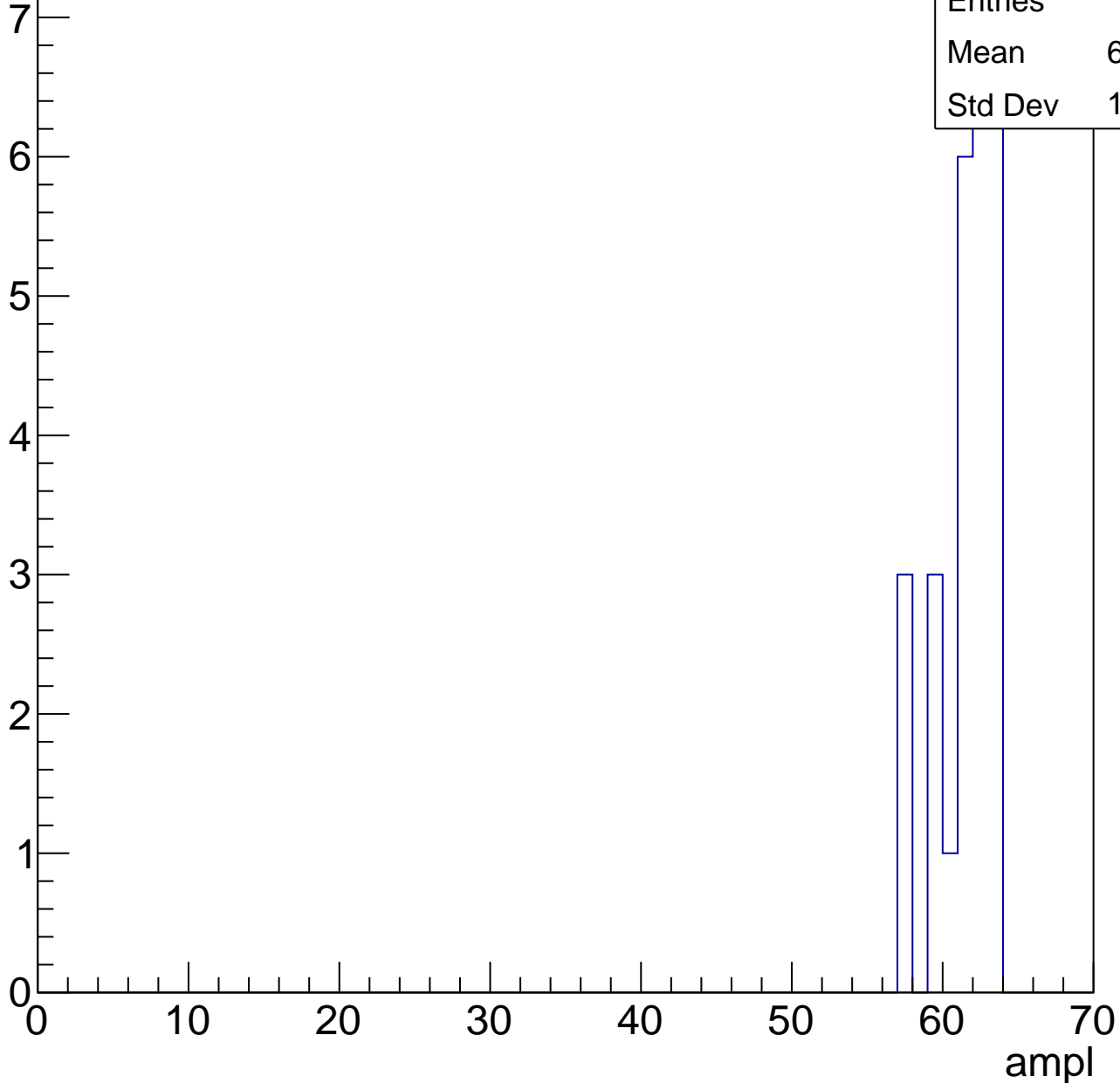


# B1L002S, U3-ch125, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	27
Mean	61.07
Std Dev	1.884





# B1L002S, U3-ch125, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch126, adc0

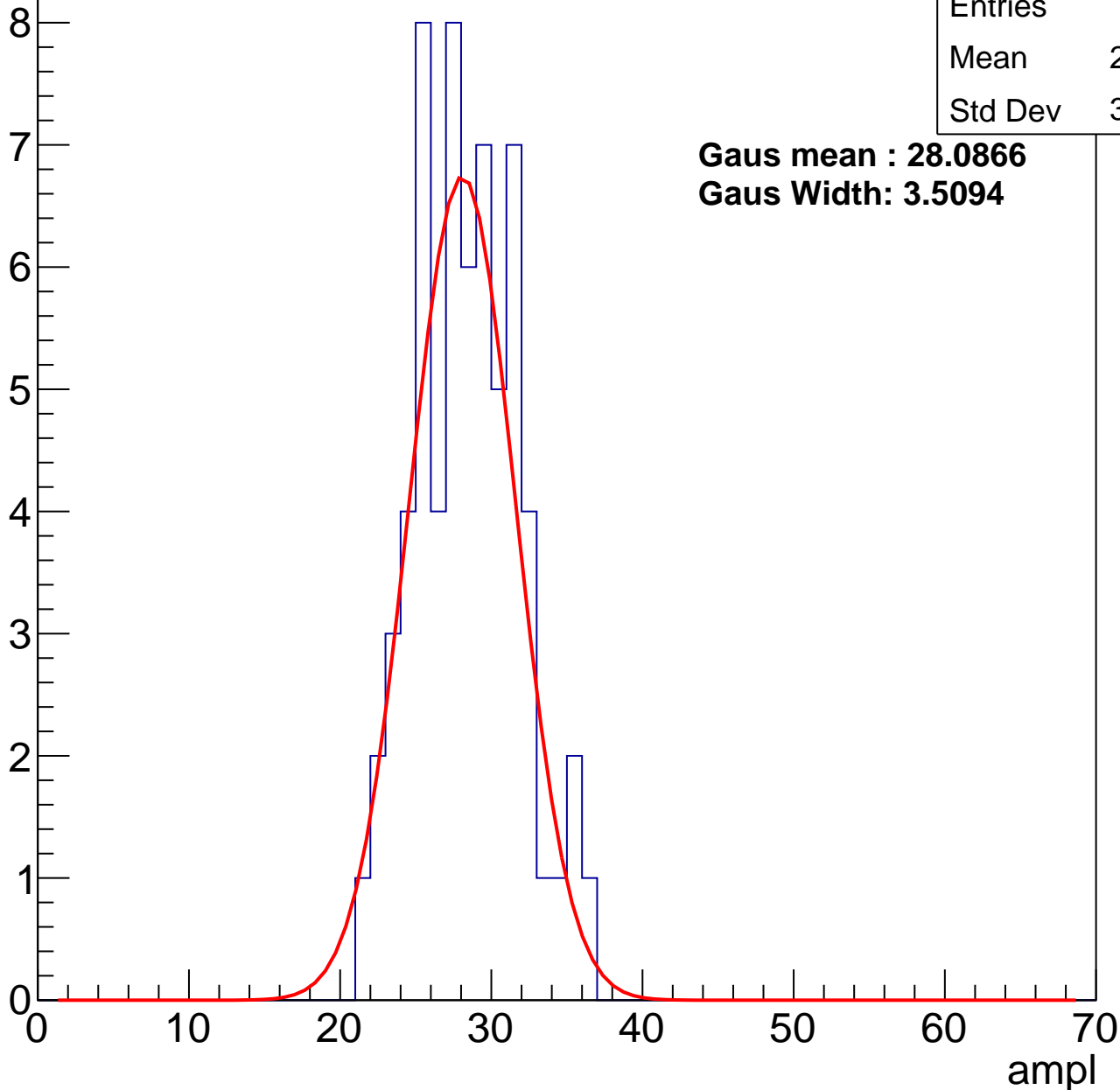
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	64
Mean	27.95
Std Dev	3.384

**Gaus mean : 28.0866**

**Gaus Width: 3.5094**



# B1L002S, U3-ch126, adc1

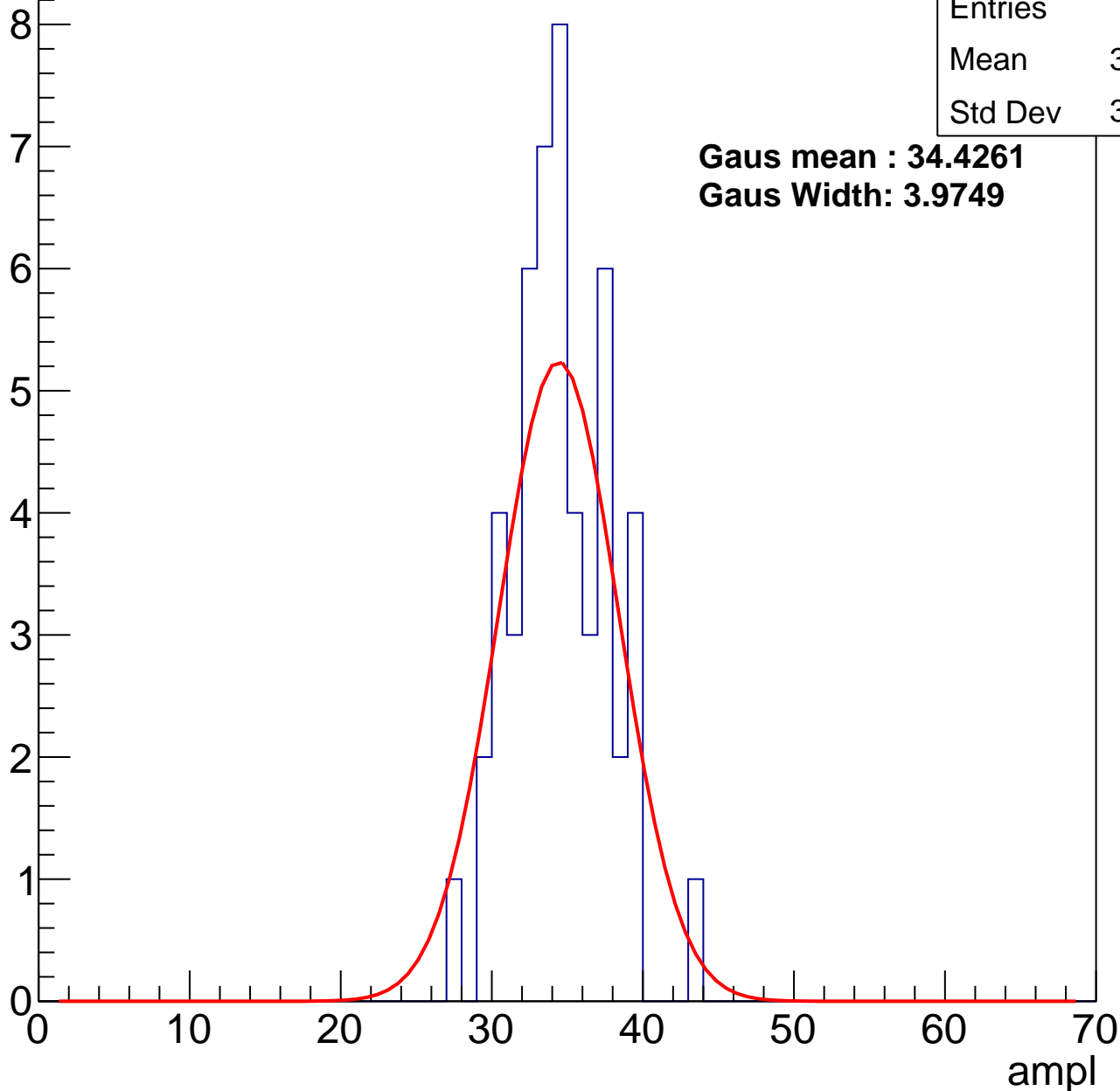
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	51
Mean	34.08
Std Dev	3.143

**Gaus mean : 34.4261**

**Gaus Width: 3.9749**



# B1L002S, U3-ch126, adc2

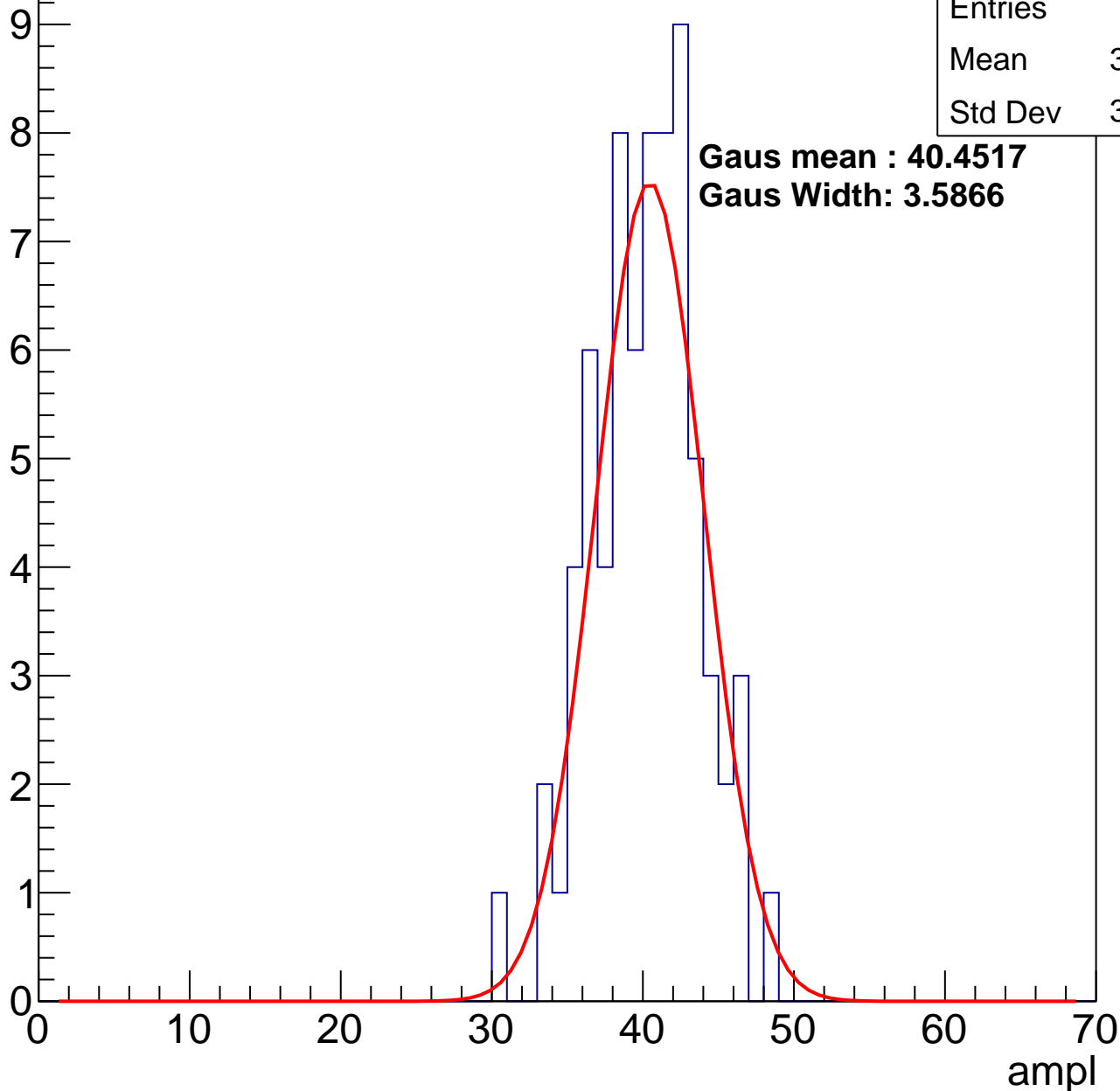
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	71
Mean	39.73
Std Dev	3.476

**Gaus mean : 40.4517**

**Gaus Width: 3.5866**

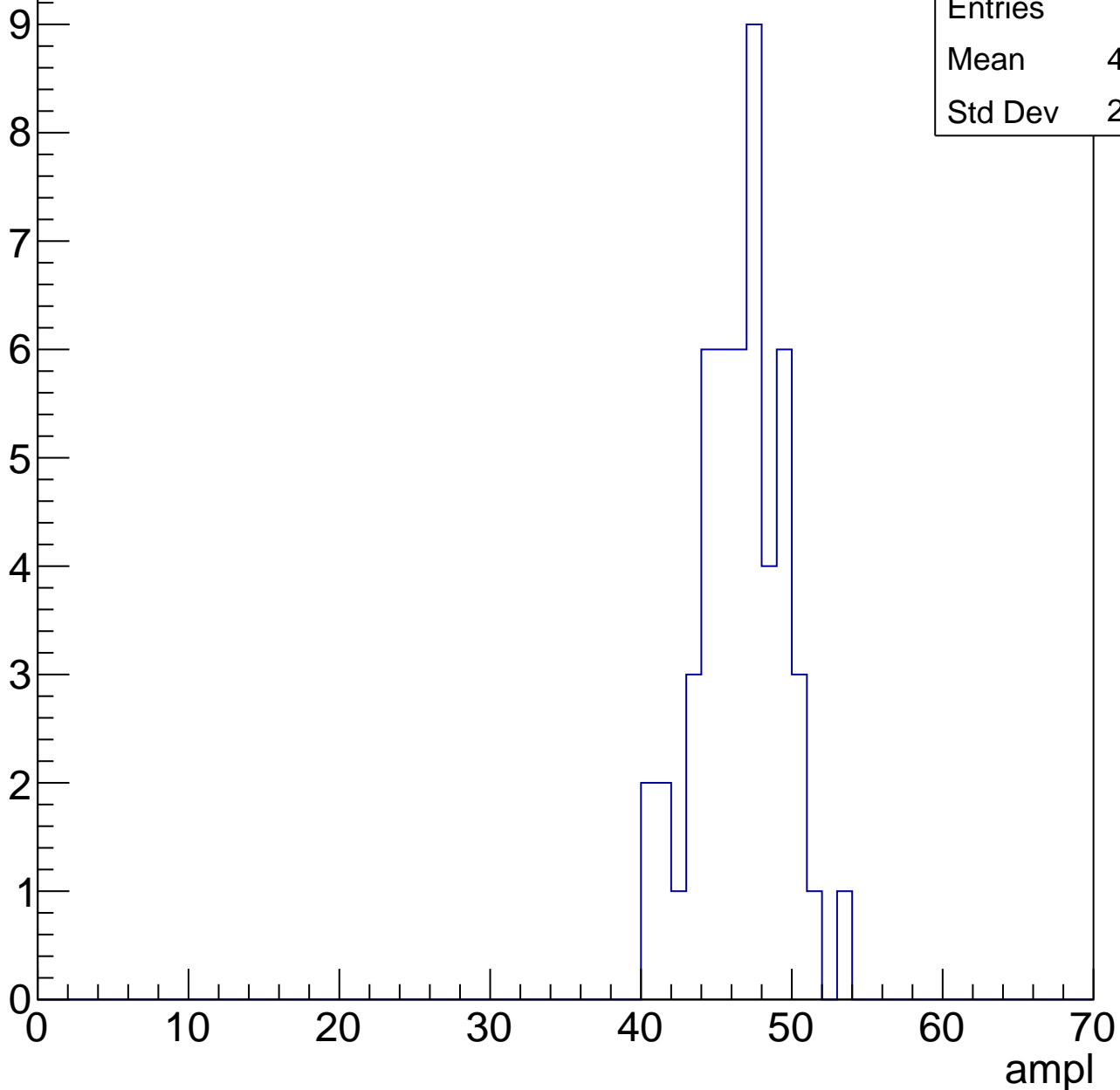


# B1L002S, U3-ch126, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	50
Mean	46.12
Std Dev	2.812

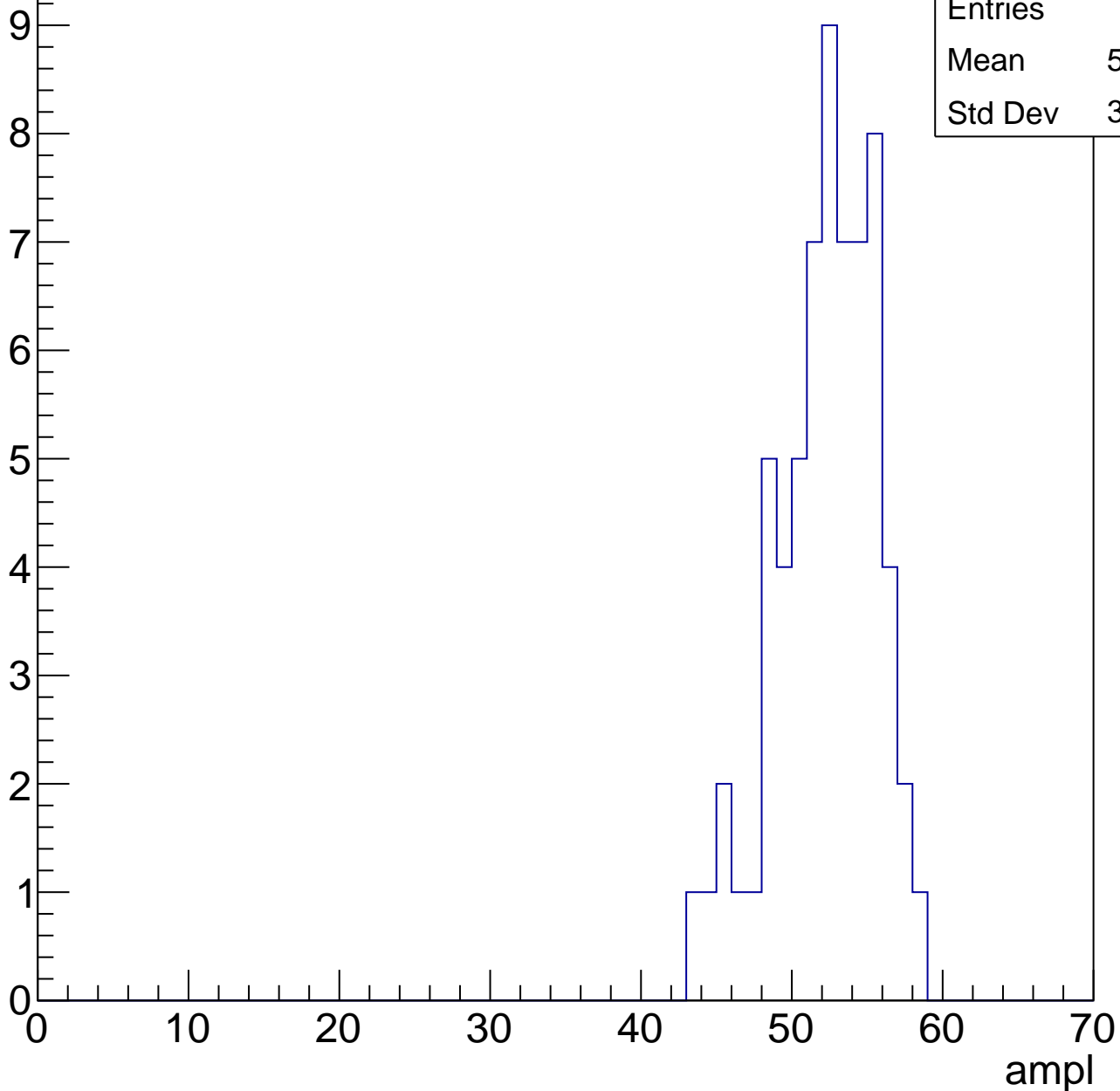


# B1L002S, U3-ch126, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	65
Mean	51.78
Std Dev	3.284

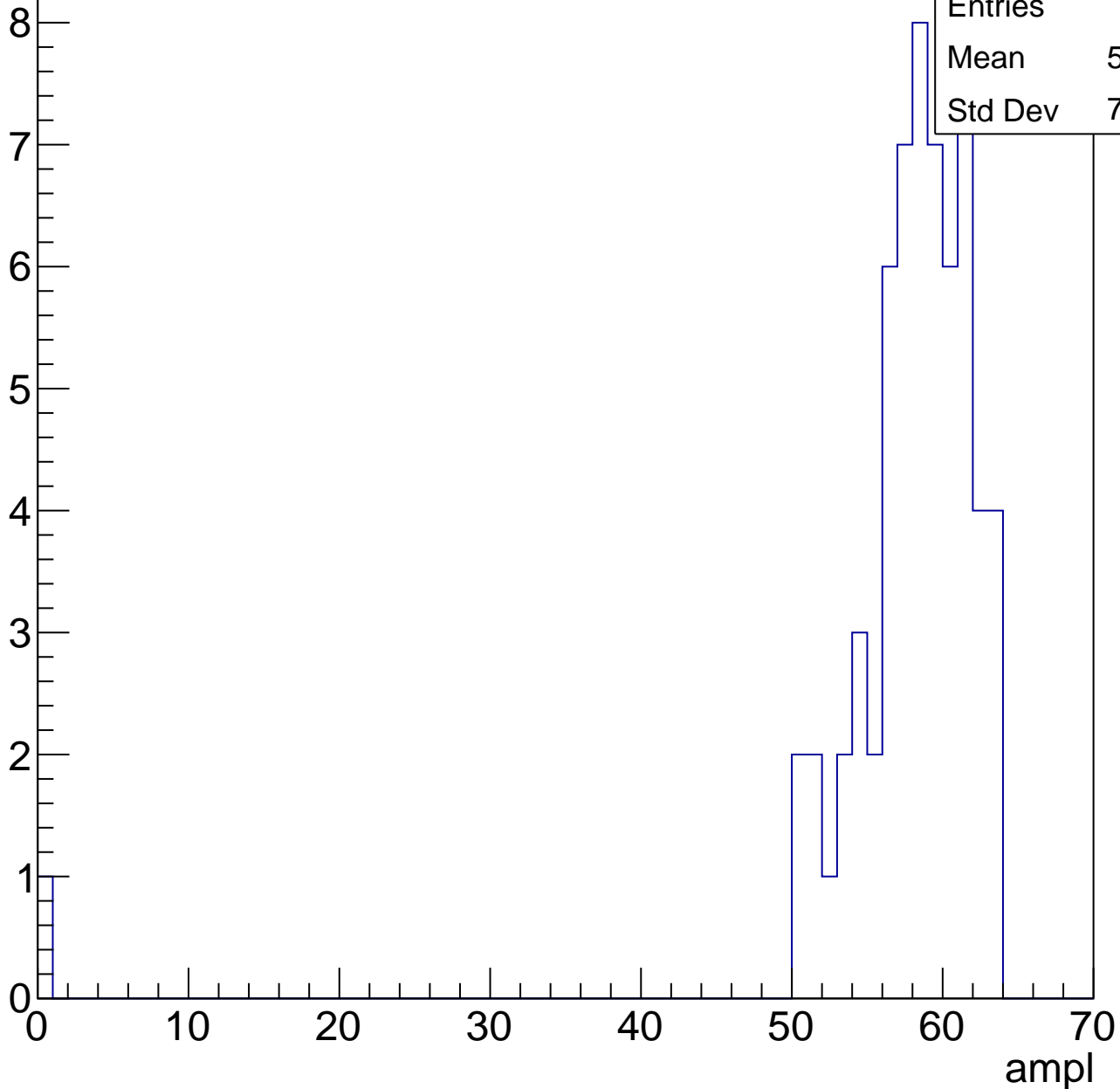


# B1L002S, U3-ch126, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	63
Mean	57.02
Std Dev	7.947

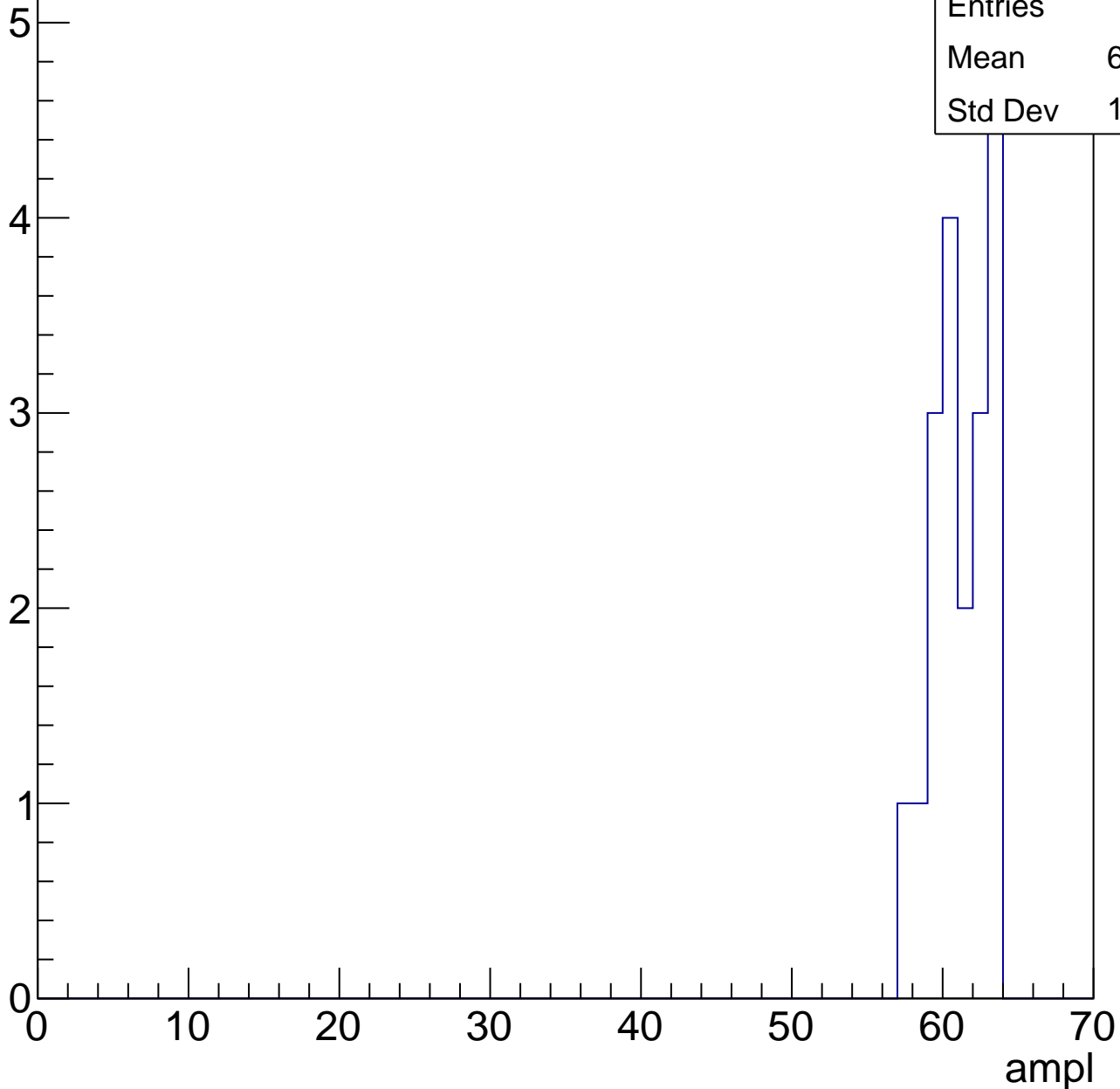


# B1L002S, U3-ch126, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	19
Mean	60.79
Std Dev	1.823

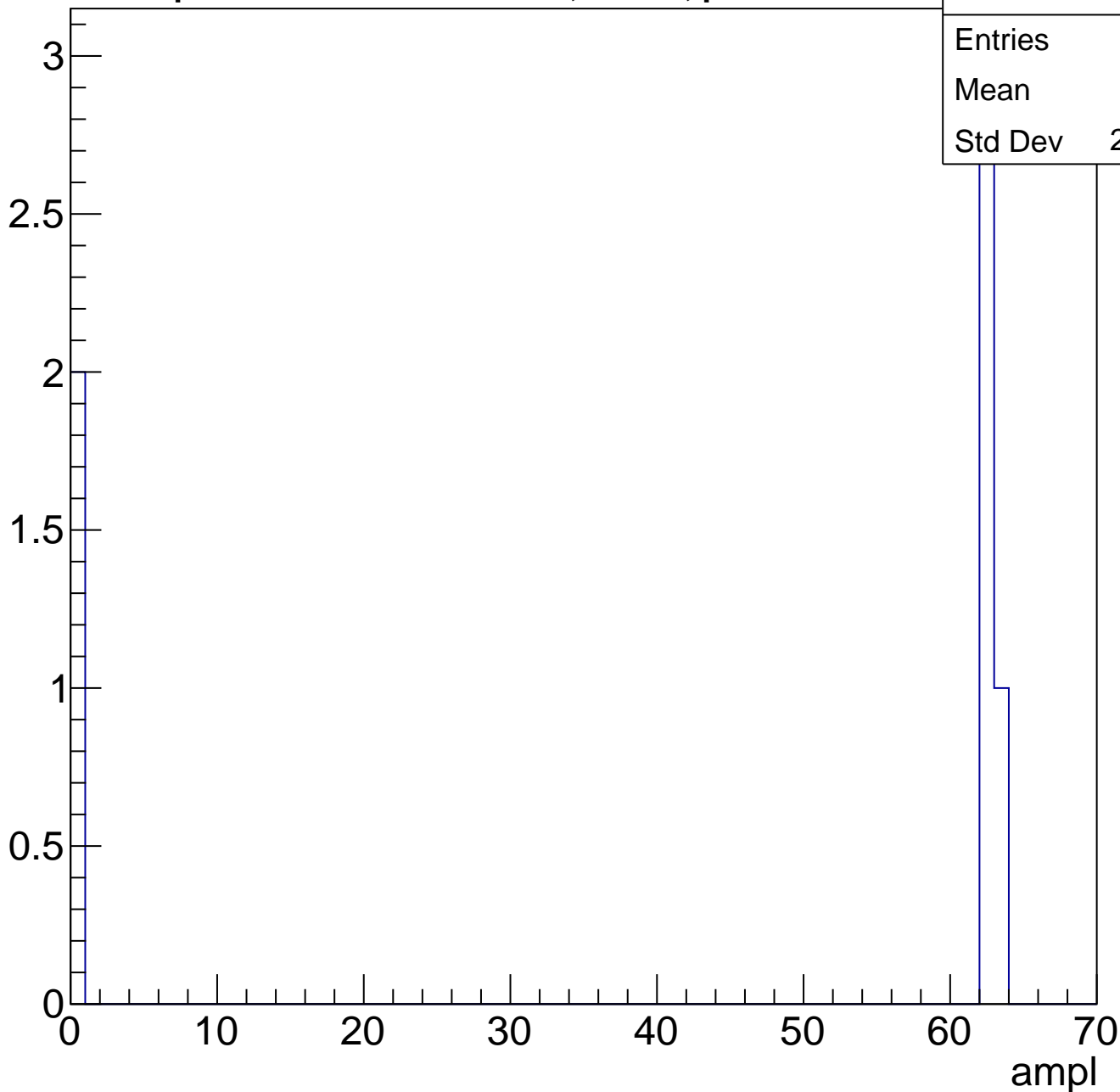




# B1L002S, U3-ch126, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch127, adc0

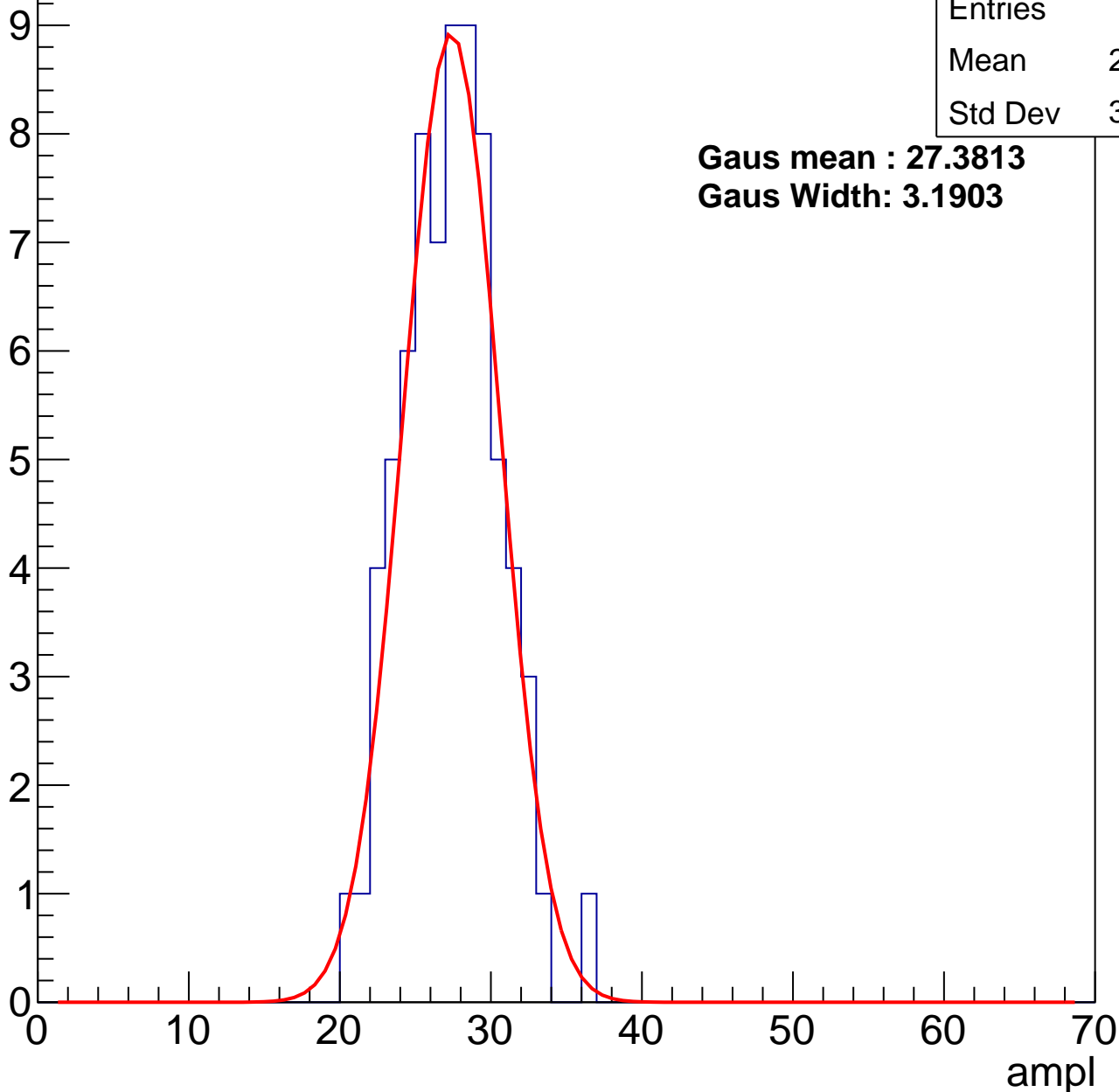
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	72
Mean	26.89
Std Dev	3.116

**Gaus mean : 27.3813**

**Gaus Width: 3.1903**



# B1L002S, U3-ch127, adc1

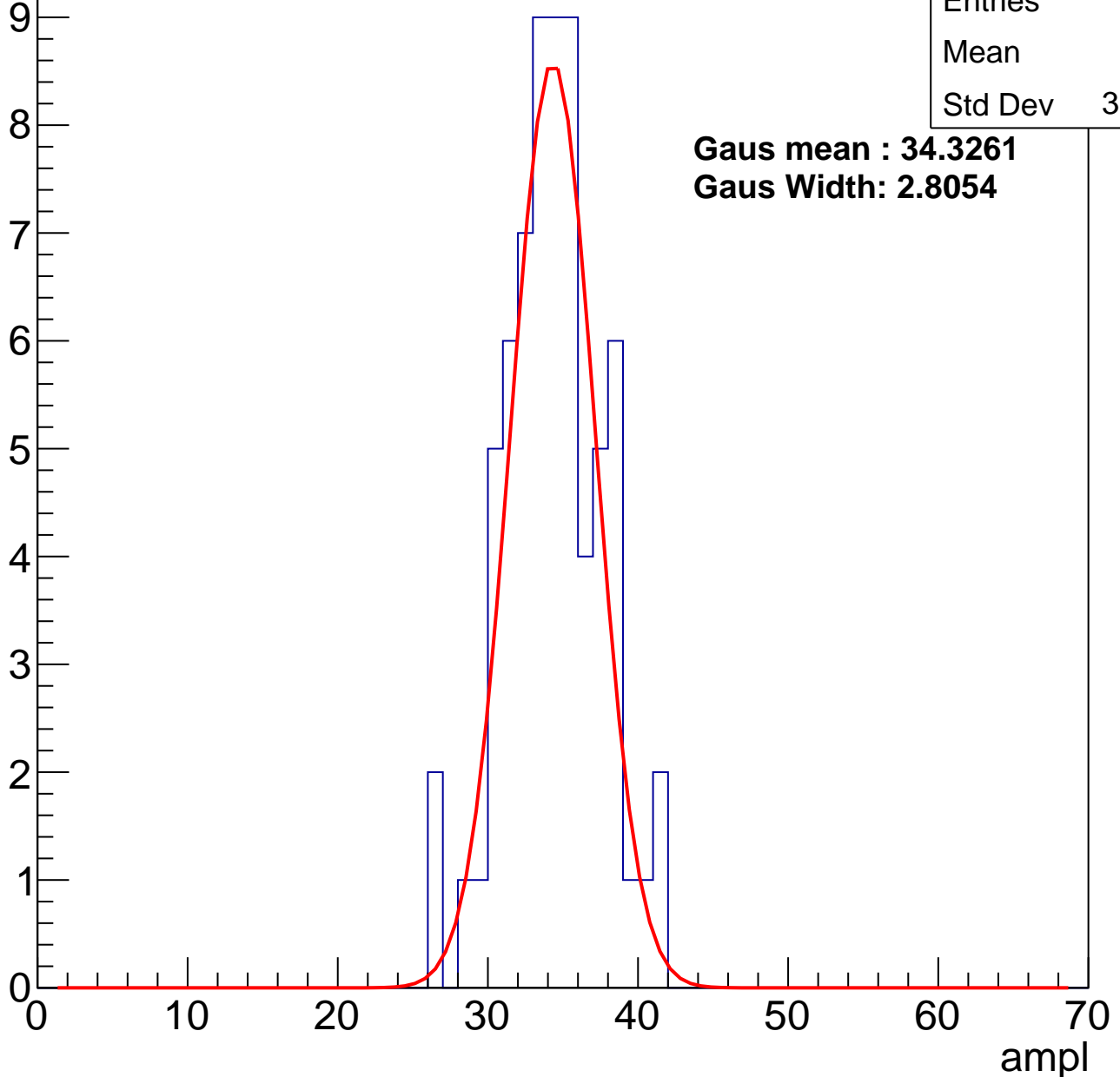
calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	68
Mean	33.9
Std Dev	3.172

**Gaus mean : 34.3261**

**Gaus Width: 2.8054**



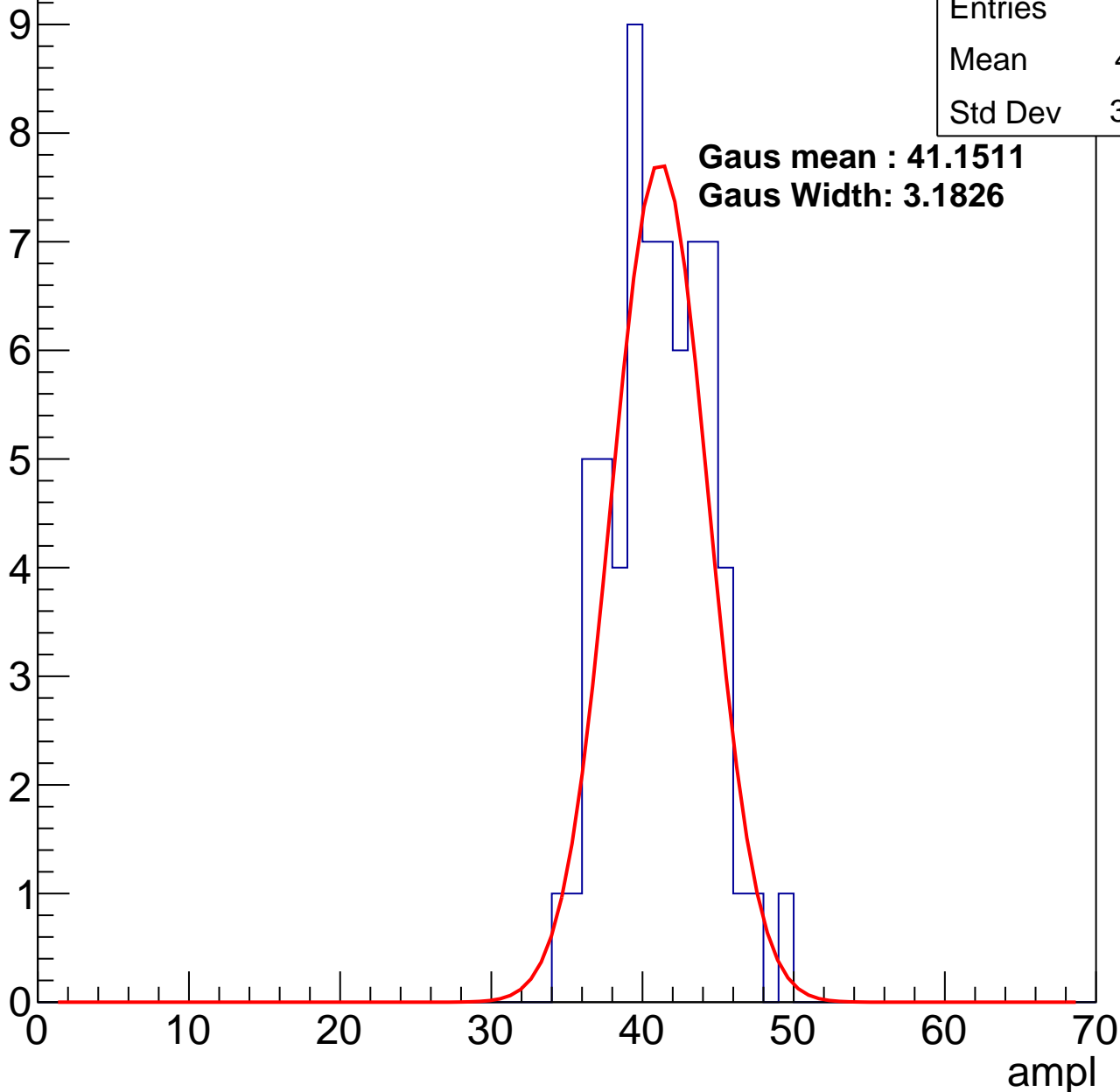
# B1L002S, U3-ch127, adc2

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	66
Mean	40.71
Std Dev	3.137

**Gaus mean : 41.1511**  
**Gaus Width: 3.1826**

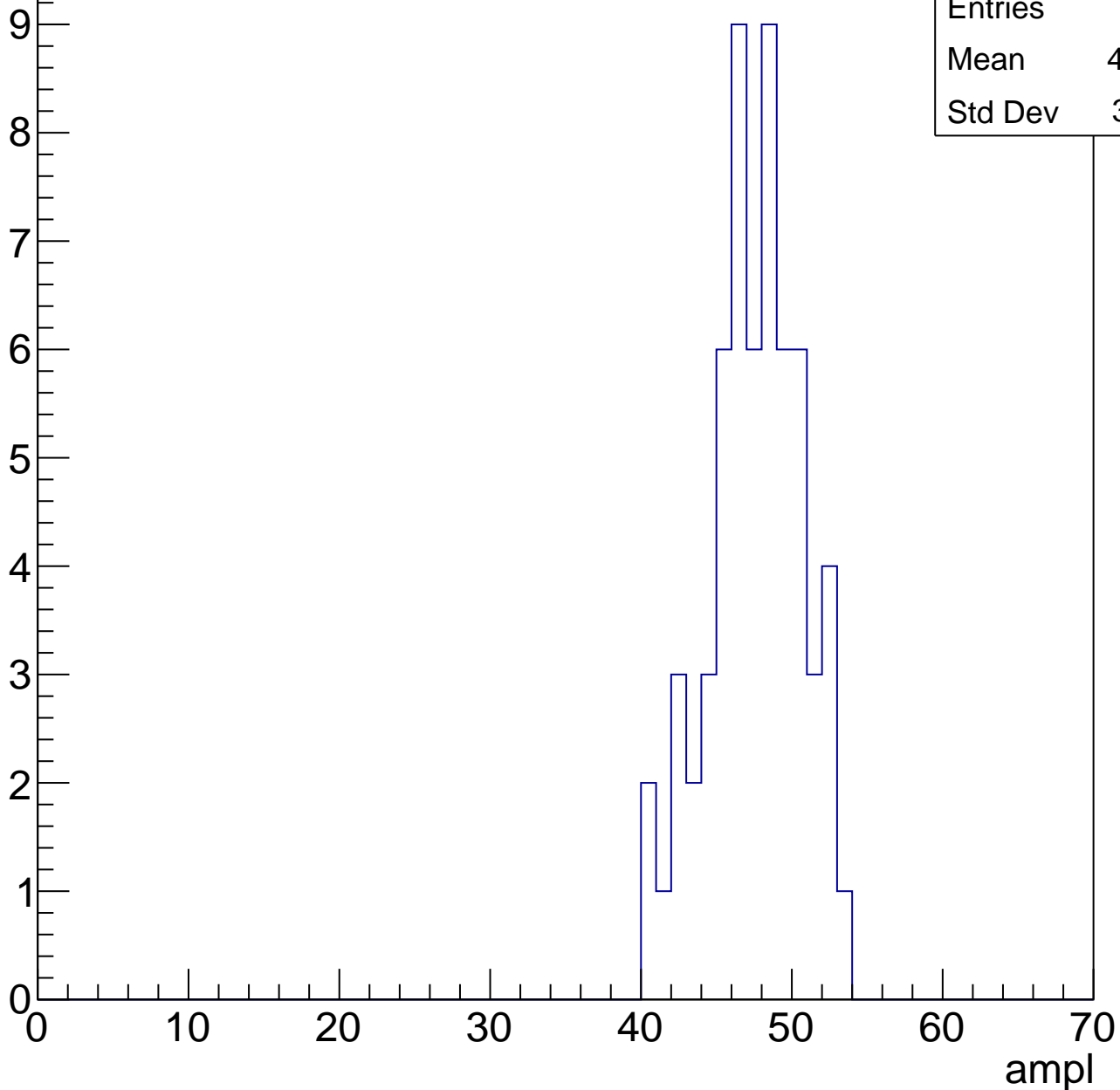


# B1L002S, U3-ch127, adc3

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	61
Mean	47.07
Std Dev	3.061

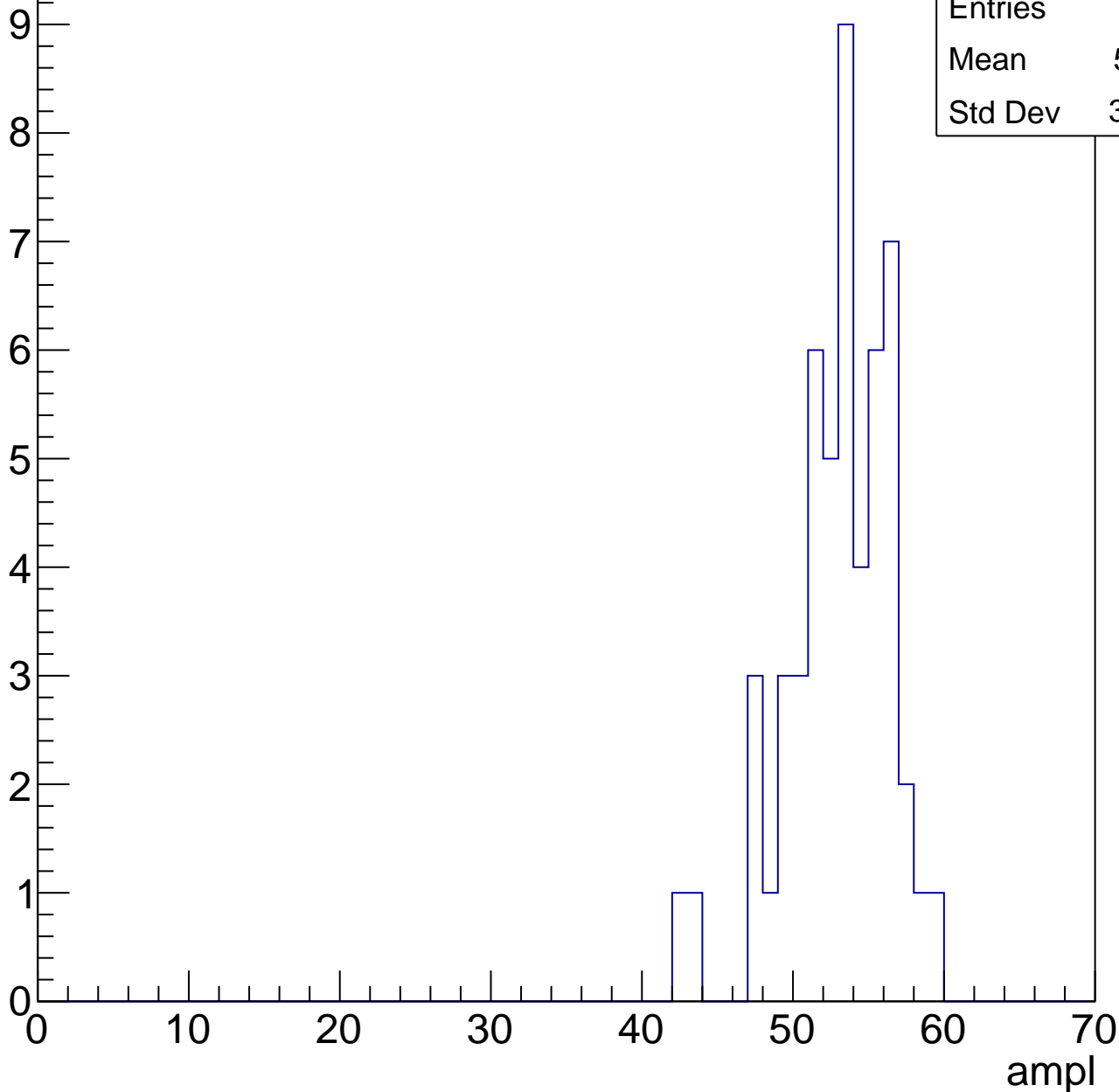


# B1L002S, U3-ch127, adc4

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	53
Mean	52.51
Std Dev	3.446

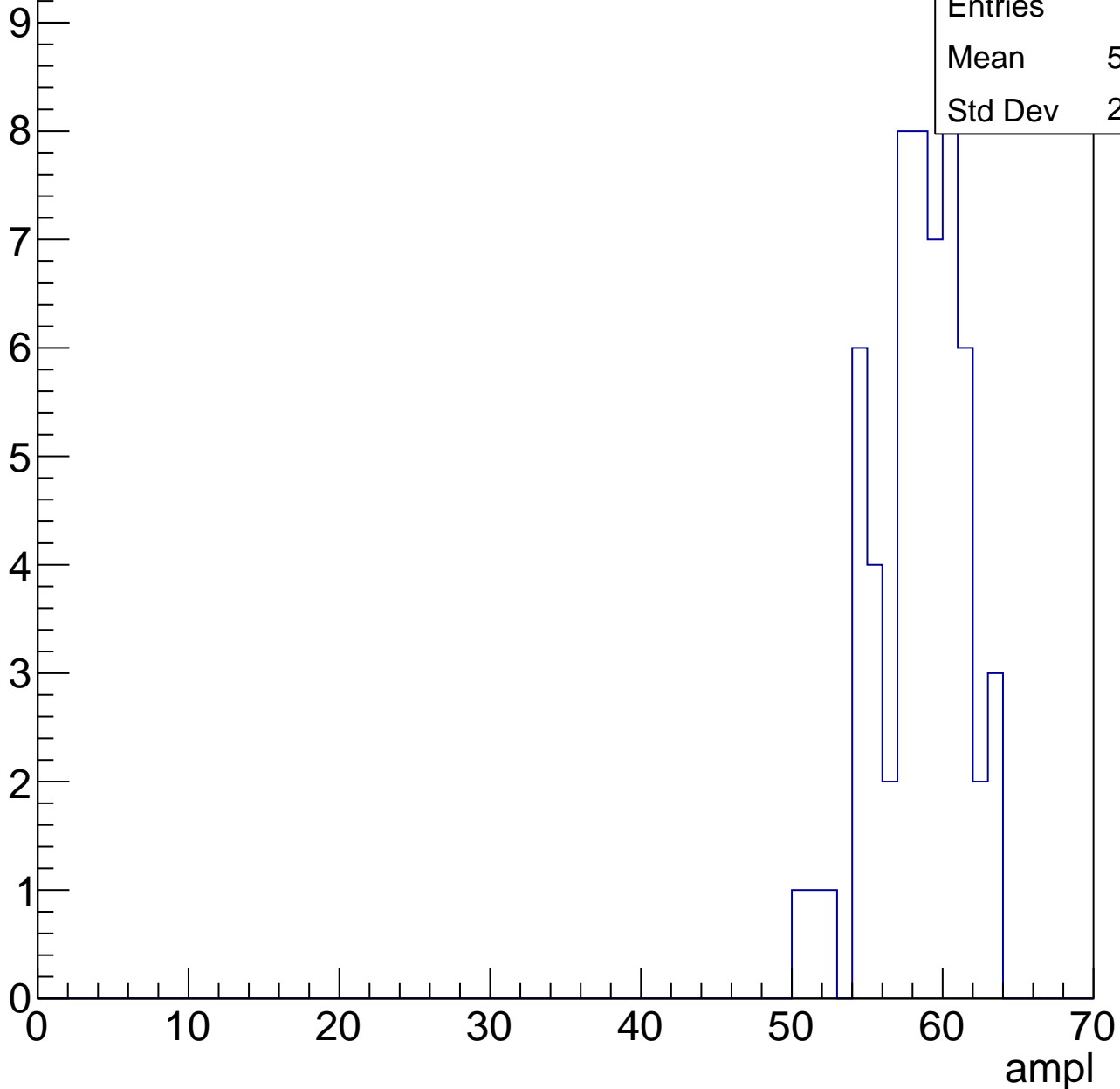


# B1L002S, U3-ch127, adc5

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	58
Mean	57.95
Std Dev	2.939

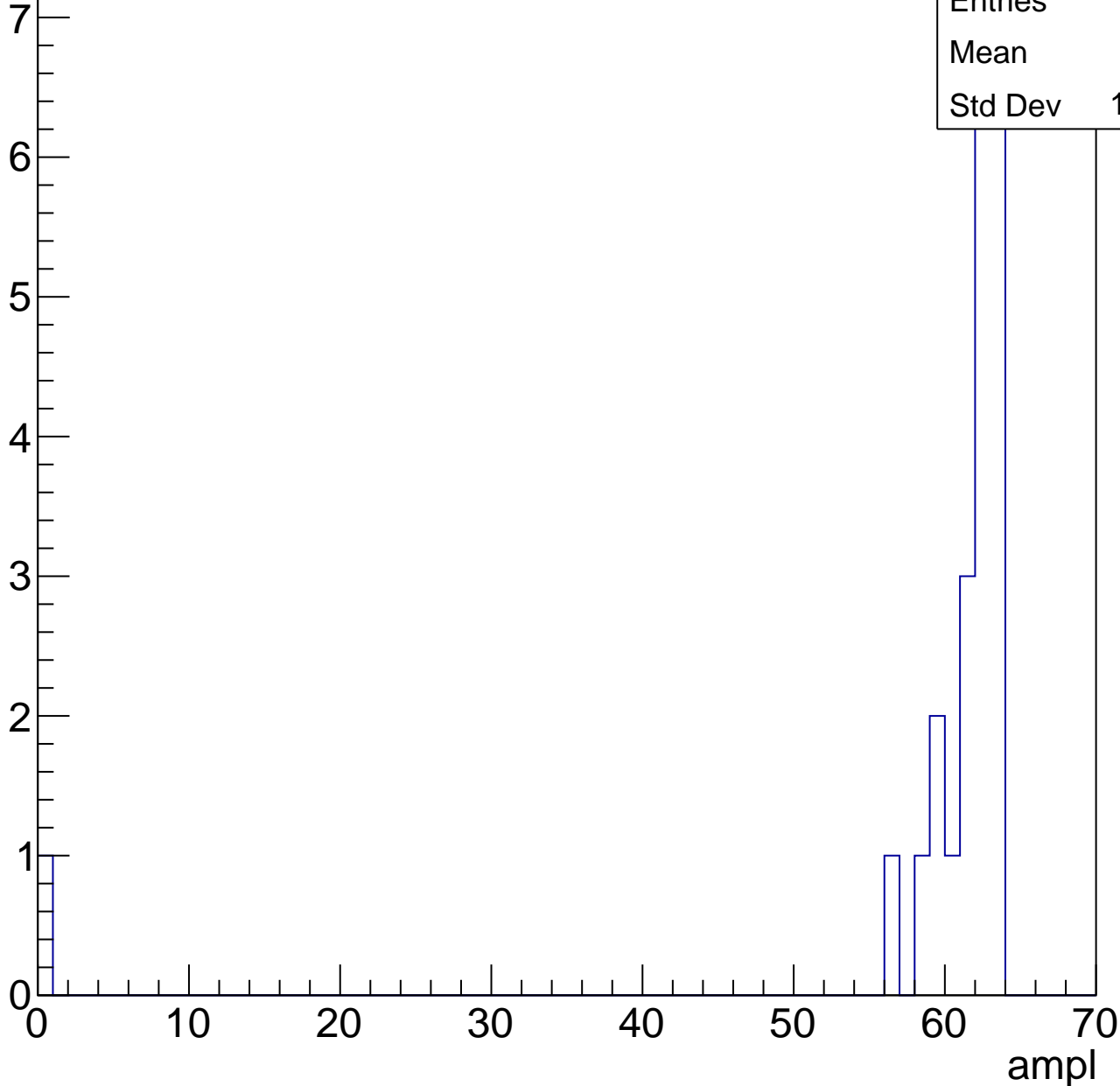


# B1L002S, U3-ch127, adc6

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

Entries	23
Mean	58.7
Std Dev	12.64

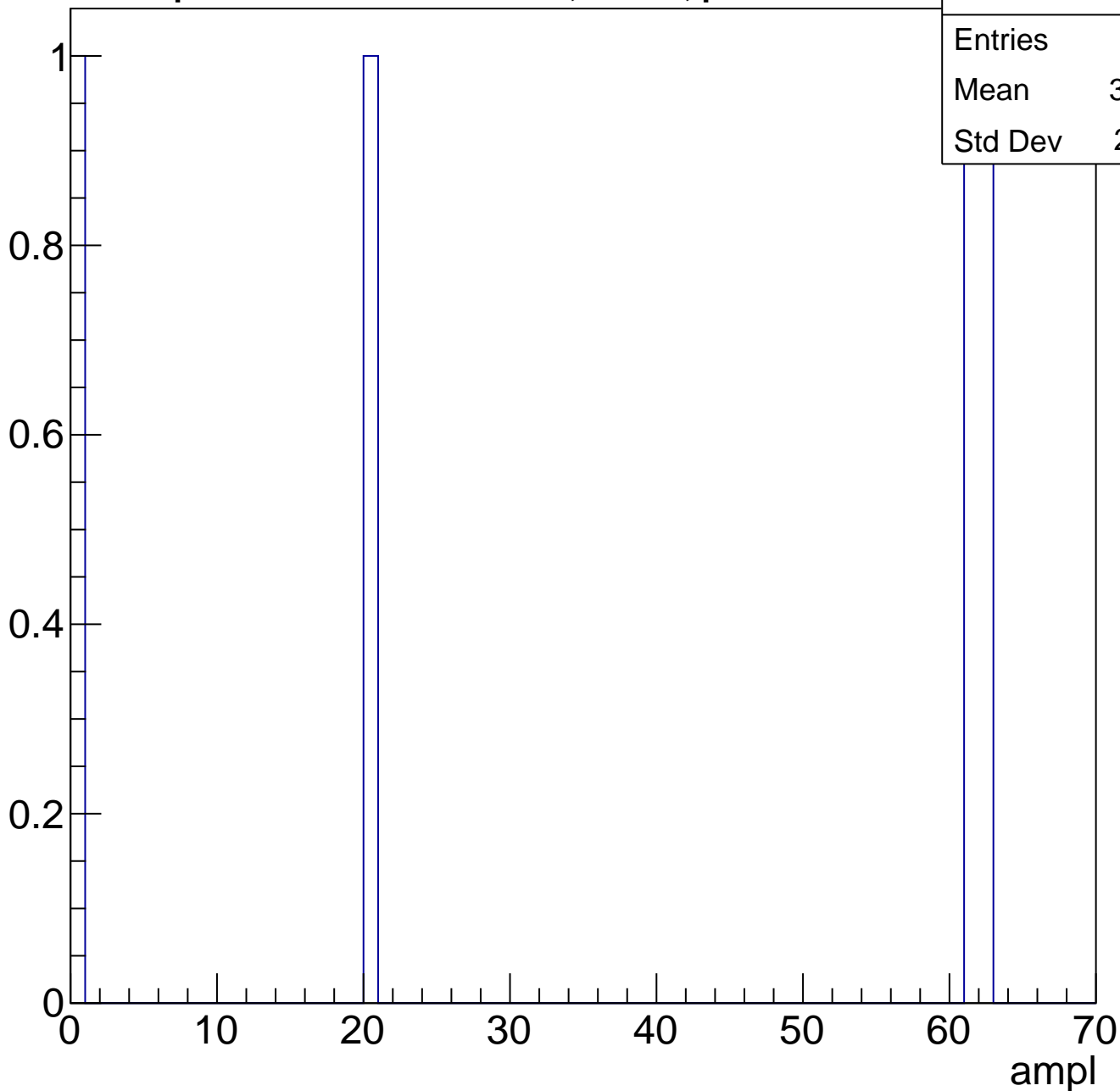




# B1L002S, U3-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry



# B1L002S, U3-ch127, adc7

calib\_packv5\_042523\_0143.root, FC#10, port B3

Entry

