

B1L104S, U11-ch0

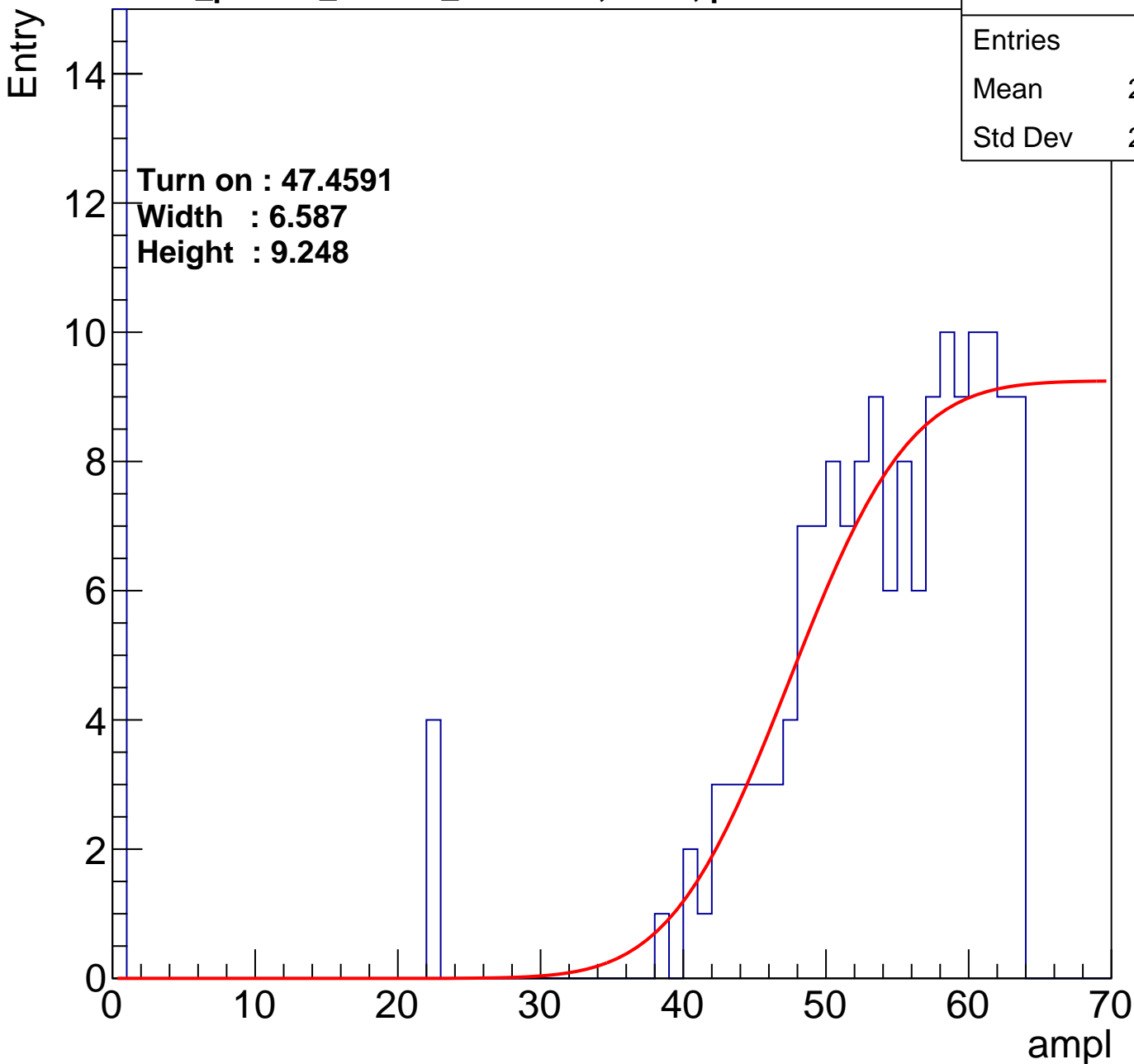
calib_packv5_033123_0516.root, FC#4, port A1

Entries	300
Mean	28.28
Std Dev	27.24

Turn on : 47.4591

Width : 6.587

Height : 9.248



B1L104S, U11-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	29.18
Std Dev	29.45

Turn on : 55.7656

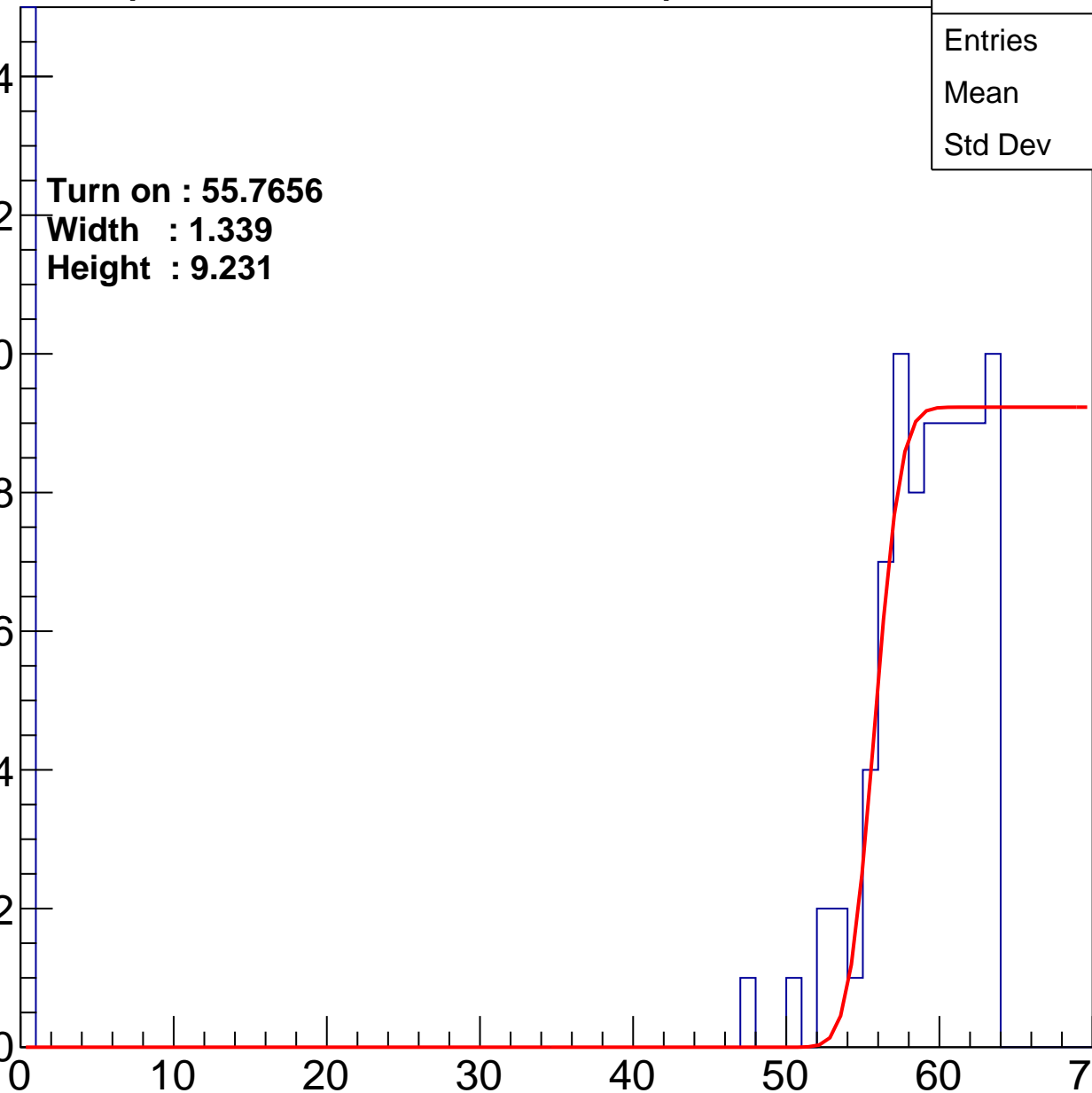
Width : 1.339

Height : 9.231

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch2

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	34.63
Std Dev	27.89

Turn on : 52.5589

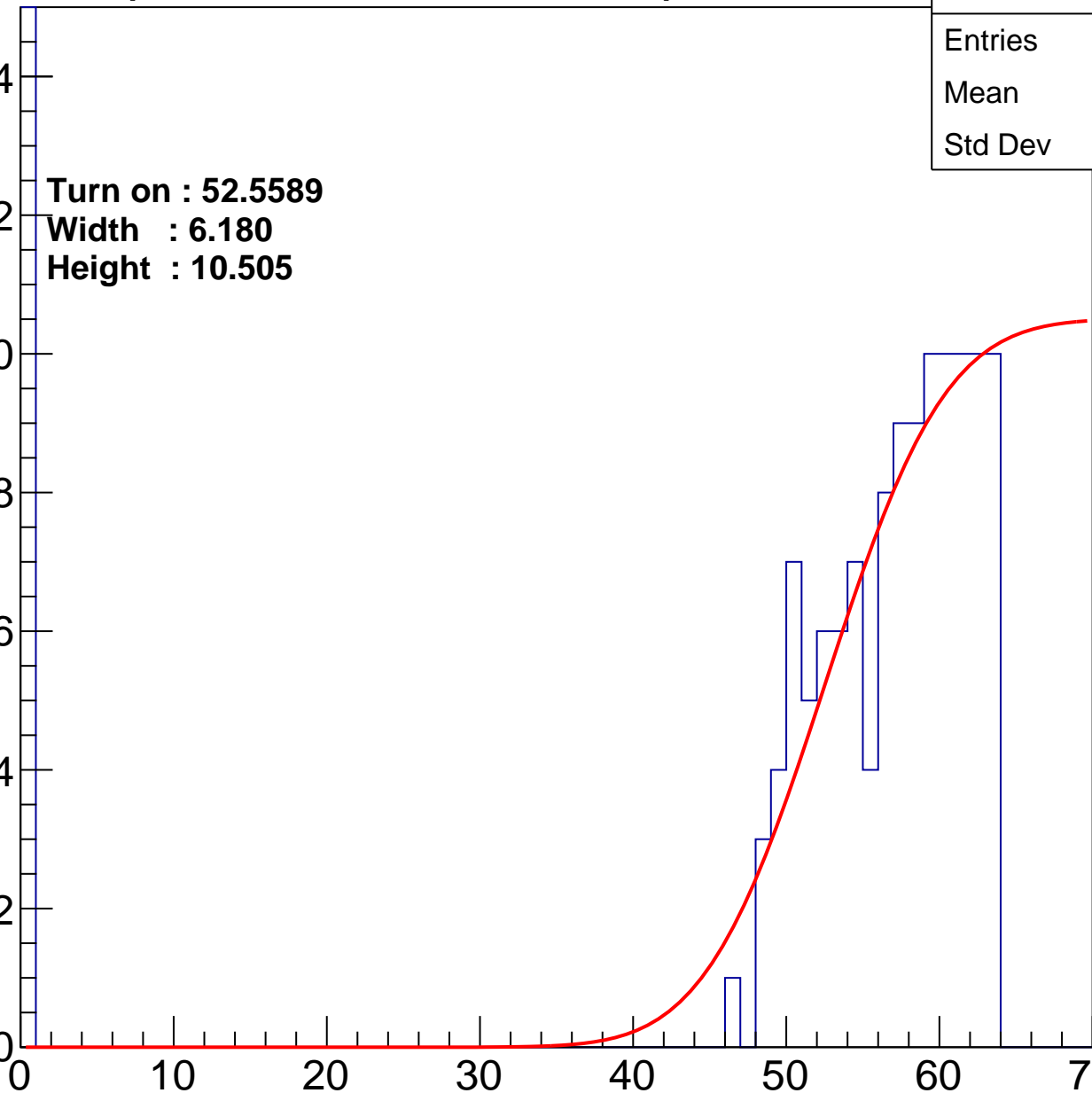
Width : 6.180

Height : 10.505

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch3

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	28.75
Std Dev	29.17

Turn on : 53.9418

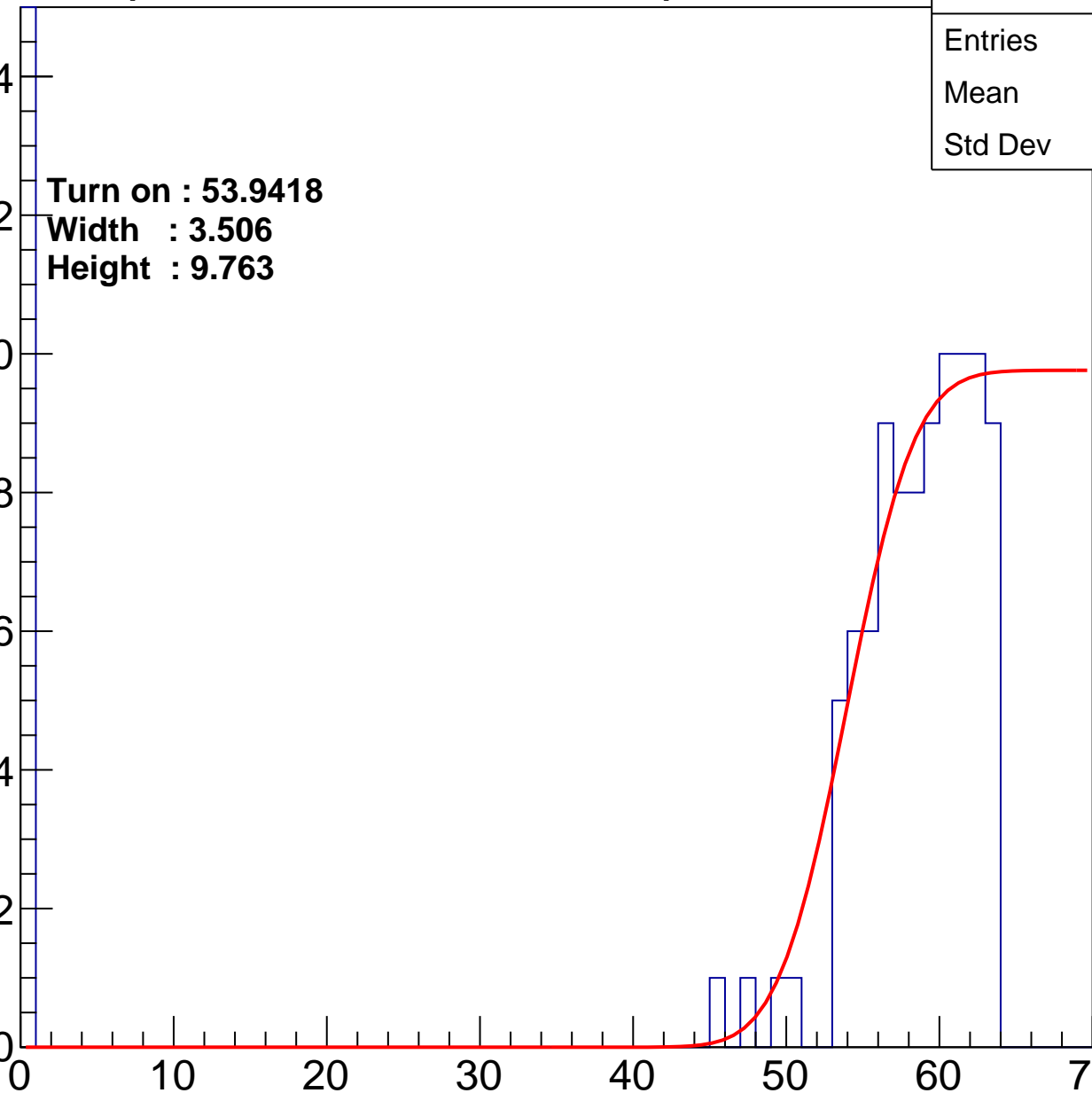
Width : 3.506

Height : 9.763

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.29
Std Dev	28.37

Turn on : 54.9964

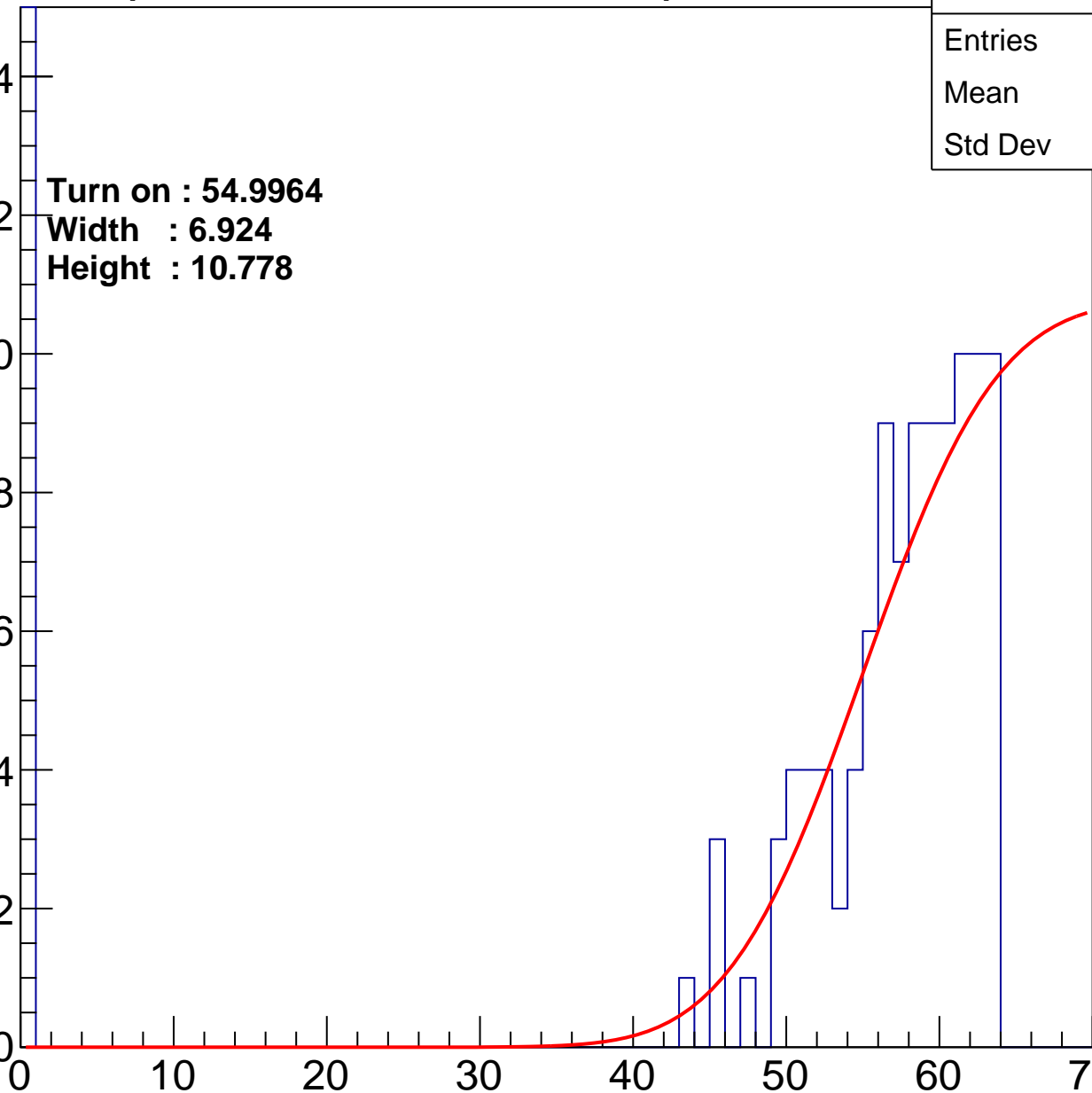
Width : 6.924

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch5

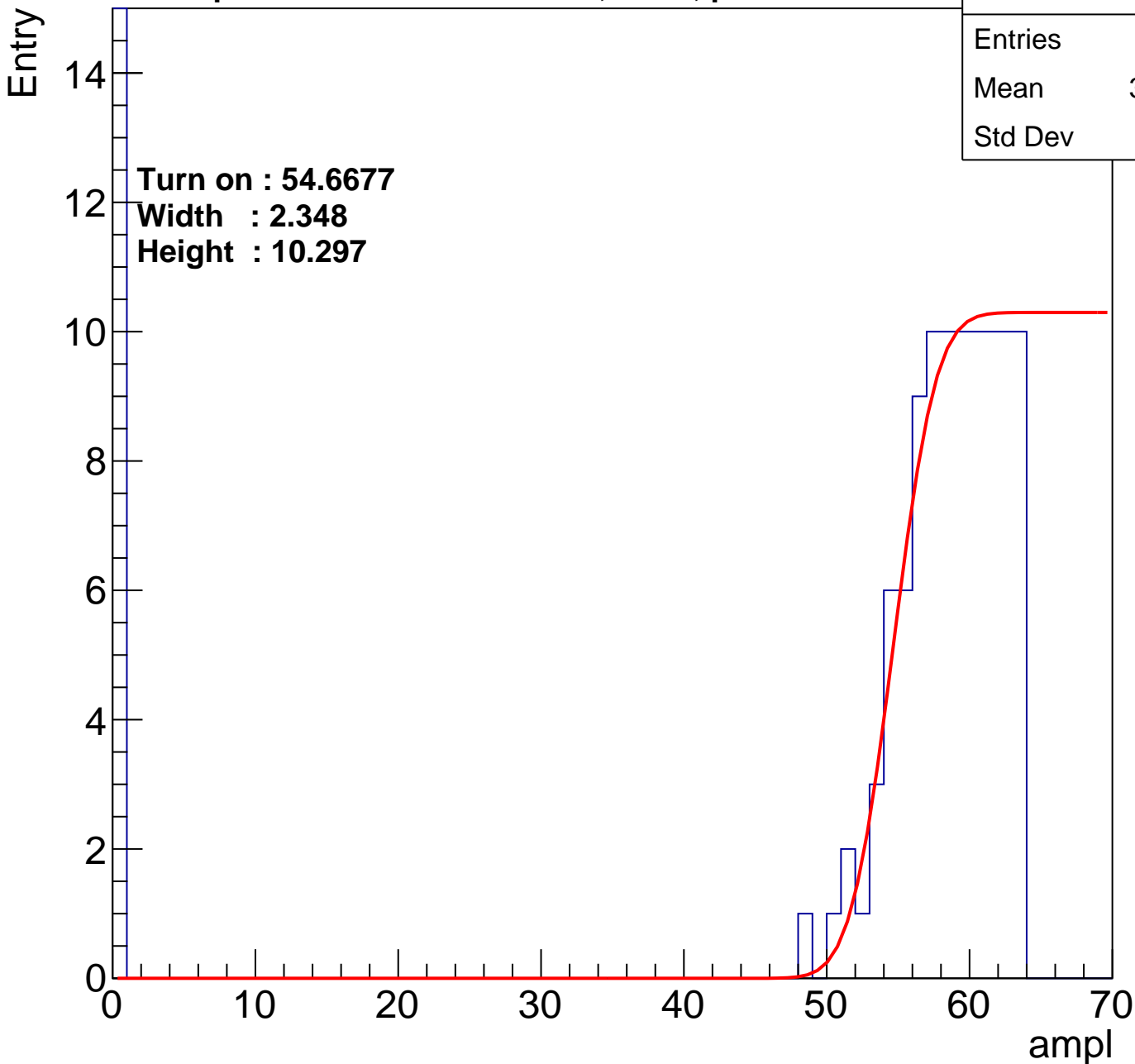
calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.05
Std Dev	29.1

Turn on : 54.6677

Width : 2.348

Height : 10.297



B1L104S, U11-ch6

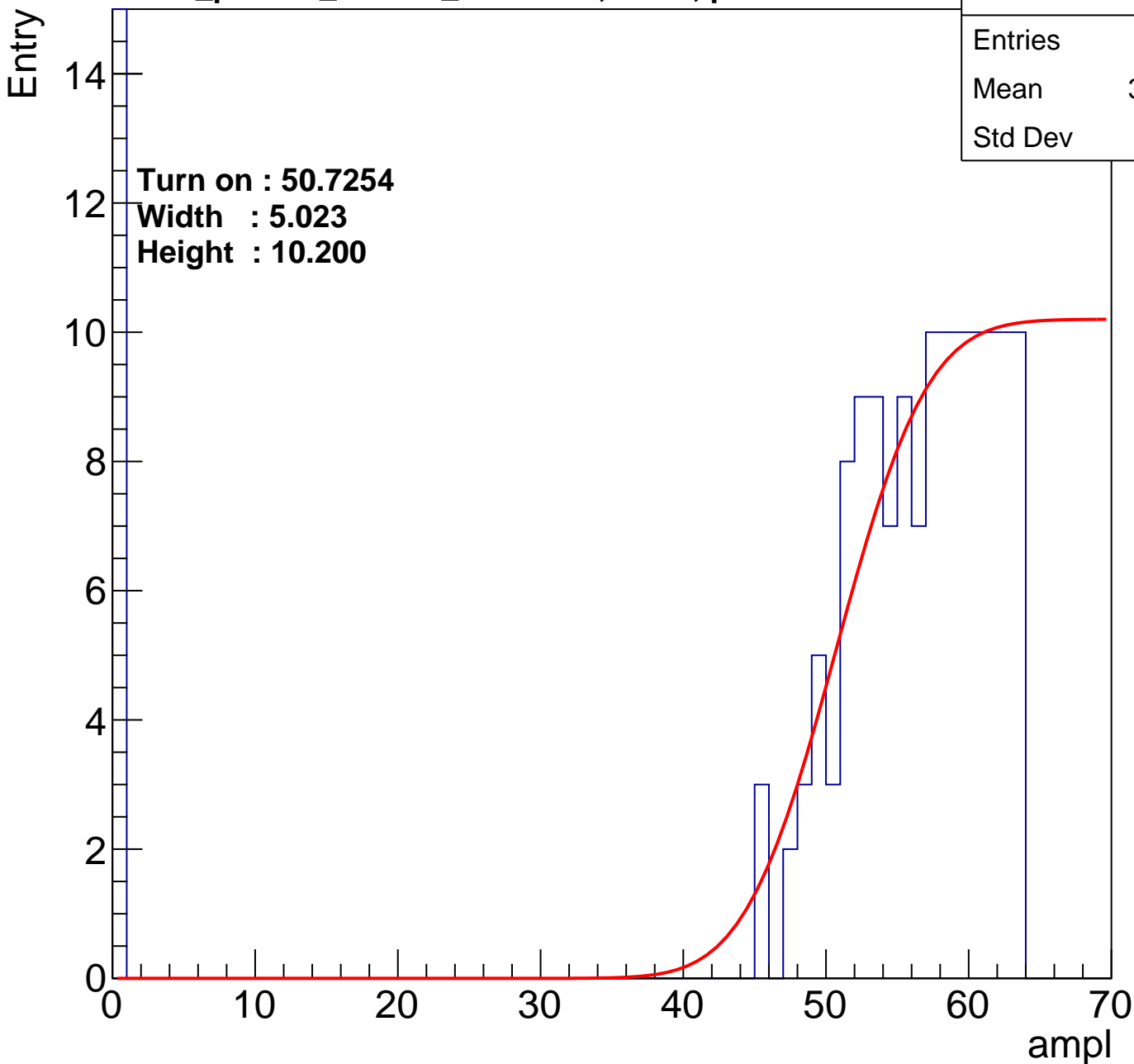
calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	34.48
Std Dev	27.6

Turn on : 50.7254

Width : 5.023

Height : 10.200

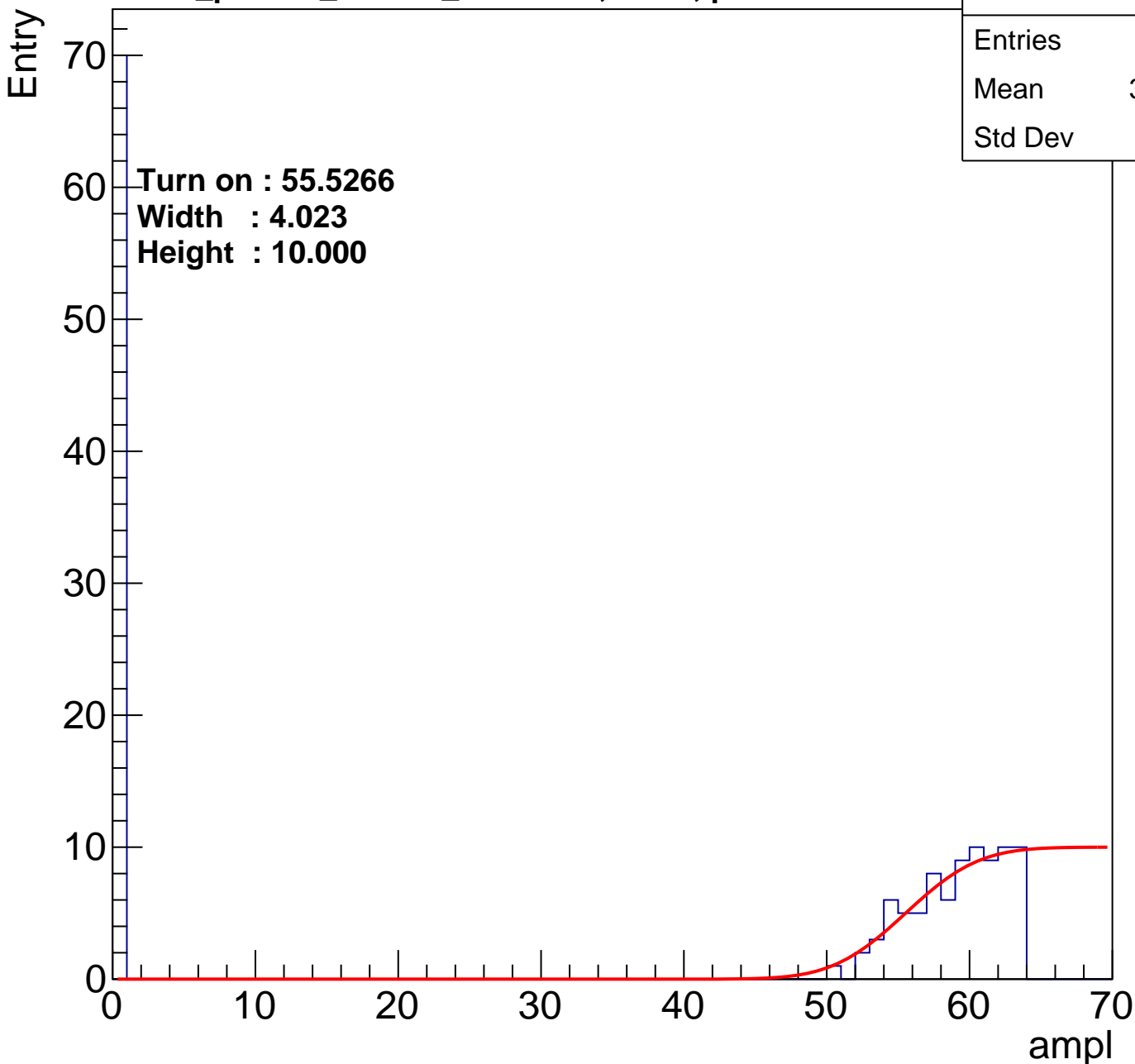


B1L104S, U11-ch7

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	31.99
Std Dev	29.3

Turn on : 55.5266
Width : 4.023
Height : 10.000



B1L104S, U11-ch8

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	32.75
Std Dev	28

Turn on : 51.9893

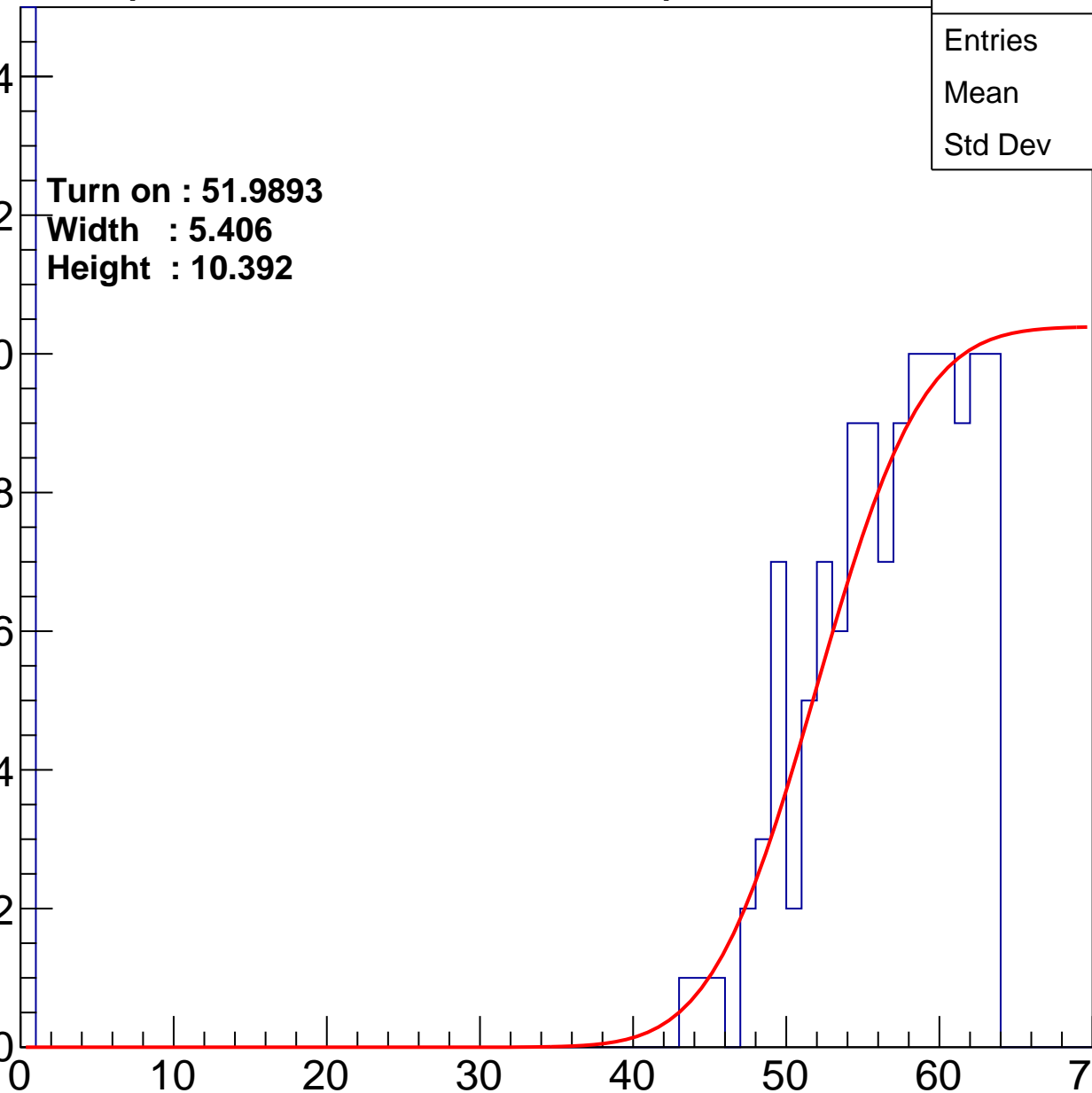
Width : 5.406

Height : 10.392

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch9

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	34.19
Std Dev	28.75

Turn on : 54.8809

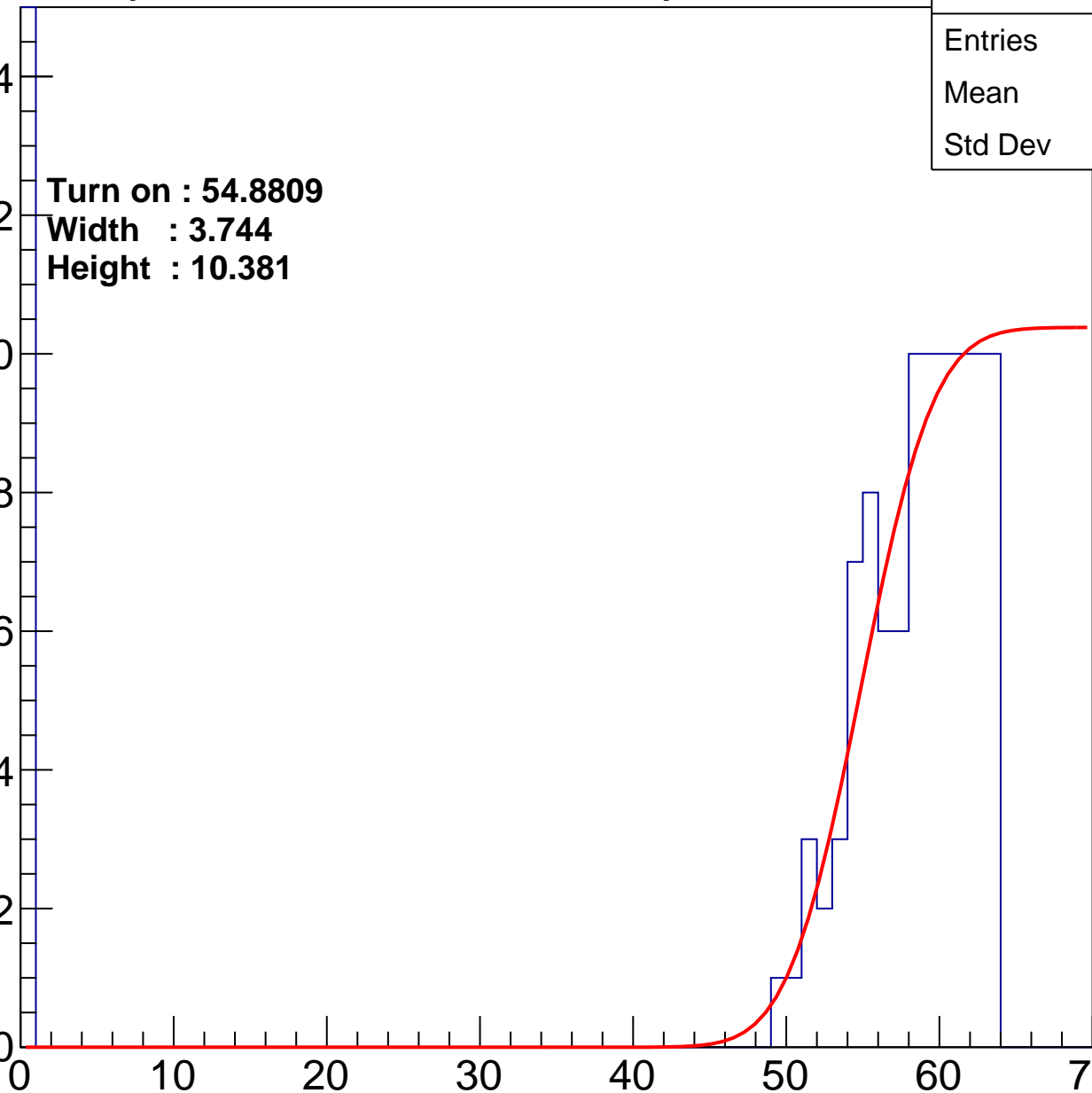
Width : 3.744

Height : 10.381

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch10

calib_packv5_033123_0516.root, FC#4, port A1

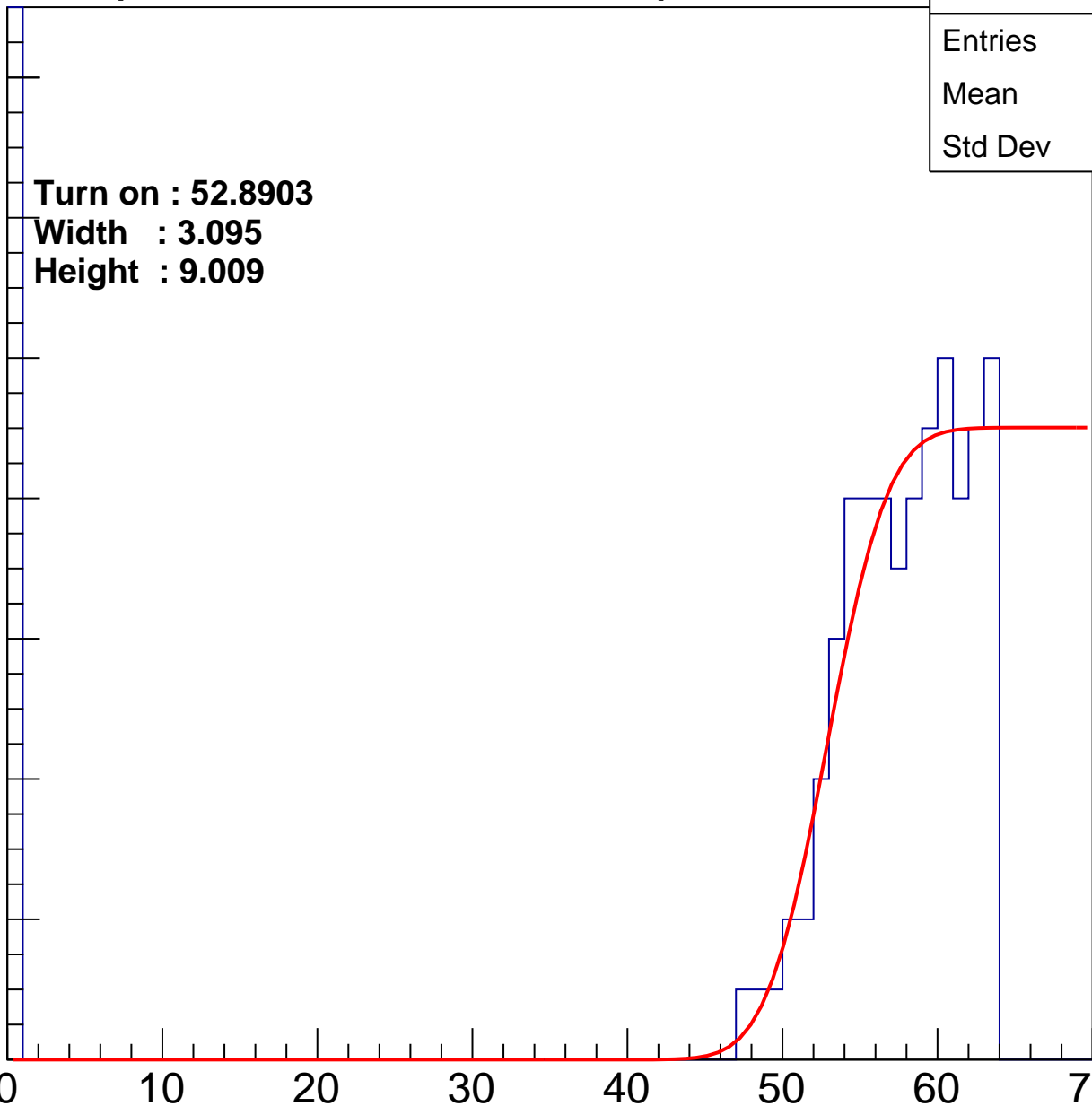
Entry

14
12
10
8
6
4
2
0

Turn on : 52.8903
Width : 3.095
Height : 9.009

Entries	177
Mean	33.12
Std Dev	28.55

ampl



B1L104S, U11-ch11

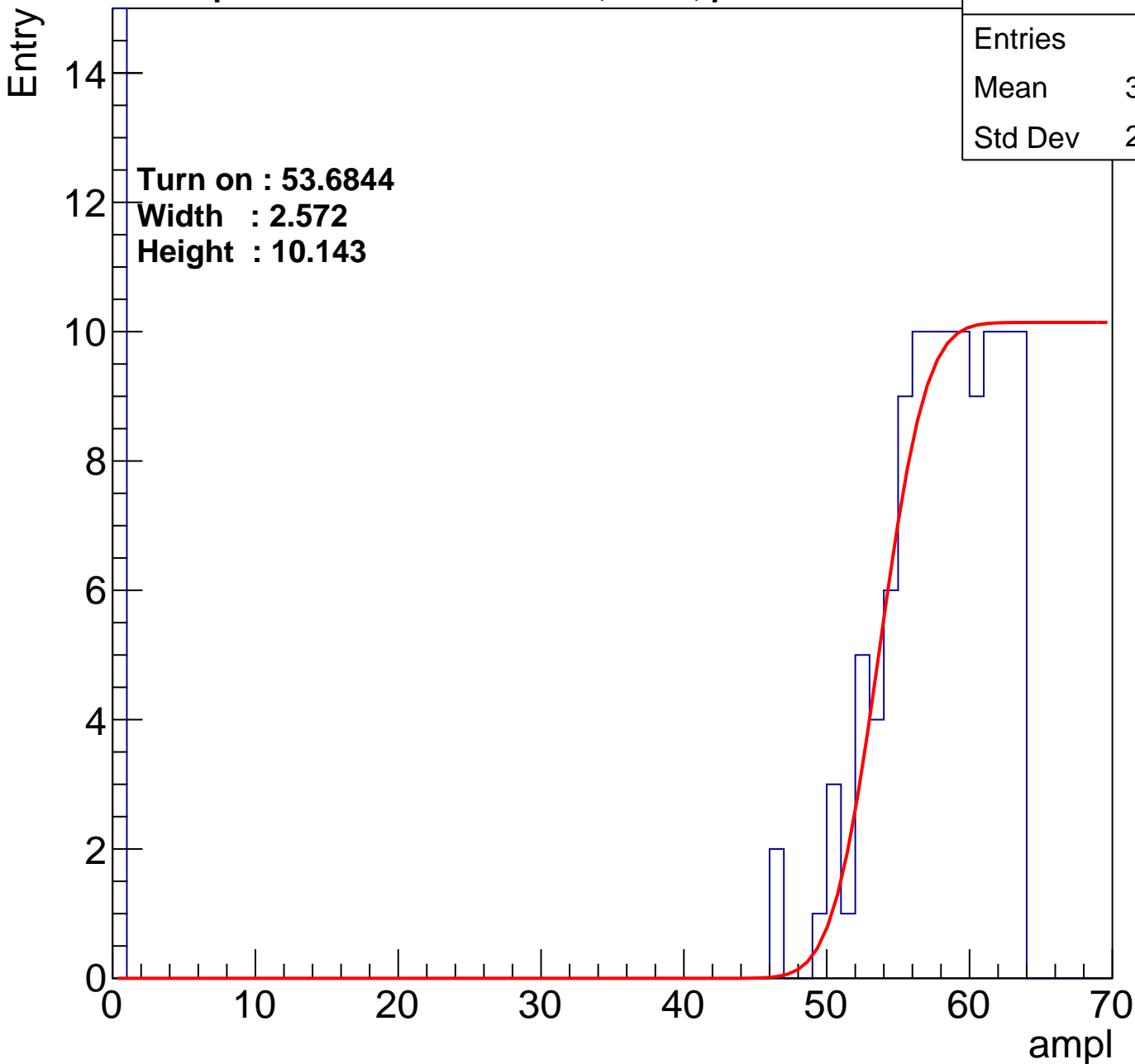
calib_packv5_033123_0516.root, FC#4, port A1

Entries	161
Mean	39.34
Std Dev	26.97

Turn on : 53.6844

Width : 2.572

Height : 10.143



B1L104S, U11-ch12

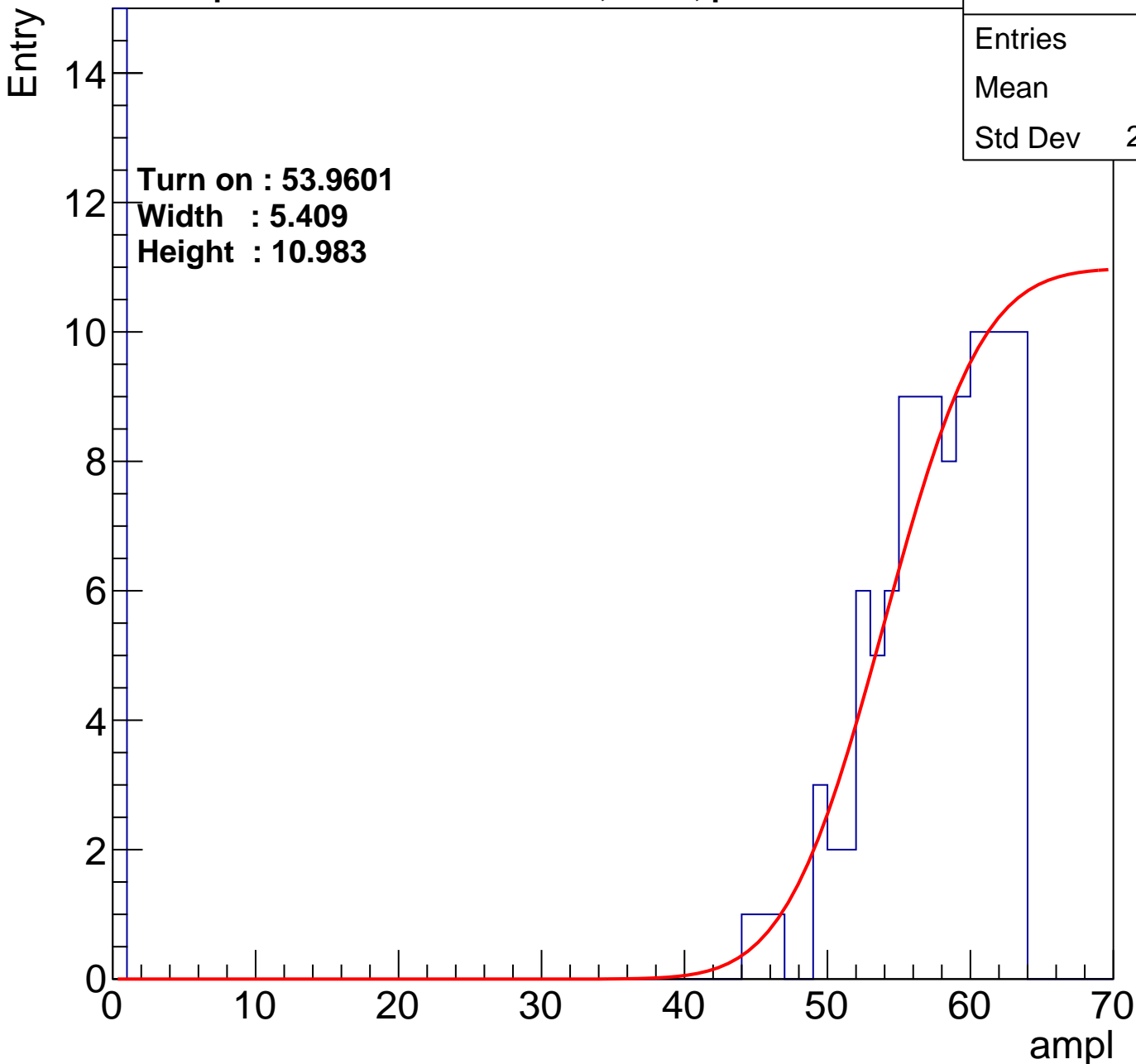
calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	31.6
Std Dev	28.63

Turn on : 53.9601

Width : 5.409

Height : 10.983



B1L104S, U11-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	158
Mean	36.31
Std Dev	28.19

Turn on : 54.8369

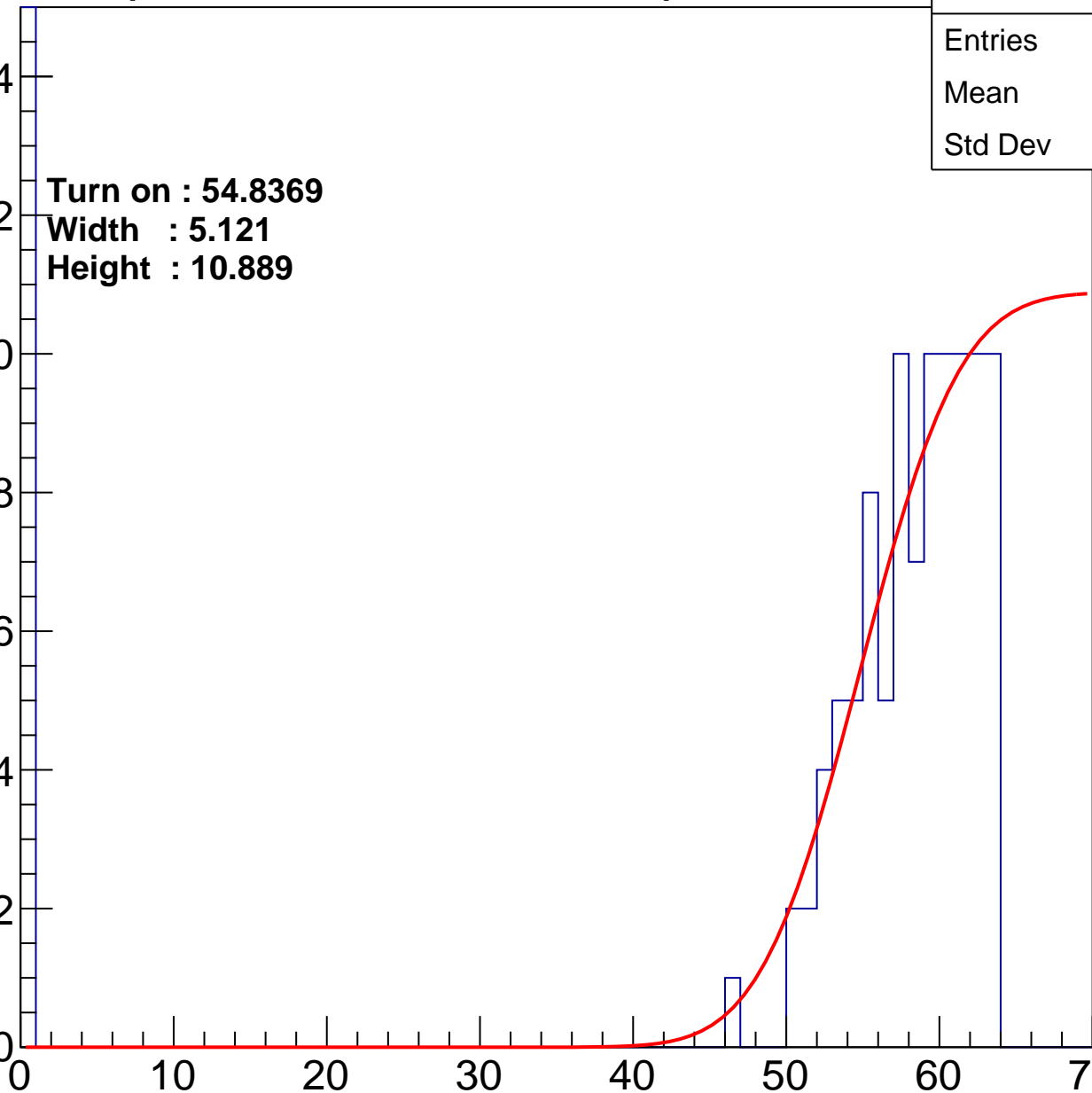
Width : 5.121

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch14

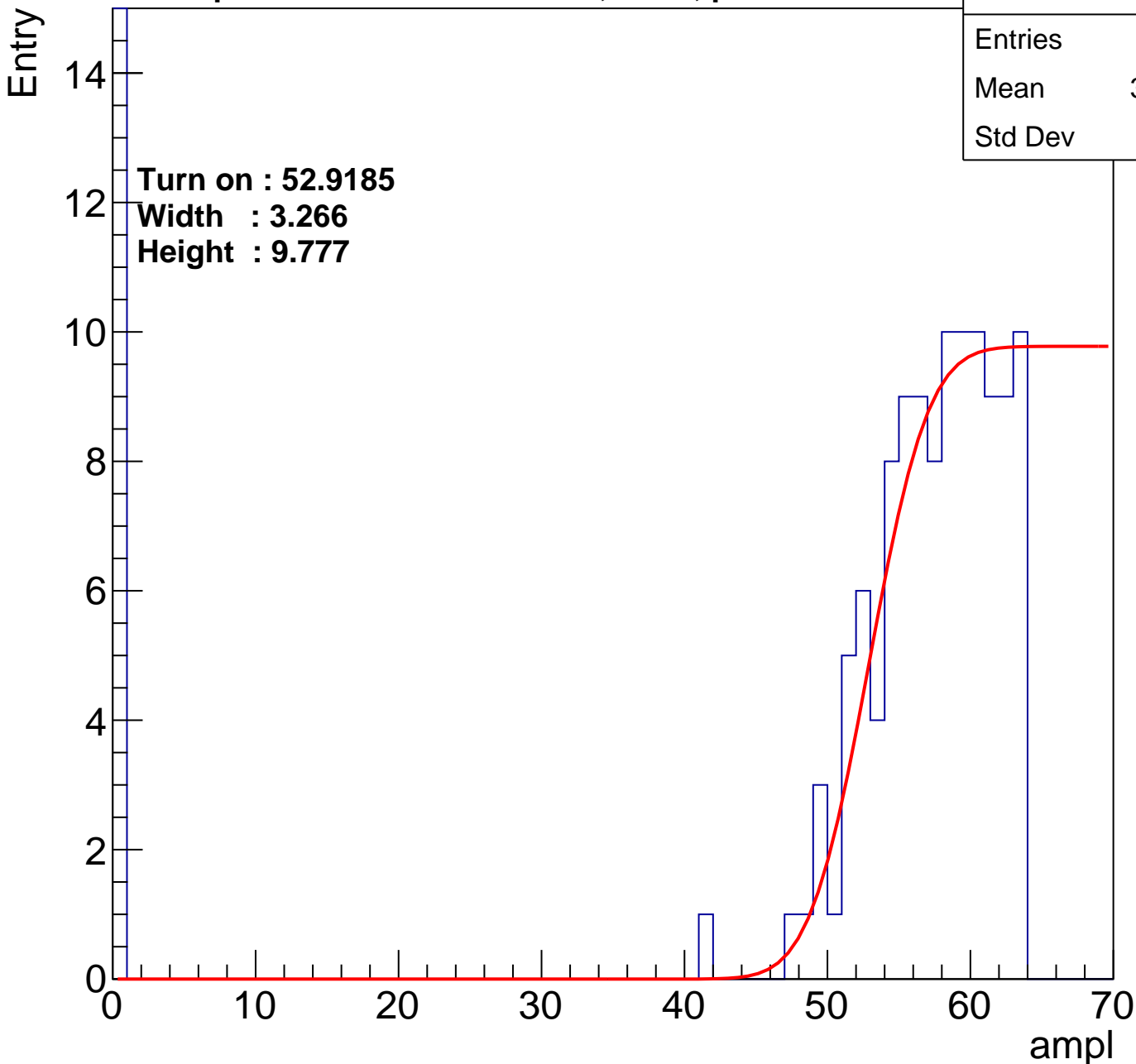
calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	32.05
Std Dev	28.5

Turn on : 52.9185

Width : 3.266

Height : 9.777



B1L104S, U11-ch15

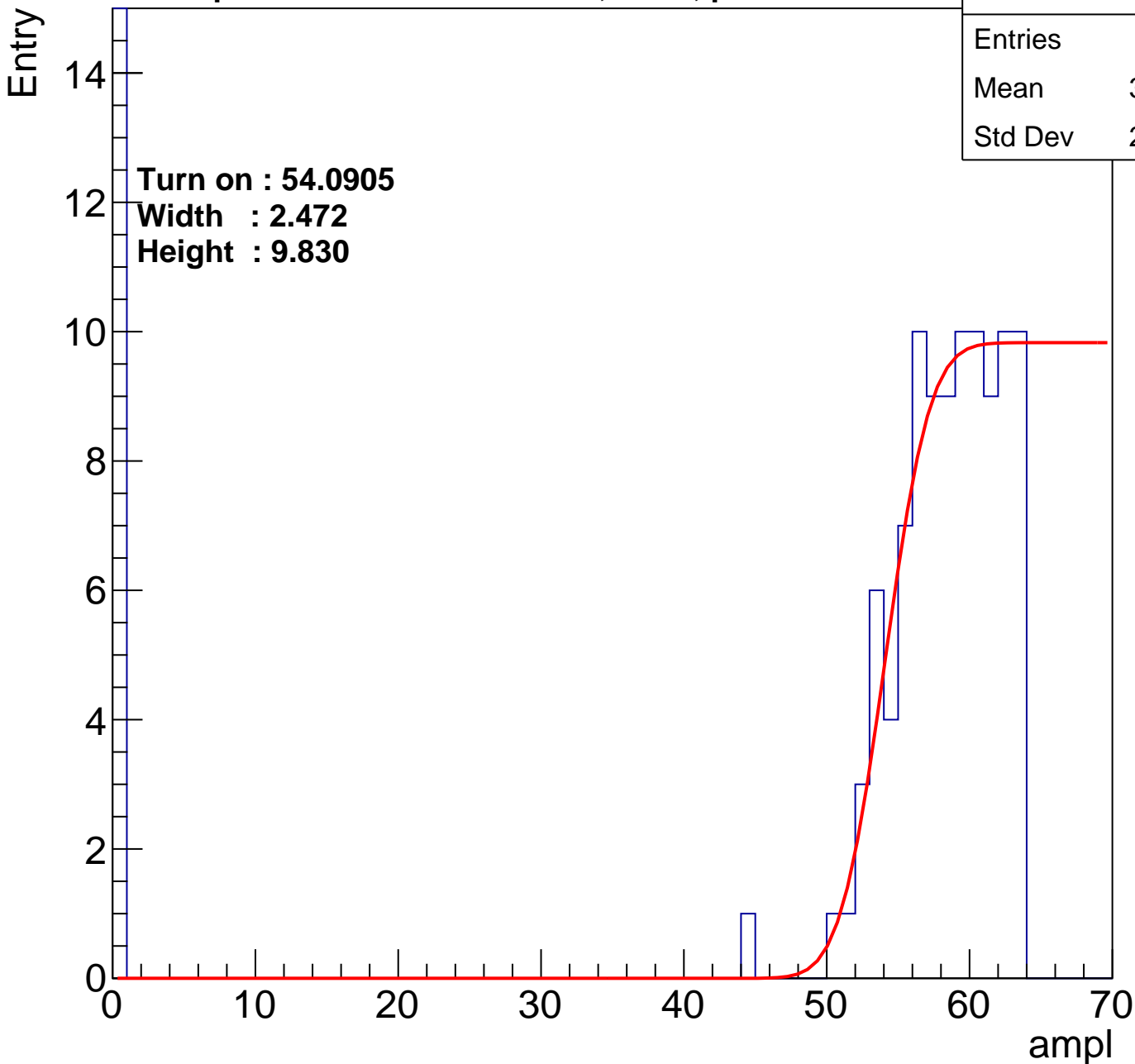
calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	38.69
Std Dev	27.52

Turn on : 54.0905

Width : 2.472

Height : 9.830



B1L104S, U11-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	232
Mean	33.59
Std Dev	26.67

Turn on : 55.0448

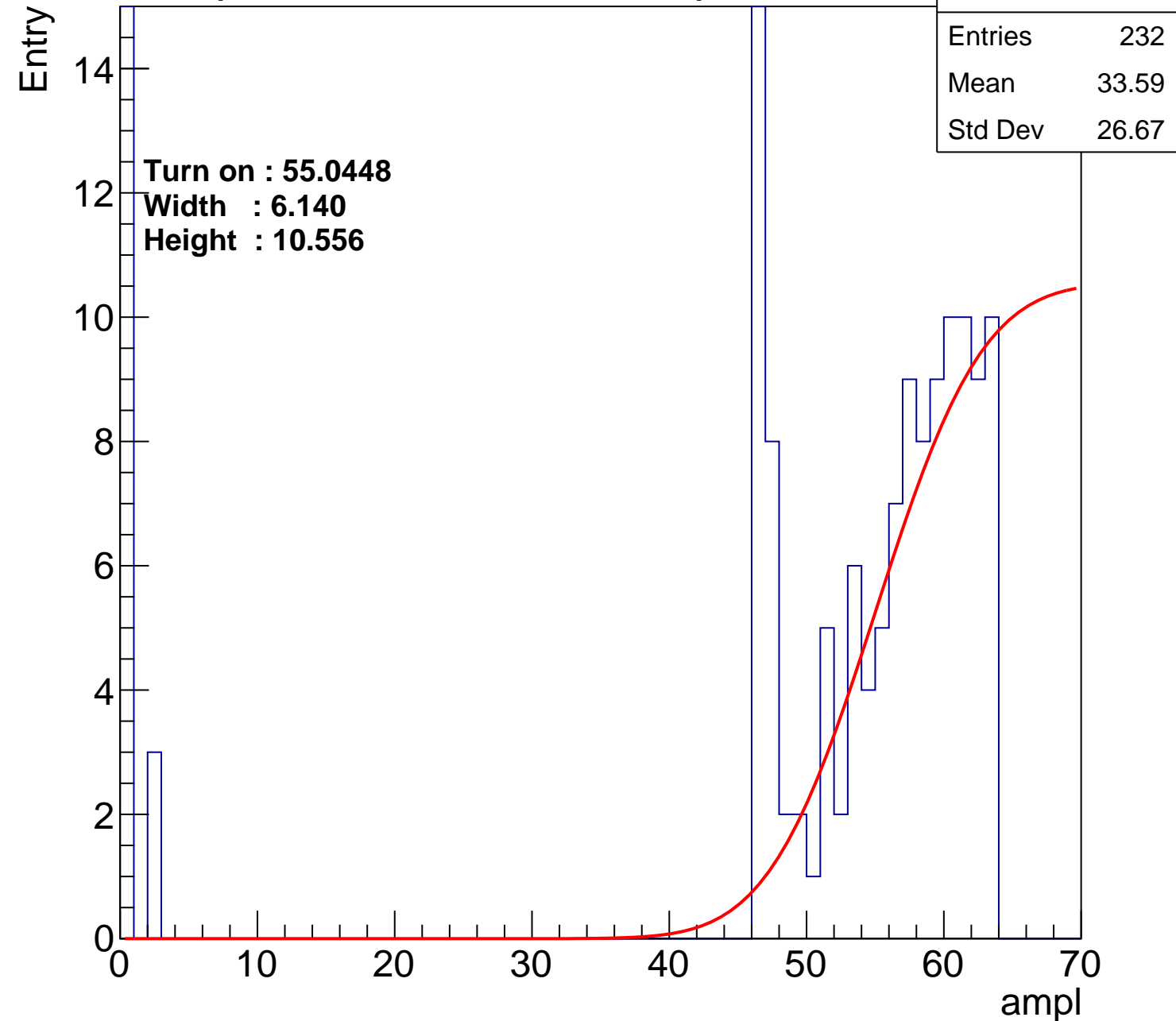
Width : 6.140

Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch17

calib_packv5_033123_0516.root, FC#4, port A1

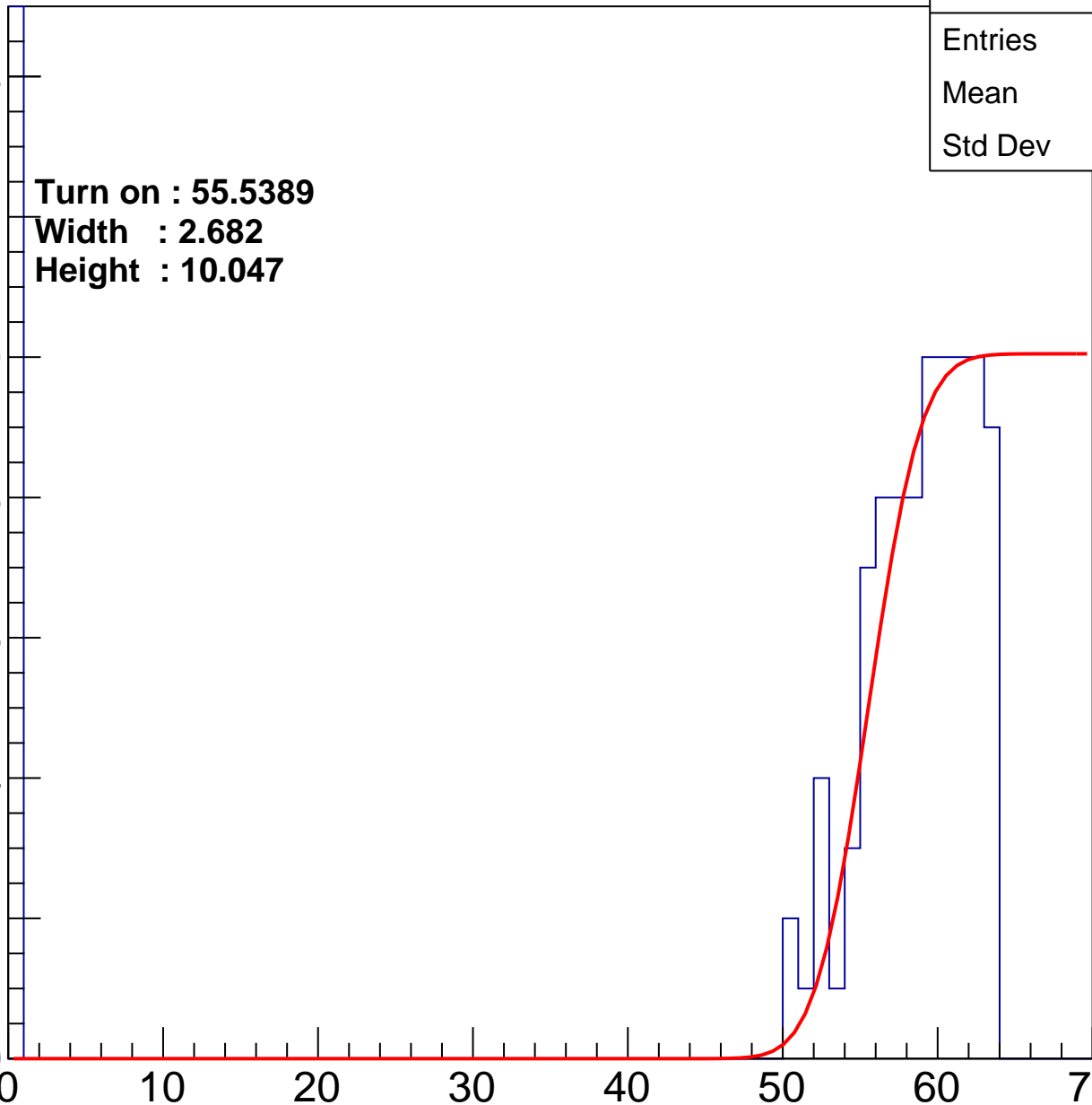
Entry

14
12
10
8
6
4
2
0

Turn on : 55.5389
Width : 2.682
Height : 10.047

Entries	154
Mean	34.51
Std Dev	28.82

ampl



B1L104S, U11-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	27.75
Std Dev	28.95

Turn on : 54.8535

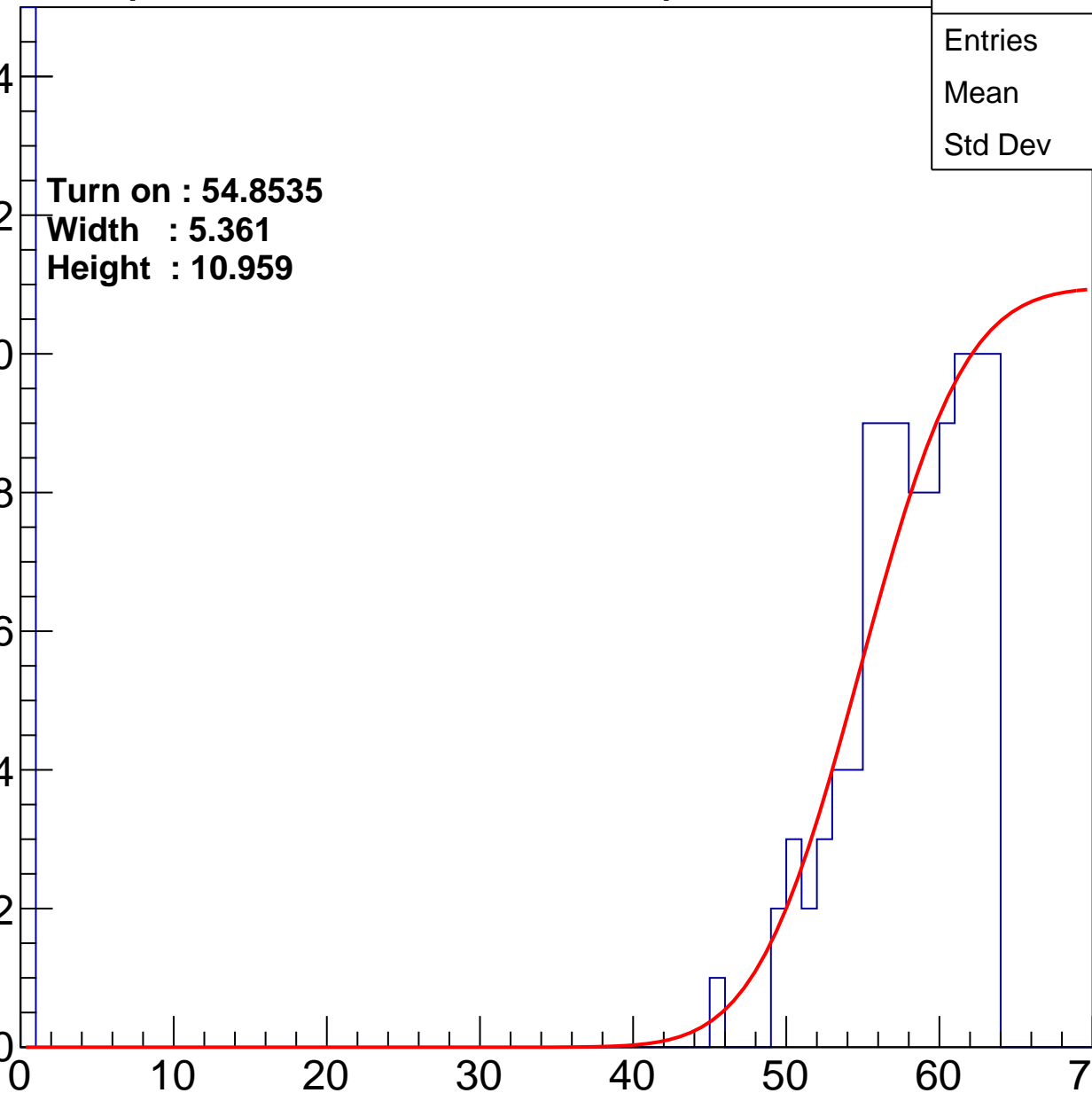
Width : 5.361

Height : 10.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	36.45
Std Dev	28.11

Turn on : 53.6930

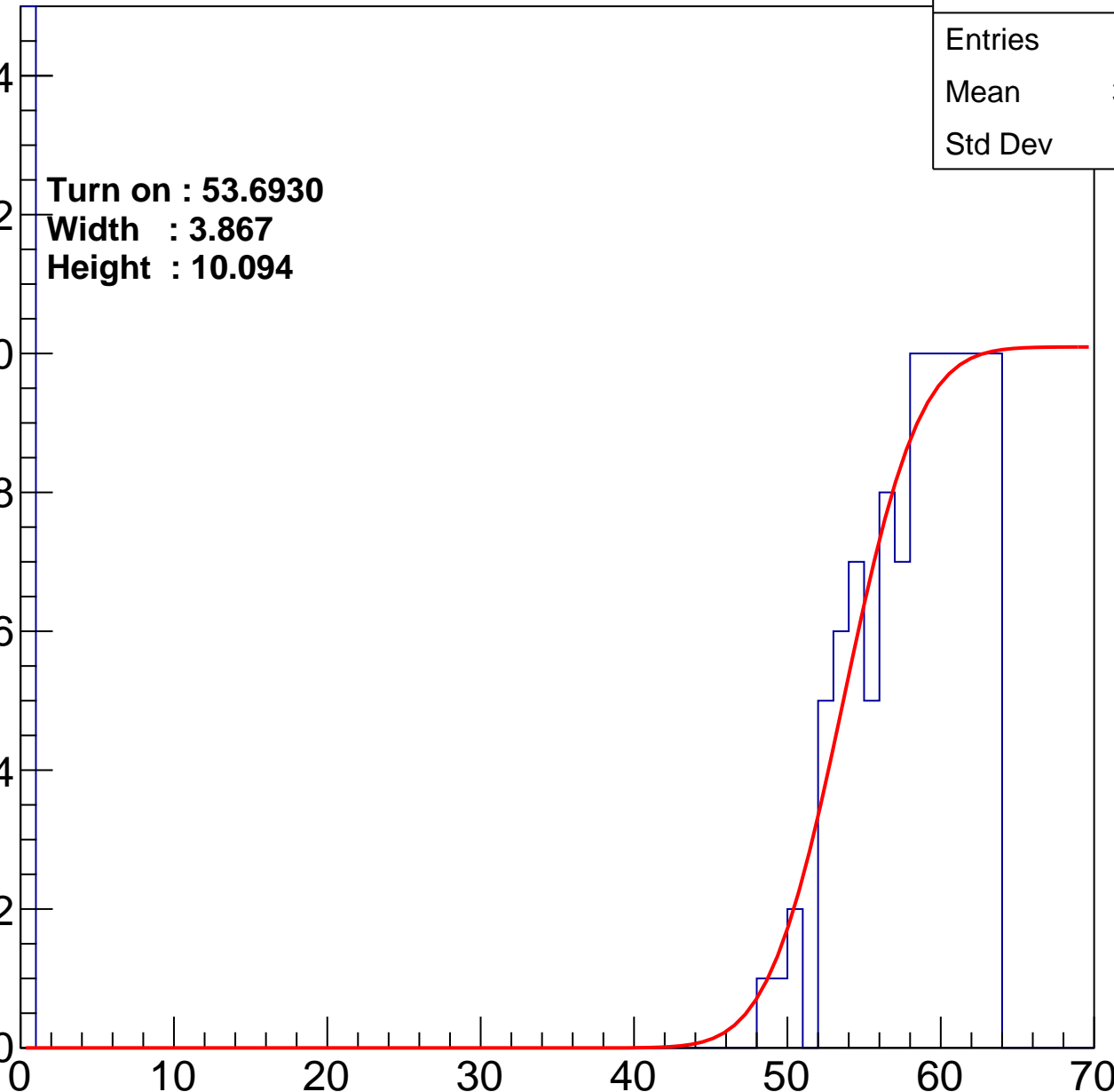
Width : 3.867

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch20

calib_packv5_033123_0516.root, FC#4, port A1

Entries	233
Mean	32.54
Std Dev	27.96

Turn on : 51.2279

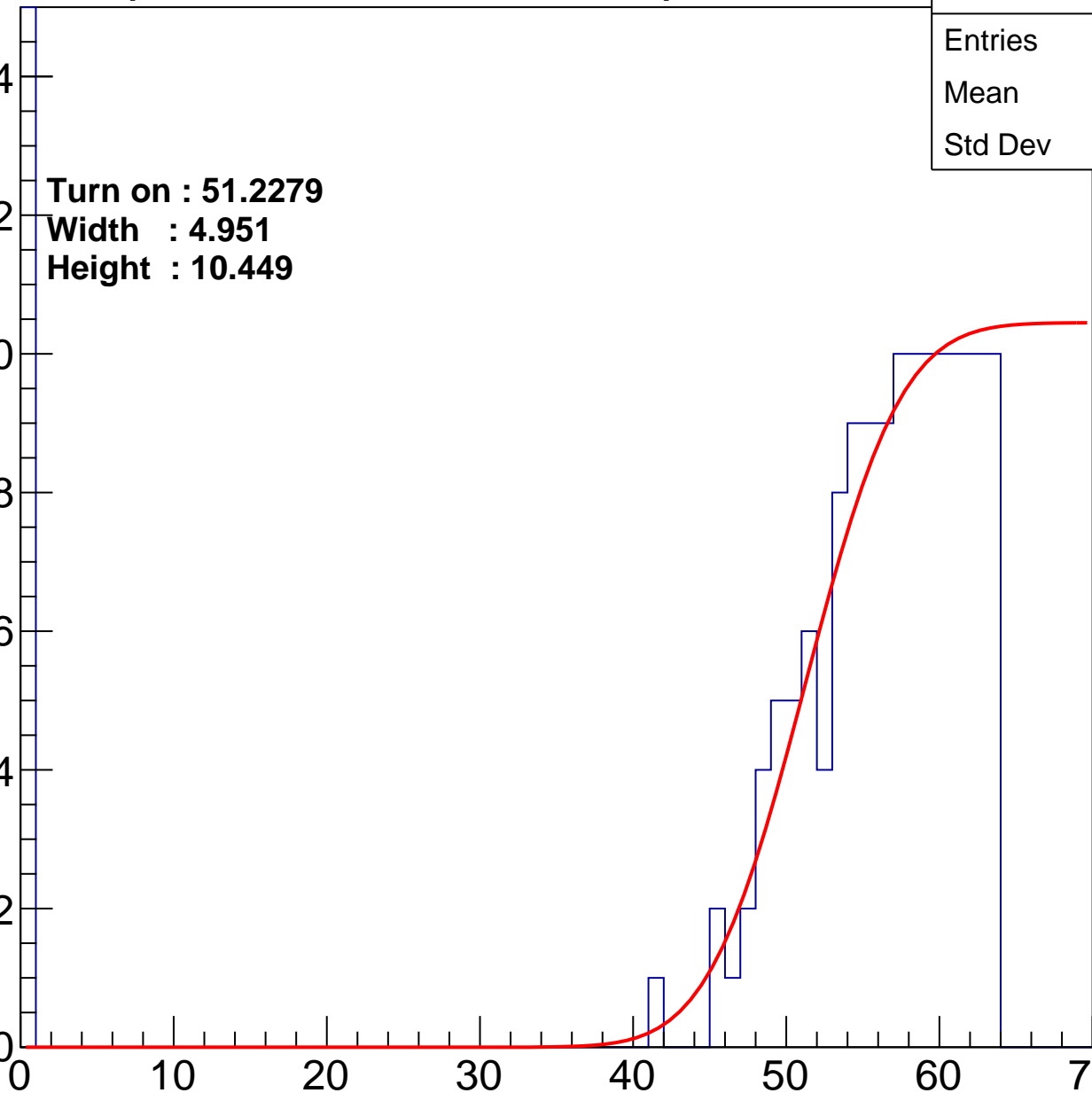
Width : 4.951

Height : 10.449

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	149
Mean	37.07
Std Dev	28.09

Turn on : 54.7185

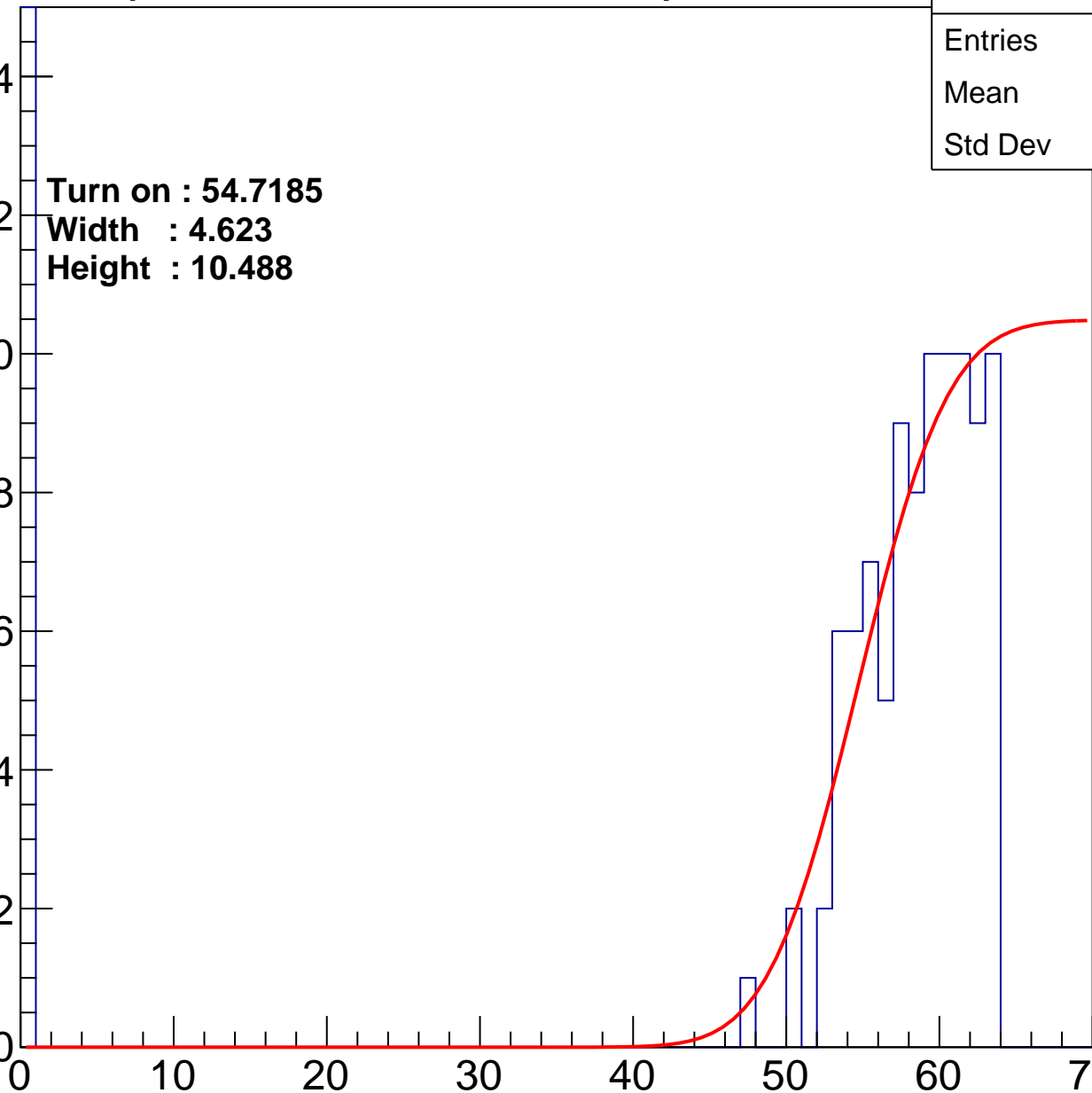
Width : 4.623

Height : 10.488

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	31.76
Std Dev	28.66

Turn on : 54.6546

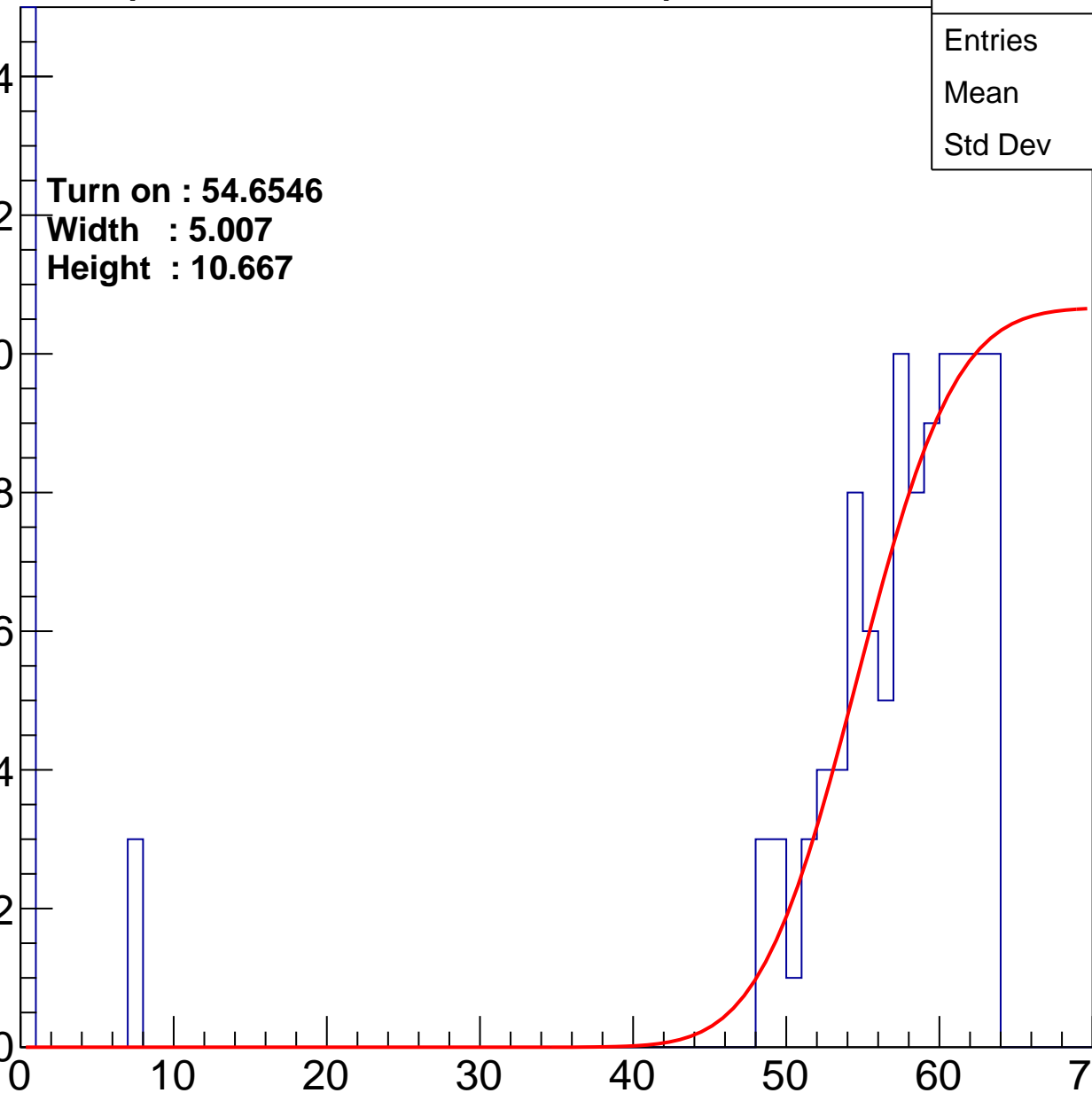
Width : 5.007

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	36.78
Std Dev	27.61

Turn on : 52.2911

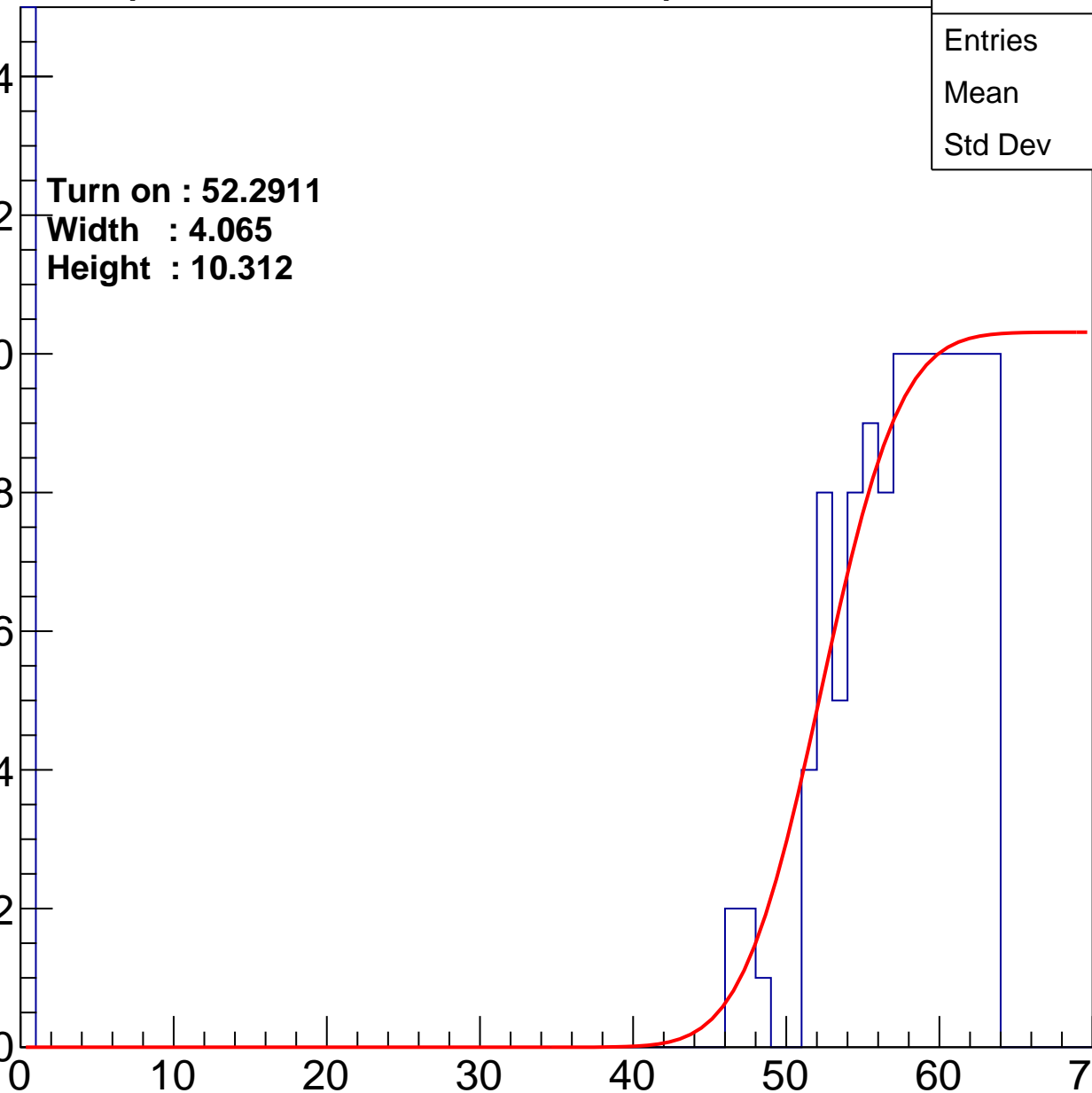
Width : 4.065

Height : 10.312

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	31.6
Std Dev	28.64

Turn on : 54.5119

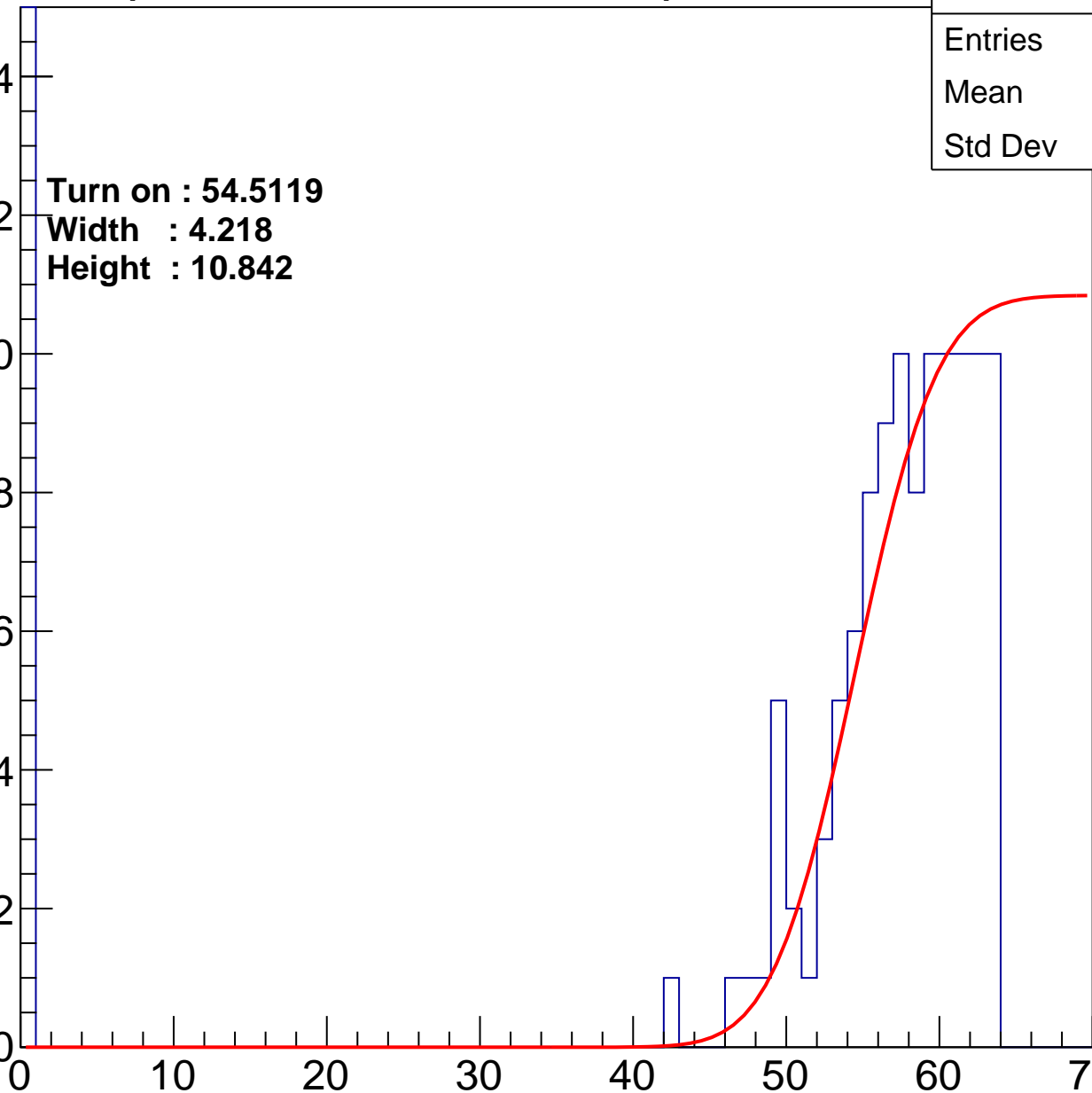
Width : 4.218

Height : 10.842

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	34.93
Std Dev	28.08

Turn on : 51.9032

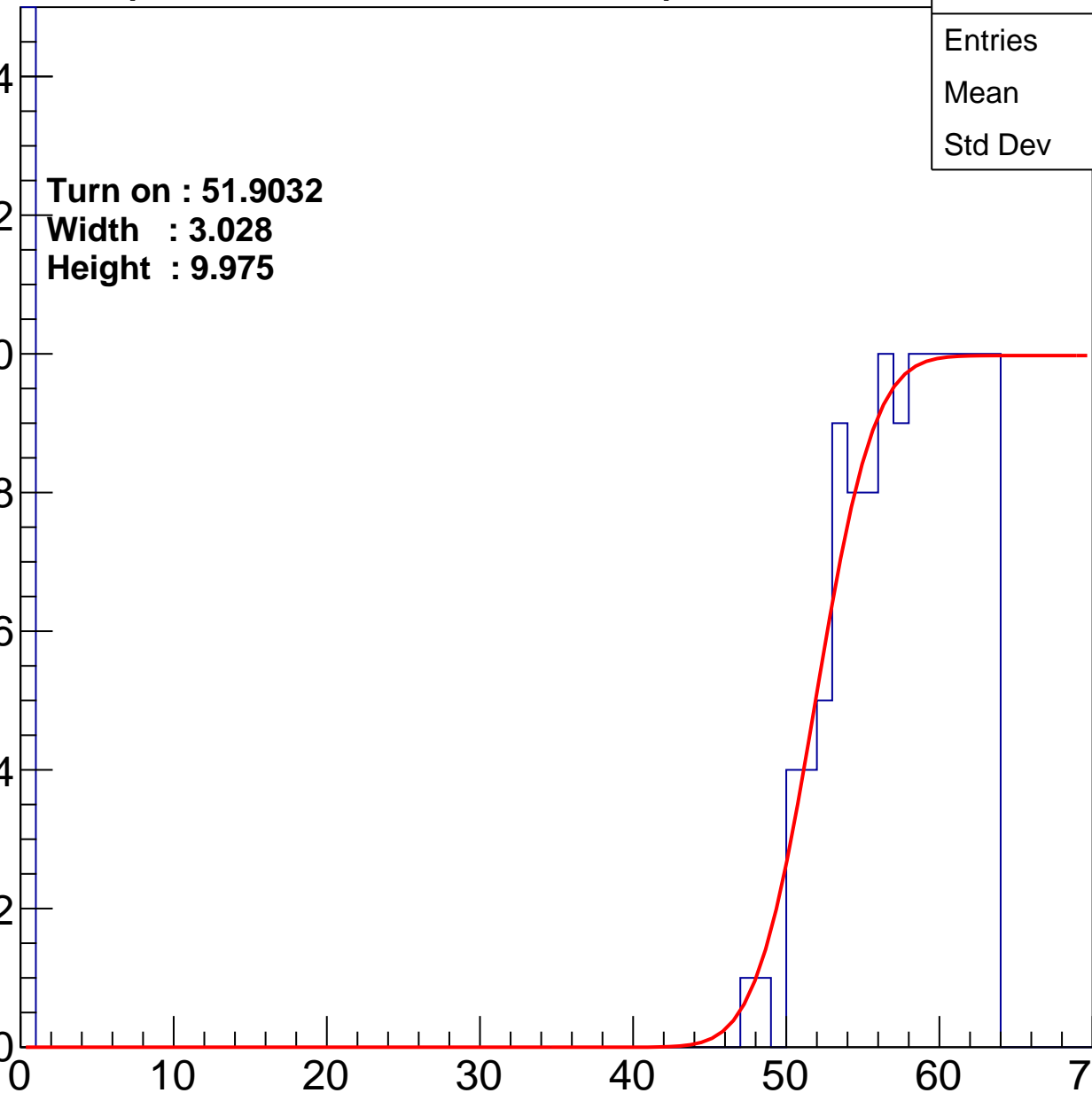
Width : 3.028

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	28.55
Std Dev	28.98

Turn on : 54.3003

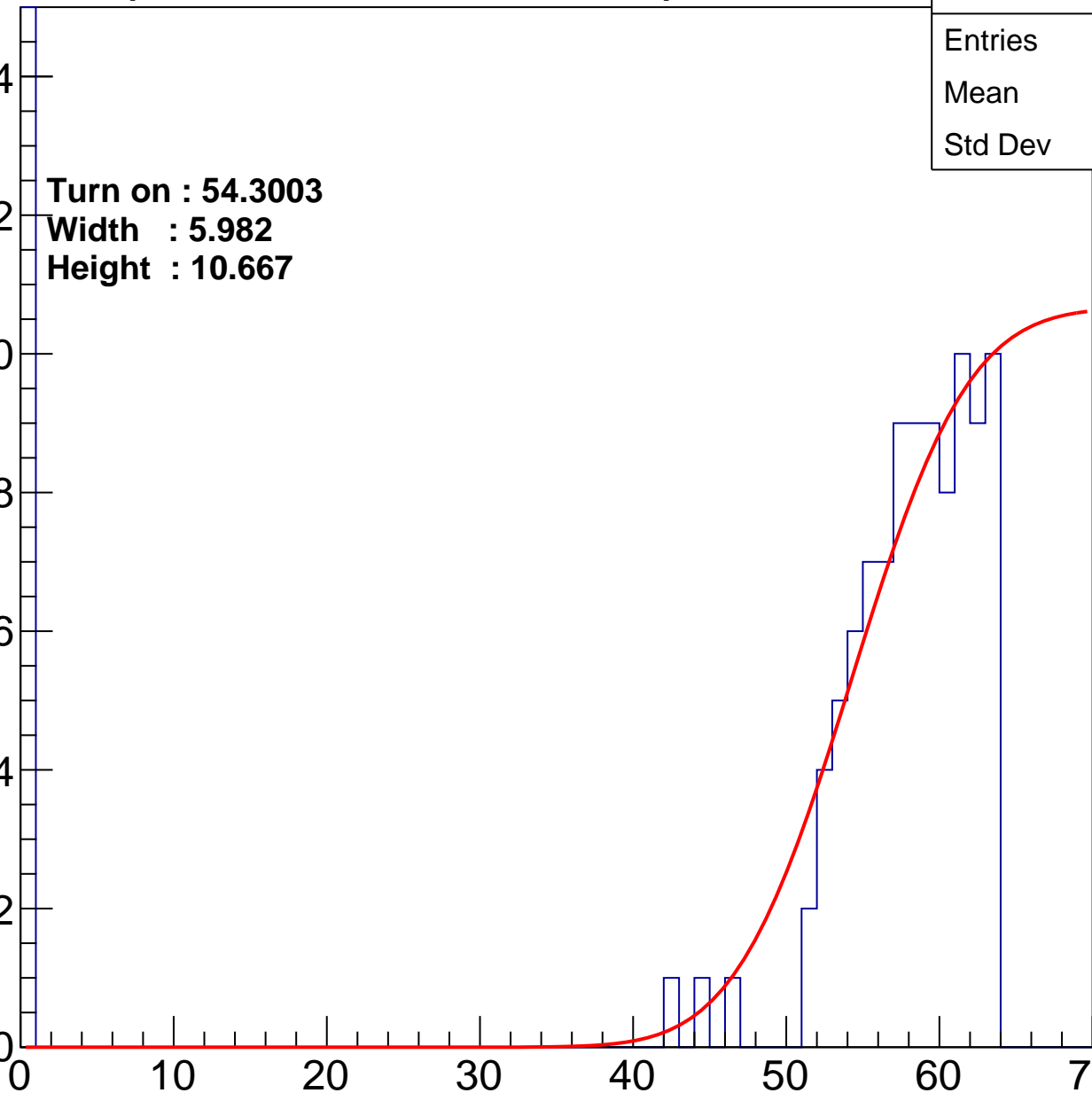
Width : 5.982

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch27

calib_packv5_033123_0516.root, FC#4, port A1

Entries	165
Mean	33.1
Std Dev	28.92

Turn on : 55.7804

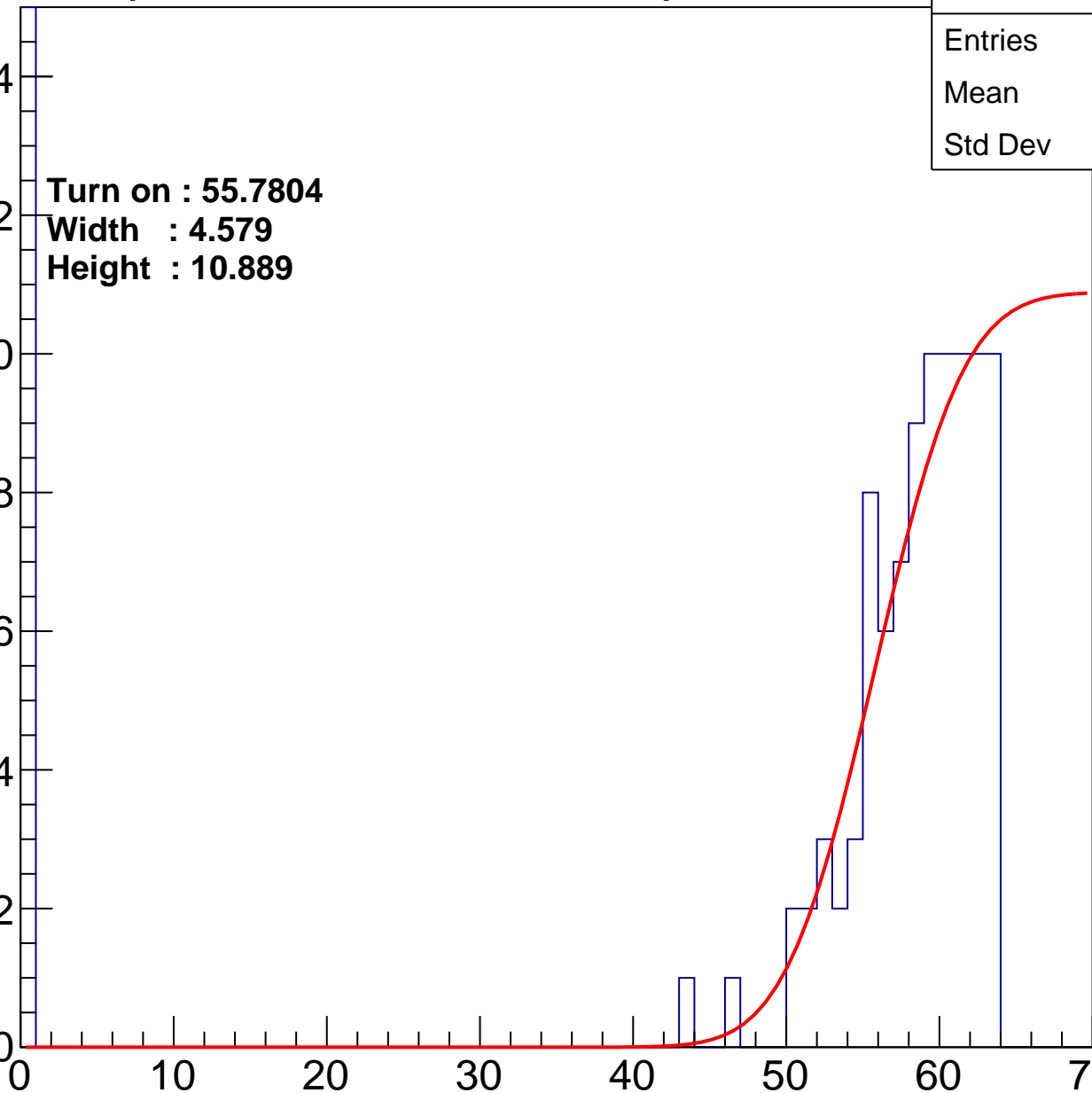
Width : 4.579

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch28

calib_packv5_033123_0516.root, FC#4, port A1

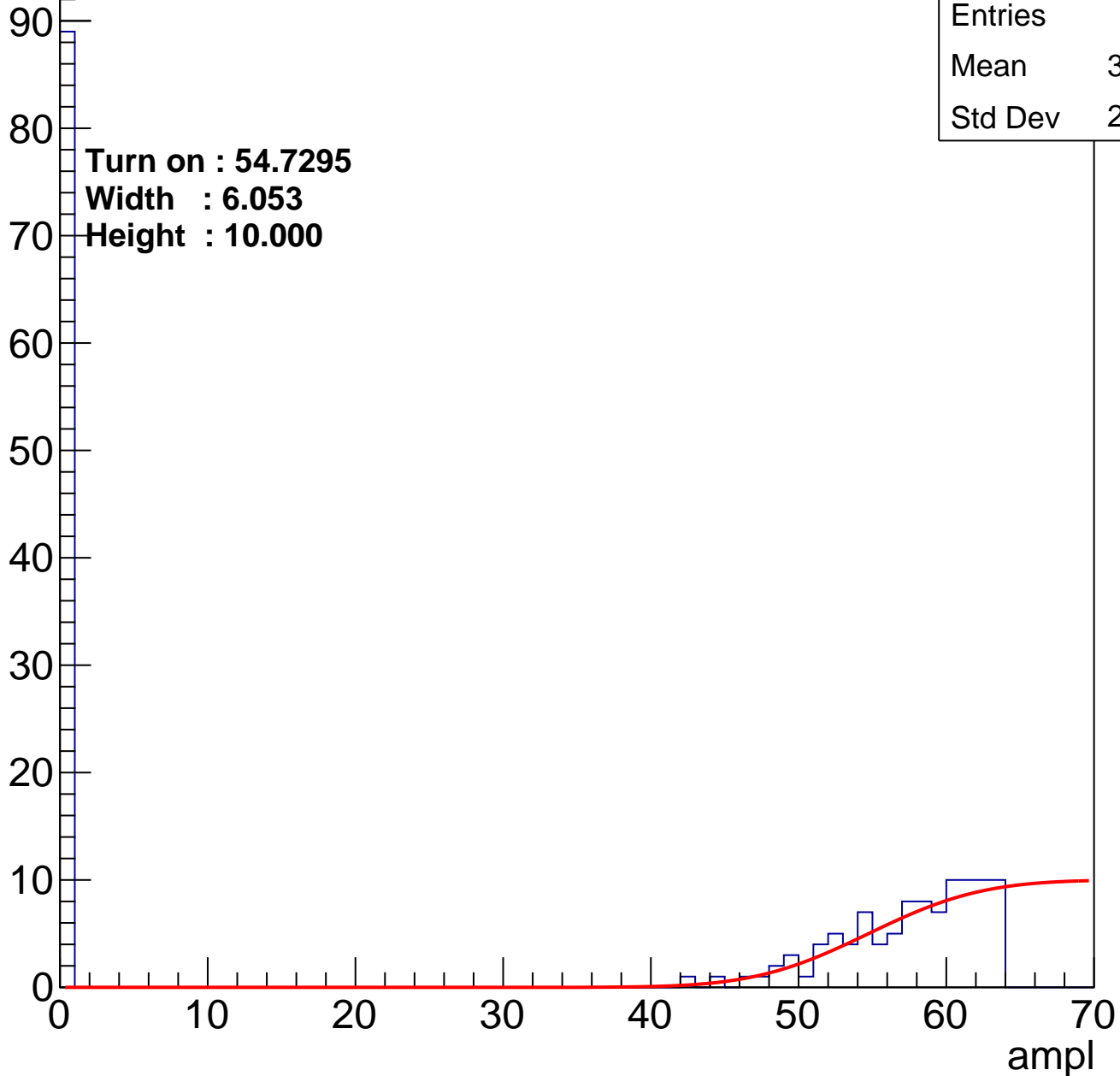
Entries	191
Mean	30.47
Std Dev	28.67

Turn on : 54.7295

Width : 6.053

Height : 10.000

Entry



B1L104S, U11-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	34.74
Std Dev	28.58

Turn on : 54.7352

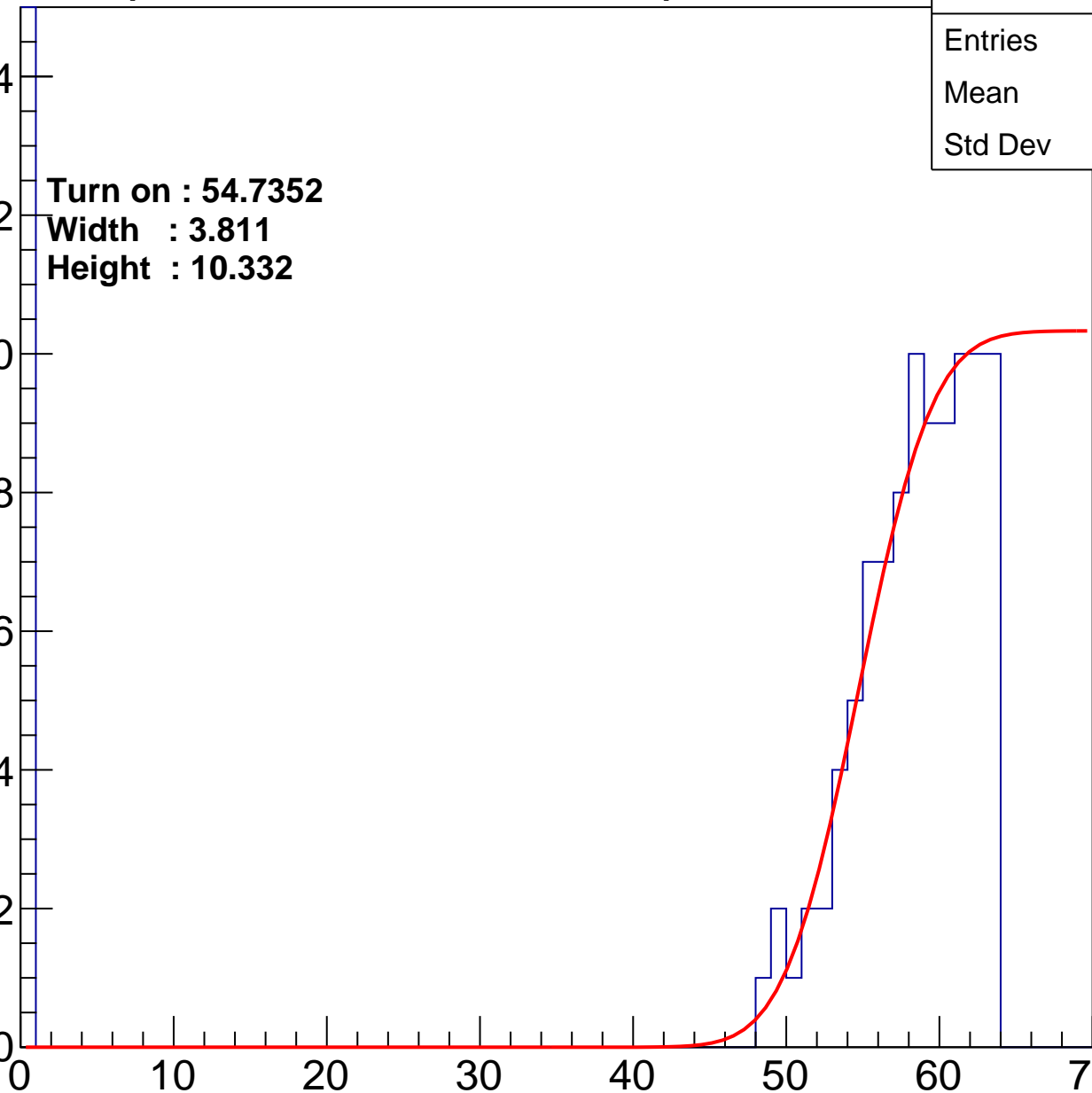
Width : 3.811

Height : 10.332

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	230
Mean	32.67
Std Dev	27.91

Turn on : 52.5840

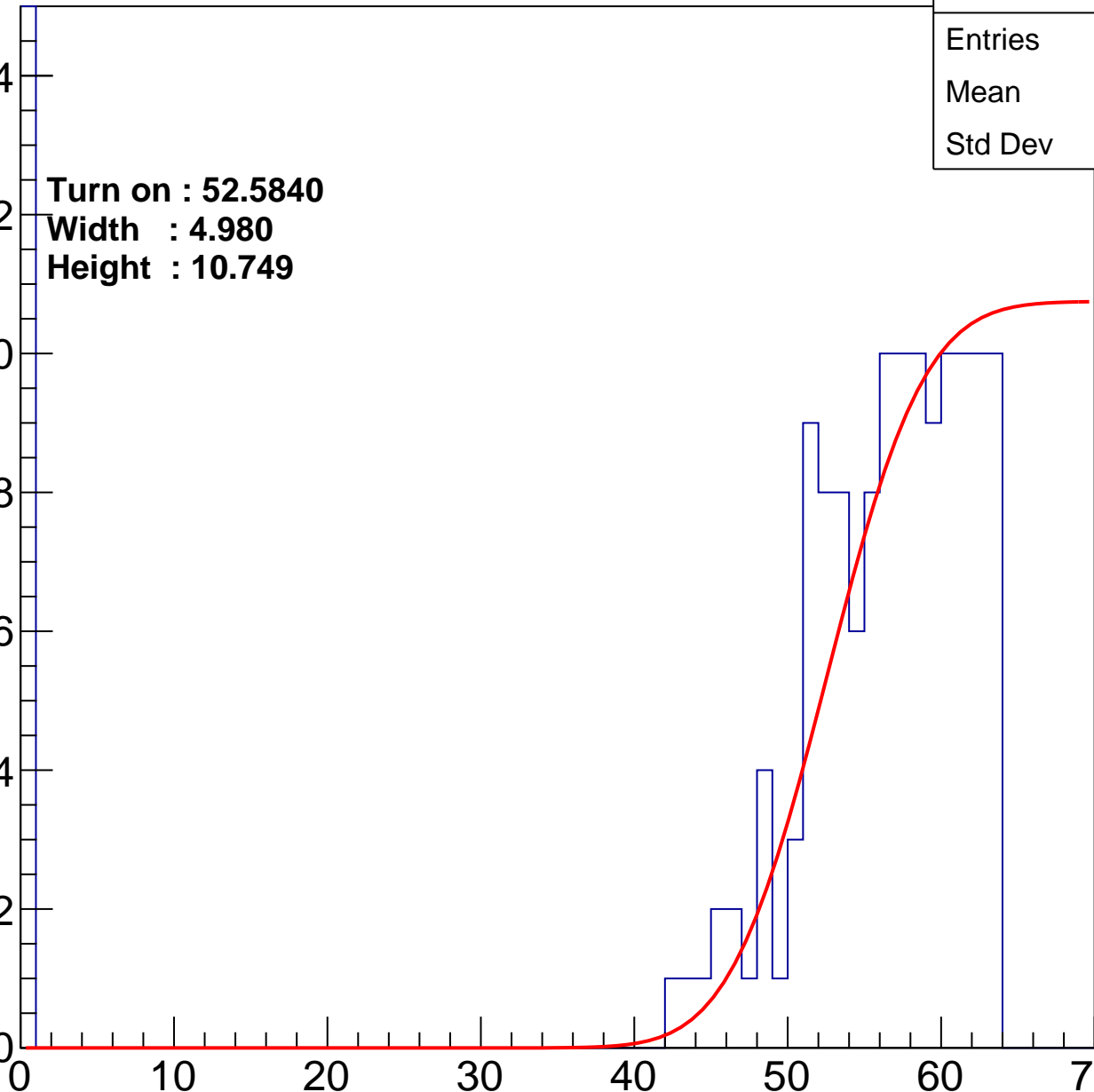
Width : 4.980

Height : 10.749

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	29.01
Std Dev	29.58

Turn on : 55.4997

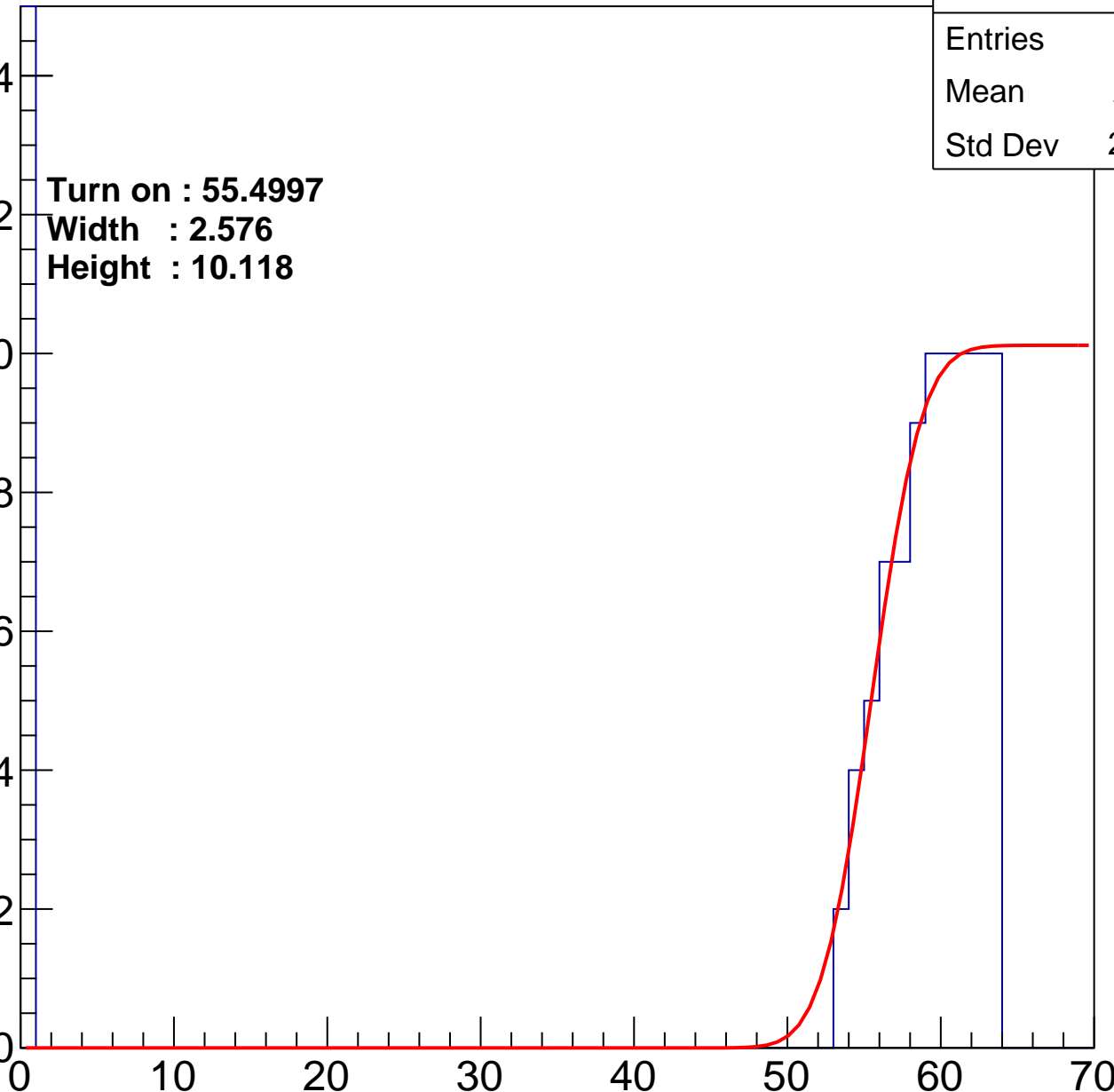
Width : 2.576

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	30.81
Std Dev	28.65

Turn on : 54.0310

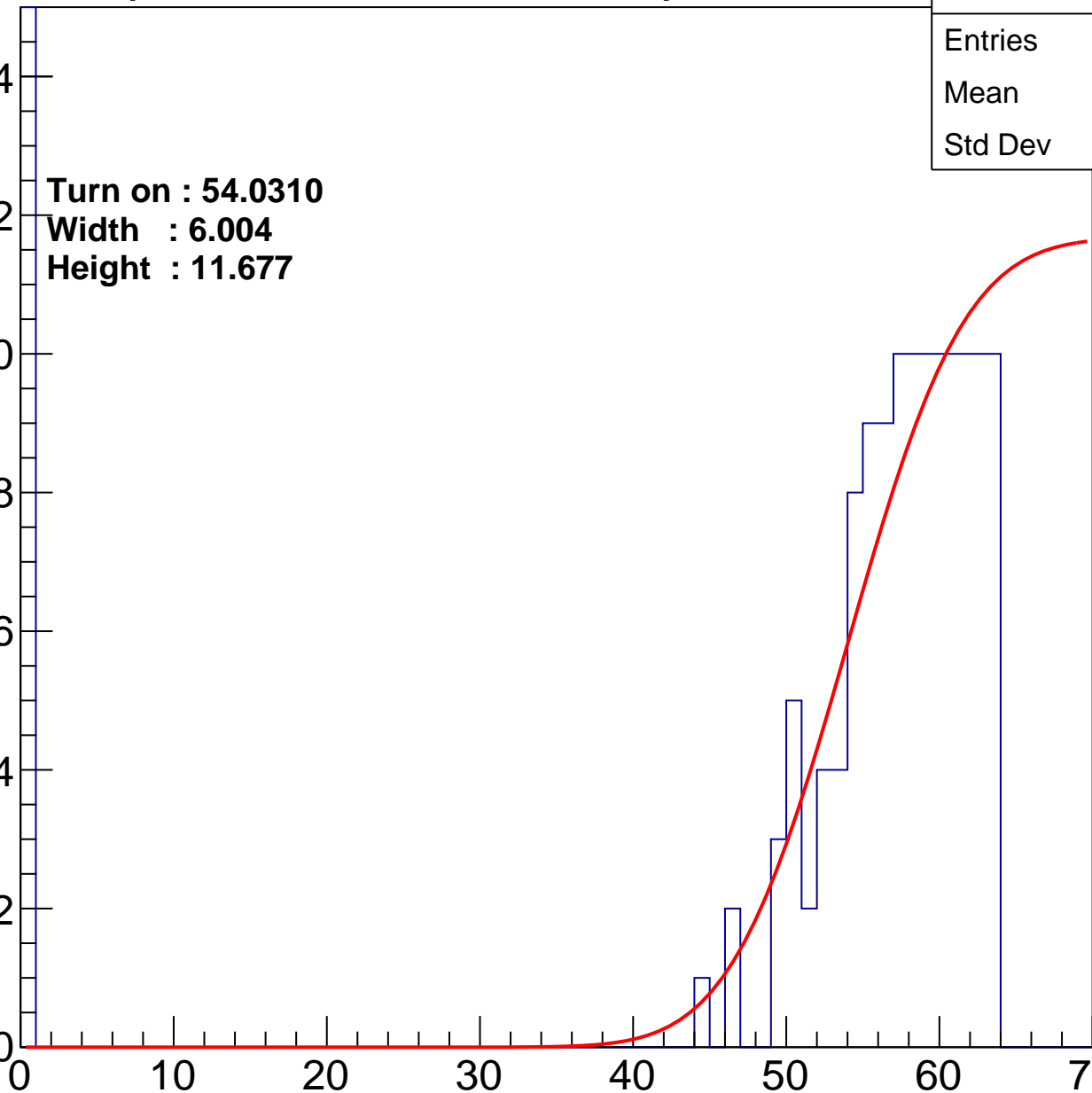
Width : 6.004

Height : 11.677

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	38.23
Std Dev	26.83

Turn on : 51.2367

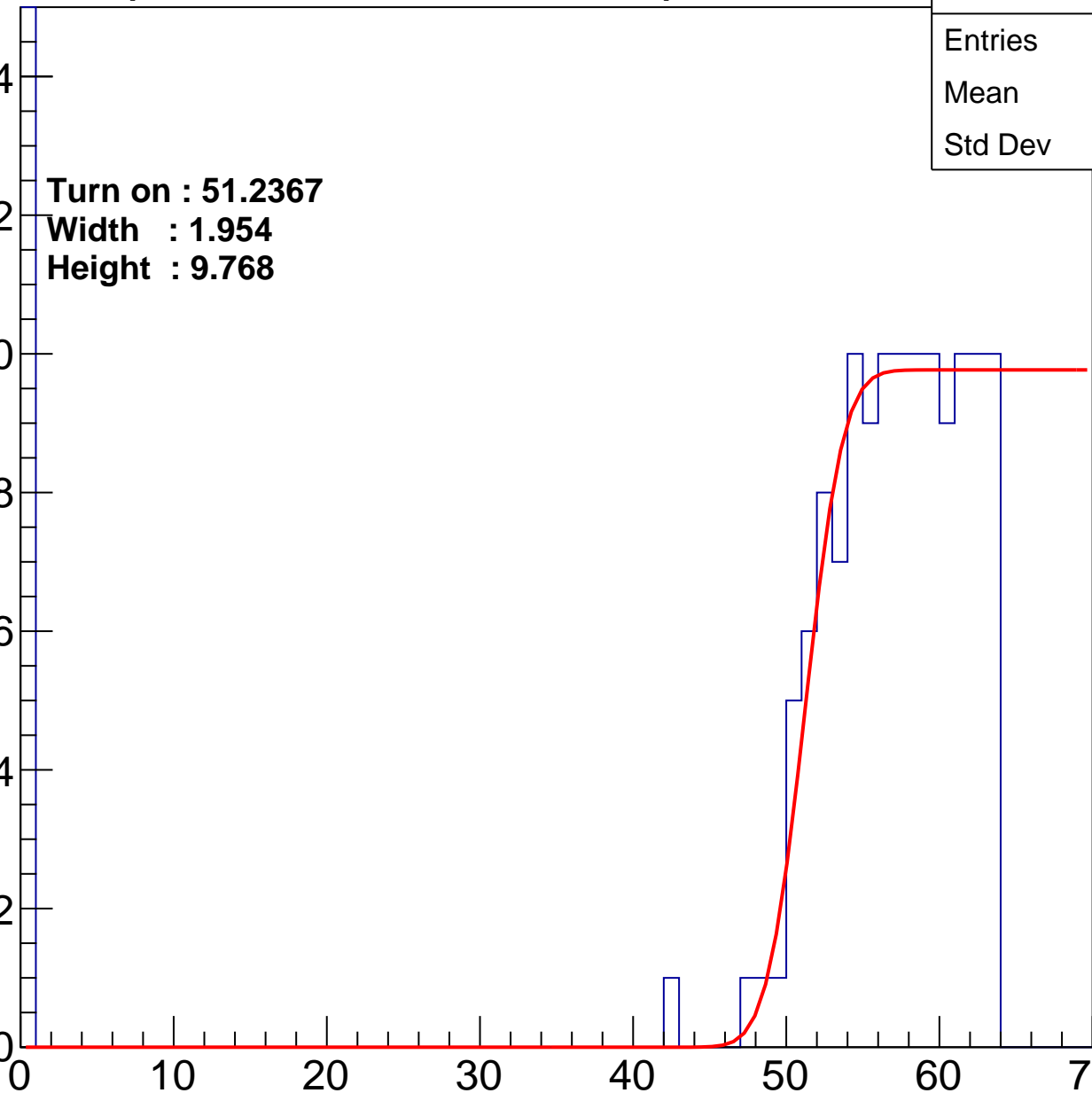
Width : 1.954

Height : 9.768

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	36.08
Std Dev	27.63

Turn on : 52.7026

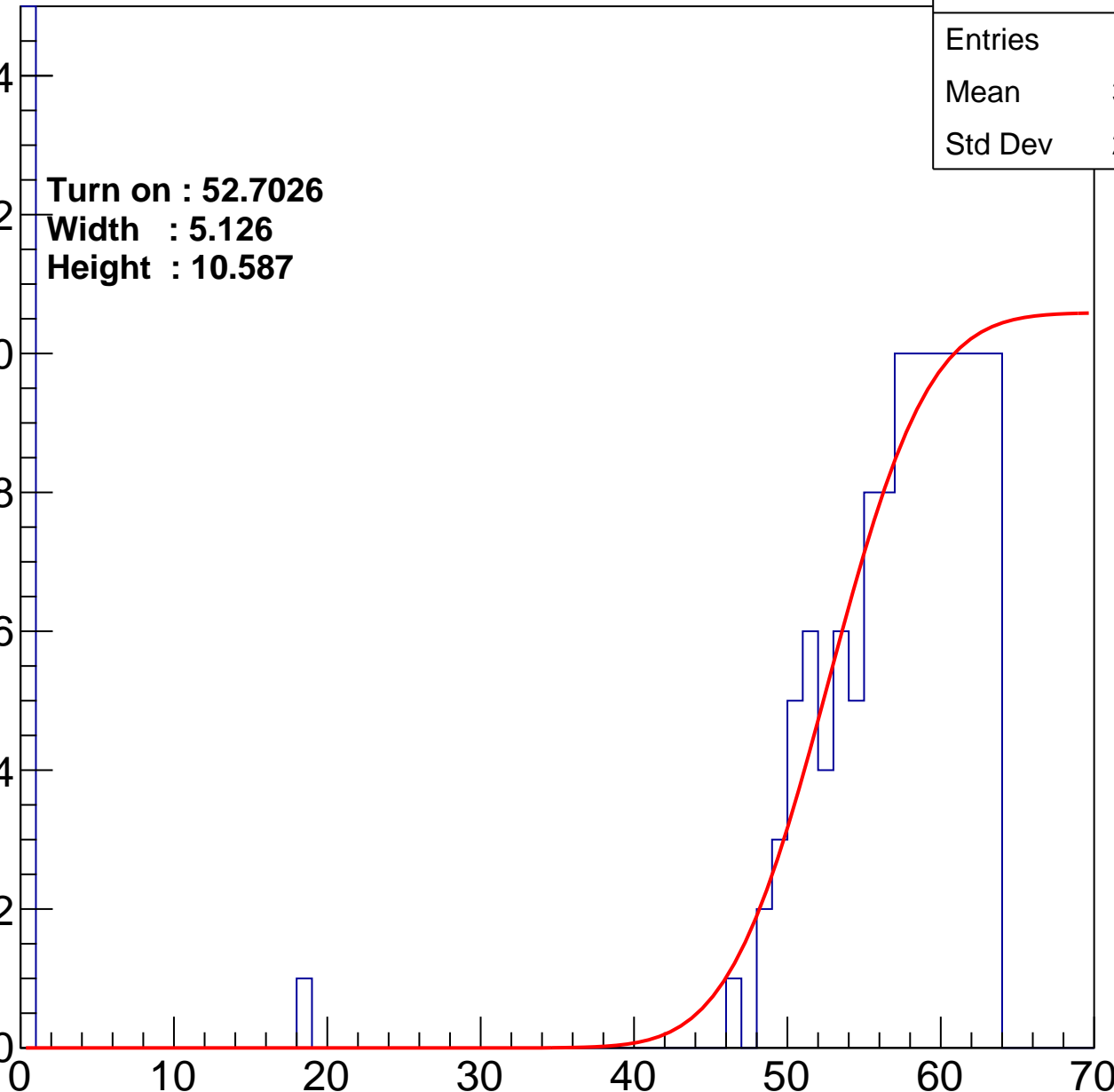
Width : 5.126

Height : 10.587

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	36.77
Std Dev	27.78

Turn on : 53.0637

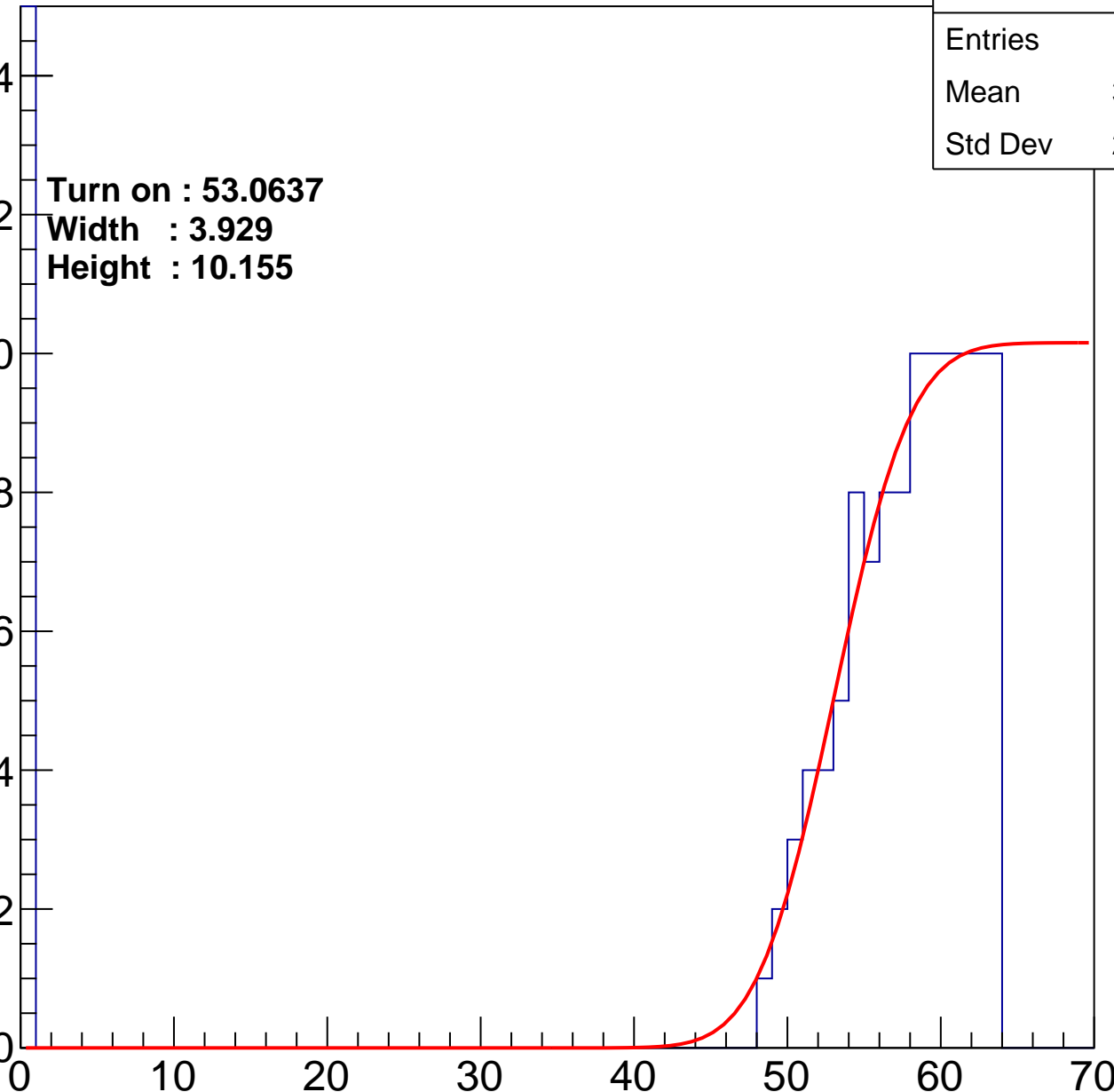
Width : 3.929

Height : 10.155

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	34.52
Std Dev	27.71

Turn on : 51.1772

Width : 3.873

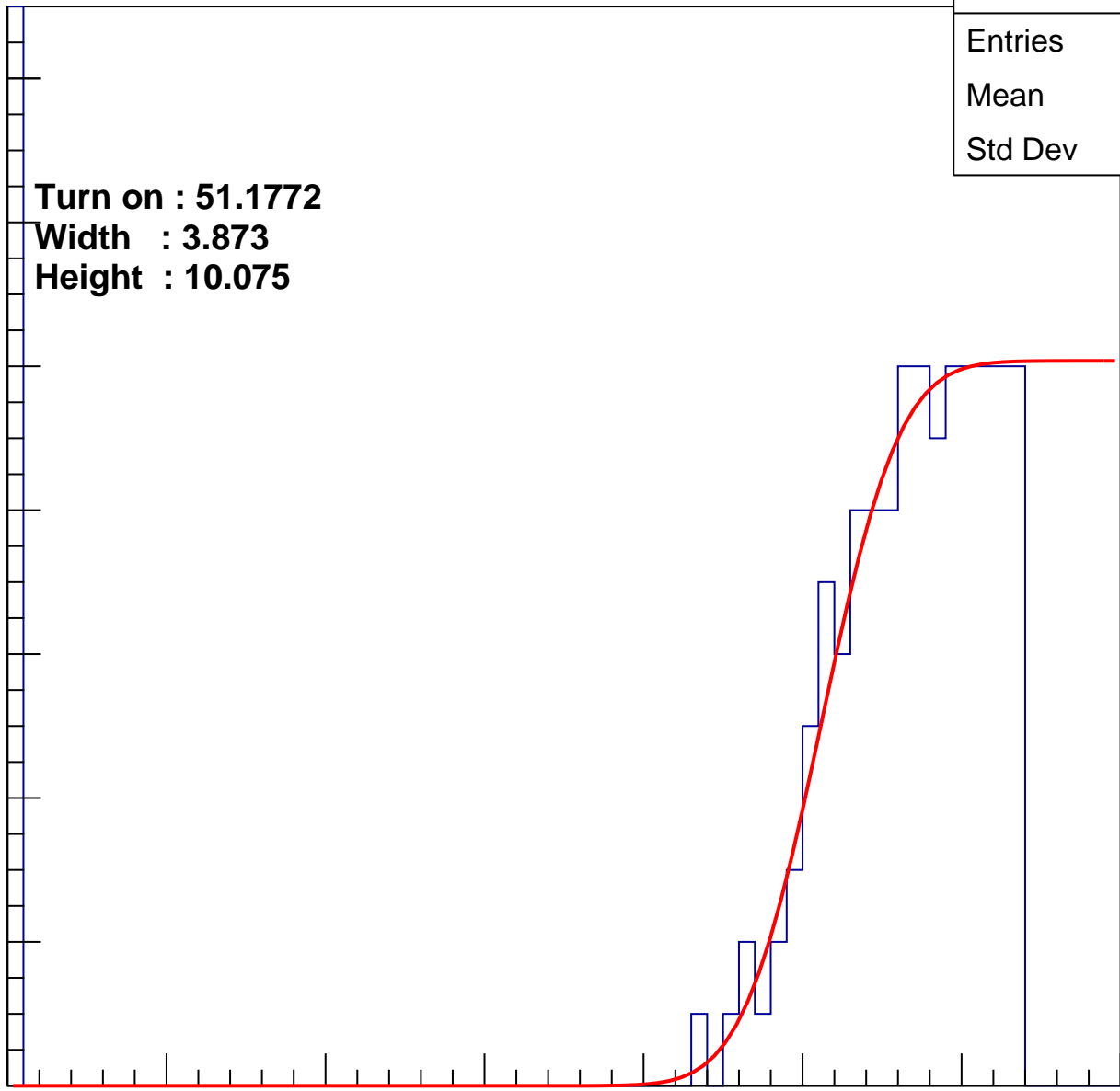
Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch37

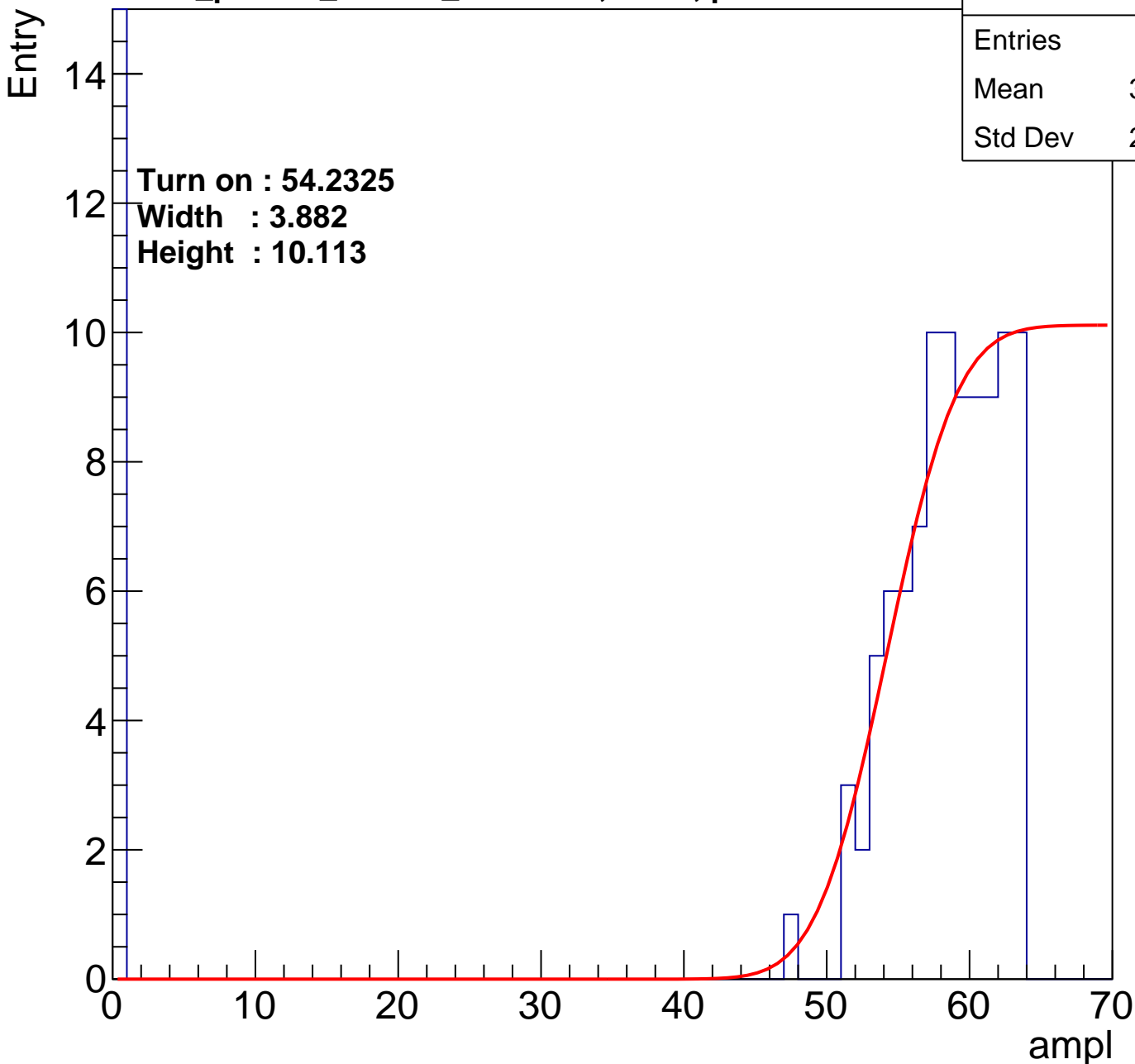
calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	32.57
Std Dev	28.95

Turn on : 54.2325

Width : 3.882

Height : 10.113



B1L104S, U11-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	33.84
Std Dev	27.94

Turn on : 53.0418

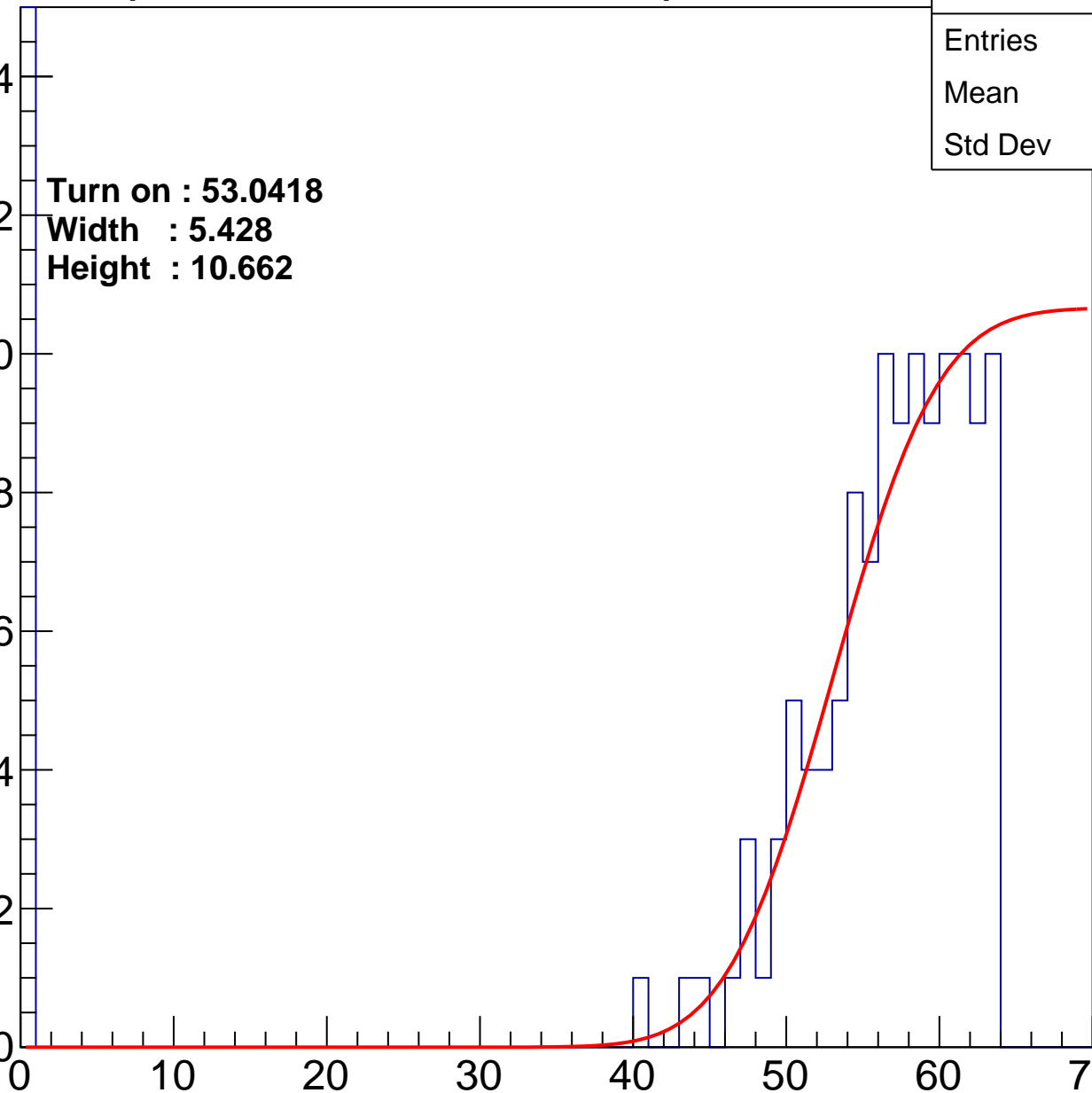
Width : 5.428

Height : 10.662

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	35.5
Std Dev	28.09

Turn on : 53.4754

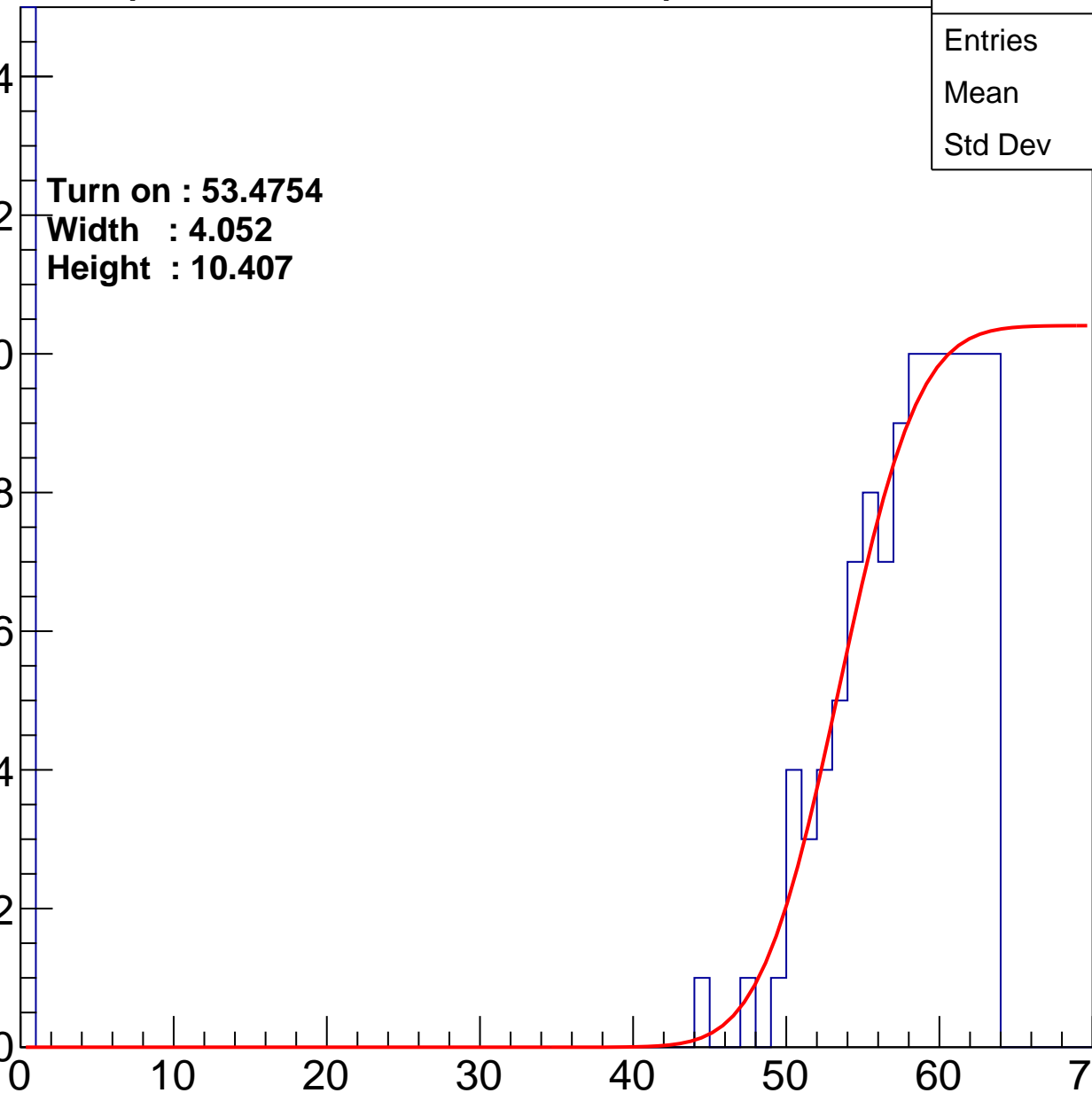
Width : 4.052

Height : 10.407

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	29.53
Std Dev	28.68

Turn on : 54.0996

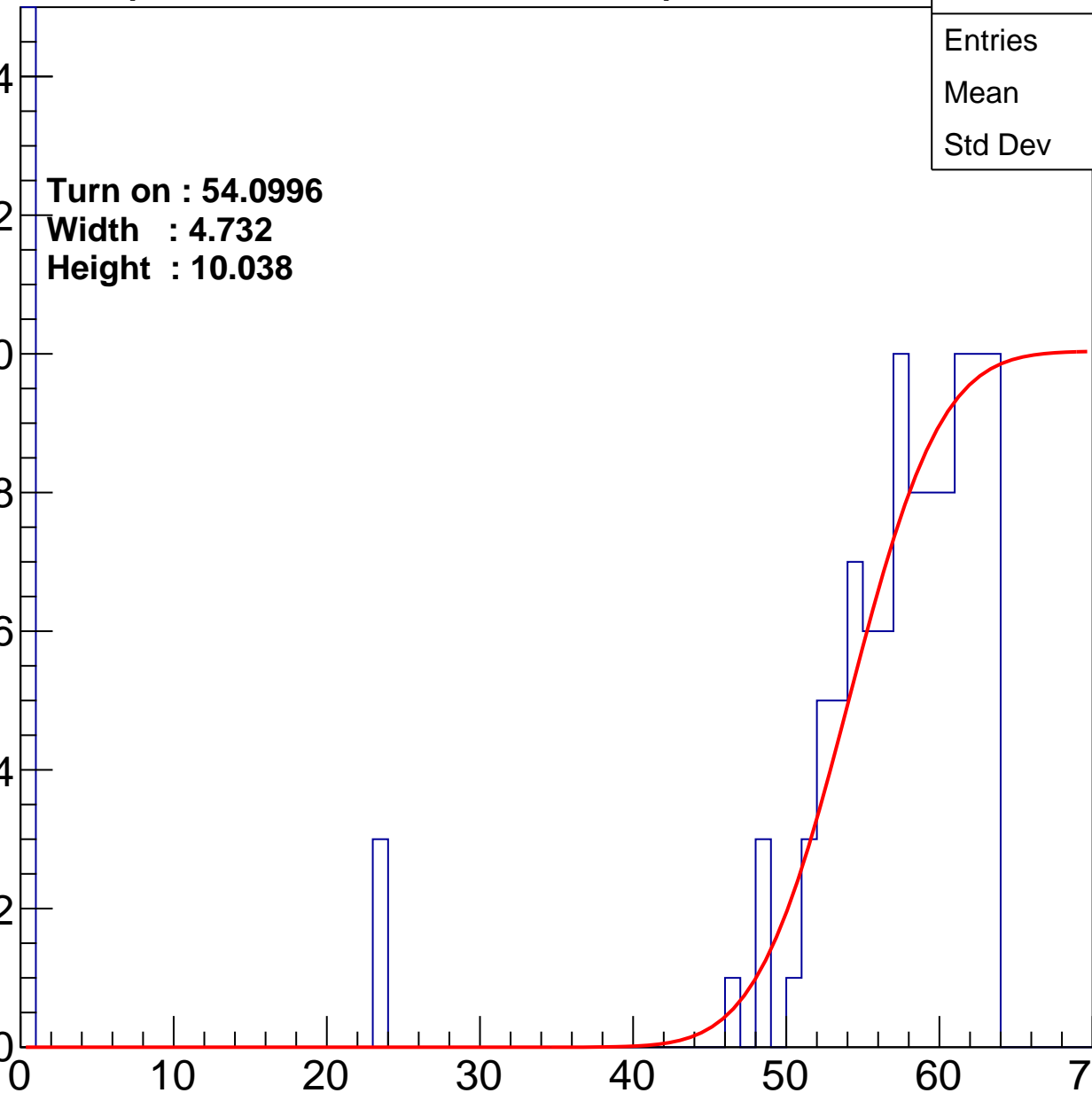
Width : 4.732

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	29.35
Std Dev	29.05

Turn on : 53.7552

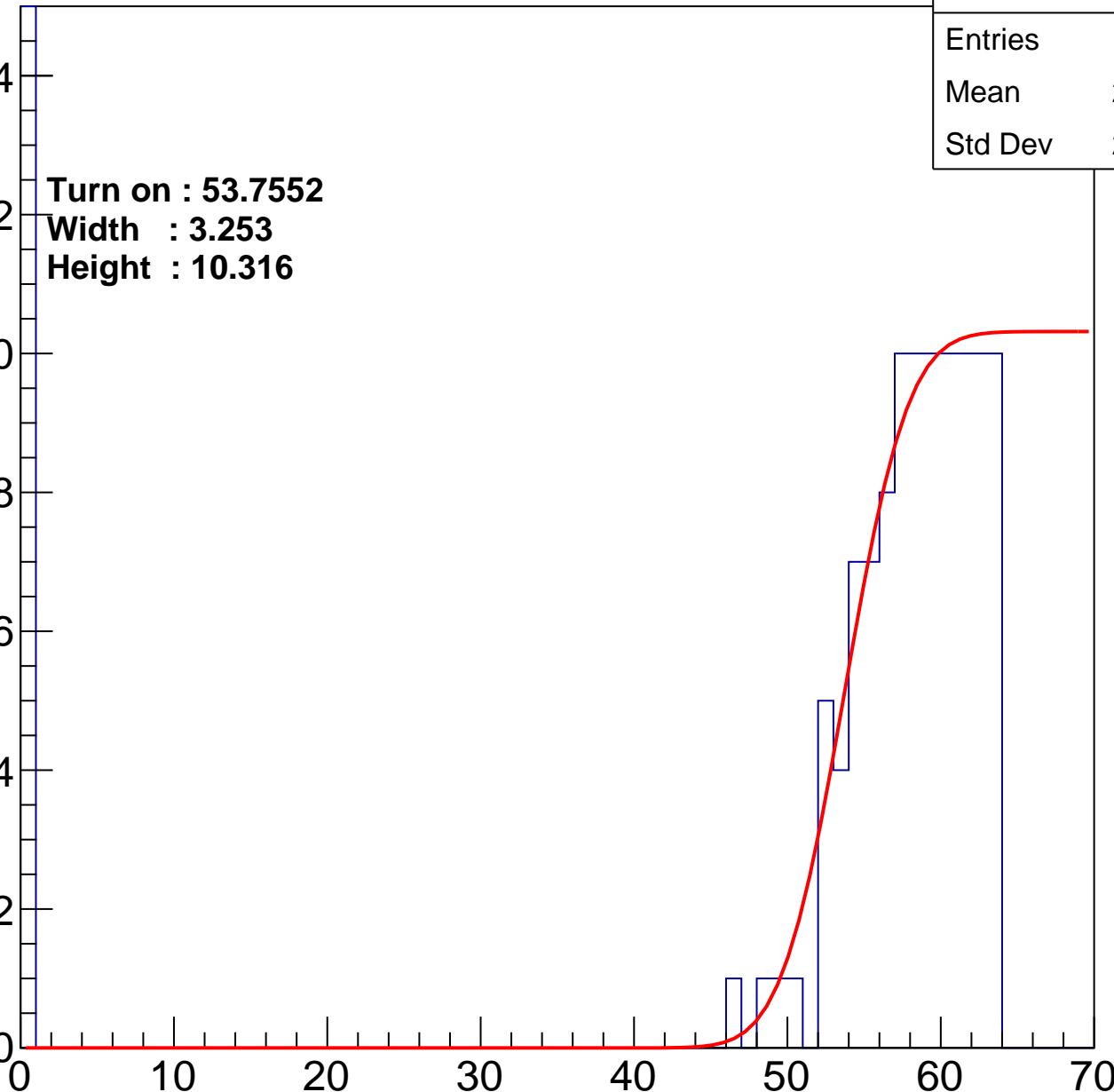
Width : 3.253

Height : 10.316

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch42

calib_packv5_033123_0516.root, FC#4, port A1

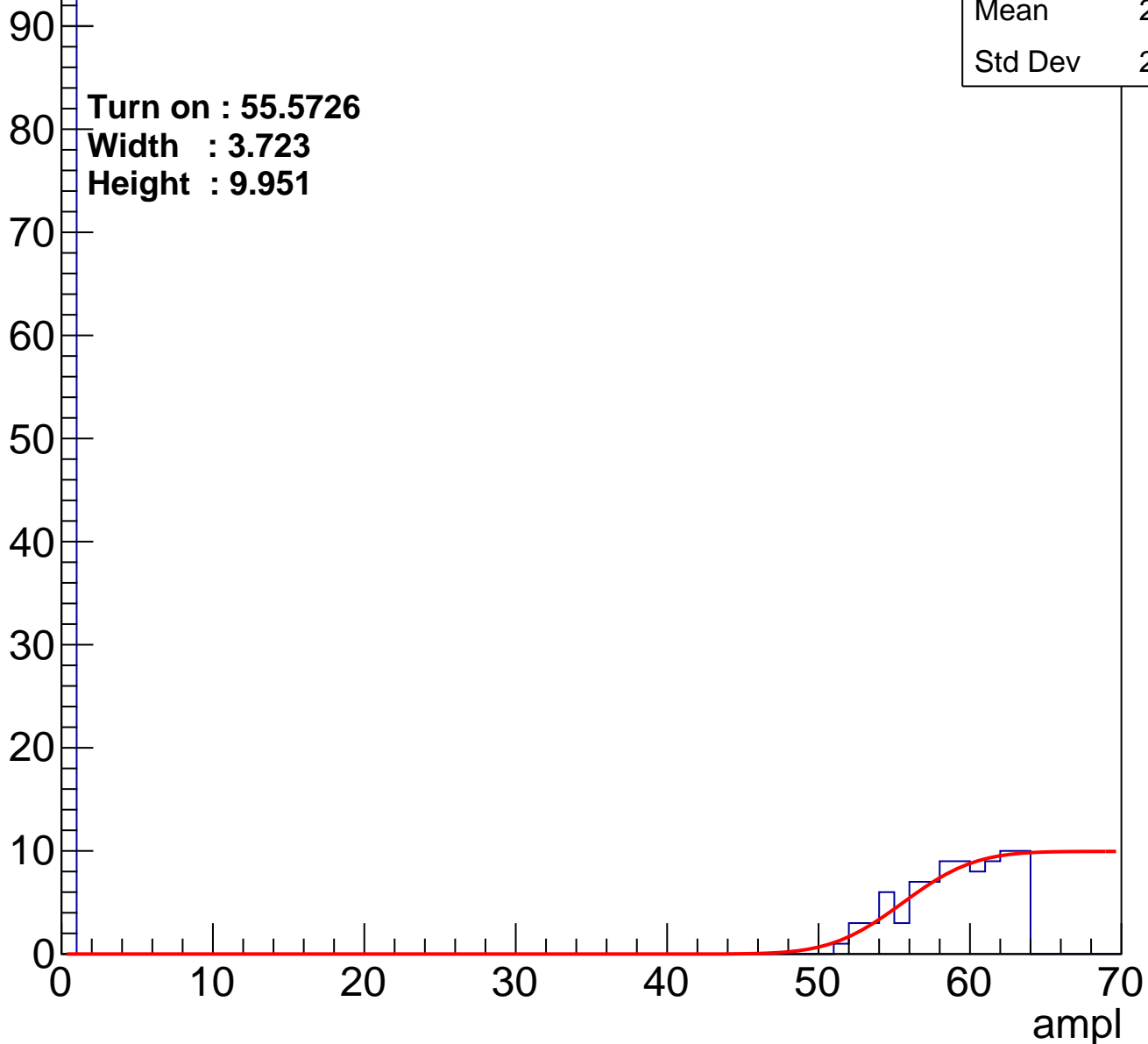
Entries	180
Mean	27.66
Std Dev	29.32

Turn on : 55.5726

Width : 3.723

Height : 9.951

Entry



B1L104S, U11-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	30.42
Std Dev	29.14

Turn on : 54.3228

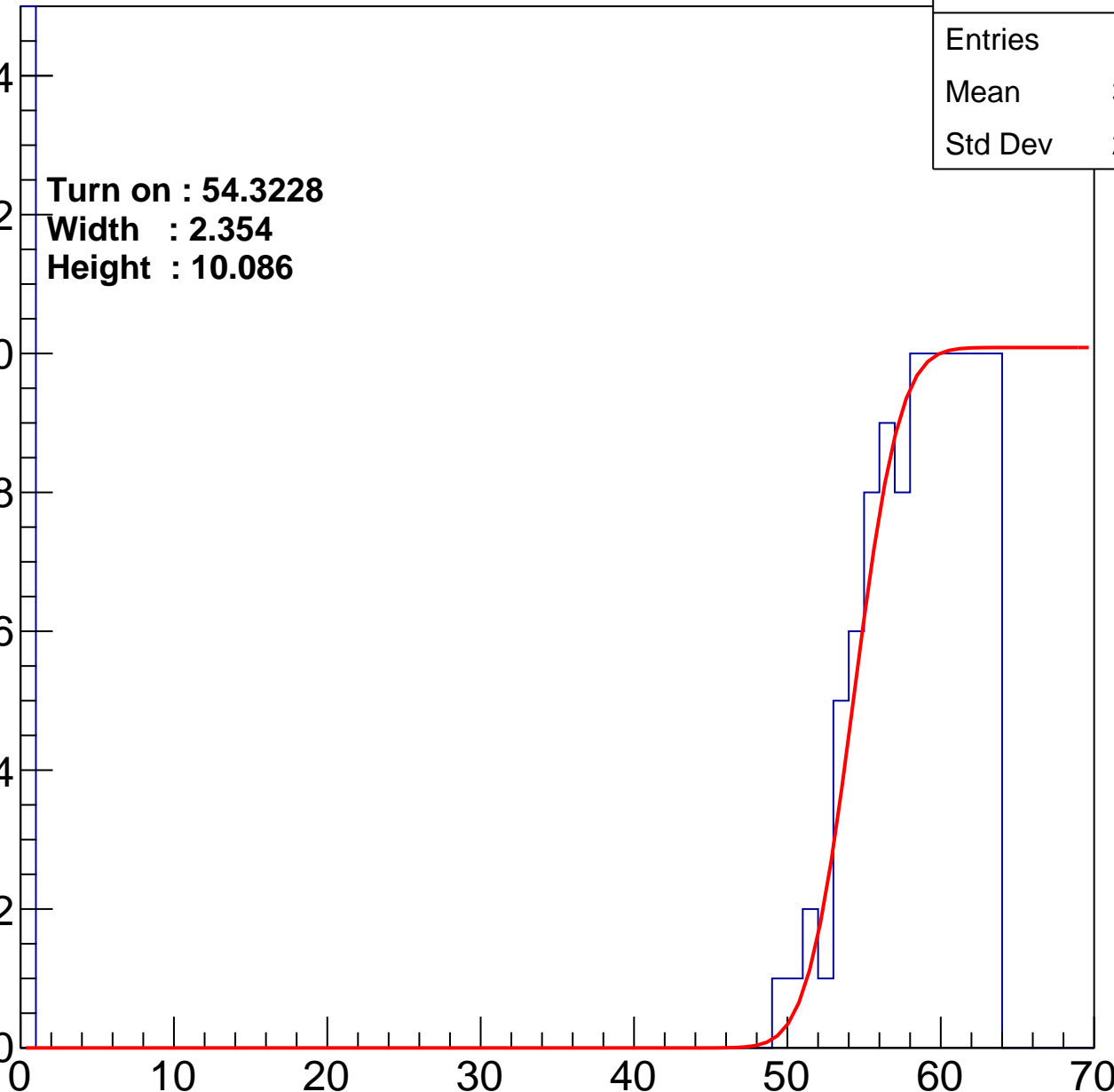
Width : 2.354

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	248
Mean	32.26
Std Dev	27.7

Turn on : 50.0942

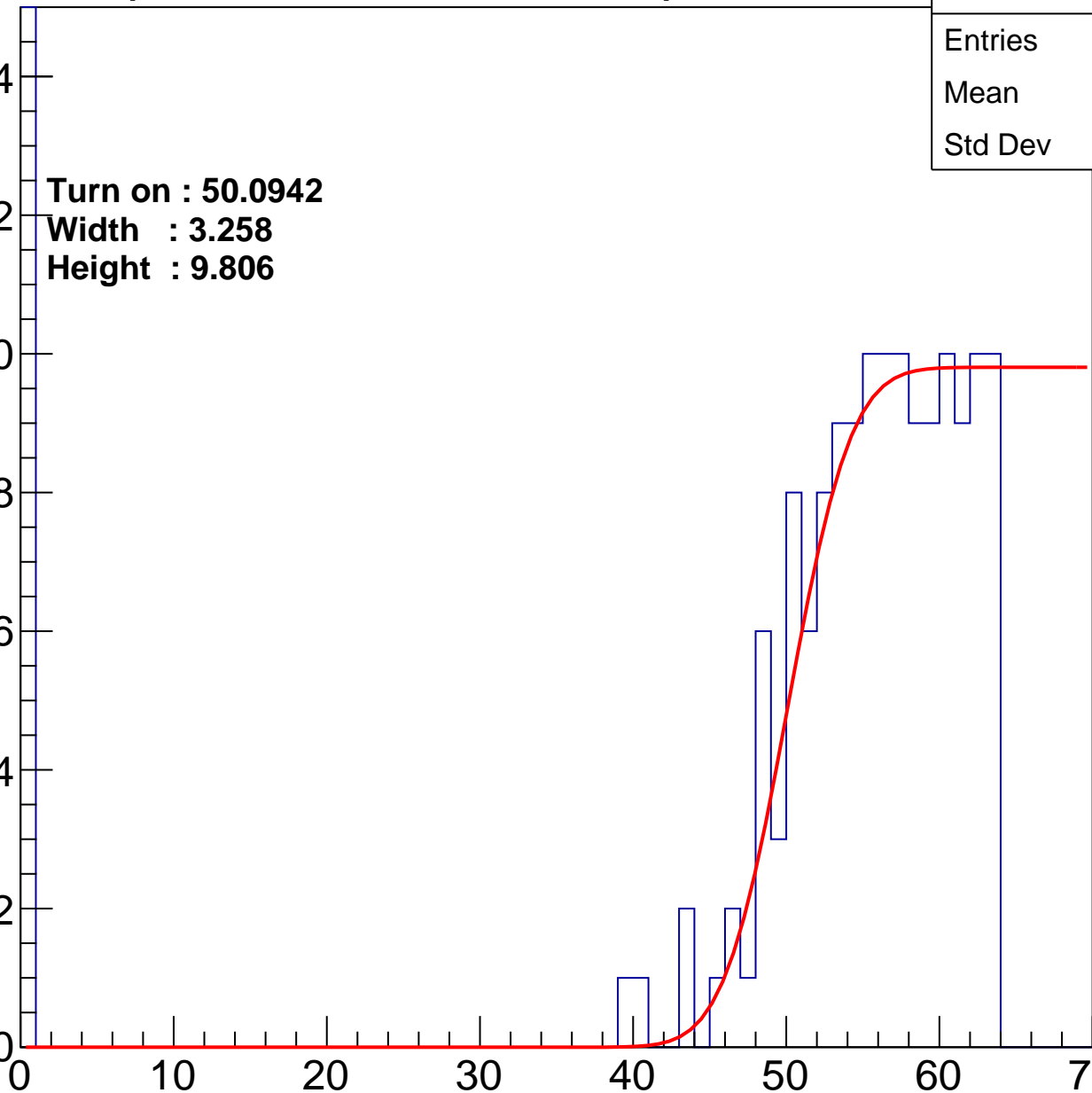
Width : 3.258

Height : 9.806

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	164
Mean	36.14
Std Dev	28.31

Turn on : 53.7324

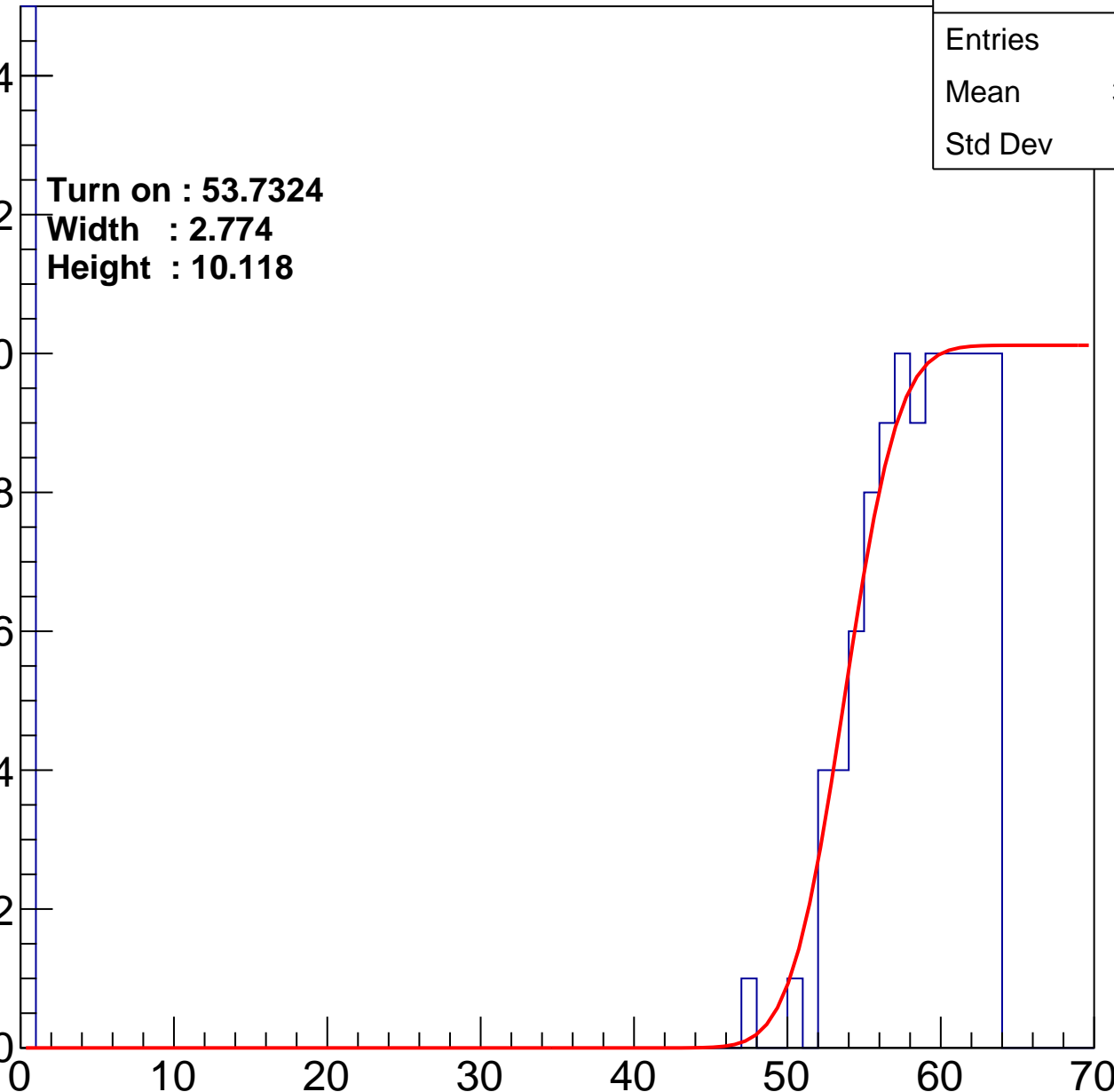
Width : 2.774

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.2
Std Dev	28.3

Turn on : 53.2006

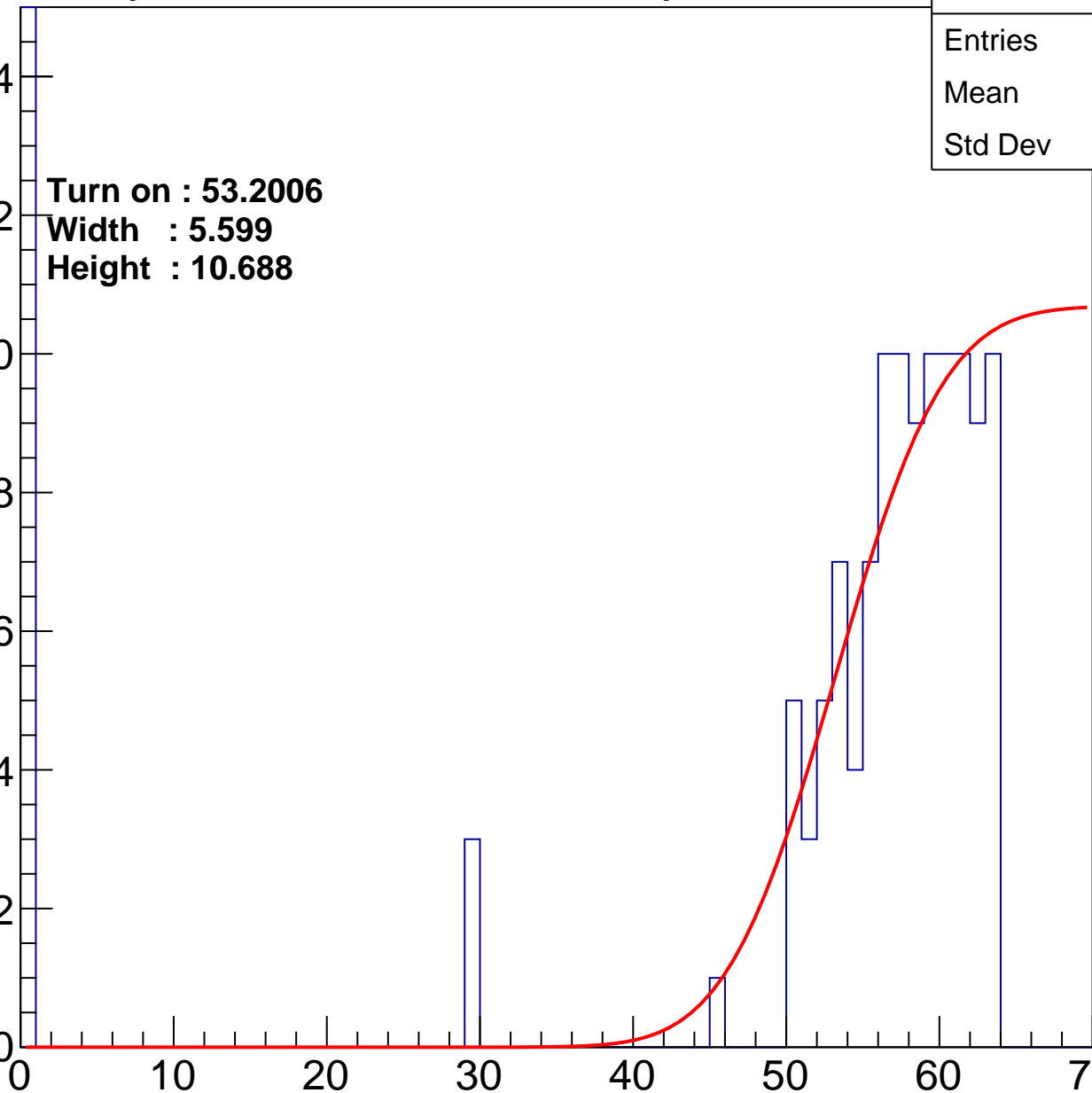
Width : 5.599

Height : 10.688

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	34.66
Std Dev	28.34

Turn on : 53.9473

Width : 3.567

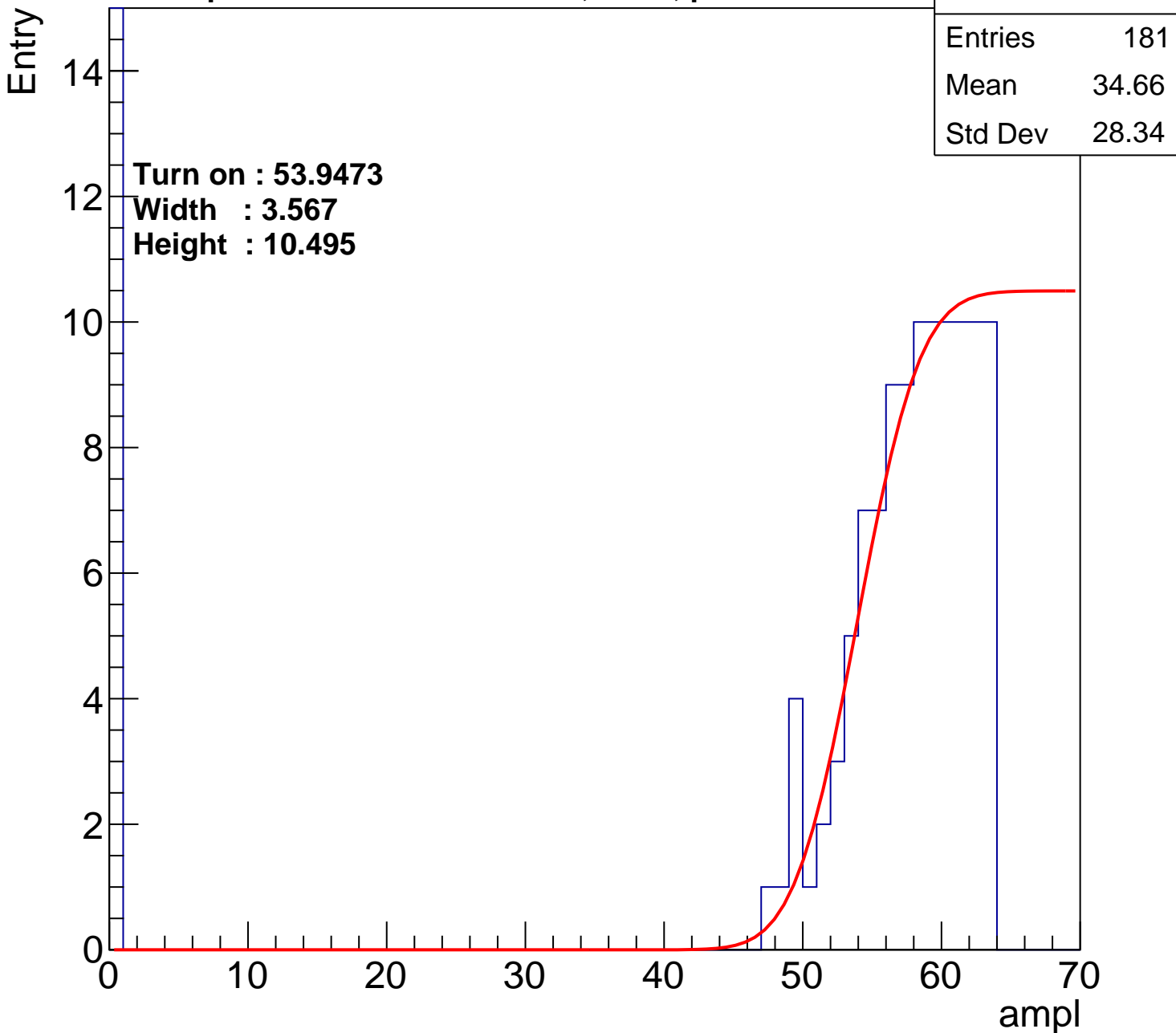
Height : 10.495

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch48

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	31.87
Std Dev	28.38

Turn on : 52.6954

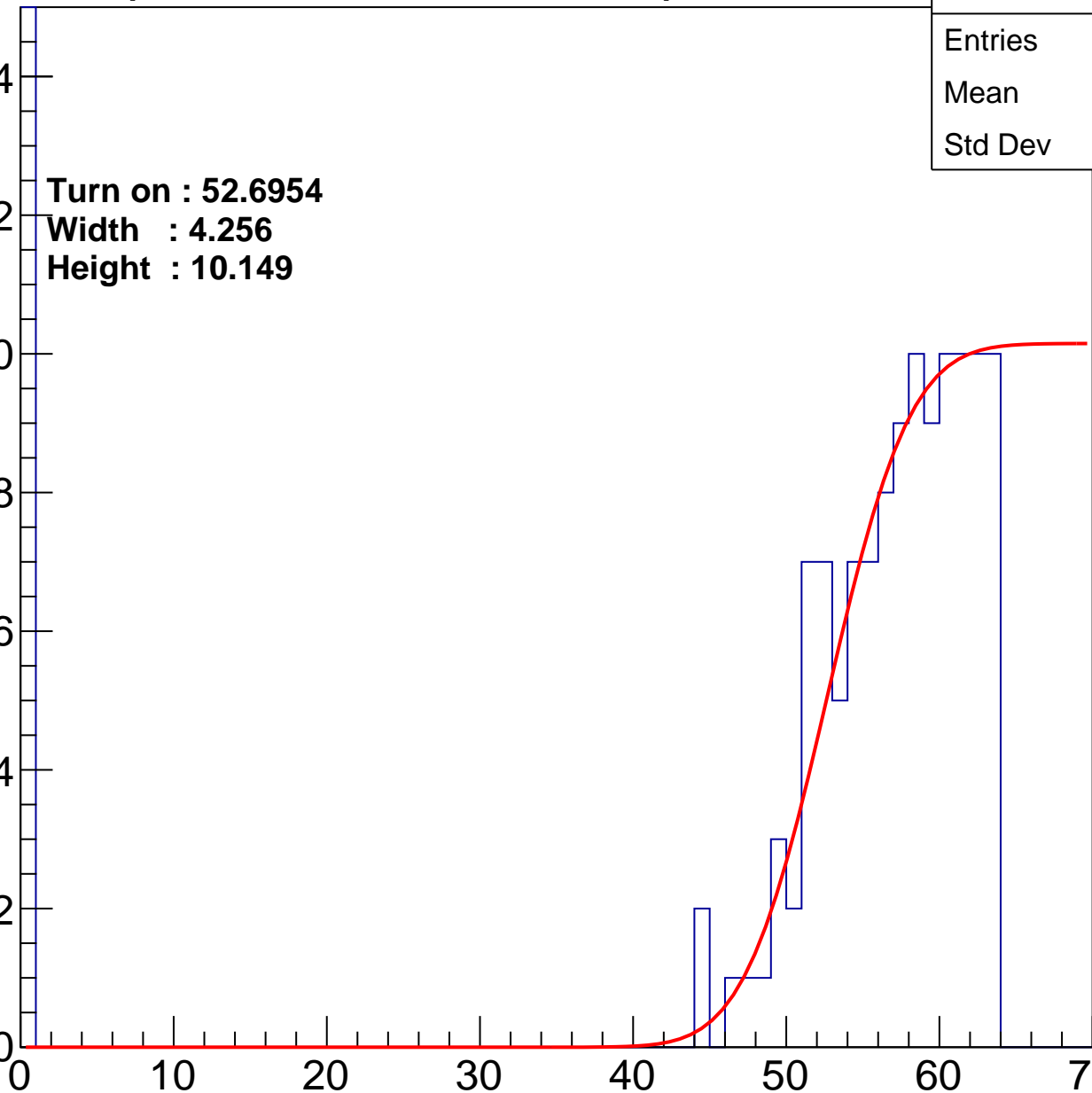
Width : 4.256

Height : 10.149

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	33.37
Std Dev	28.54

Turn on : 53.1046

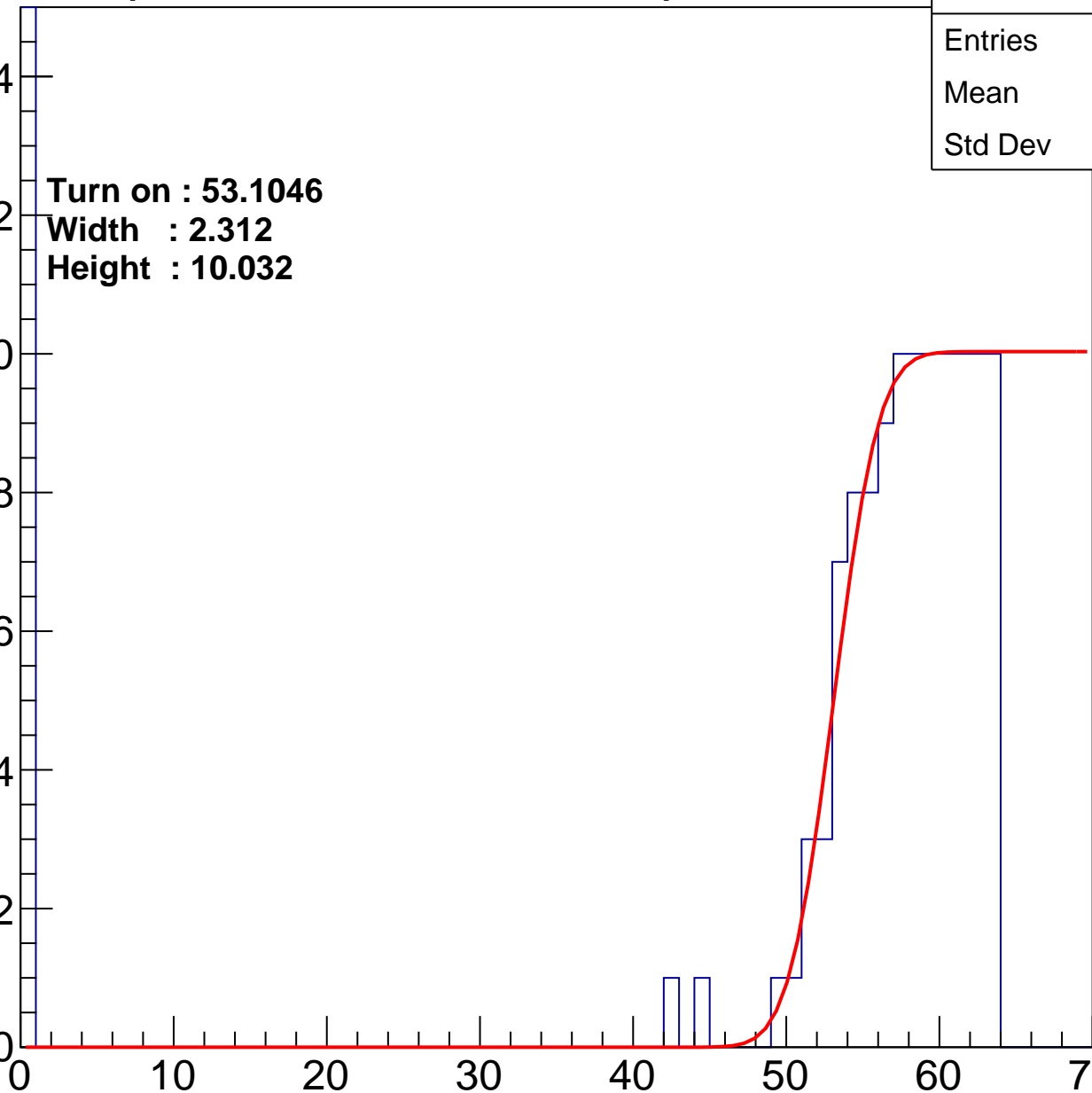
Width : 2.312

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	31.05
Std Dev	28.72

Turn on : 54.0527

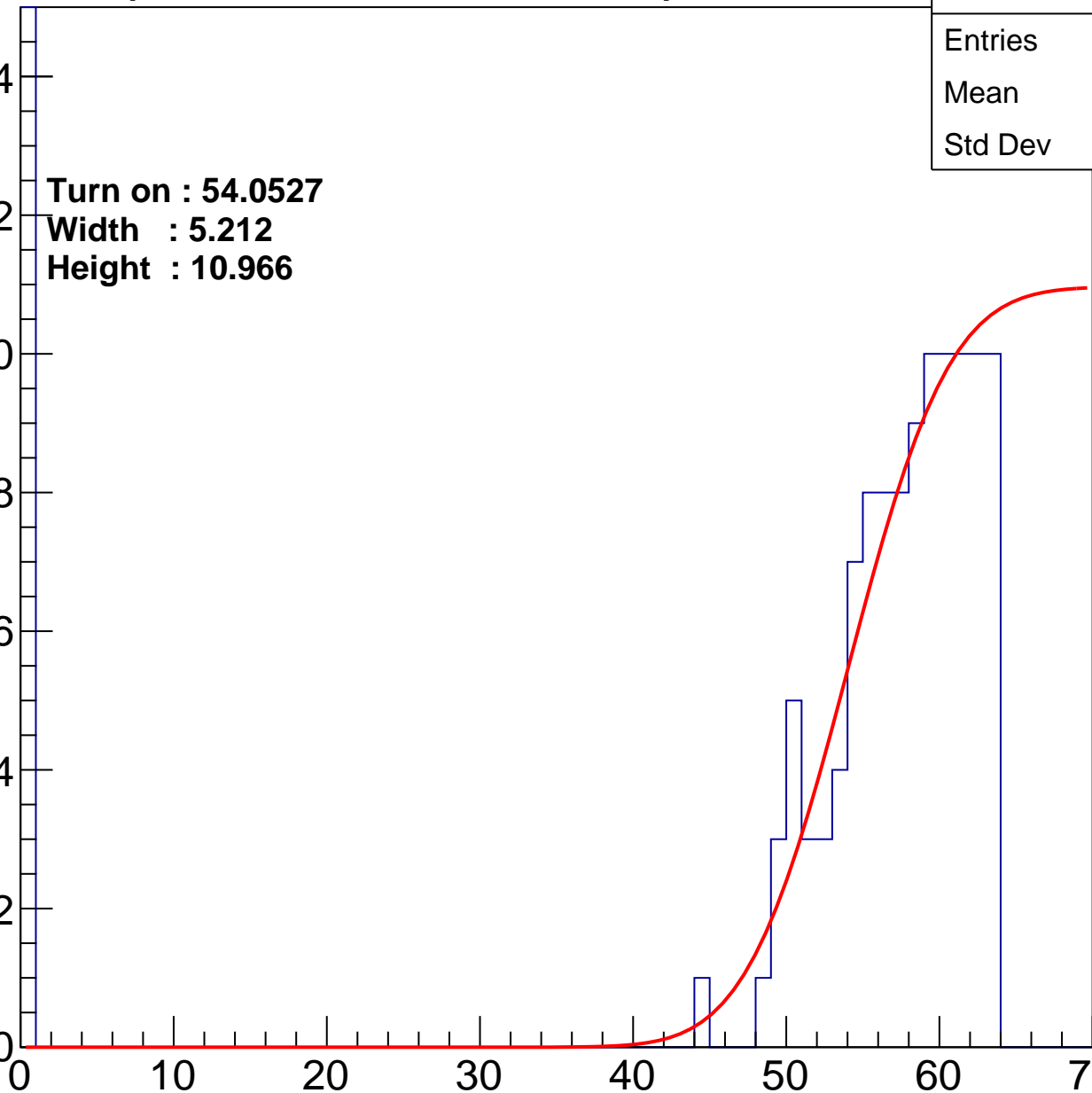
Width : 5.212

Height : 10.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch51

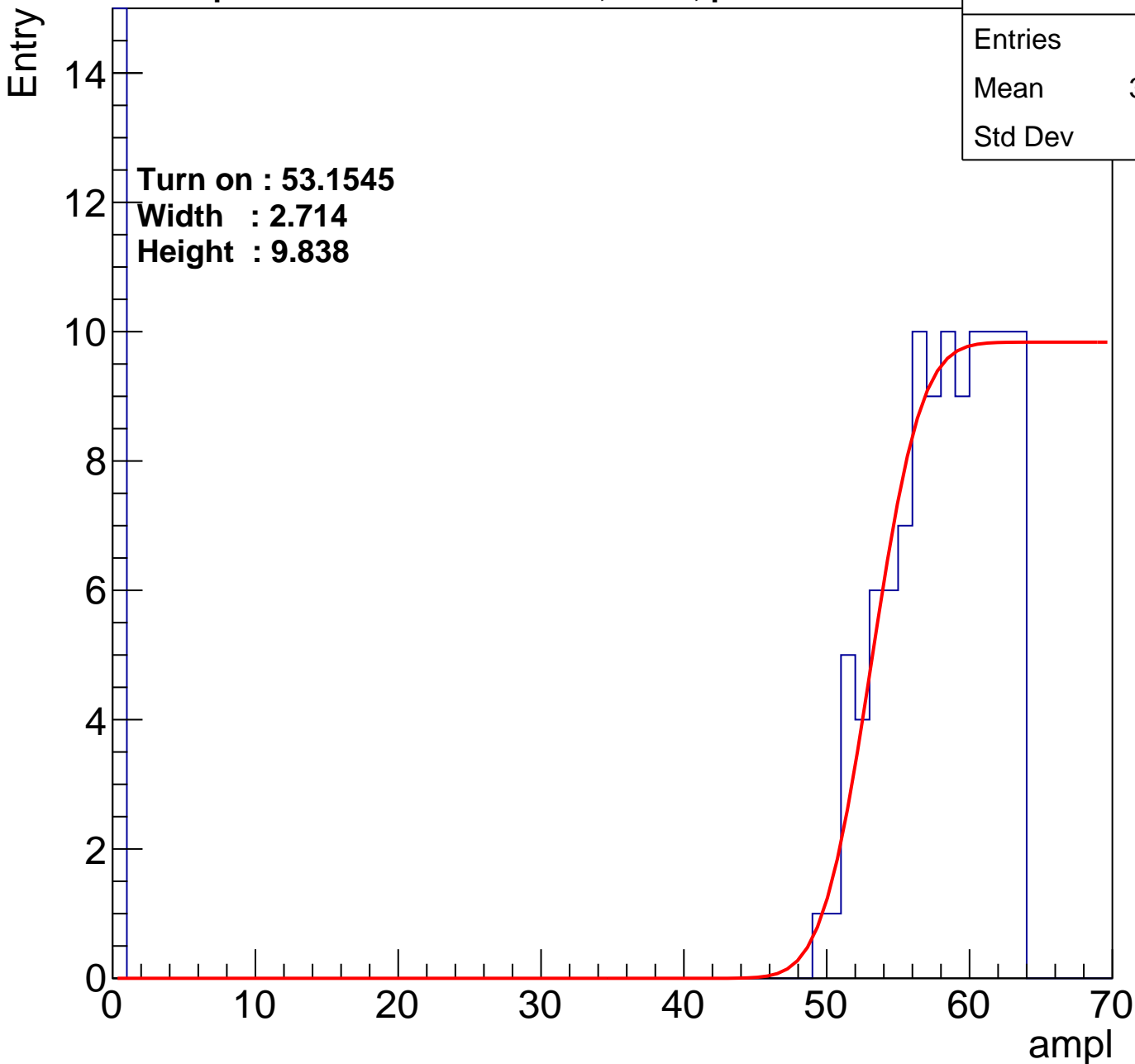
calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.62
Std Dev	28.2

Turn on : 53.1545

Width : 2.714

Height : 9.838



B1L104S, U11-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	30.61
Std Dev	28.7

Turn on : 55.3032

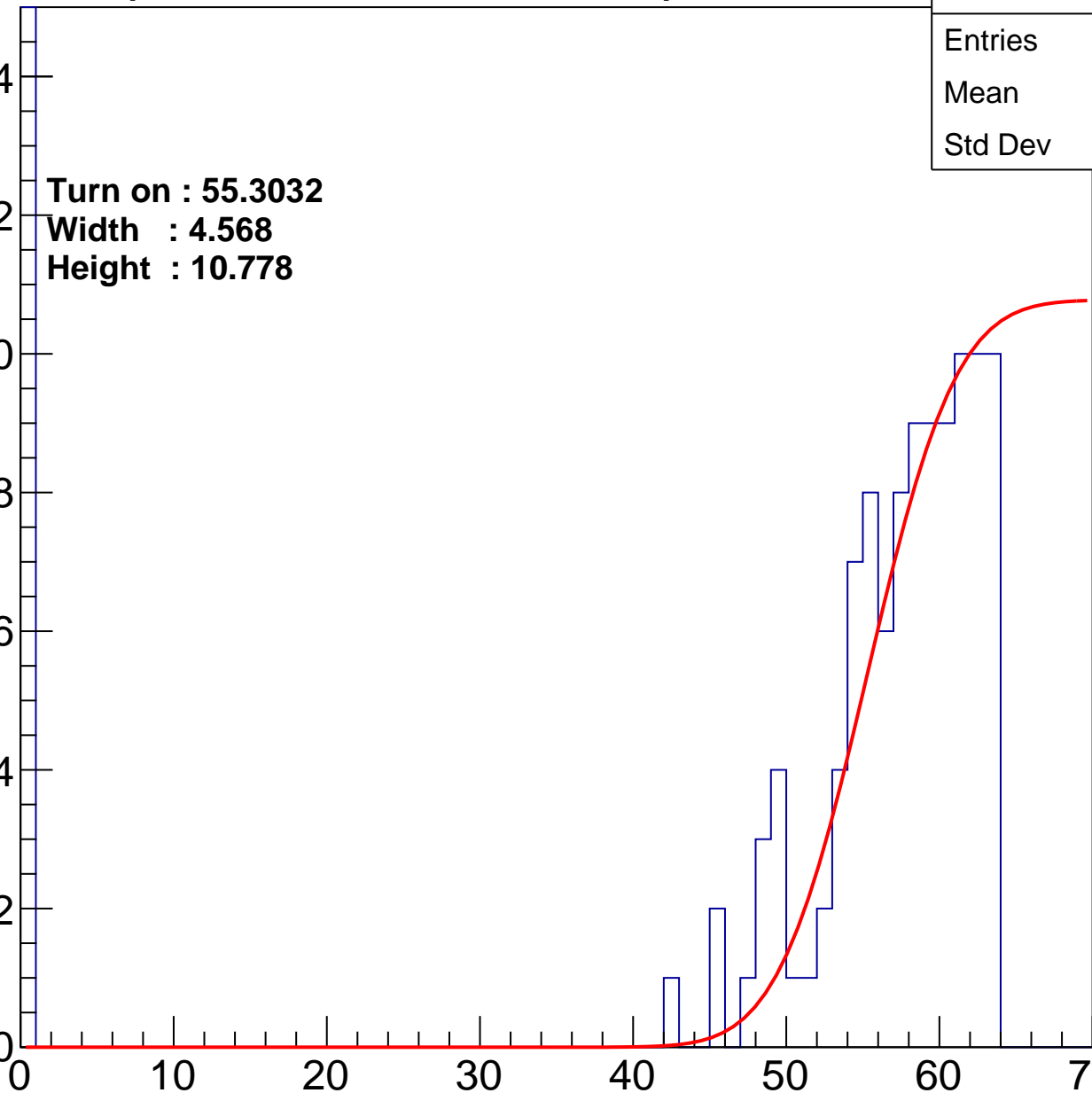
Width : 4.568

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch53

calib_packv5_033123_0516.root, FC#4, port A1

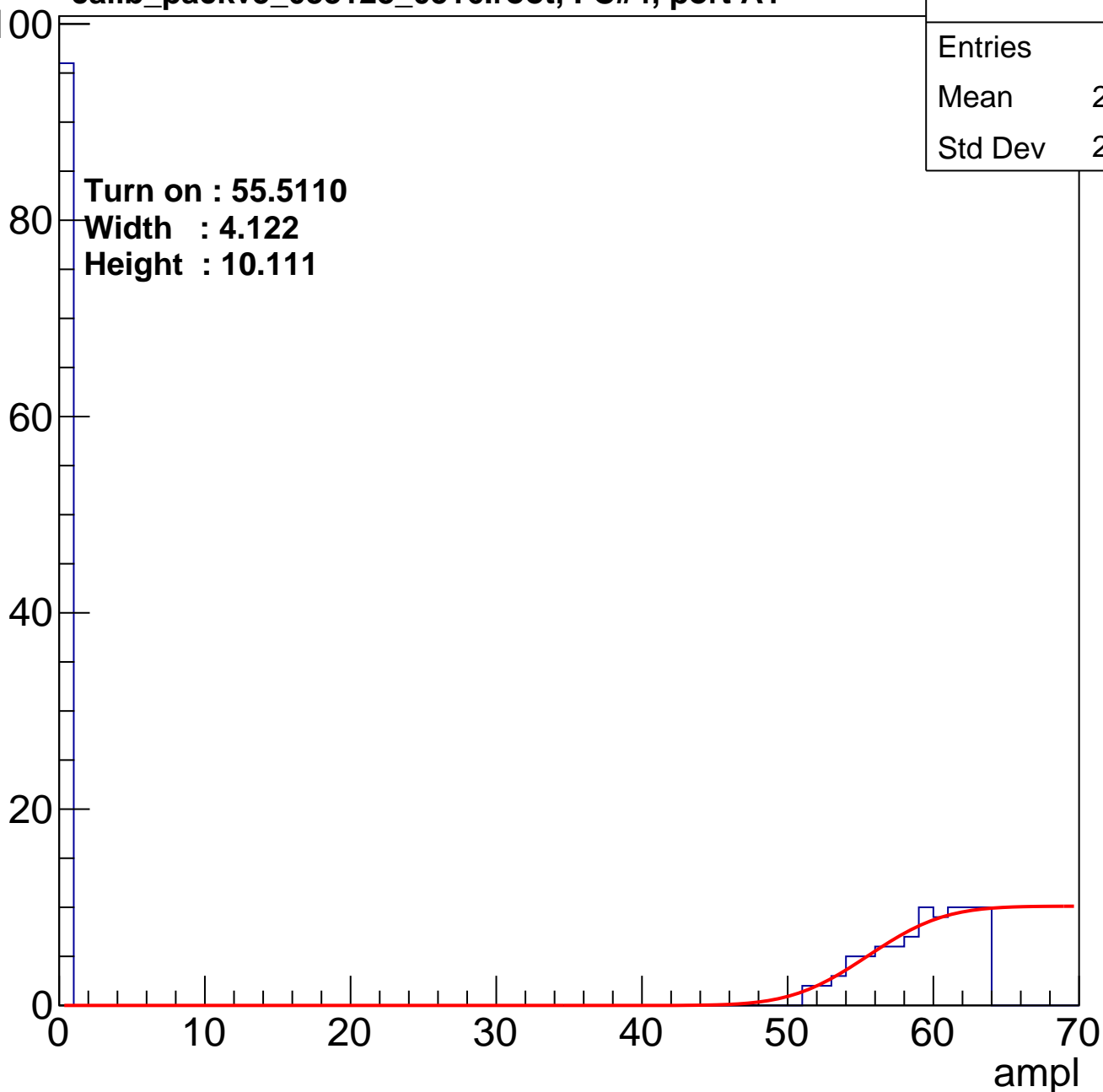
Entry

Entries	181
Mean	27.54
Std Dev	29.35

Turn on : 55.5110

Width : 4.122

Height : 10.111



B1L104S, U11-ch54

calib_packv5_033123_0516.root, FC#4, port A1

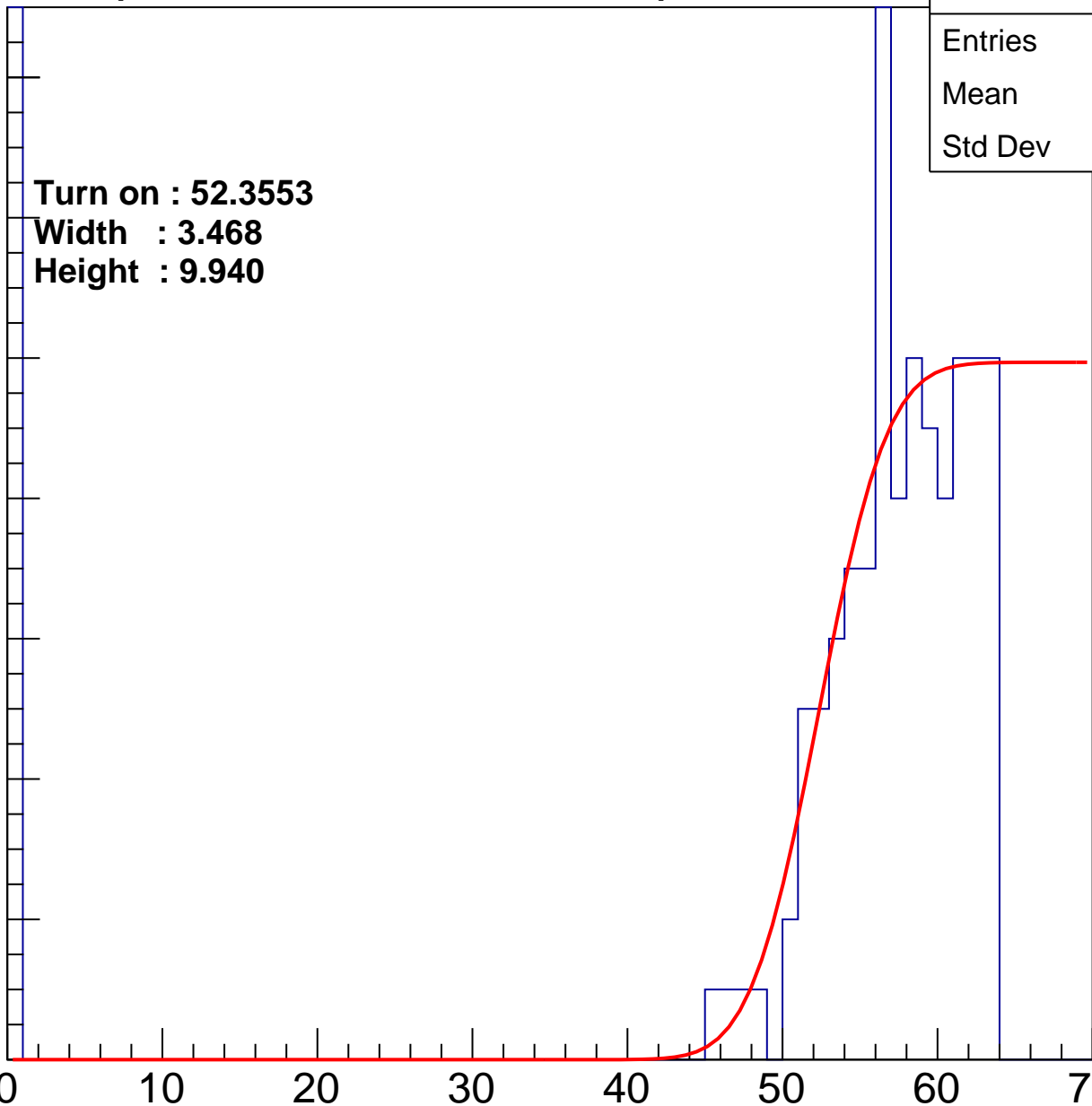
Entry

14
12
10
8
6
4
2
0

Turn on : 52.3553
Width : 3.468
Height : 9.940

Entries	224
Mean	33.59
Std Dev	28.2

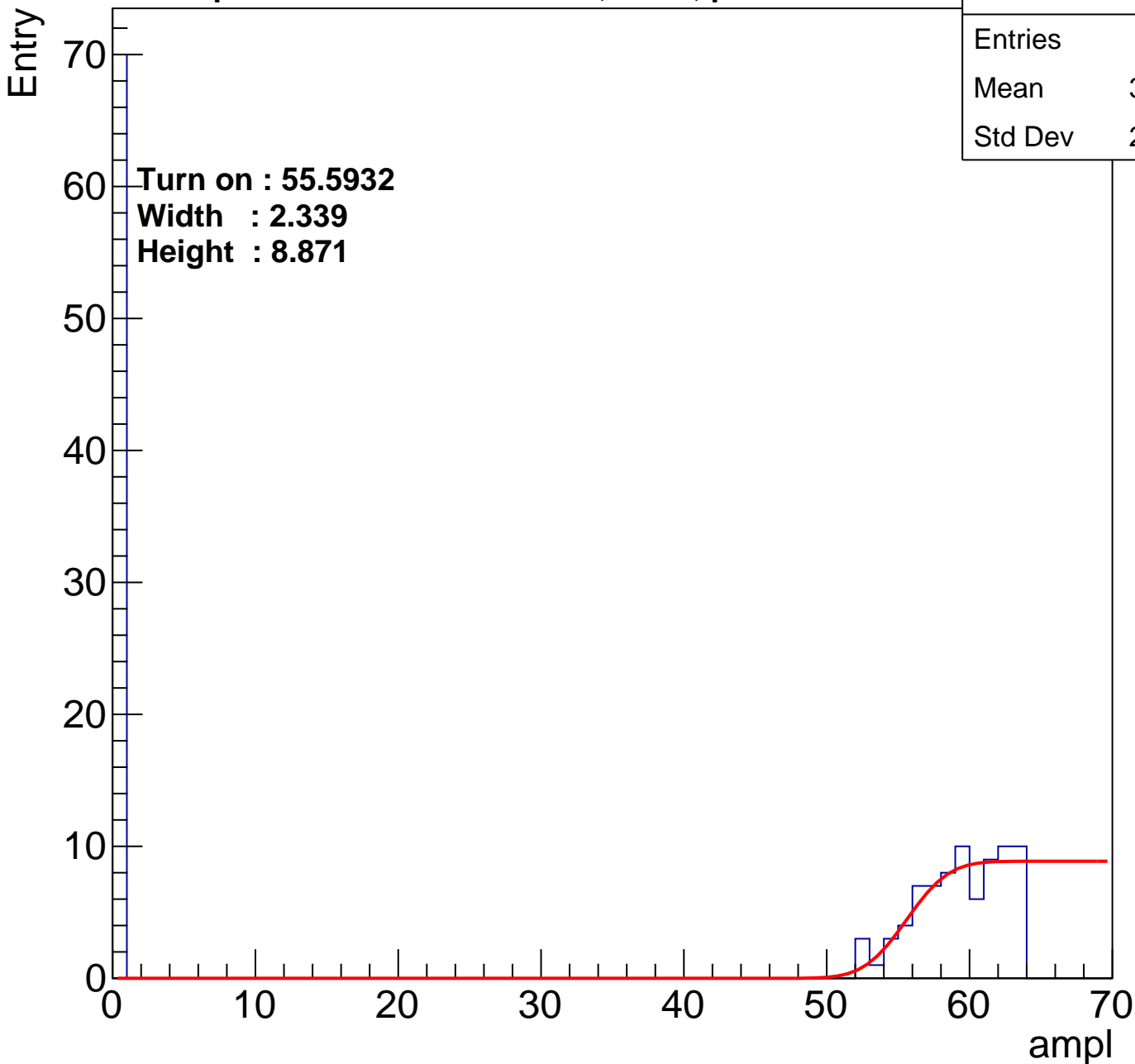
ampl



B1L104S, U11-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	148
Mean	31.05
Std Dev	29.49



B1L104S, U11-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	36.77
Std Dev	27.34

Turn on : 52.0059

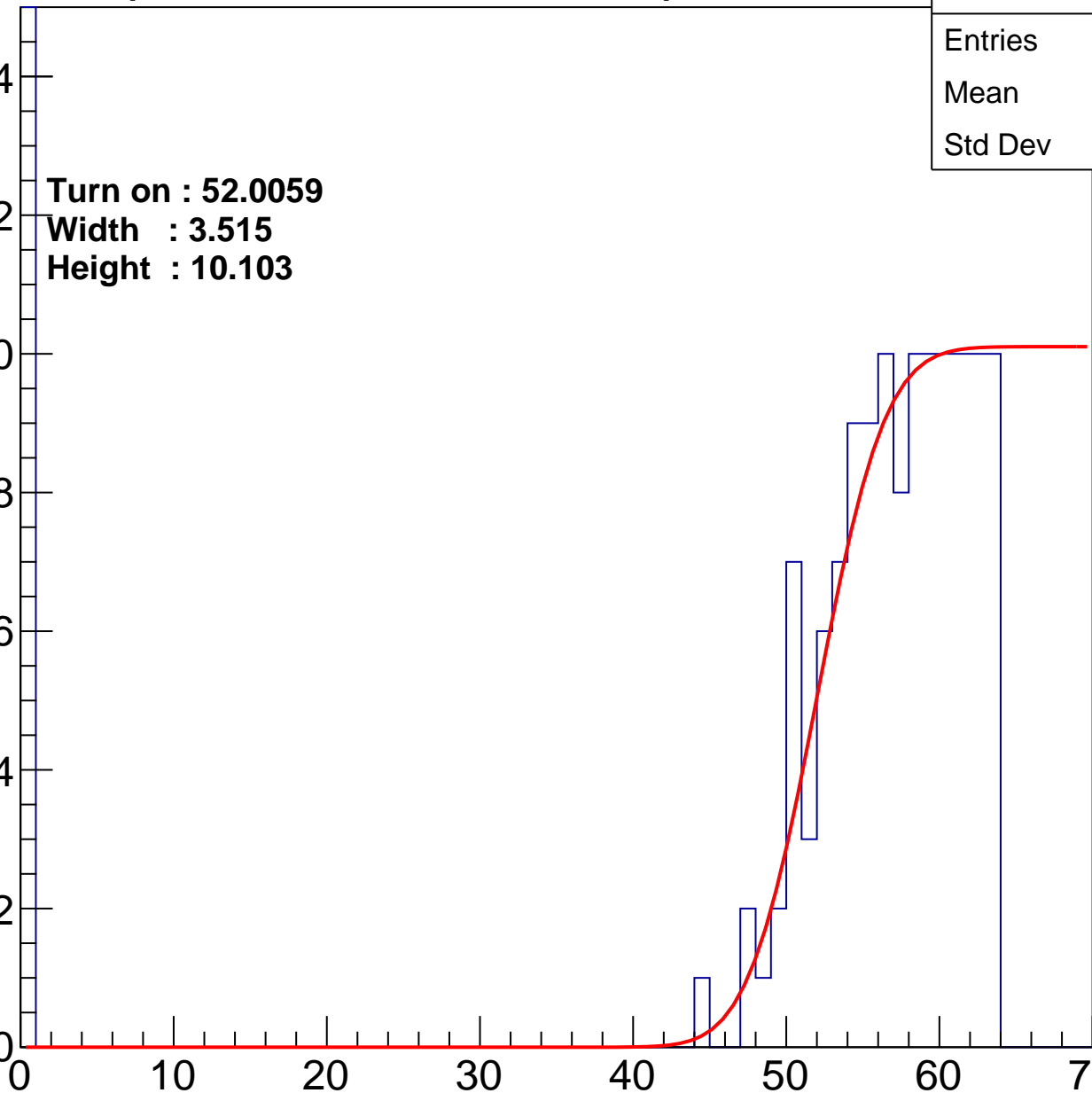
Width : 3.515

Height : 10.103

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	31.9
Std Dev	28.98

Turn on : 53.6886

Width : 2.949

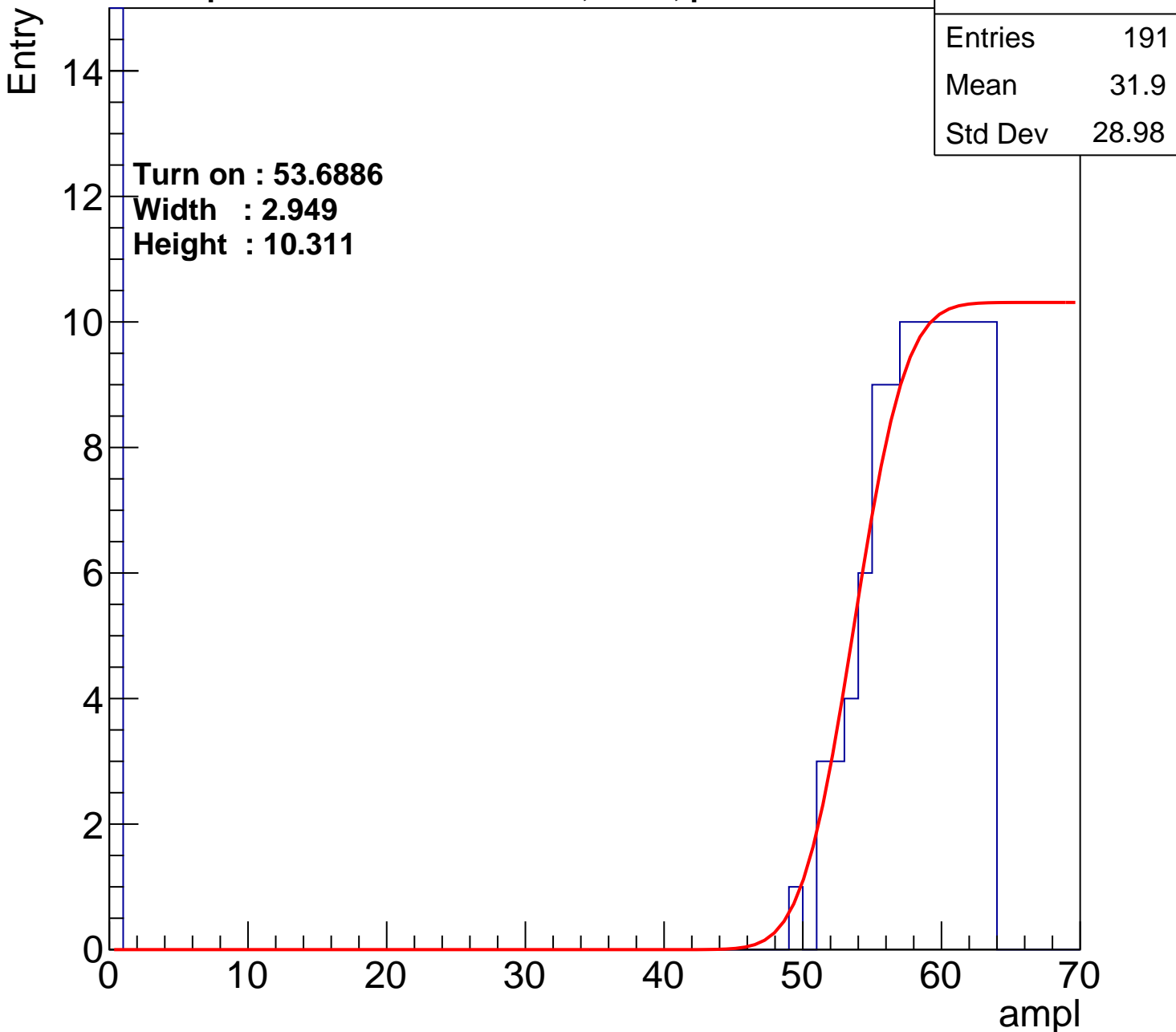
Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch58

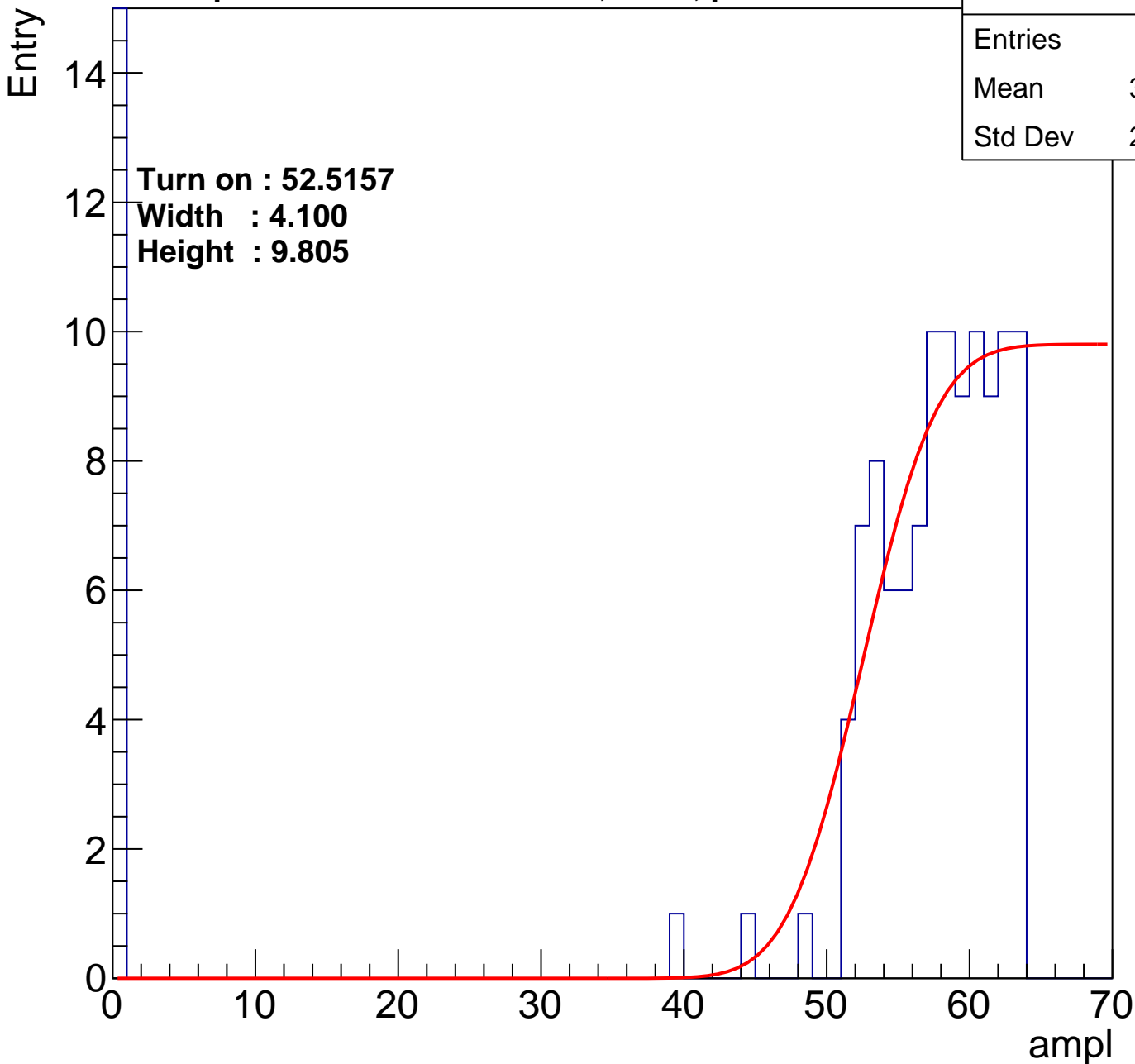
calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	33.96
Std Dev	28.36

Turn on : 52.5157

Width : 4.100

Height : 9.805



B1L104S, U11-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	31.01
Std Dev	28.7

Turn on : 53.7776

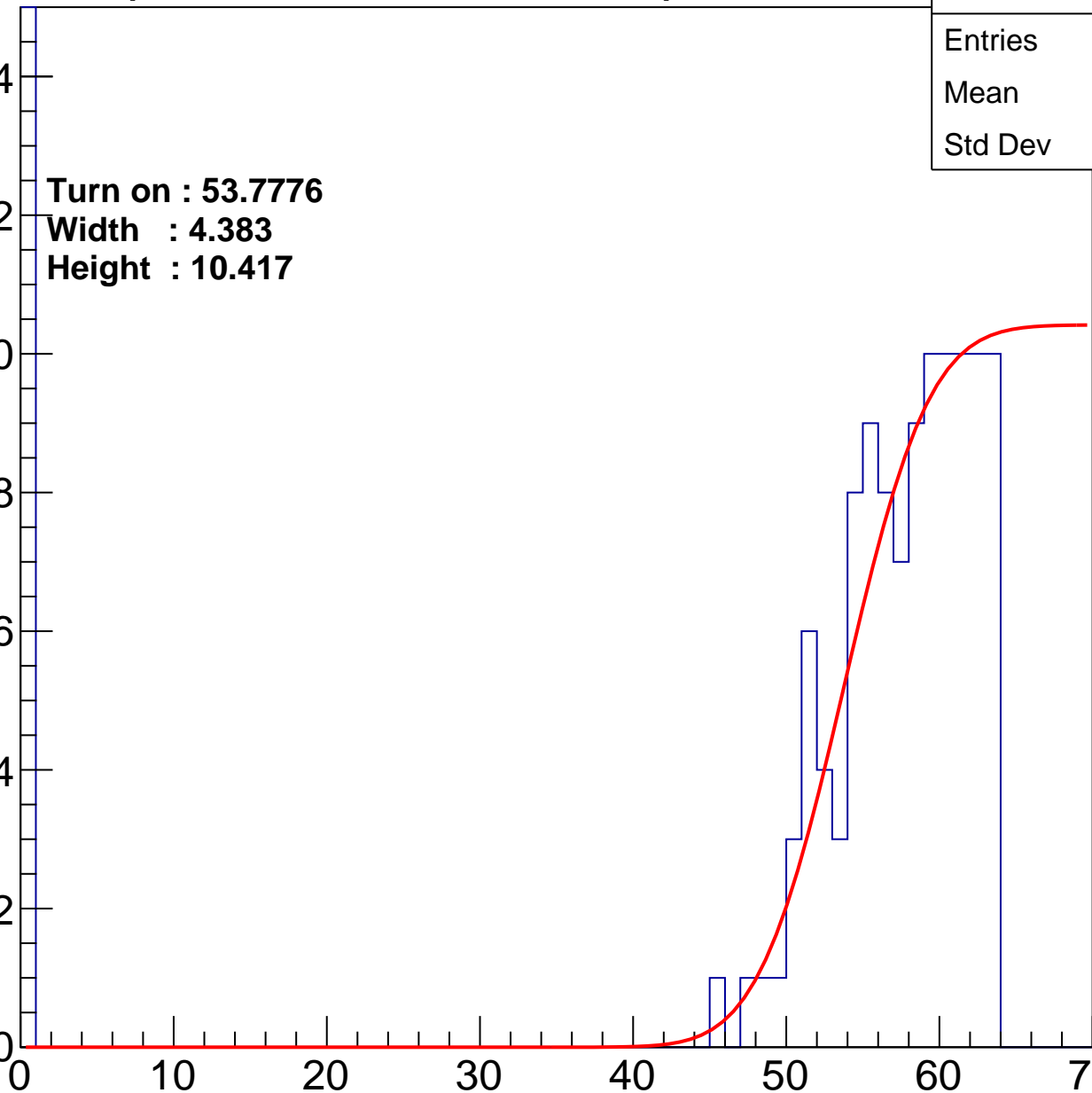
Width : 4.383

Height : 10.417

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	238
Mean	28.78
Std Dev	28.49

Turn on : 52.3521

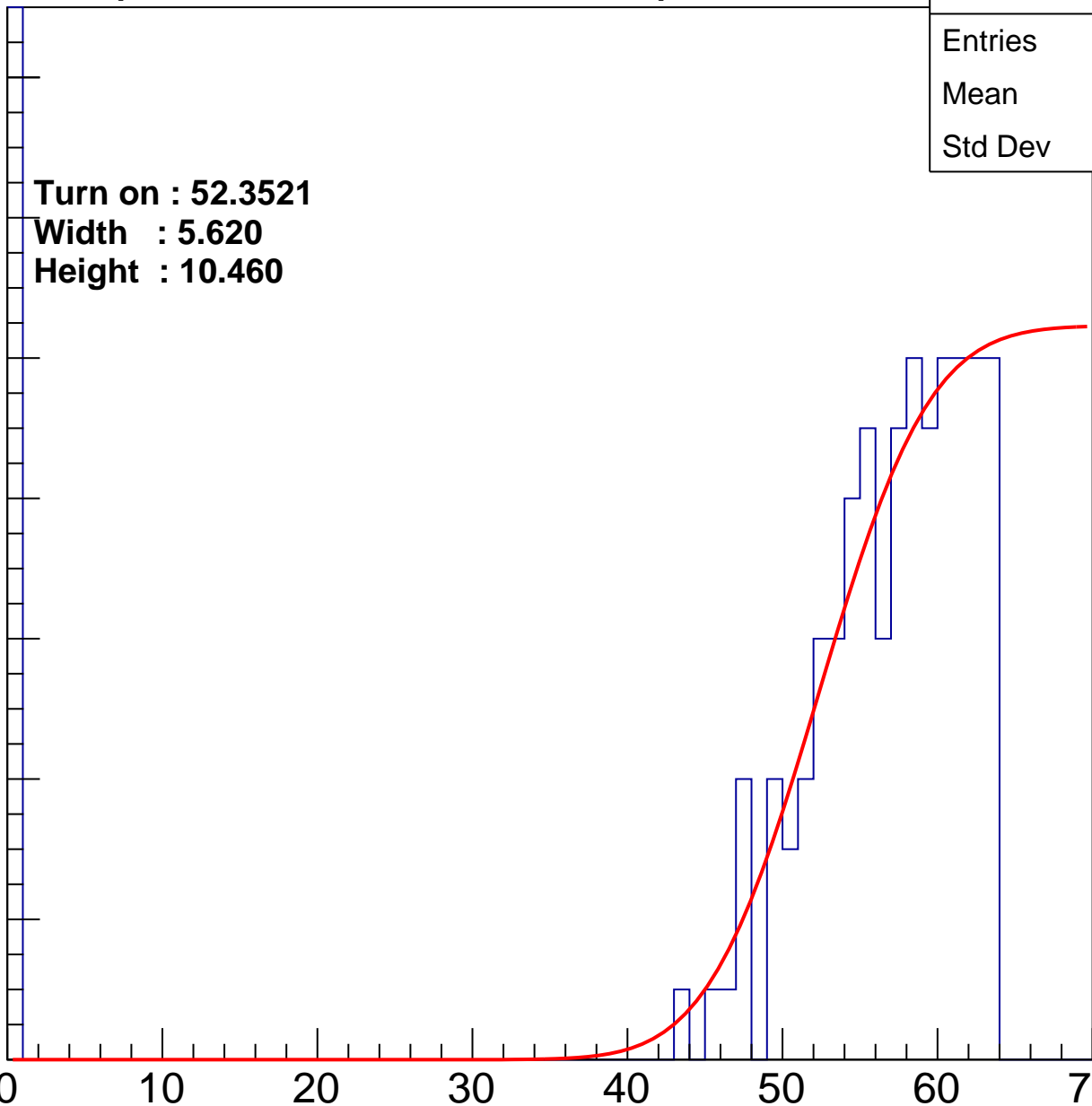
Width : 5.620

Height : 10.460

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	28.84
Std Dev	29.43

Turn on : 55.7640

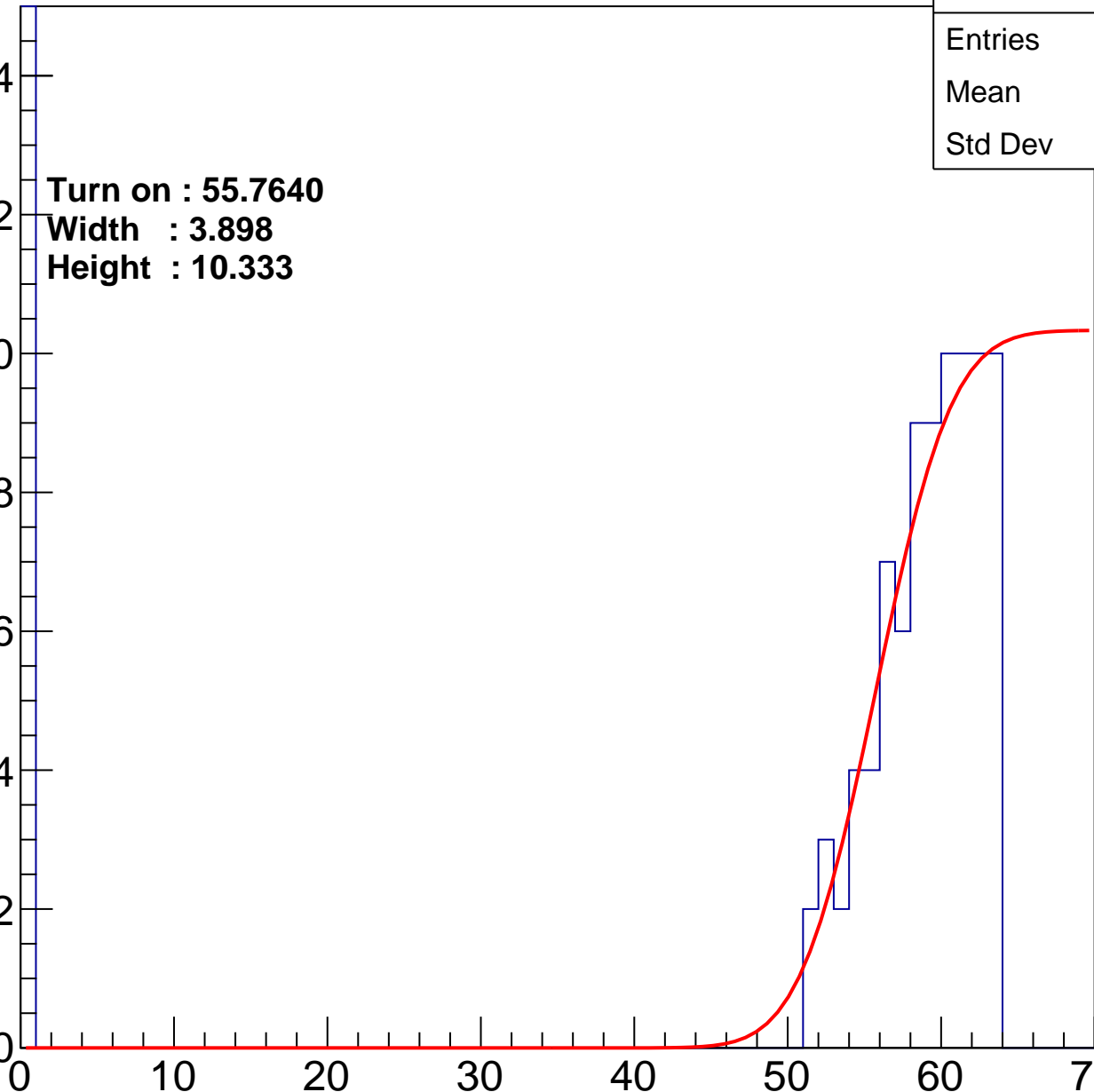
Width : 3.898

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch62

calib_packv5_033123_0516.root, FC#4, port A1

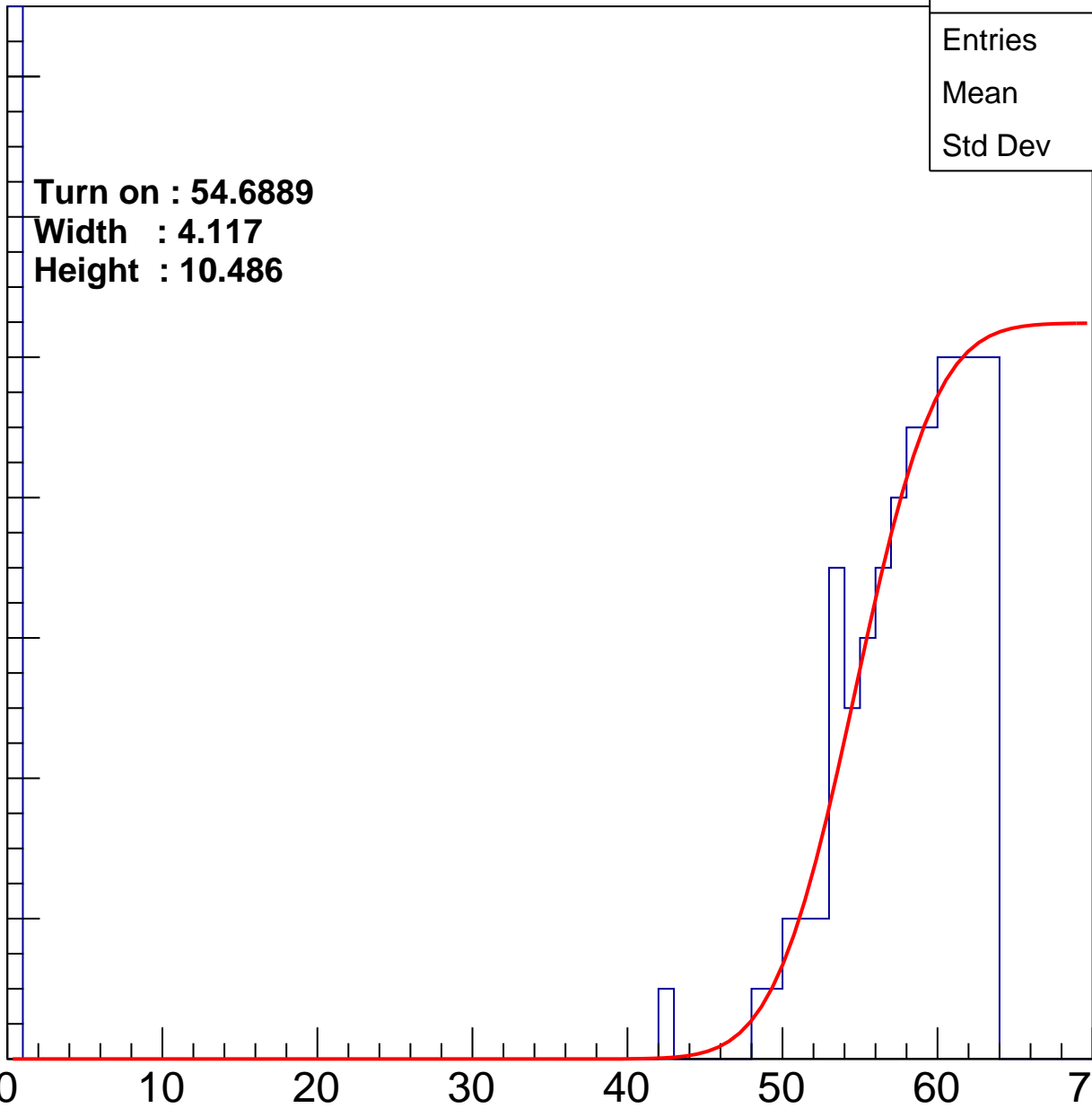
Entry

14
12
10
8
6
4
2
0

Turn on : 54.6889
Width : 4.117
Height : 10.486

Entries	182
Mean	31.74
Std Dev	28.9

ampl



B1L104S, U11-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	36.42
Std Dev	27.98

Turn on : 54.0836

Width : 3.880

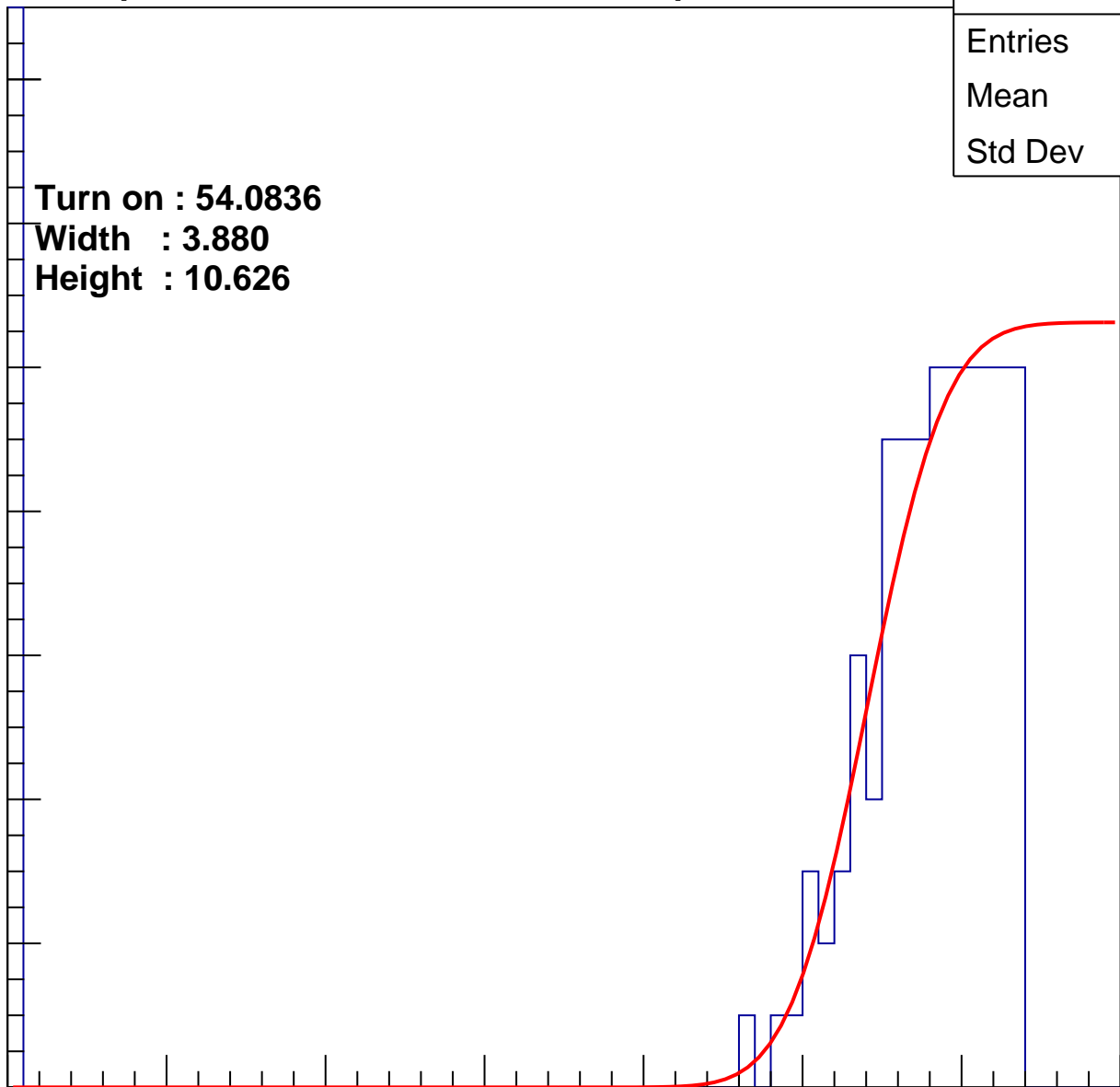
Height : 10.626

Entry

14
12
10
8
6
4
2
0

ampl

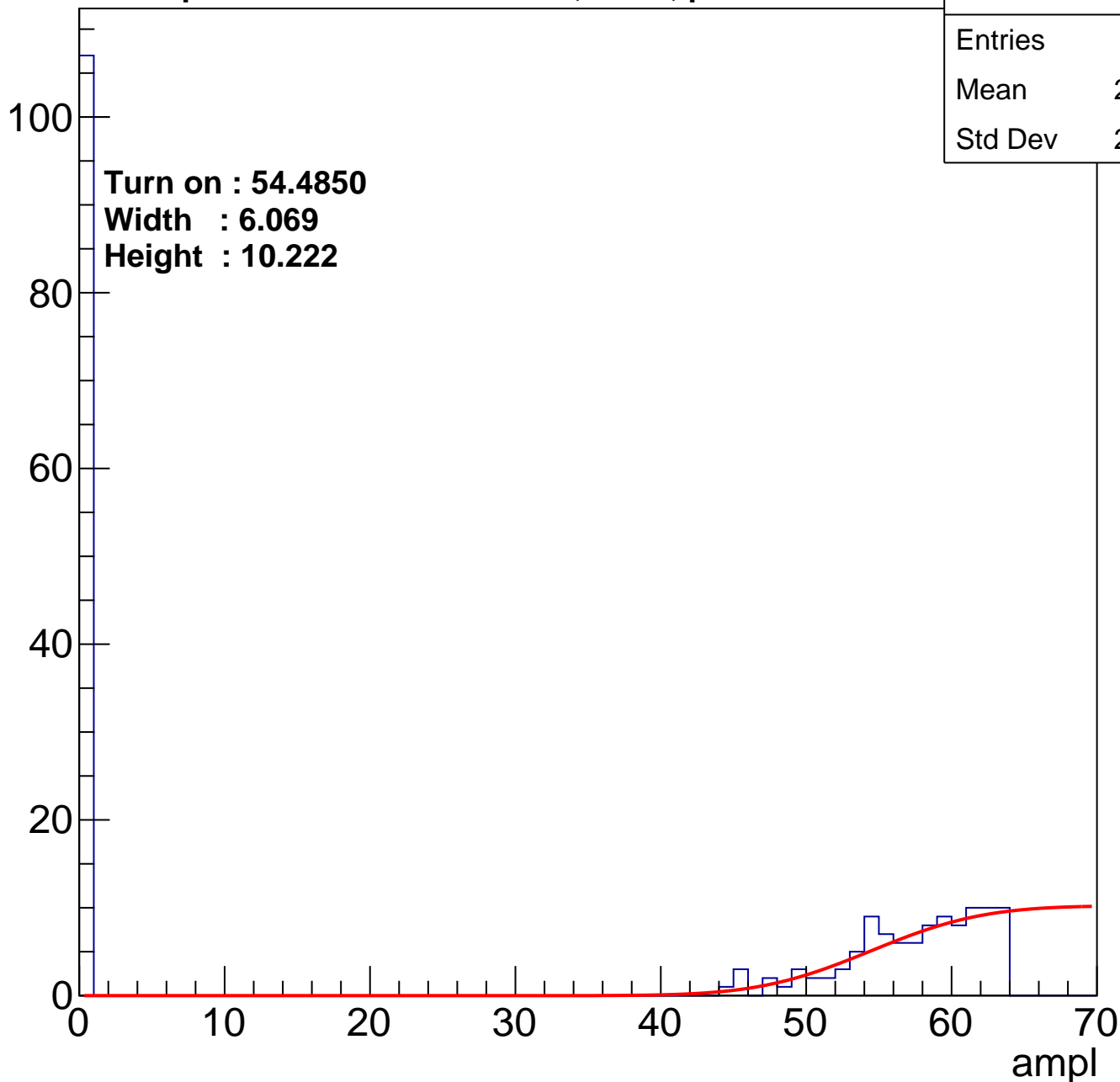
0 10 20 30 40 50 60 70



B1L104S, U11-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entry



B1L104S, U11-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	174
Mean	35.37
Std Dev	28.16

Turn on : 53.1999

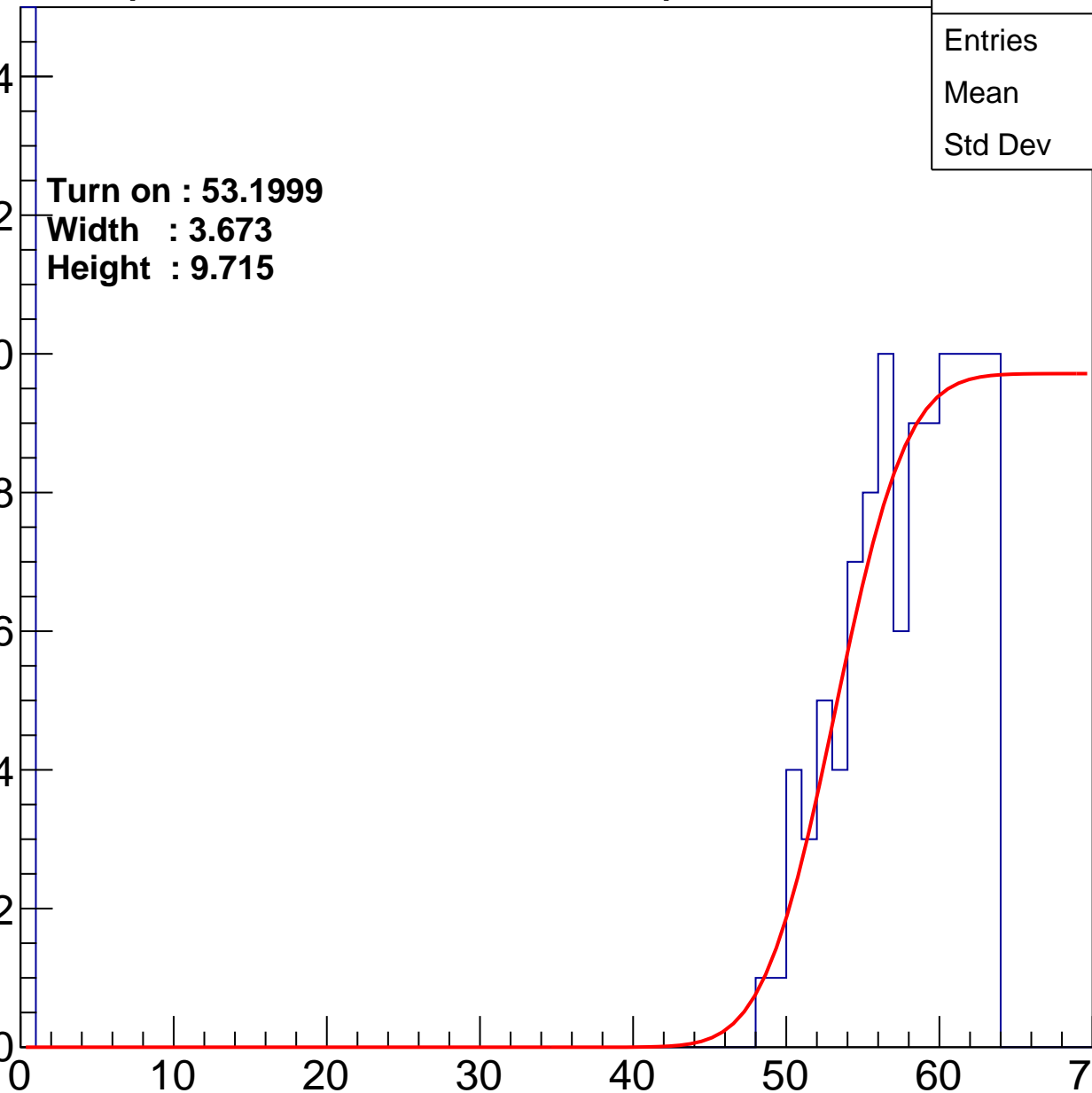
Width : 3.673

Height : 9.715

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	222
Mean	29.27
Std Dev	28.67

Turn on : 53.5355

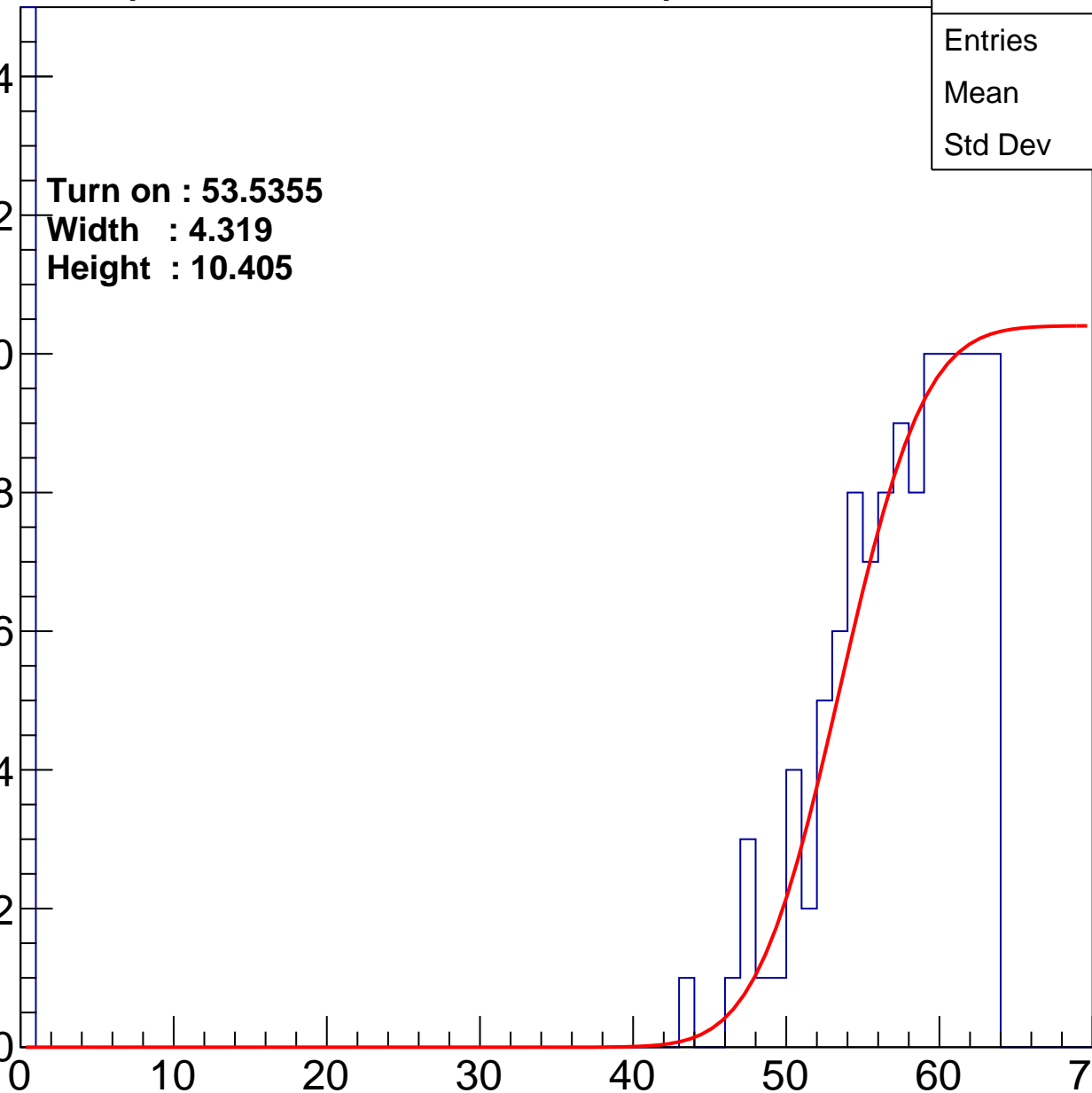
Width : 4.319

Height : 10.405

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch67

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	31.71
Std Dev	28.74

Turn on : 54.1974

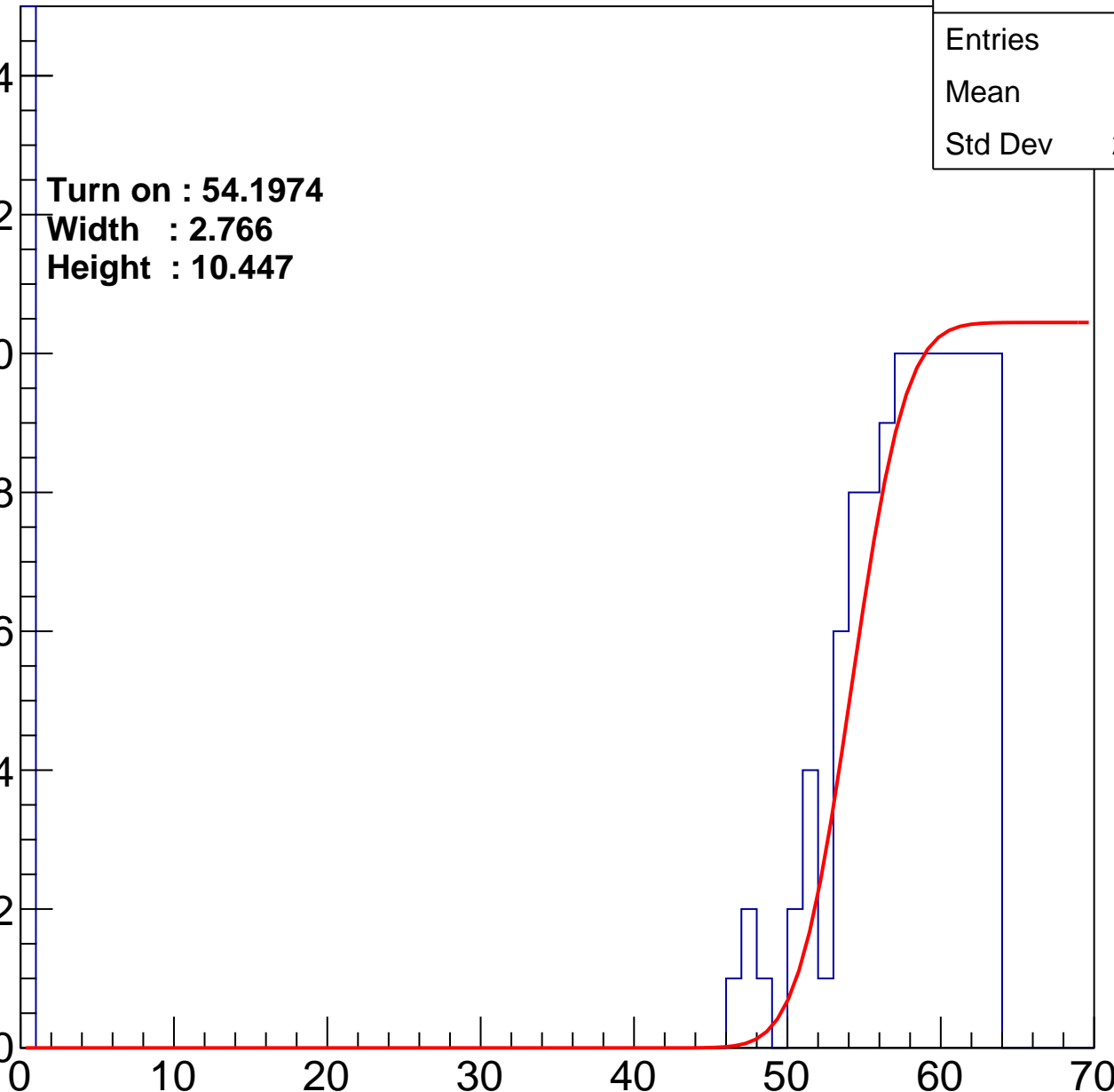
Width : 2.766

Height : 10.447

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch68

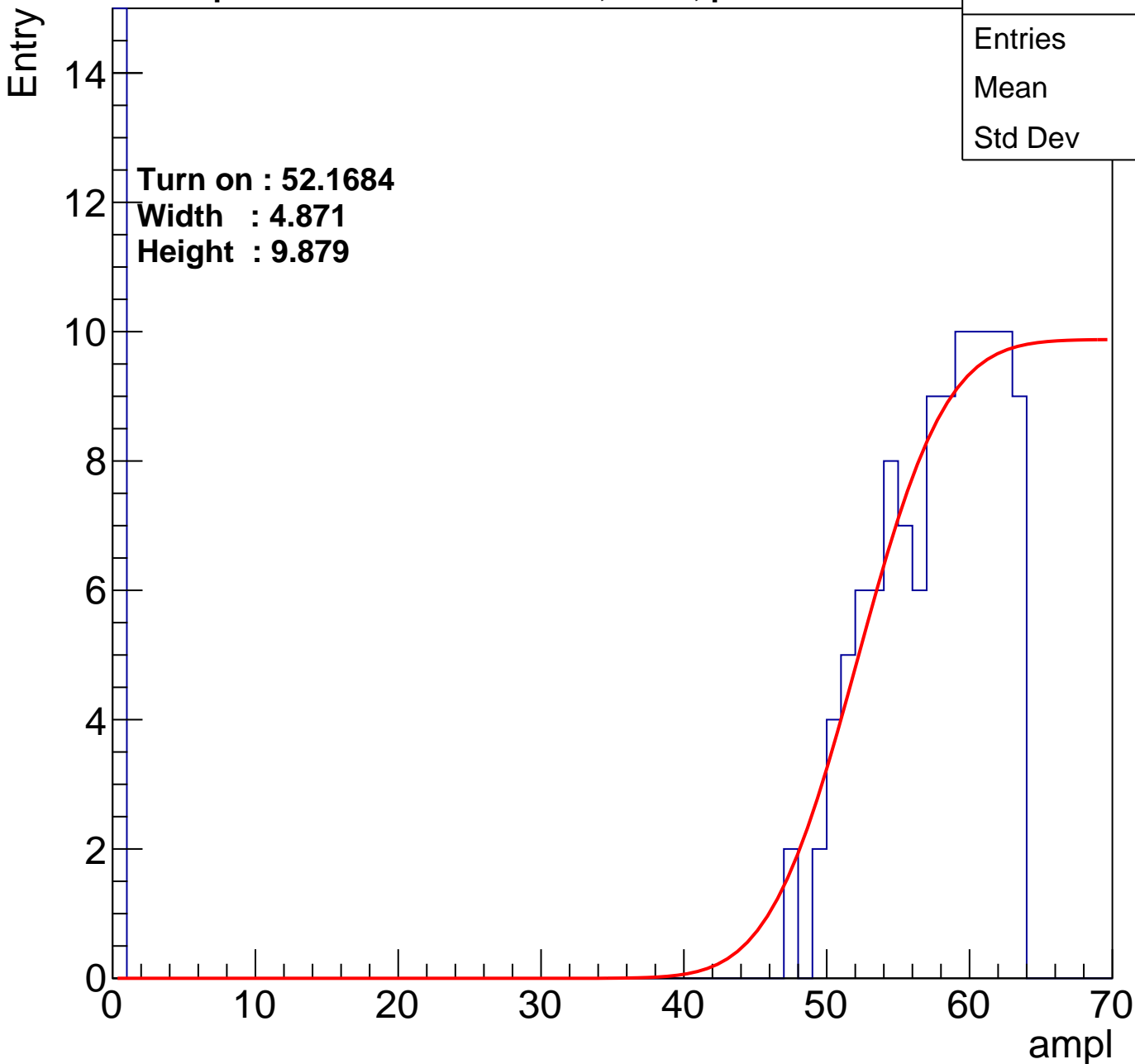
calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	32.1
Std Dev	28.5

Turn on : 52.1684

Width : 4.871

Height : 9.879



B1L104S, U11-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	27.56
Std Dev	29.33

Turn on : 55.2800

Width : 2.939

Height : 9.653

Entry

14

12

10

8

6

4

2

0

0

10

20

30

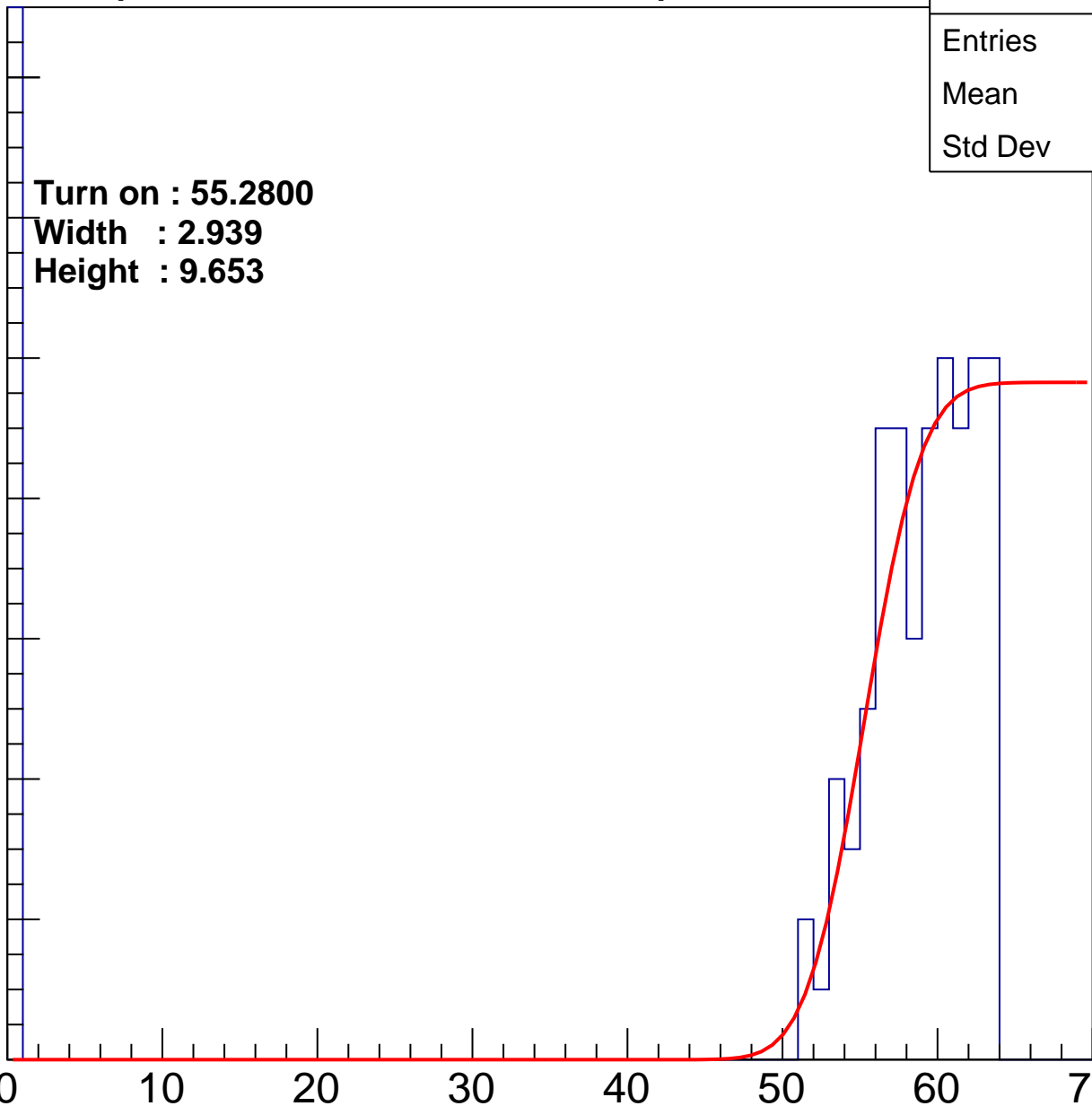
40

50

60

70

ampl



B1L104S, U11-ch70

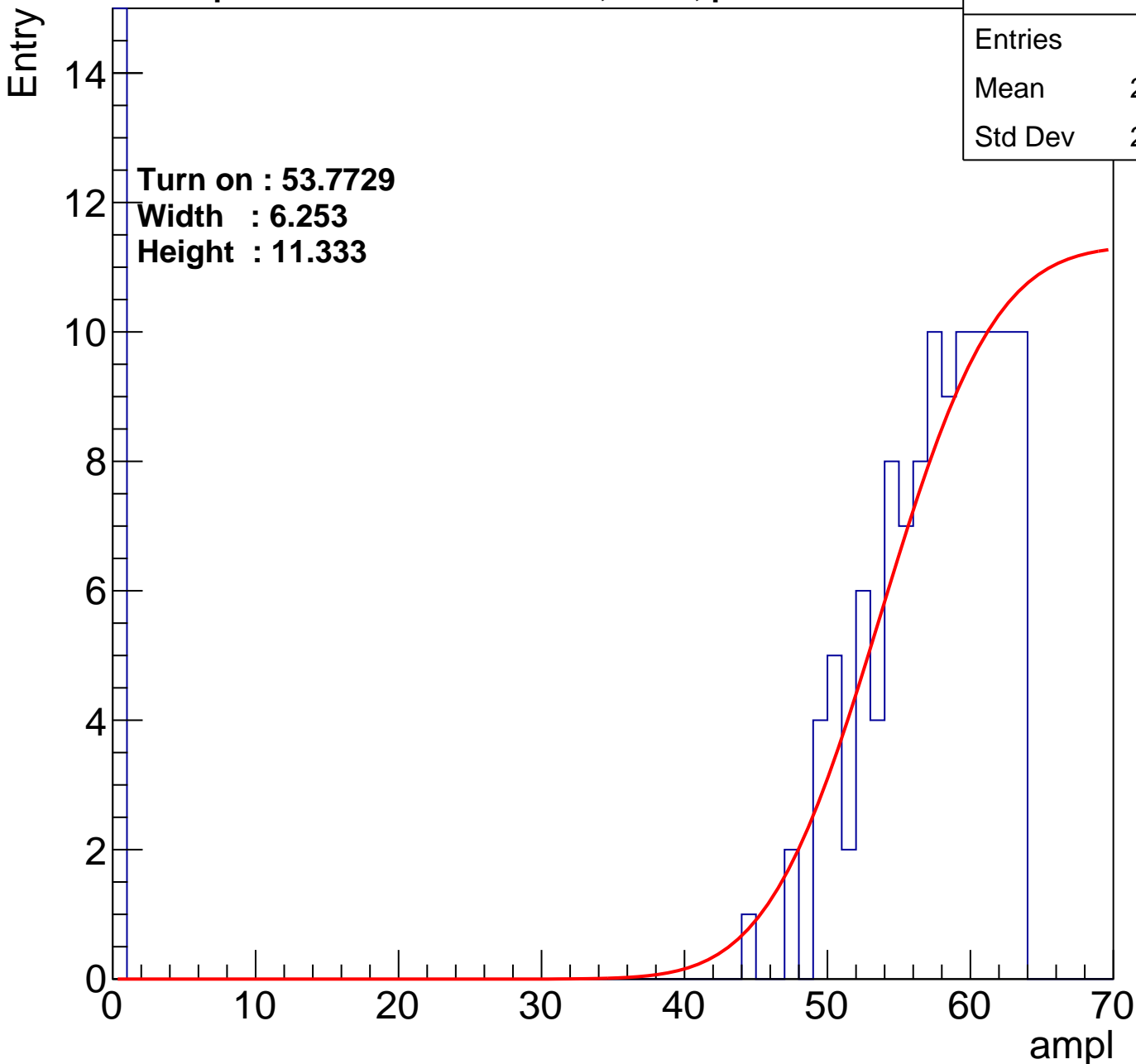
calib_packv5_033123_0516.root, FC#4, port A1

Entries	235
Mean	28.16
Std Dev	28.68

Turn on : 53.7729

Width : 6.253

Height : 11.333



B1L104S, U11-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	166
Mean	32.78
Std Dev	29.14

Turn on : 54.7631

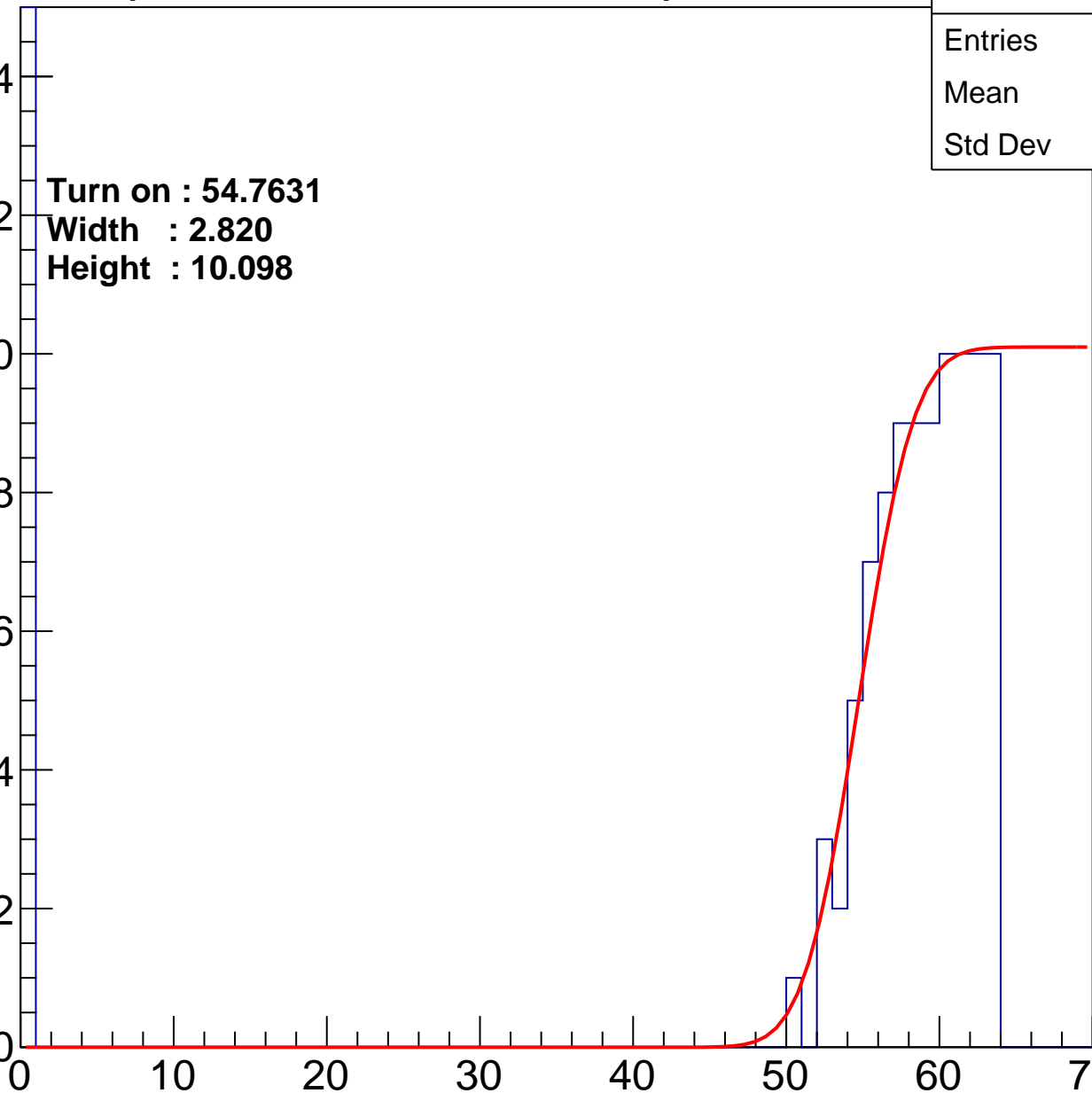
Width : 2.820

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	33.71
Std Dev	28.37

Turn on : 53.5526

Width : 2.742

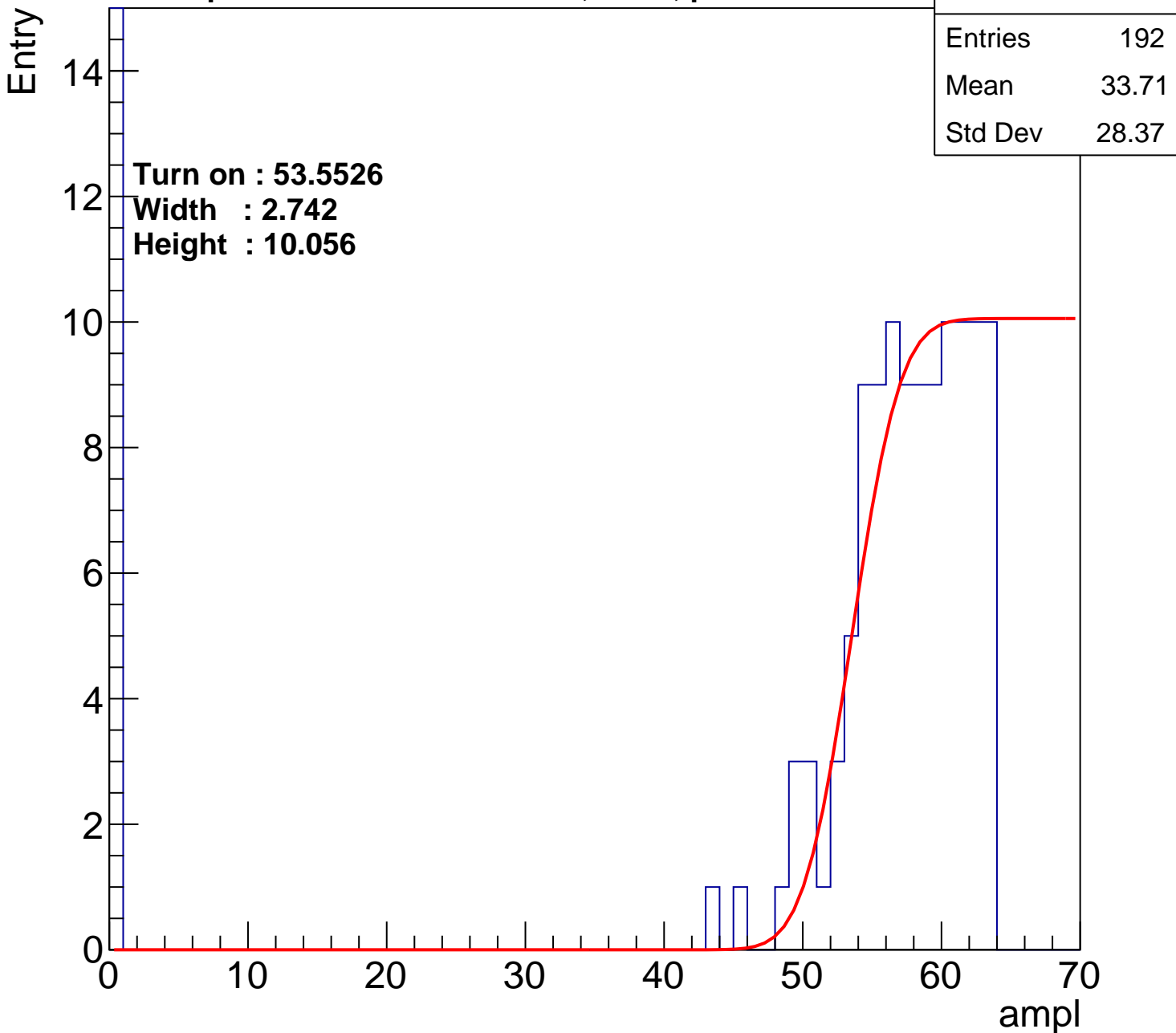
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	27.23
Std Dev	29.23

Turn on : 55.4093

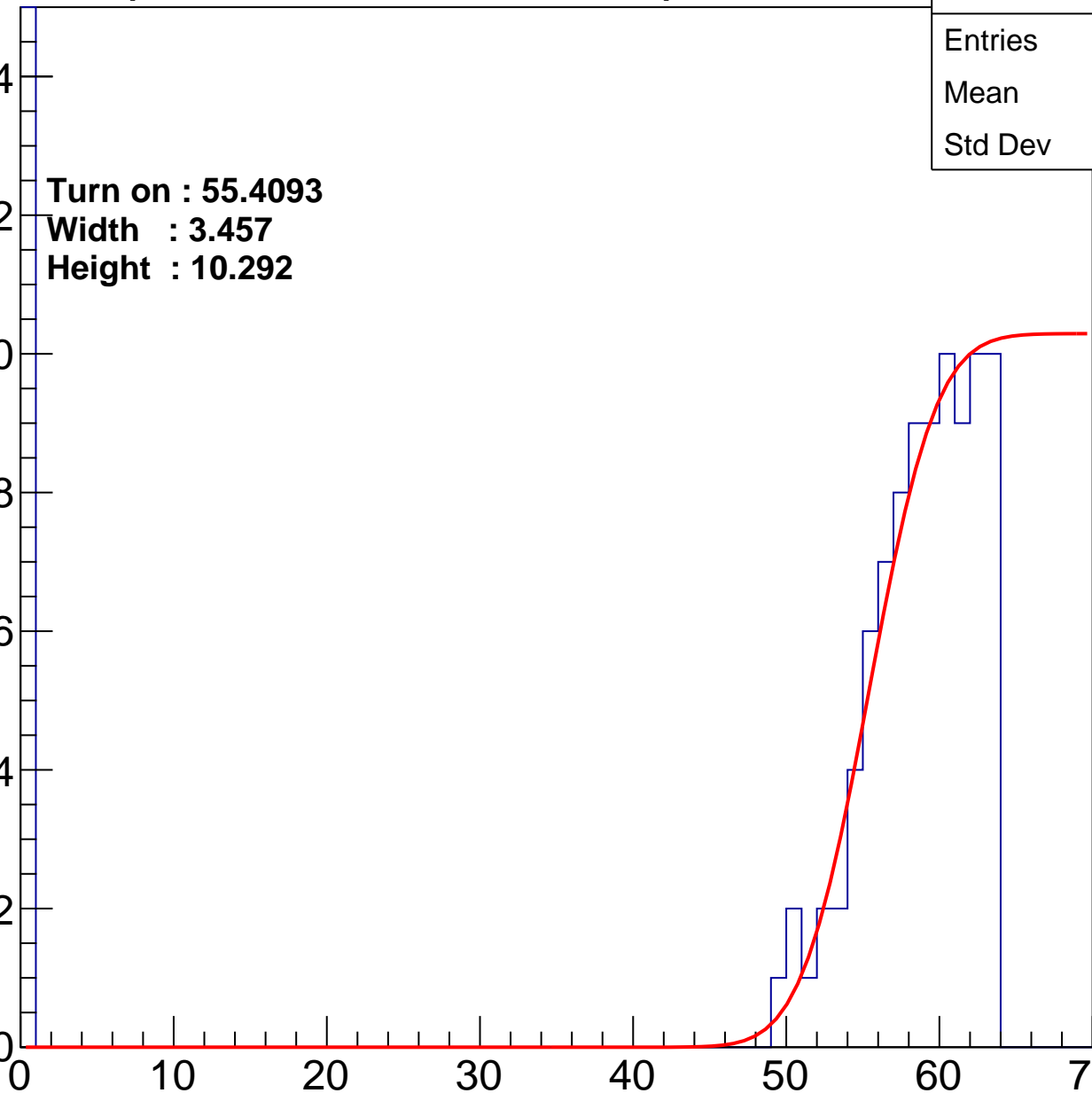
Width : 3.457

Height : 10.292

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch74

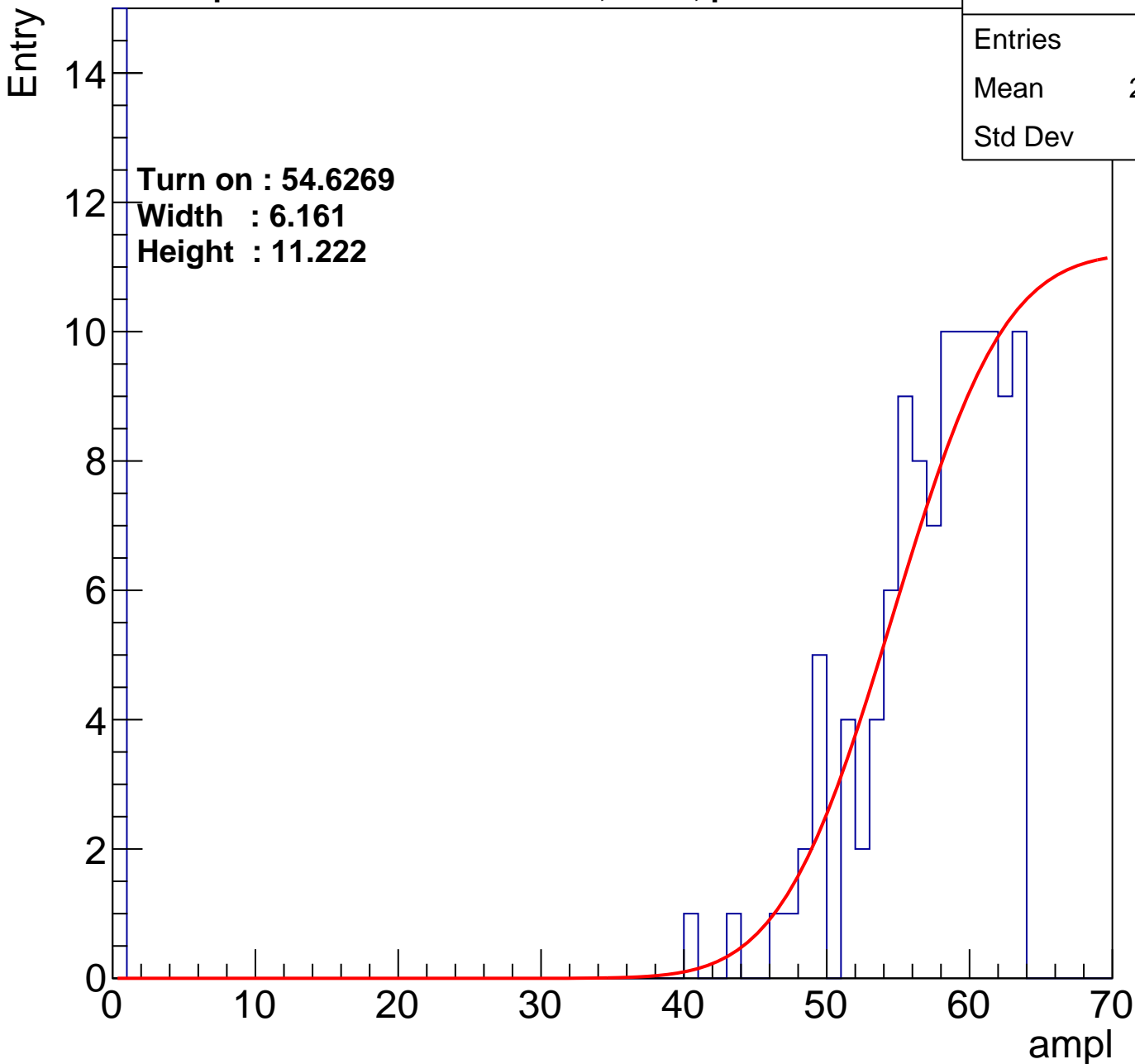
calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	28.63
Std Dev	28.7

Turn on : 54.6269

Width : 6.161

Height : 11.222



B1L104S, U11-ch75

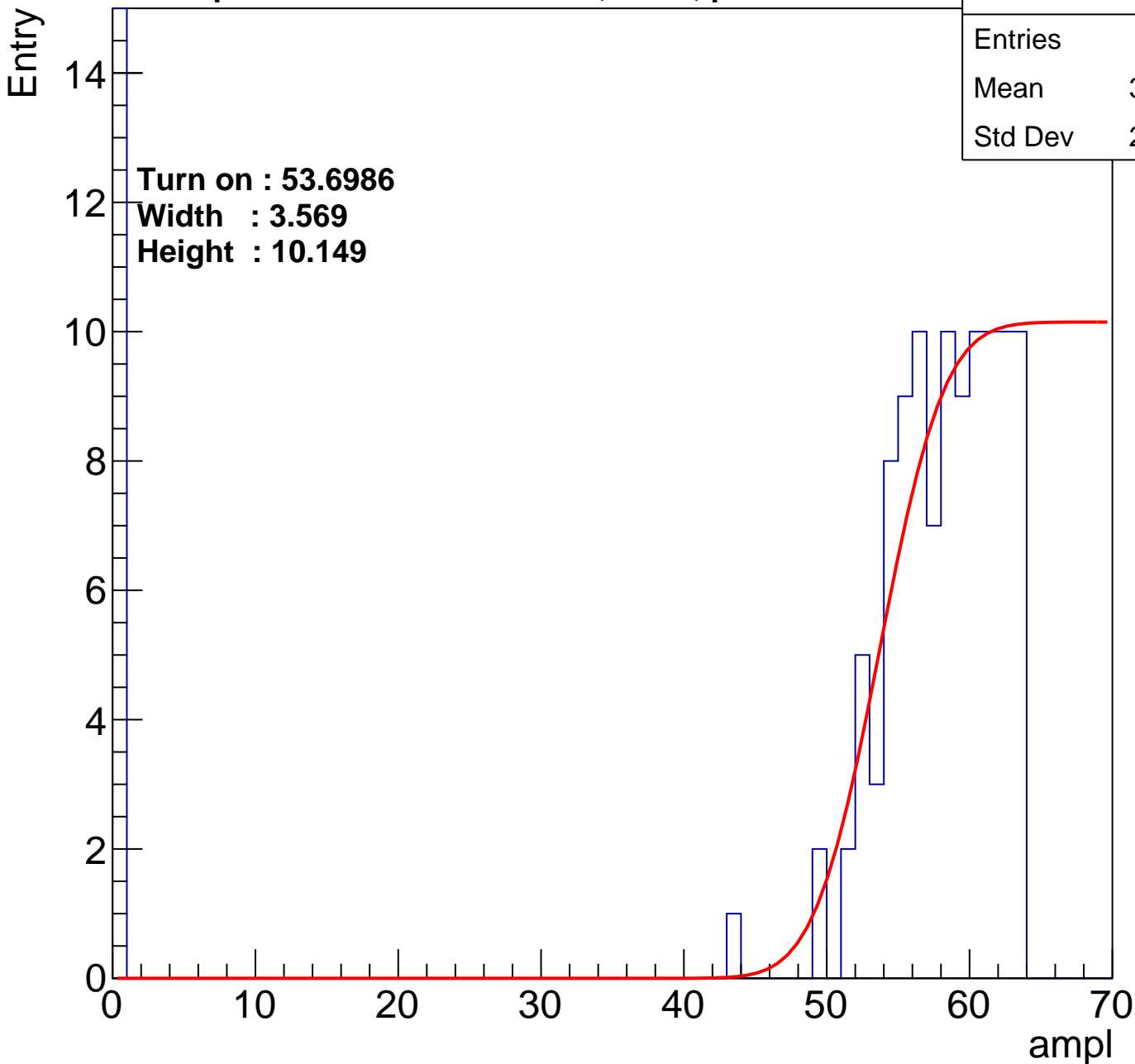
calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	34.38
Std Dev	28.48

Turn on : 53.6986

Width : 3.569

Height : 10.149



B1L104S, U11-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	33.31
Std Dev	28.08

Turn on : 52.7776

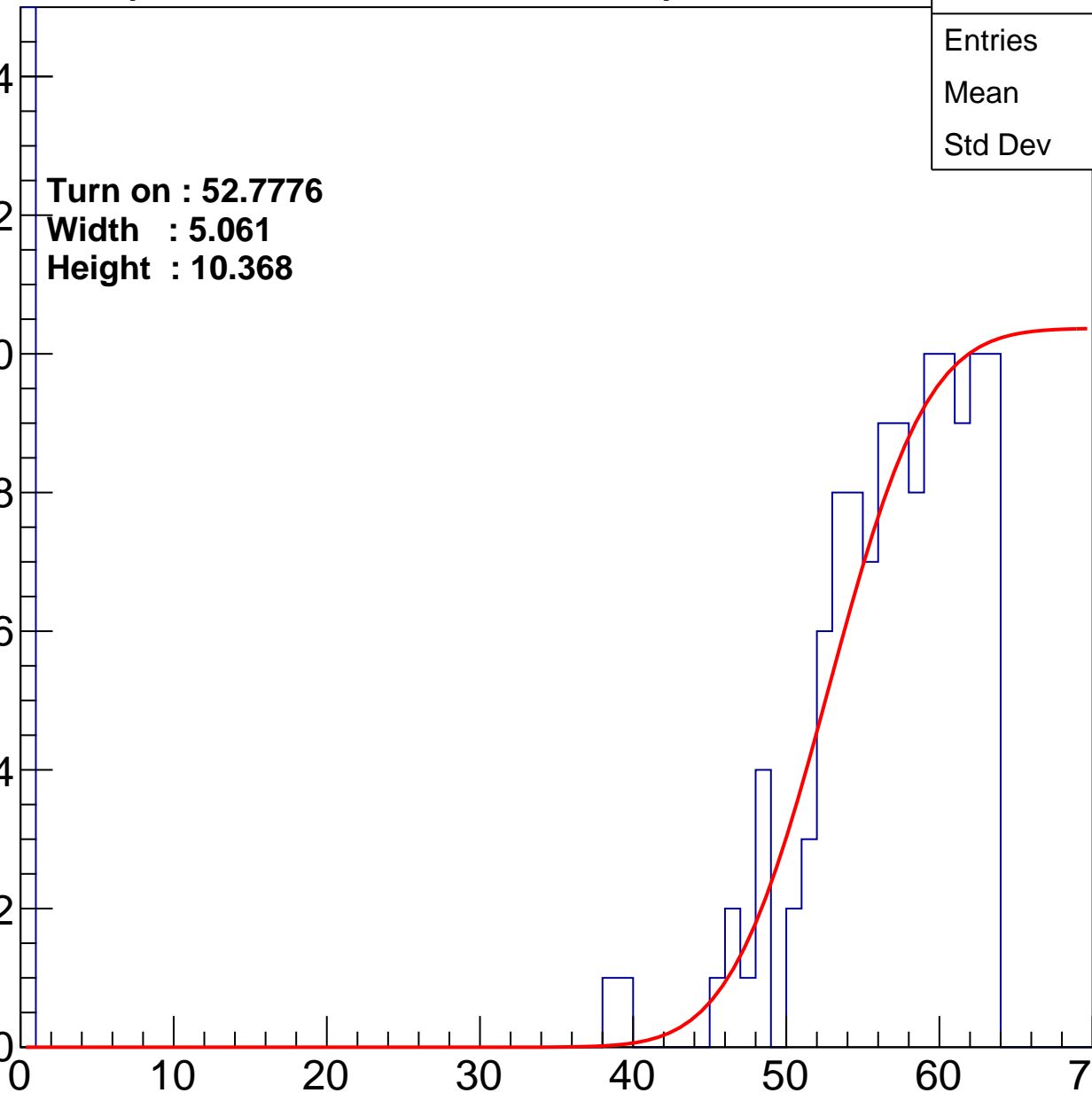
Width : 5.061

Height : 10.368

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	37.31
Std Dev	27.64

Turn on : 53.7501

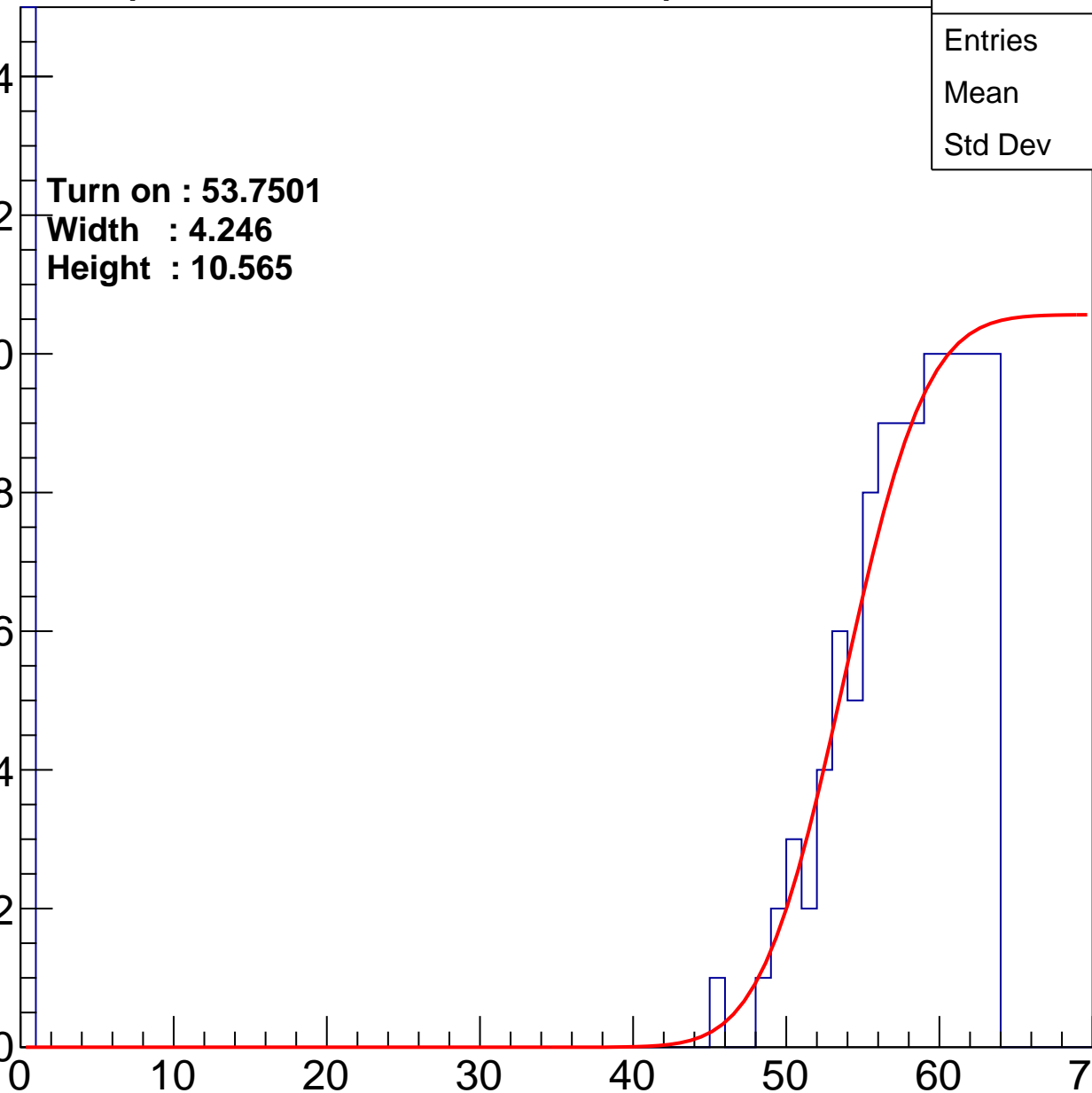
Width : 4.246

Height : 10.565

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	34.84
Std Dev	27.65

Turn on : 51.9725

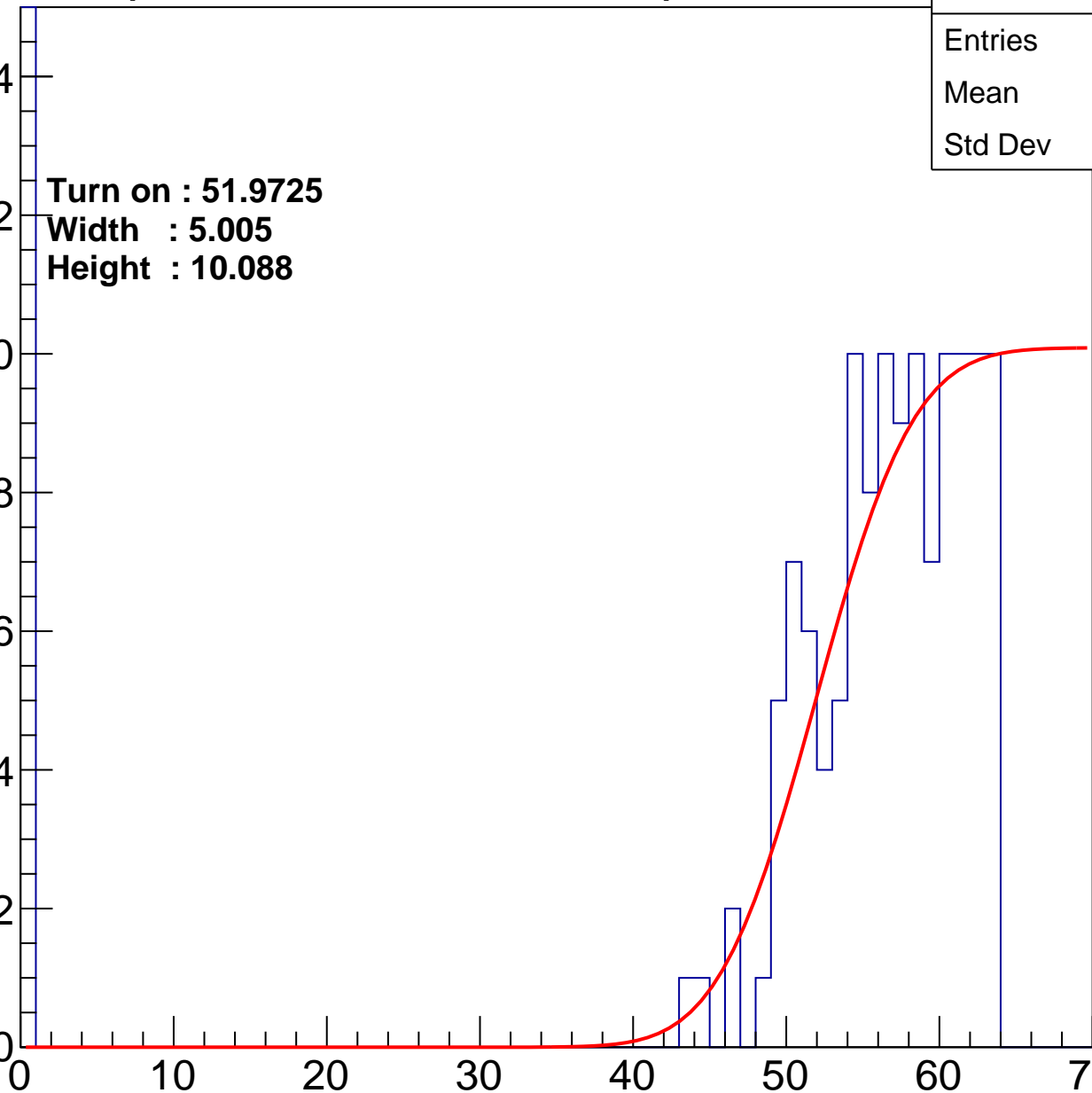
Width : 5.005

Height : 10.088

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch79

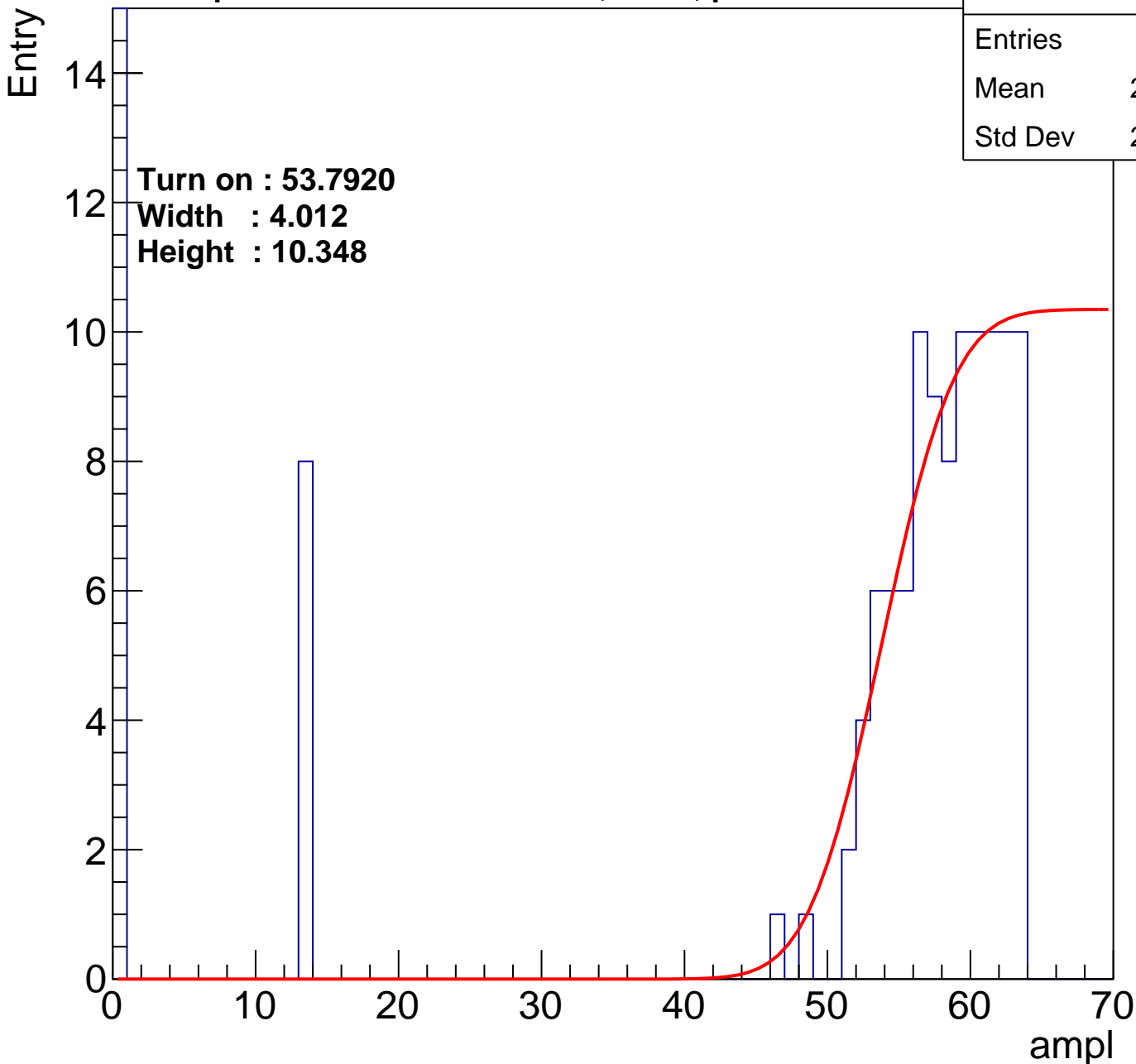
calib_packv5_033123_0516.root, FC#4, port A1

Entries	210
Mean	28.89
Std Dev	28.68

Turn on : 53.7920

Width : 4.012

Height : 10.348



B1L104S, U11-ch80

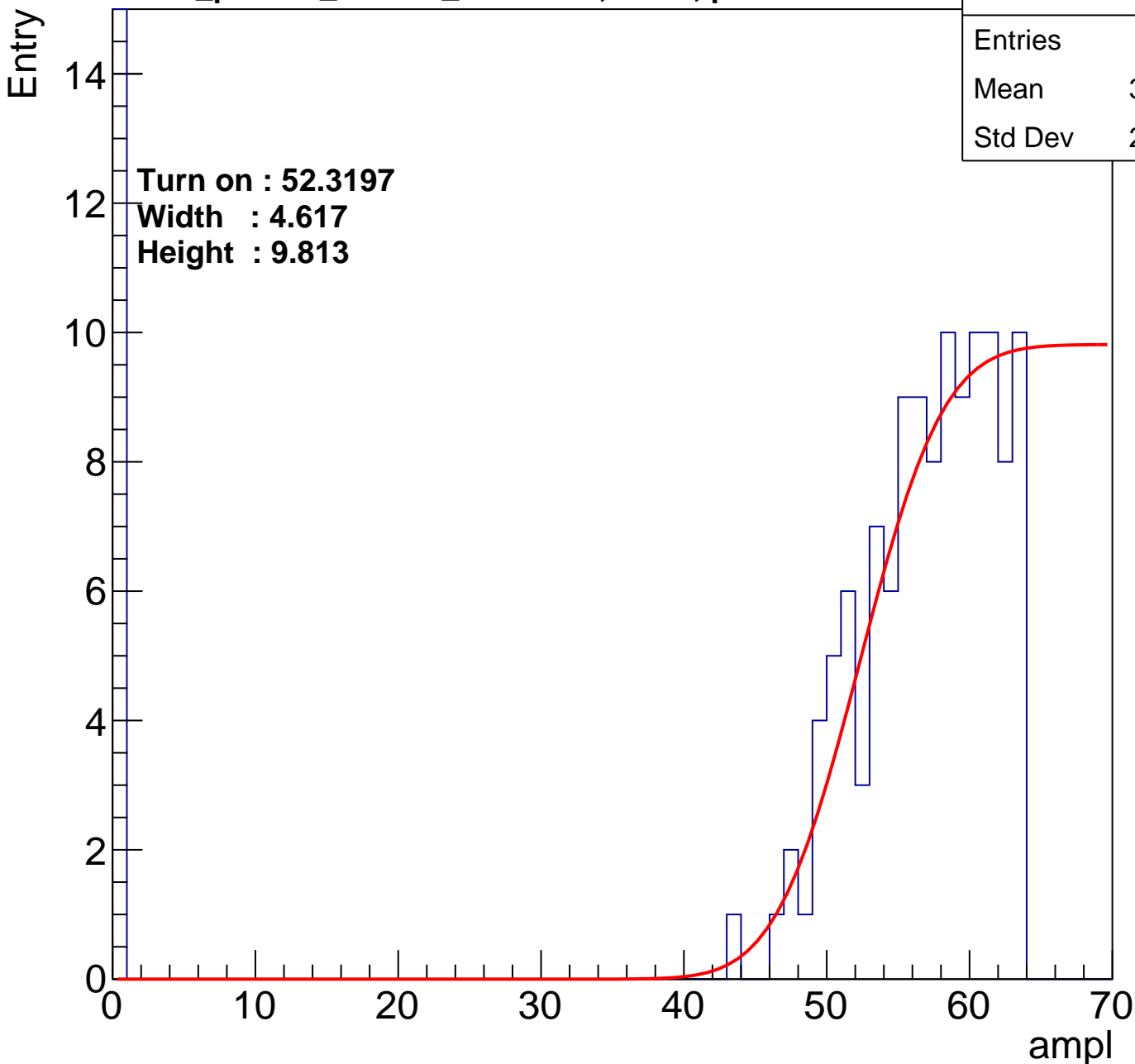
calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	33.18
Std Dev	28.09

Turn on : 52.3197

Width : 4.617

Height : 9.813



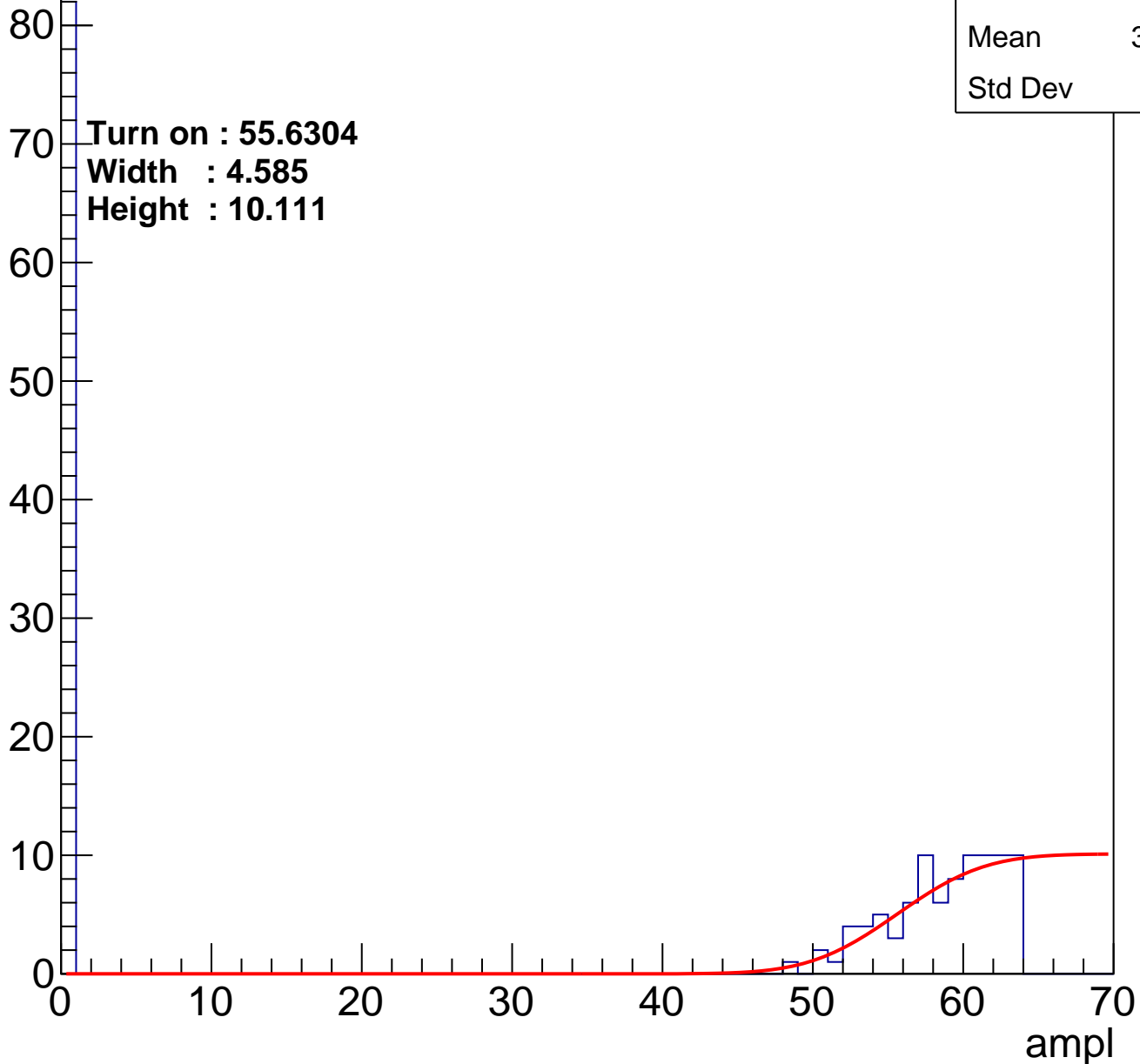
B1L104S, U11-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	30.47
Std Dev	29.2

Turn on : 55.6304
Width : 4.585
Height : 10.111

Entry



B1L104S, U11-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	27.28
Std Dev	28.75

Turn on : 54.5078

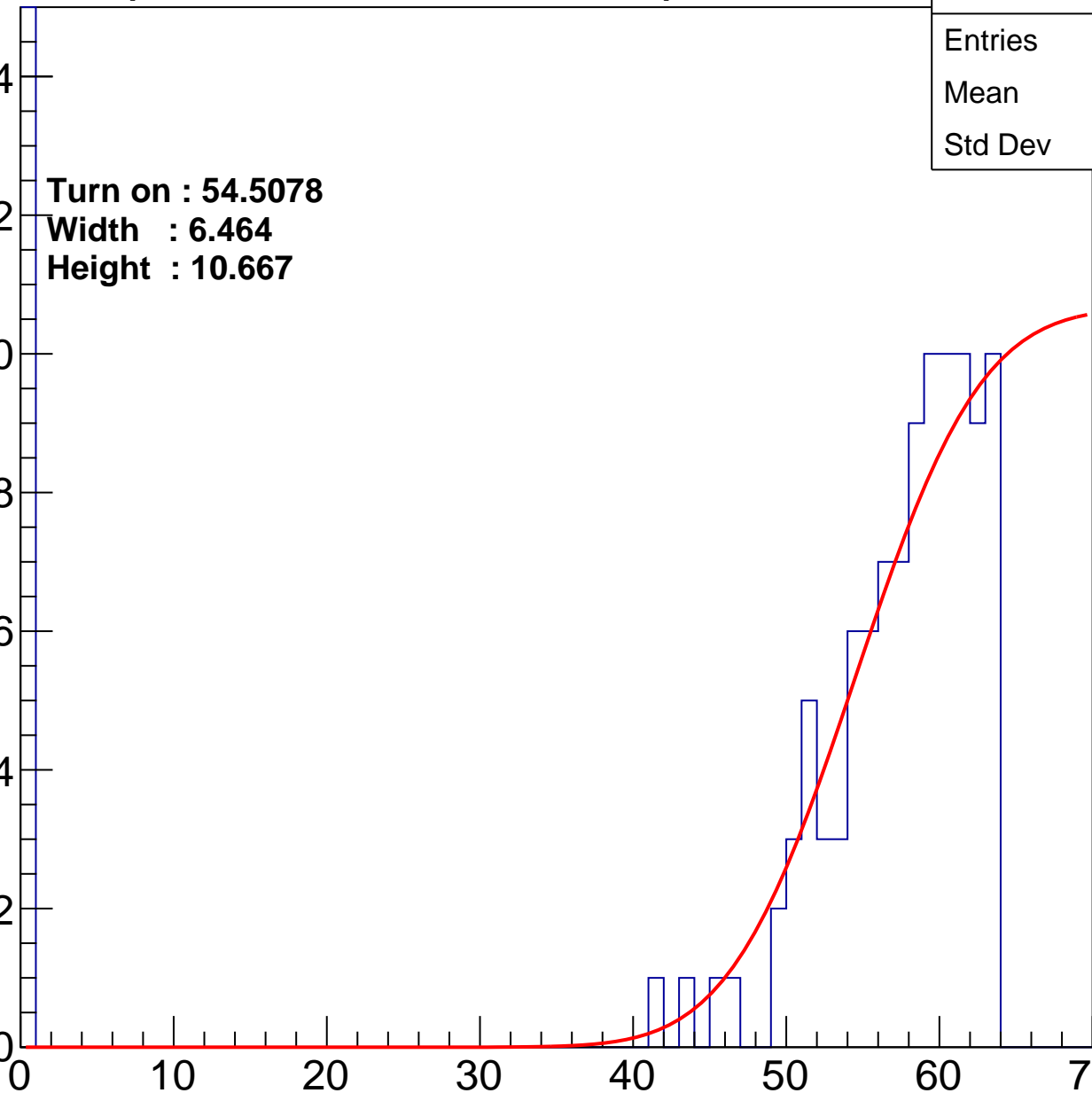
Width : 6.464

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch83

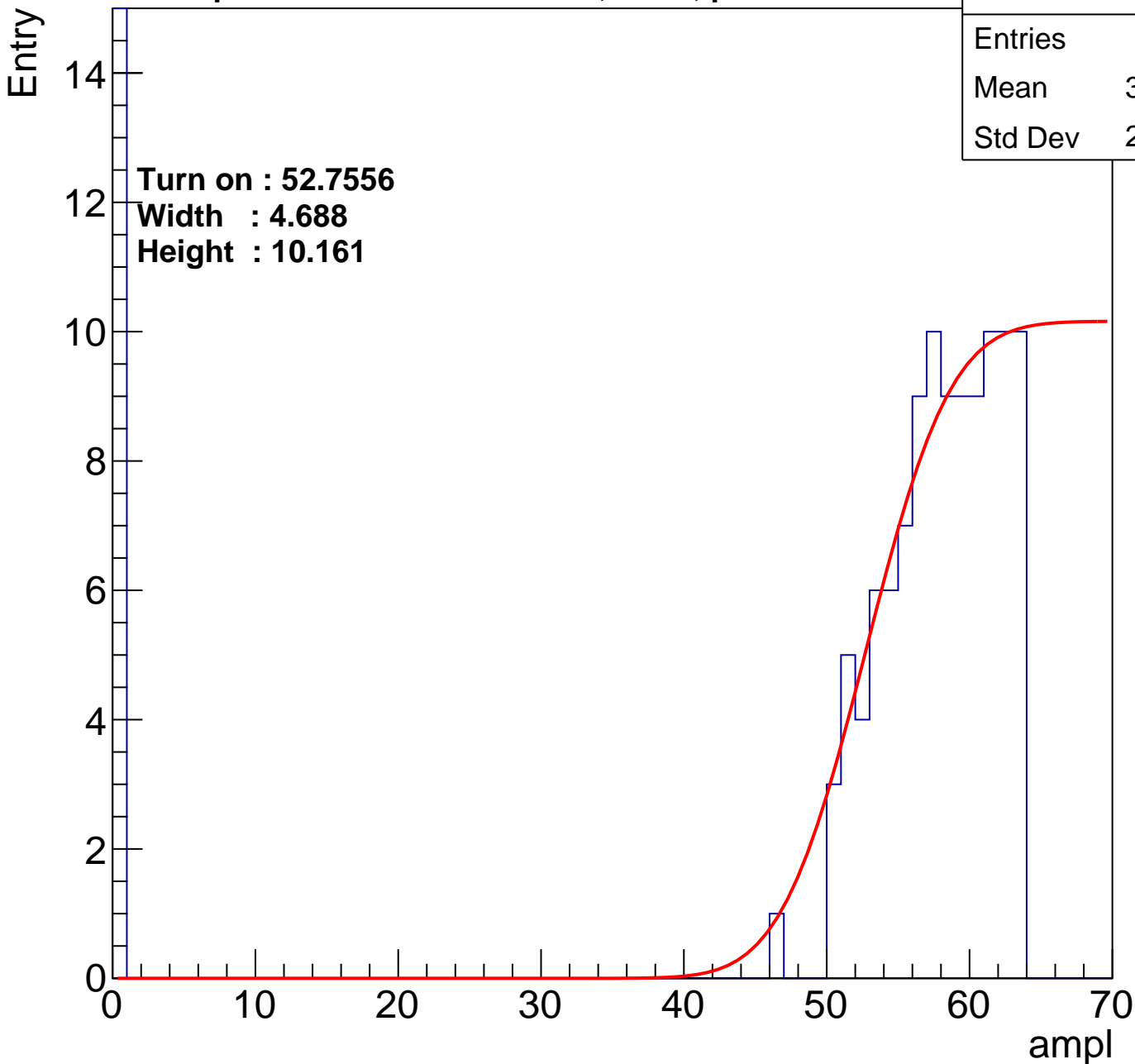
calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.53
Std Dev	28.66

Turn on : 52.7556

Width : 4.688

Height : 10.161



B1L104S, U11-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	27.62
Std Dev	28.79

Turn on : 53.8291

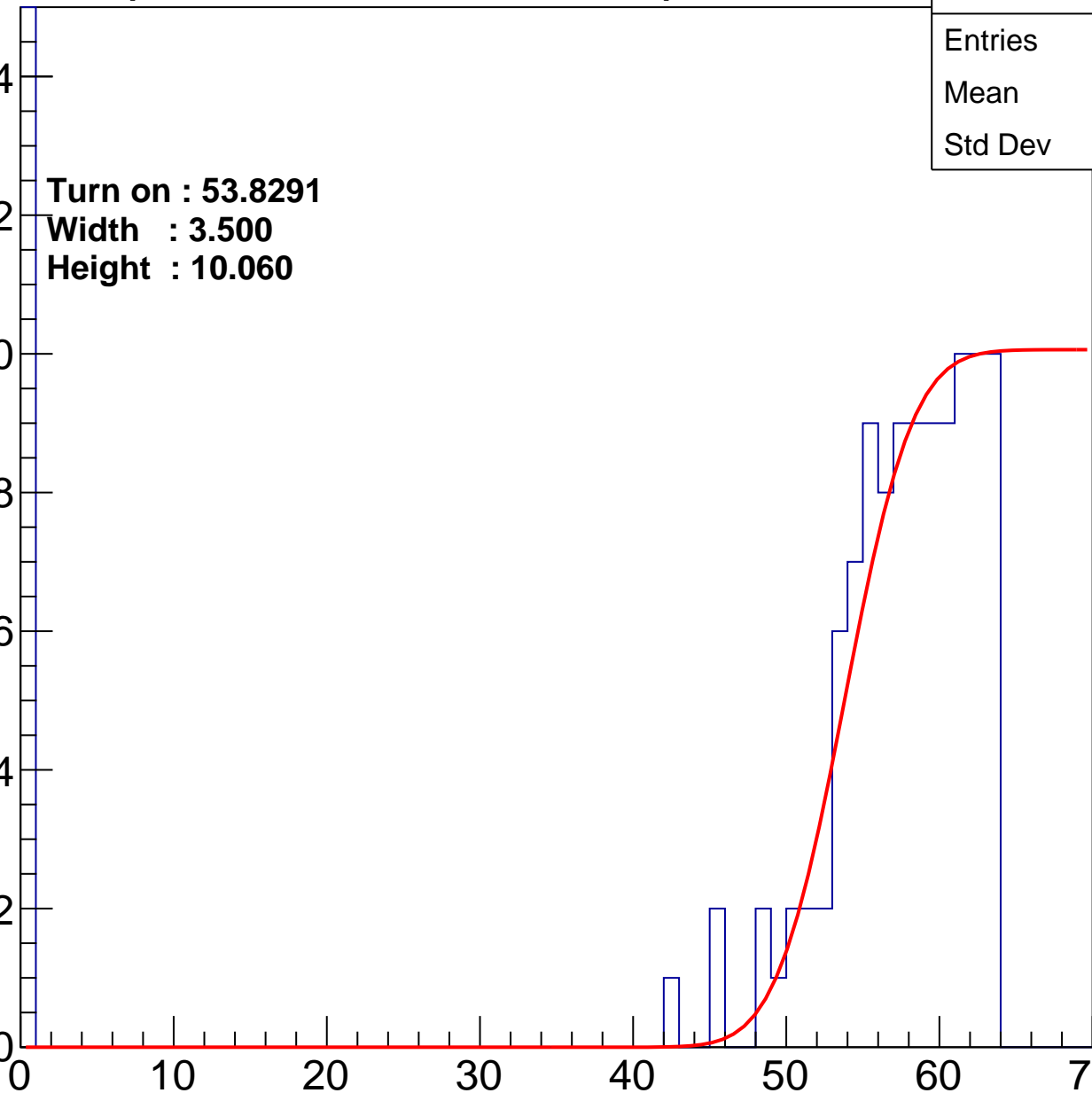
Width : 3.500

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch85

calib_packv5_033123_0516.root, FC#4, port A1

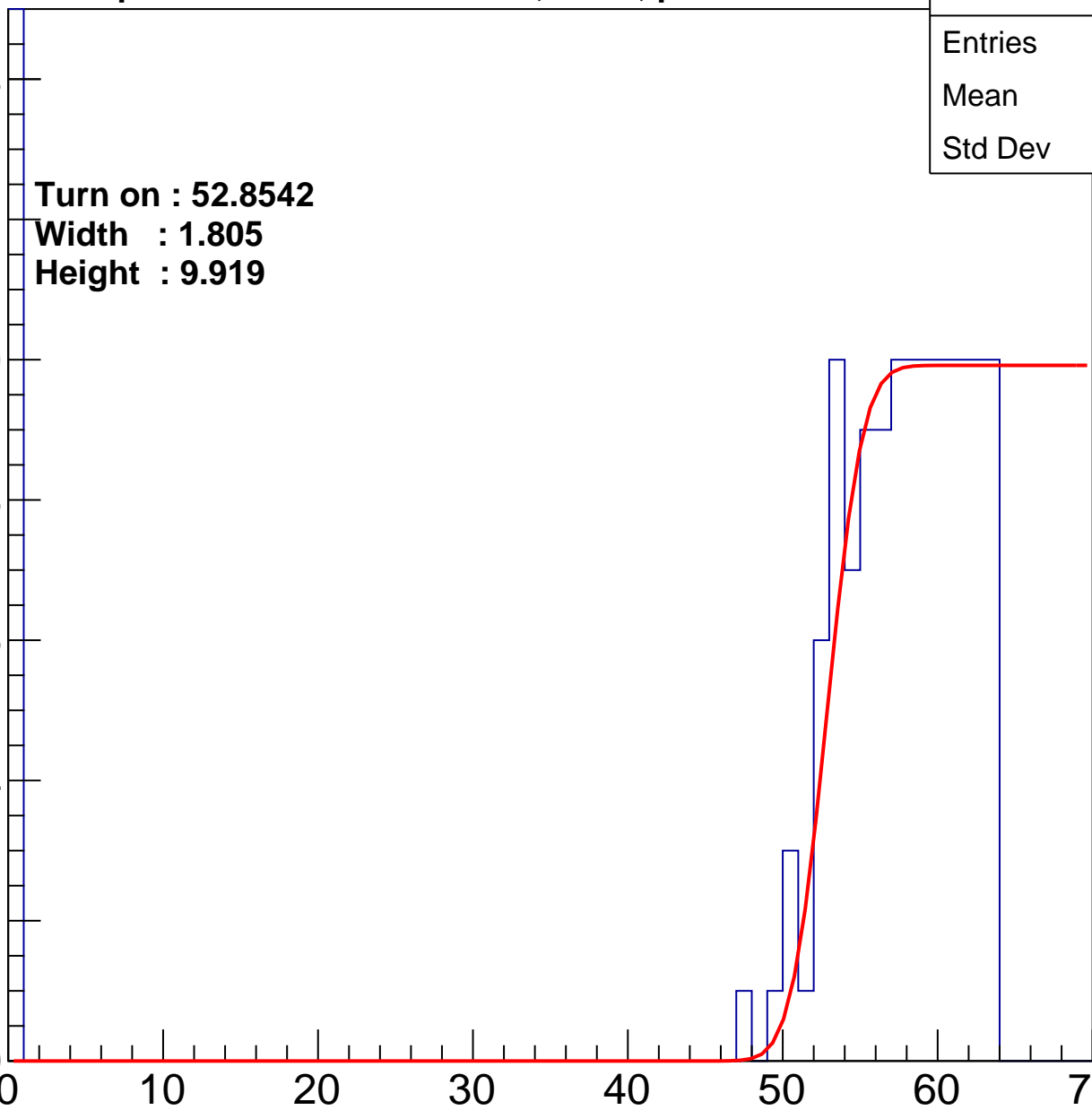
Entry

14
12
10
8
6
4
2
0

Turn on : 52.8542
Width : 1.805
Height : 9.919

Entries	184
Mean	36.5
Std Dev	27.78

ampl



B1L104S, U11-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	28.28
Std Dev	28.94

Turn on : 54.3965

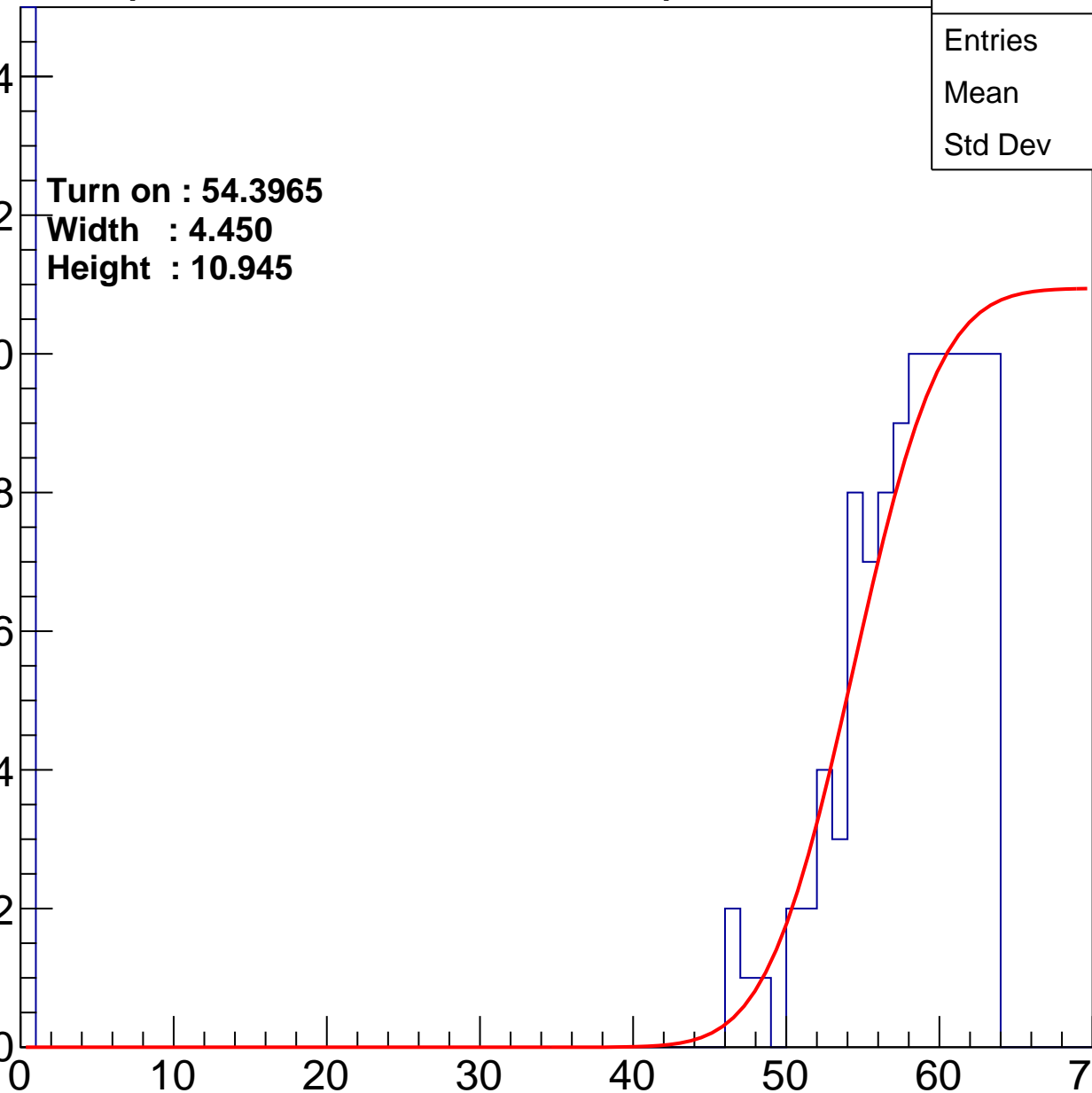
Width : 4.450

Height : 10.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch87

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	35.44
Std Dev	28.41

Turn on : 54.4675

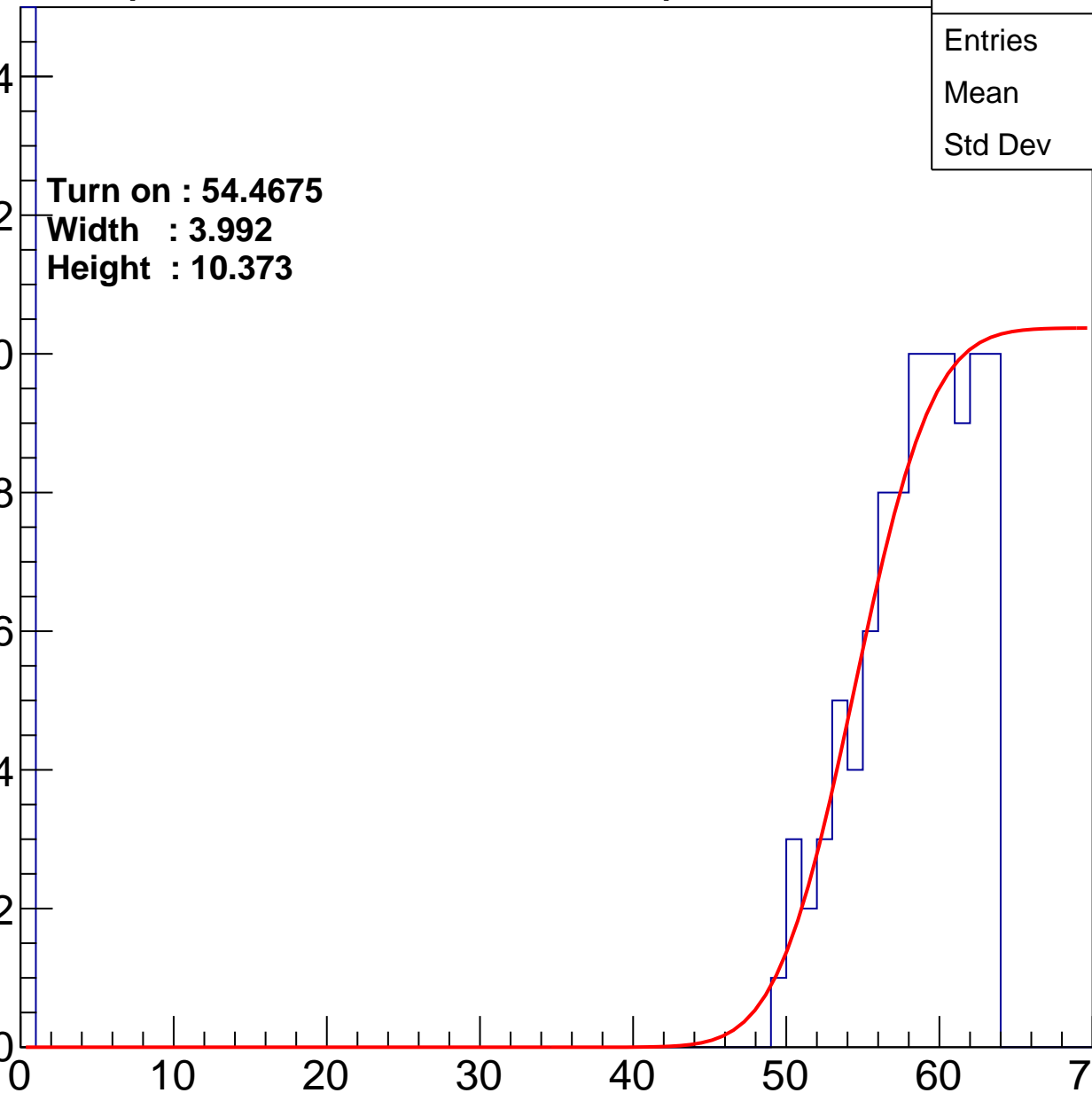
Width : 3.992

Height : 10.373

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch88

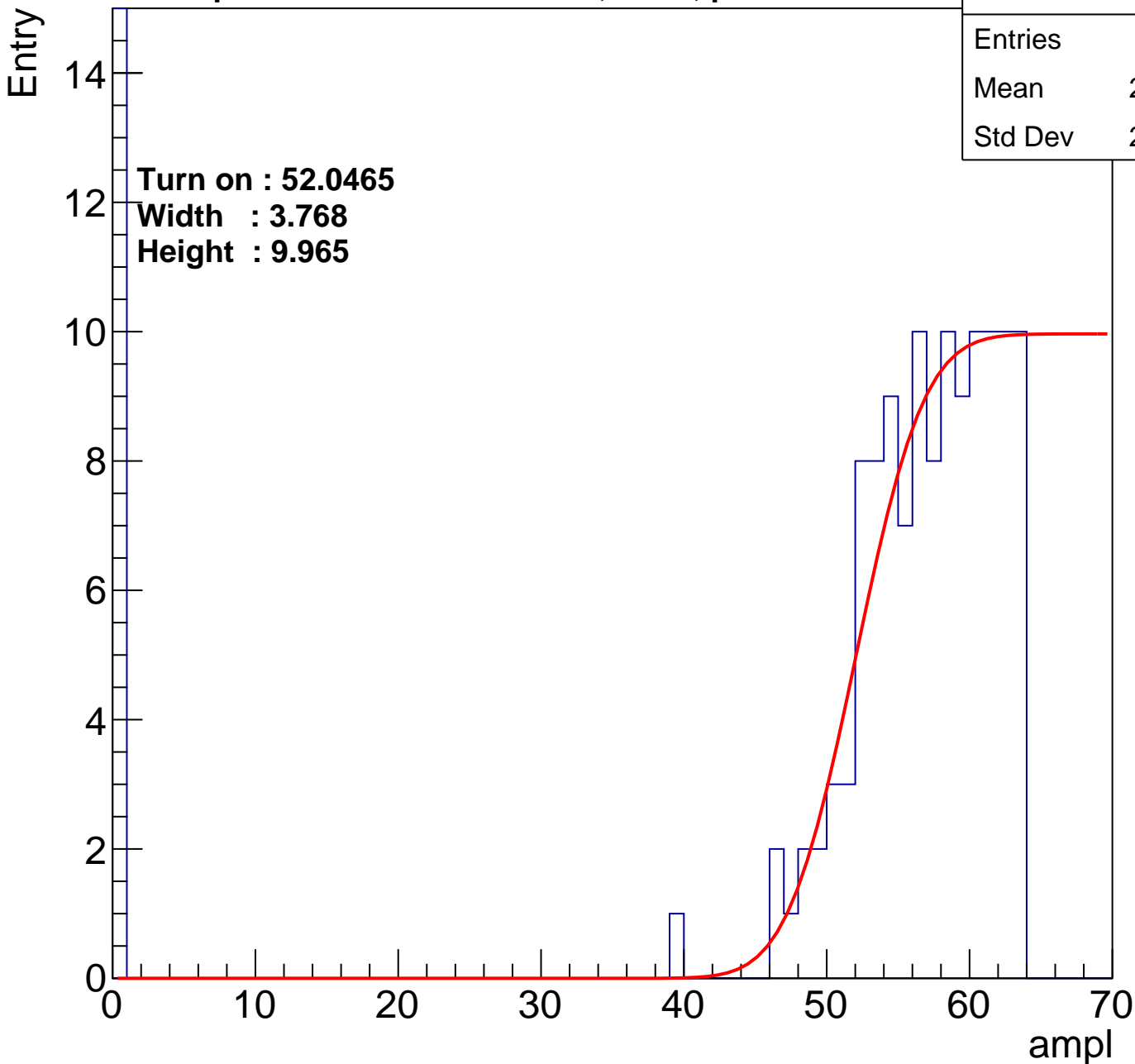
calib_packv5_033123_0516.root, FC#4, port A1

Entries	233
Mean	29.93
Std Dev	28.49

Turn on : 52.0465

Width : 3.768

Height : 9.965



B1L104S, U11-ch89

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	32.7
Std Dev	28.93

Turn on : 53.8468

Width : 2.379

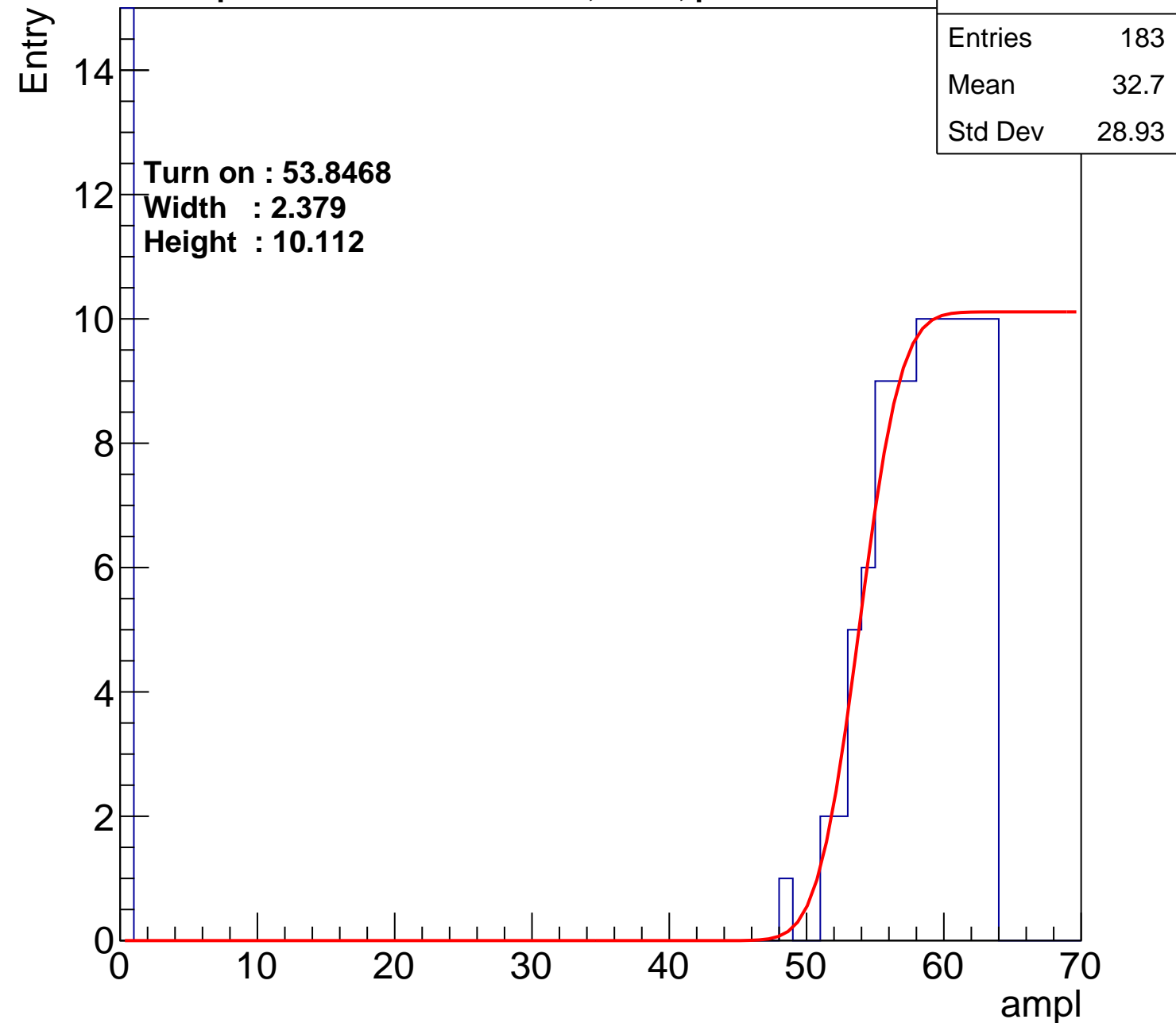
Height : 10.112

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	30.67
Std Dev	28.88

Turn on : 53.9538

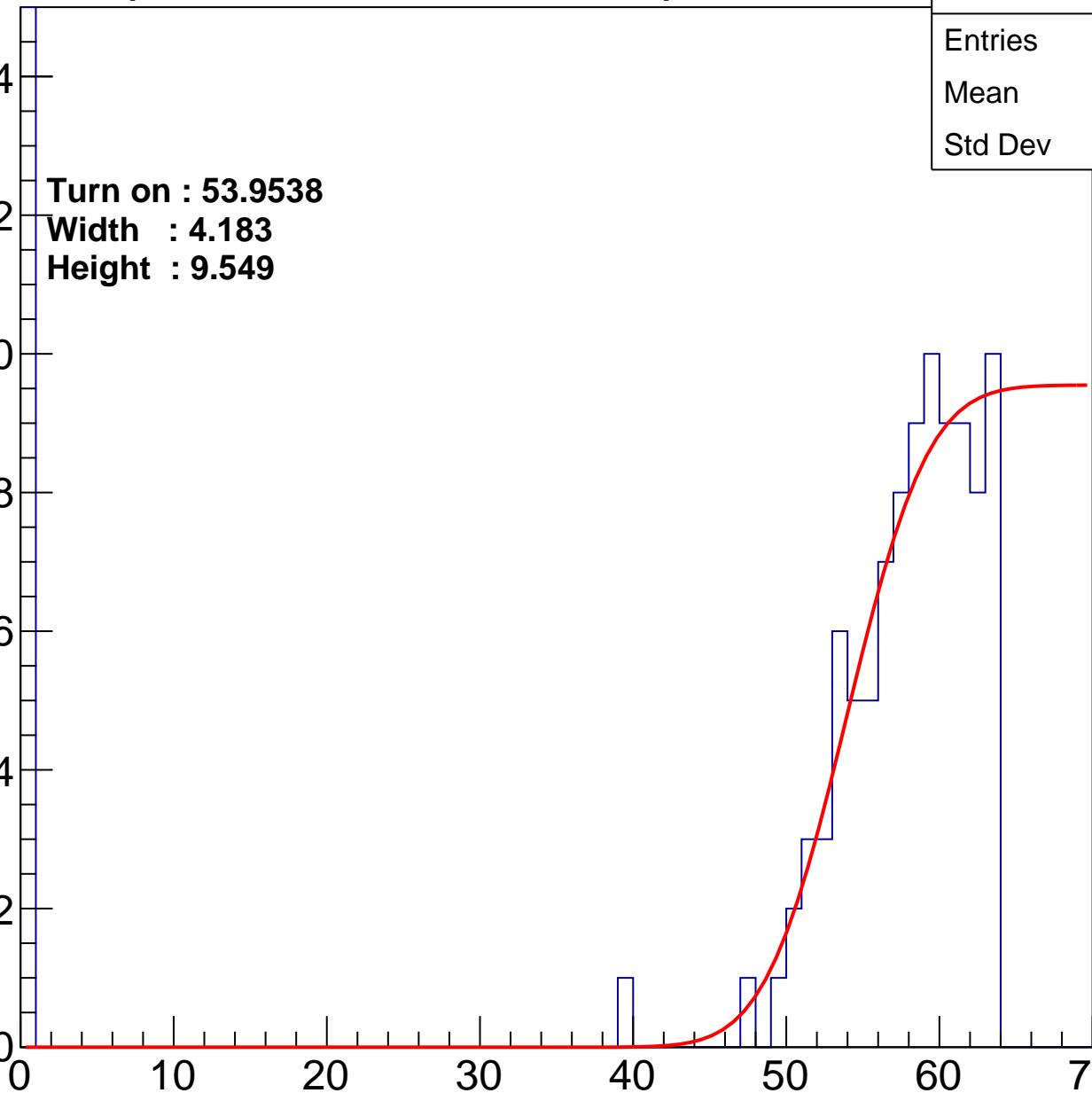
Width : 4.183

Height : 9.549

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch91

calib_packv5_033123_0516.root, FC#4, port A1

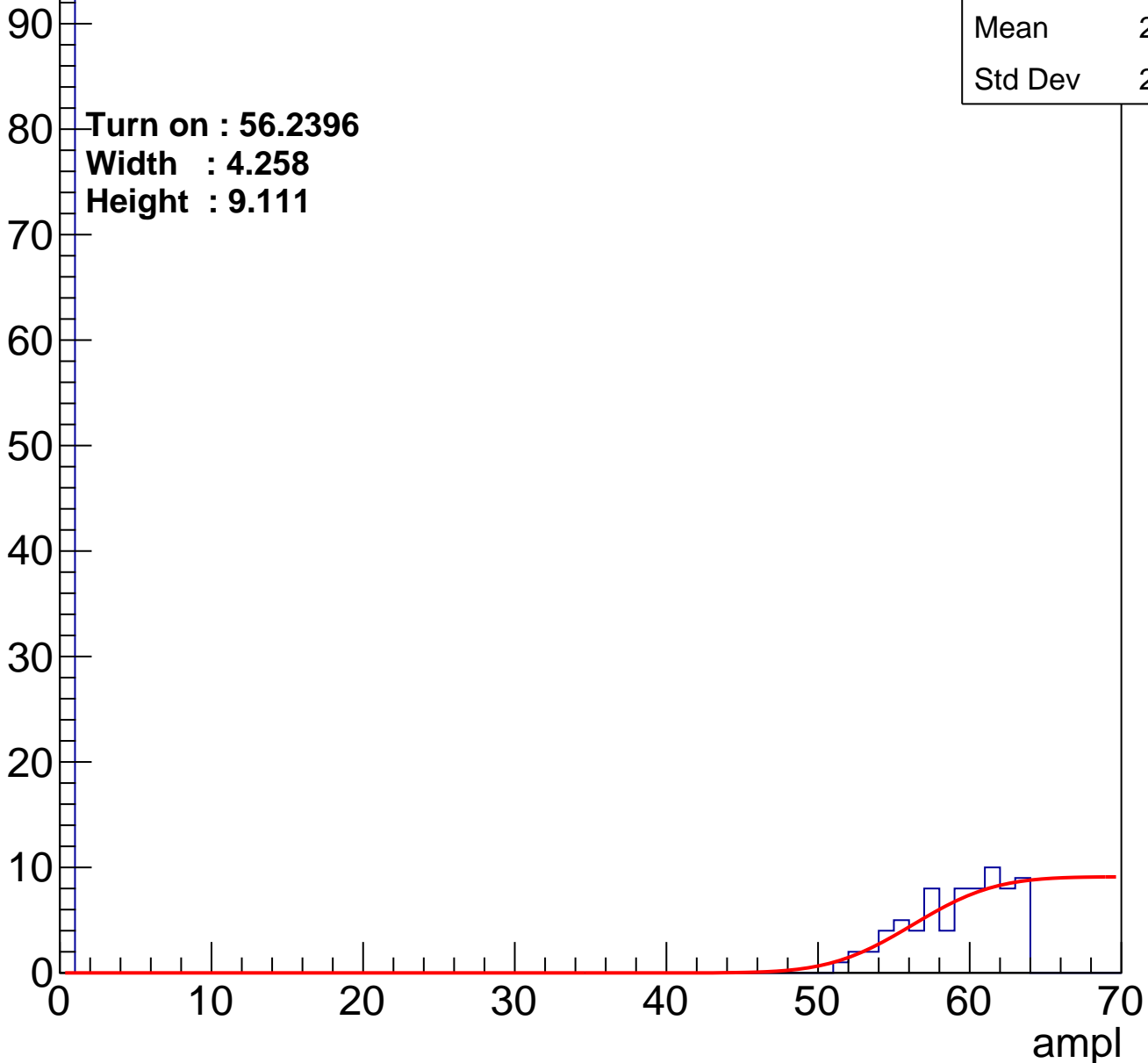
Entries	166
Mean	25.84
Std Dev	29.24

Turn on : 56.2396

Width : 4.258

Height : 9.111

Entry



B1L104S, U11-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	215
Mean	29.78
Std Dev	28.73

Turn on : 53.4599

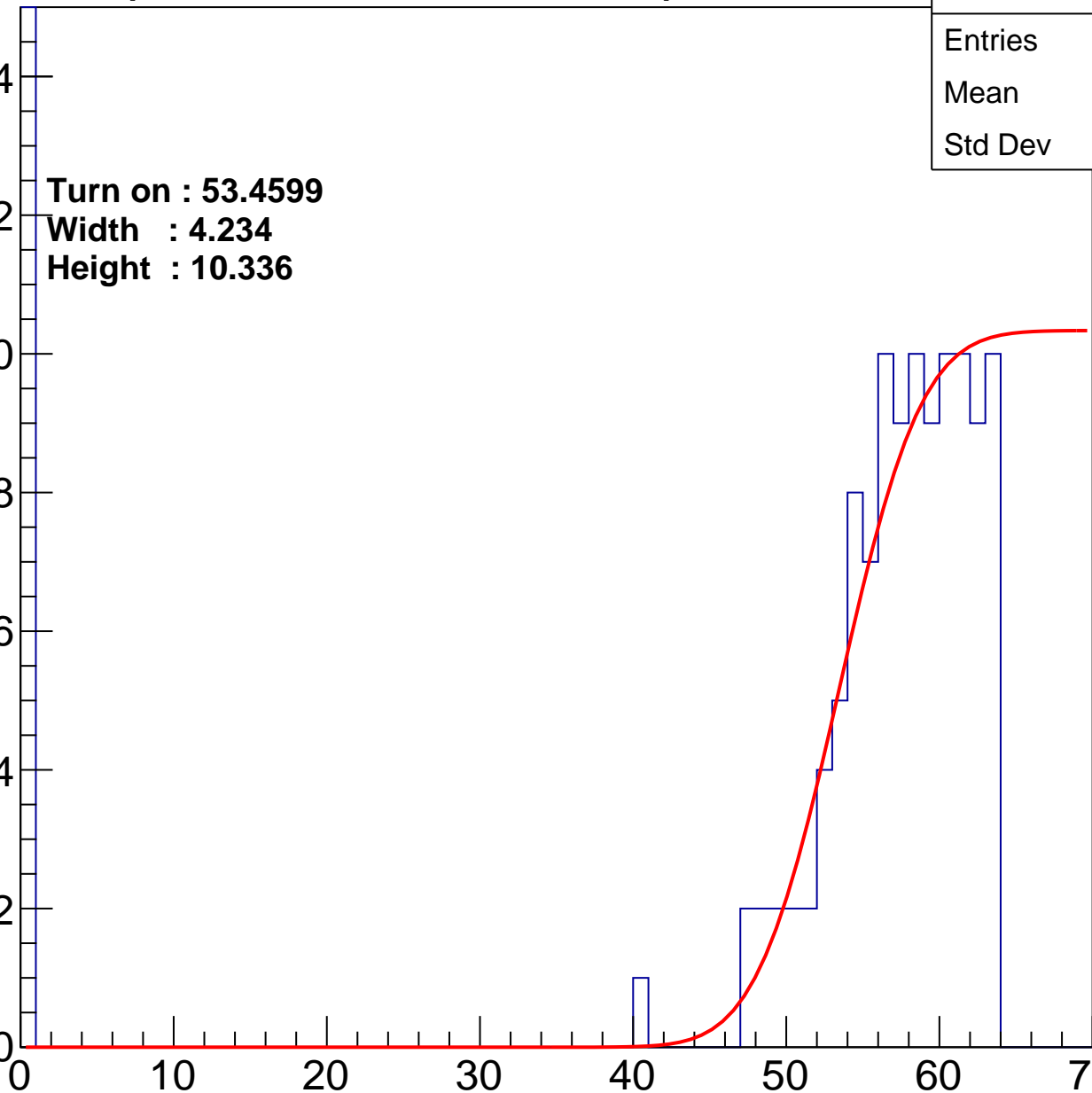
Width : 4.234

Height : 10.336

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch93

calib_packv5_033123_0516.root, FC#4, port A1

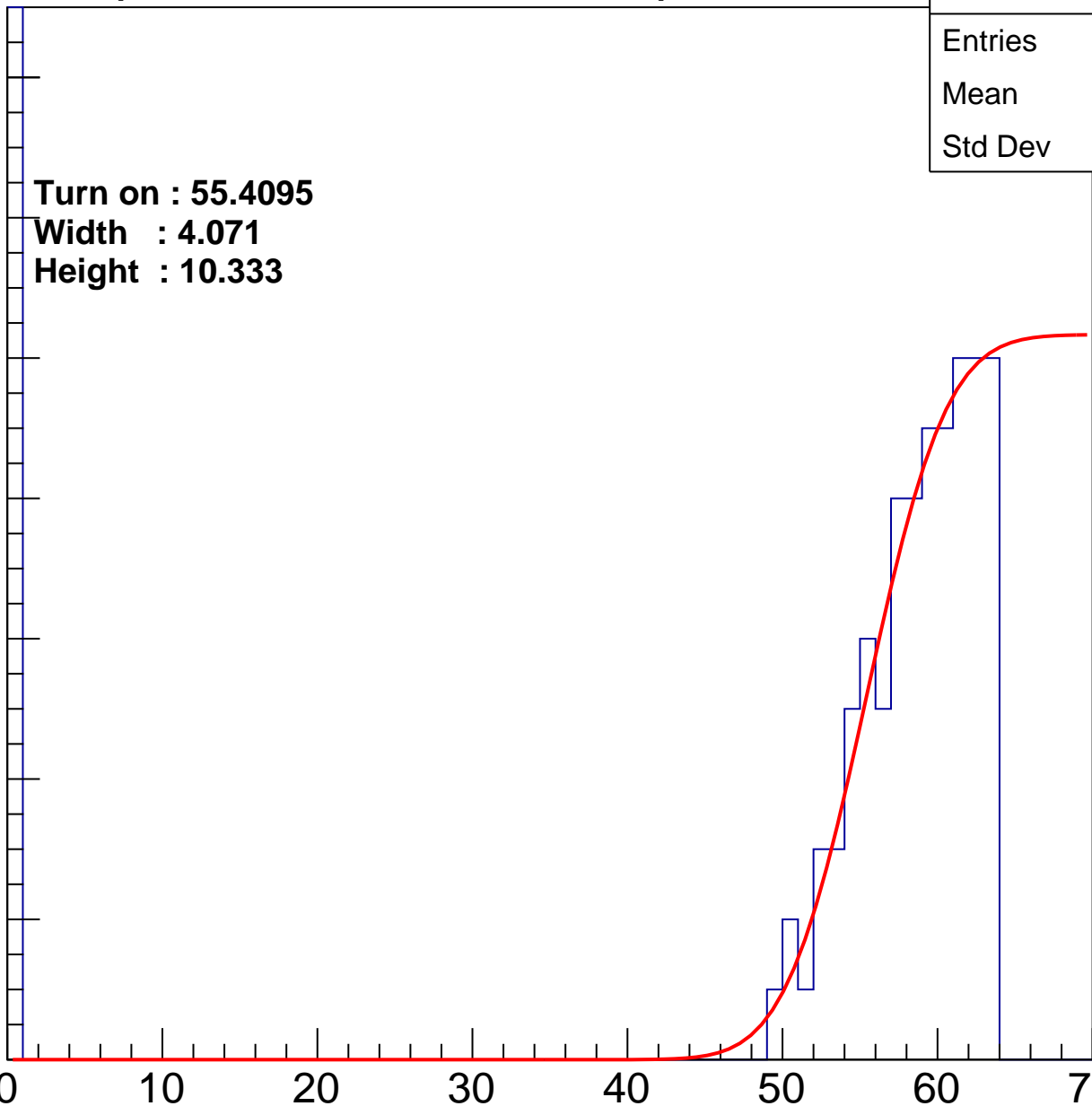
Entry

14
12
10
8
6
4
2
0

Turn on : 55.4095
Width : 4.071
Height : 10.333

Entries	157
Mean	33.41
Std Dev	28.96

ampl



B1L104S, U11-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	225
Mean	23.93
Std Dev	28.64

Turn on : 56.1533

Width : 4.342

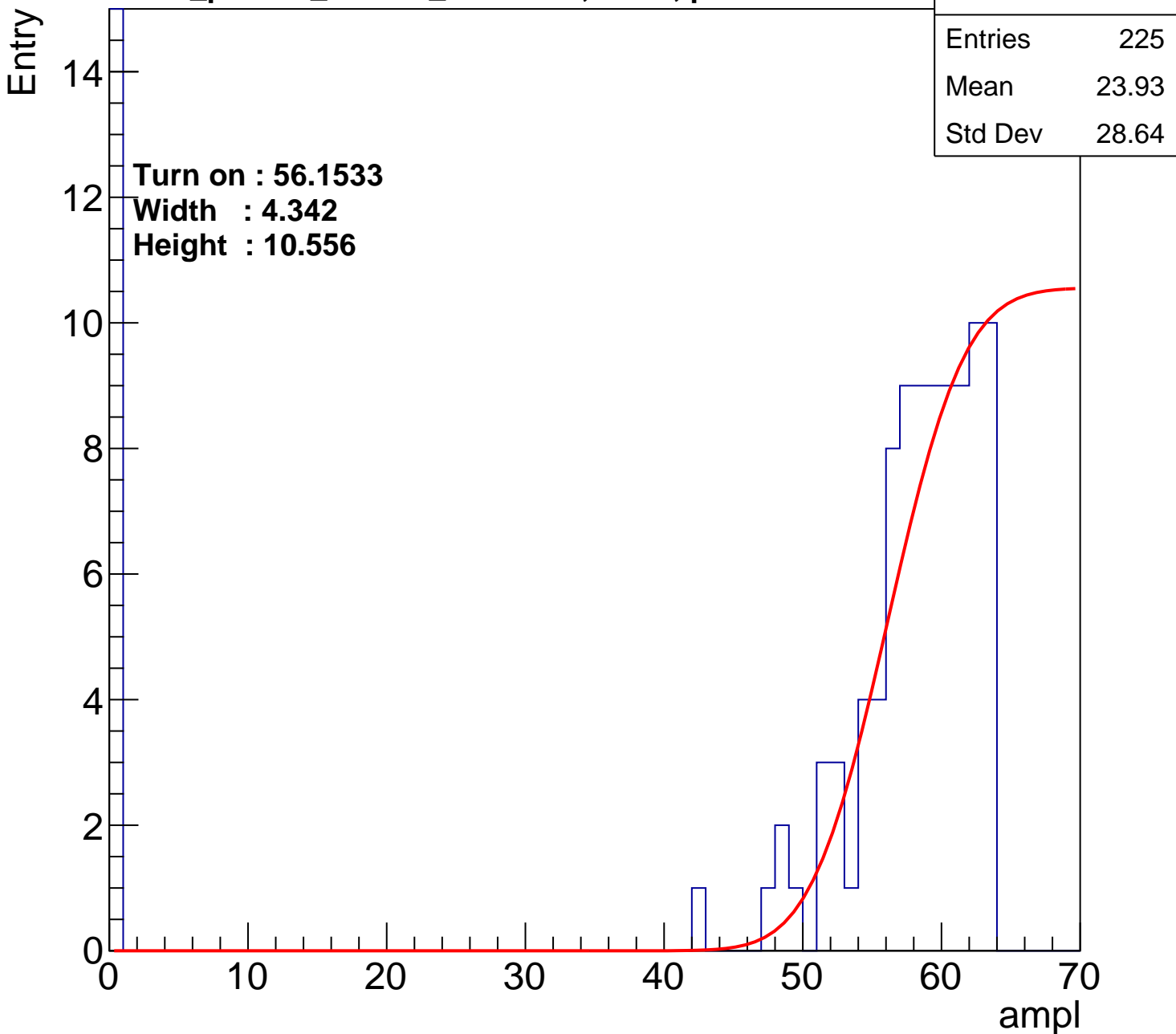
Height : 10.556

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	154
Mean	33.2
Std Dev	29.23

Turn on : 55.2175

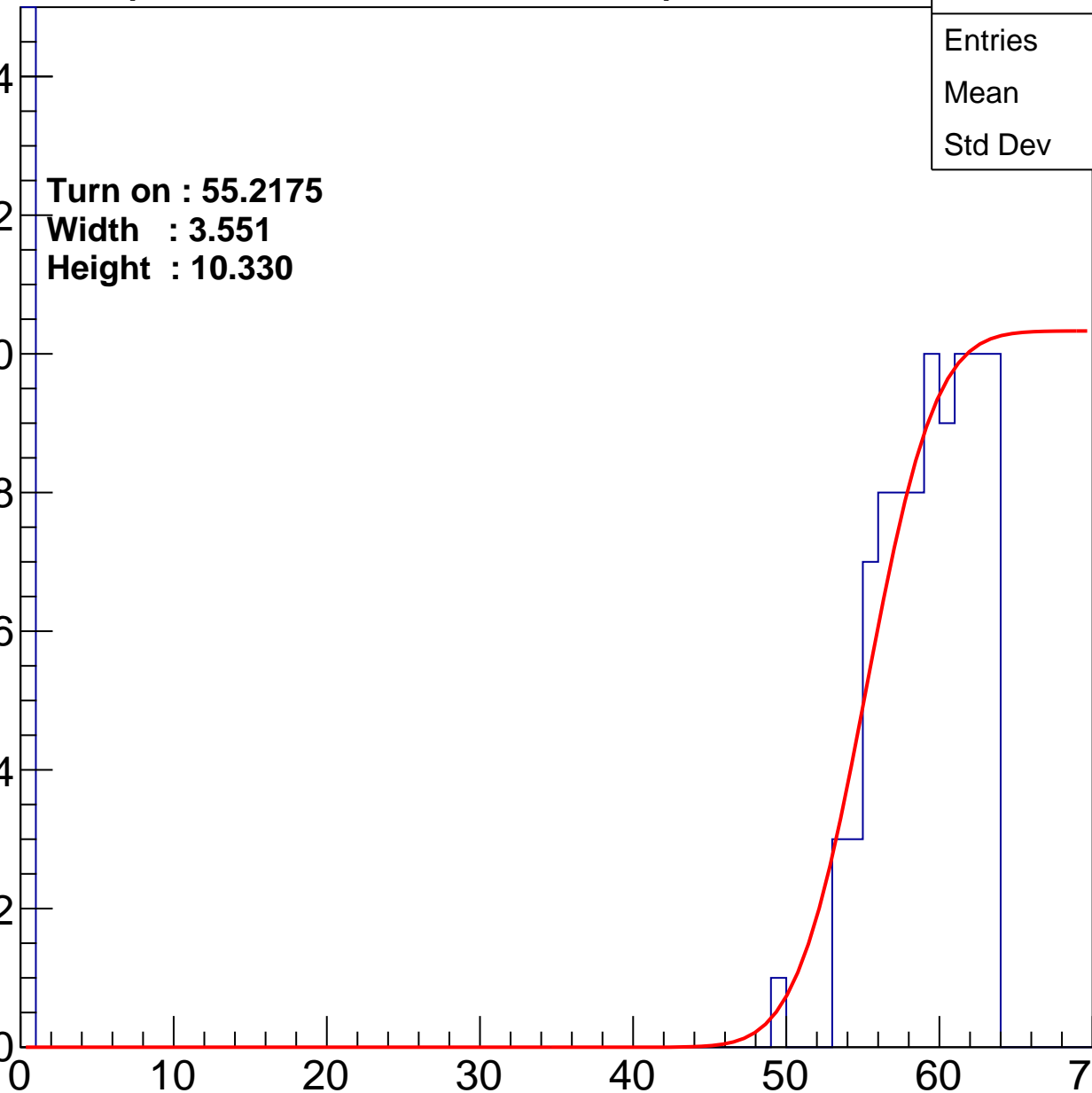
Width : 3.551

Height : 10.330

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	32.04
Std Dev	28.23

Turn on : 51.8574

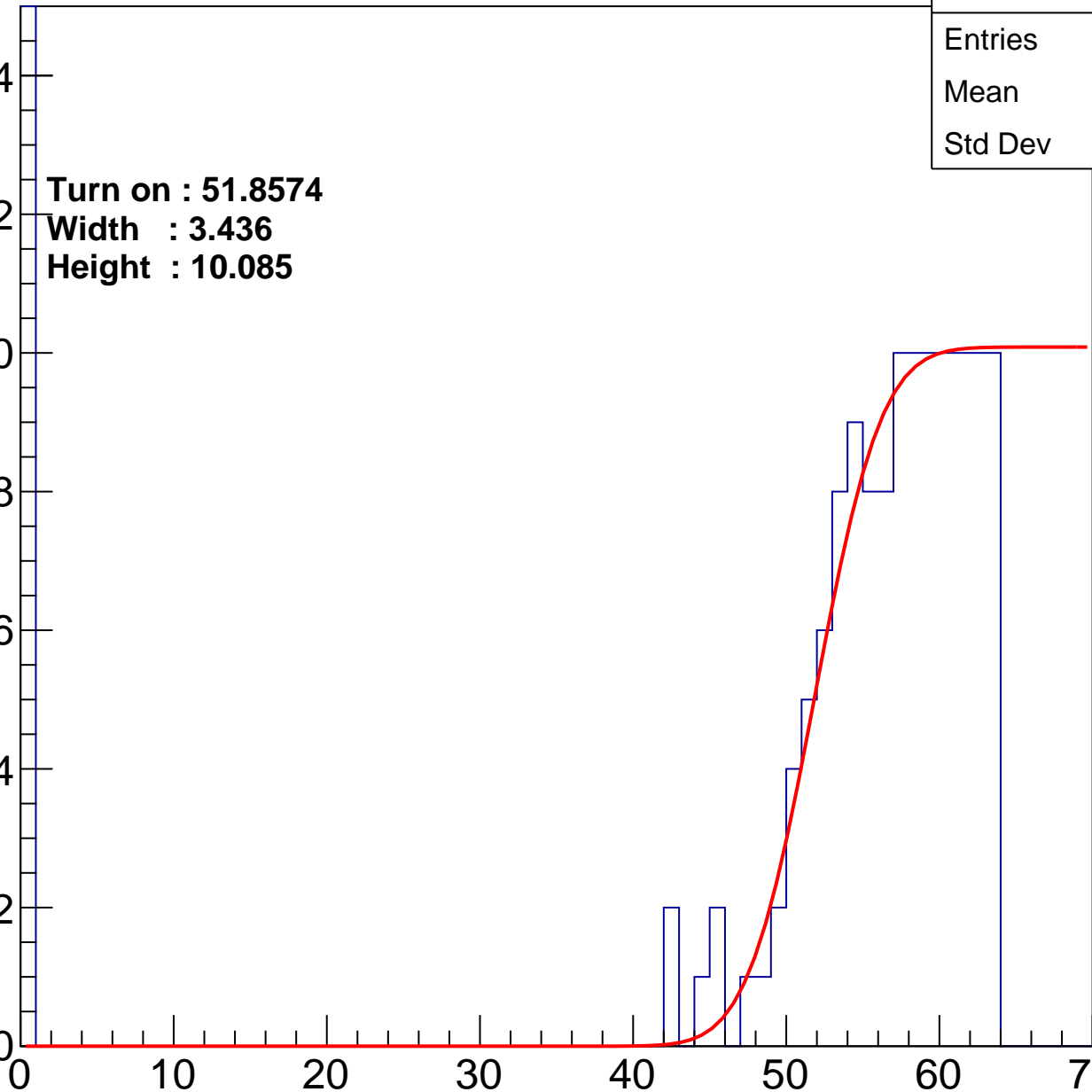
Width : 3.436

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch97

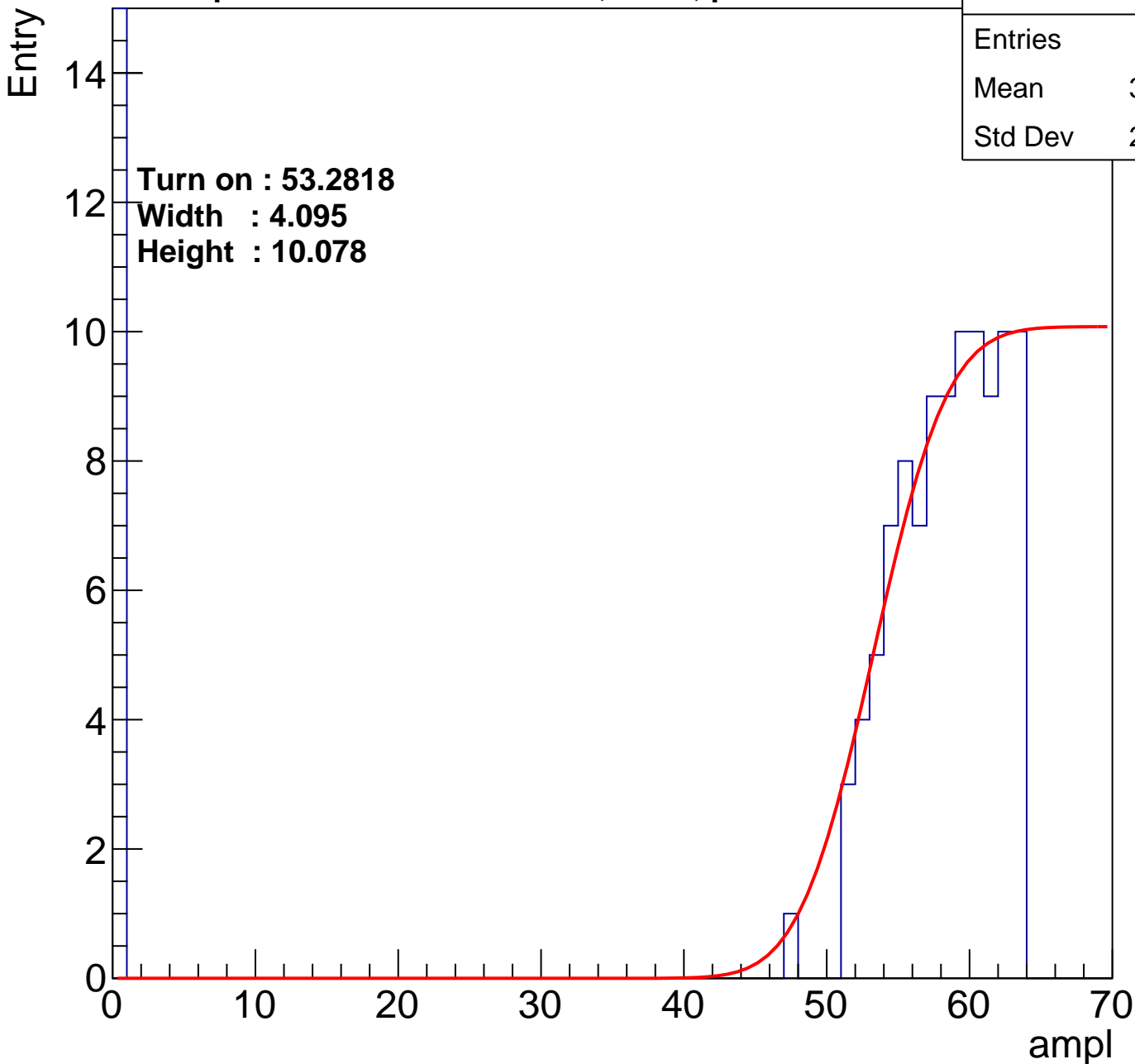
calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	31.93
Std Dev	28.92

Turn on : 53.2818

Width : 4.095

Height : 10.078



B1L104S, U11-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	31.8
Std Dev	28.26

Turn on : 51.9720

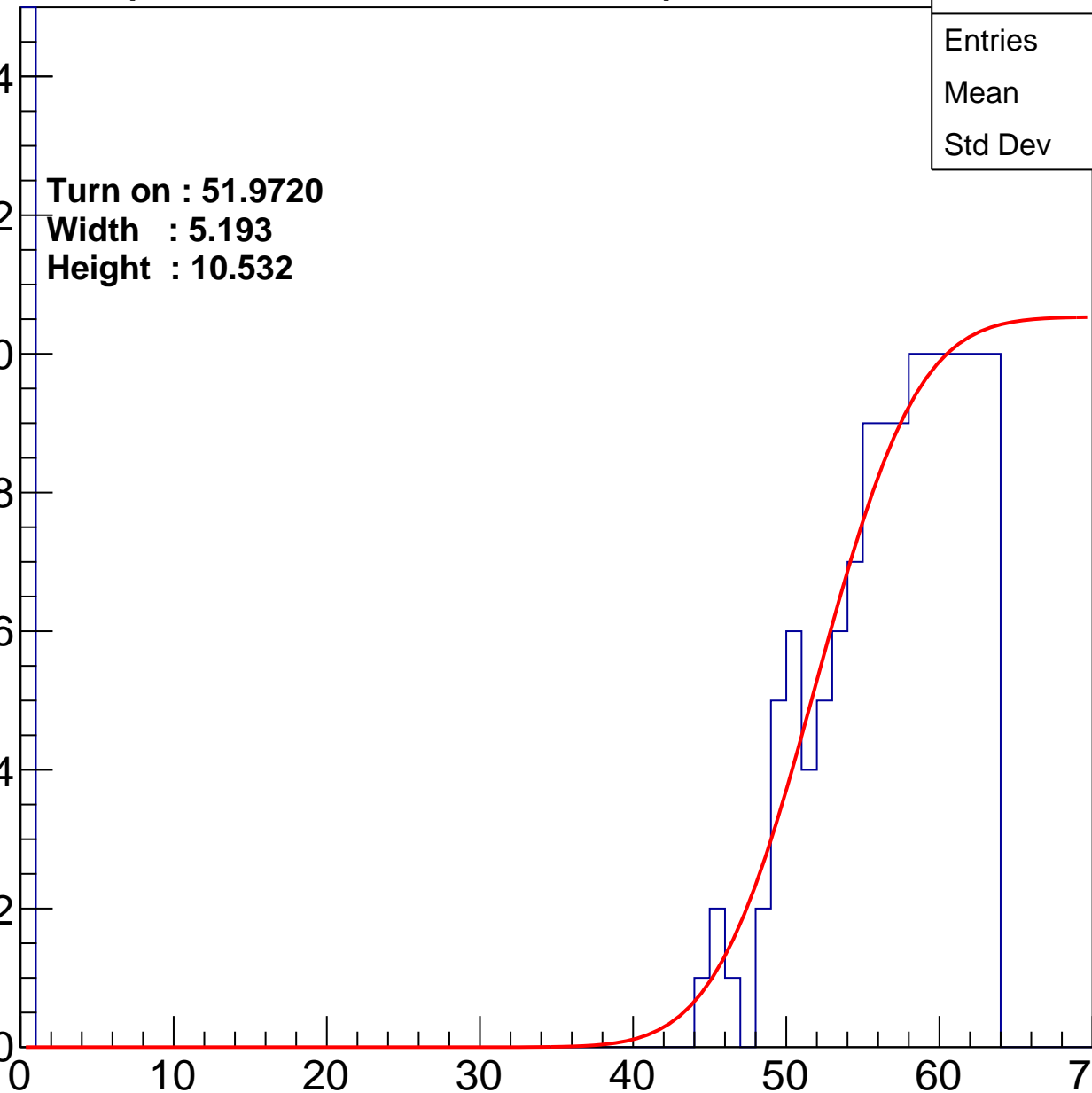
Width : 5.193

Height : 10.532

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch99

calib_packv5_033123_0516.root, FC#4, port A1

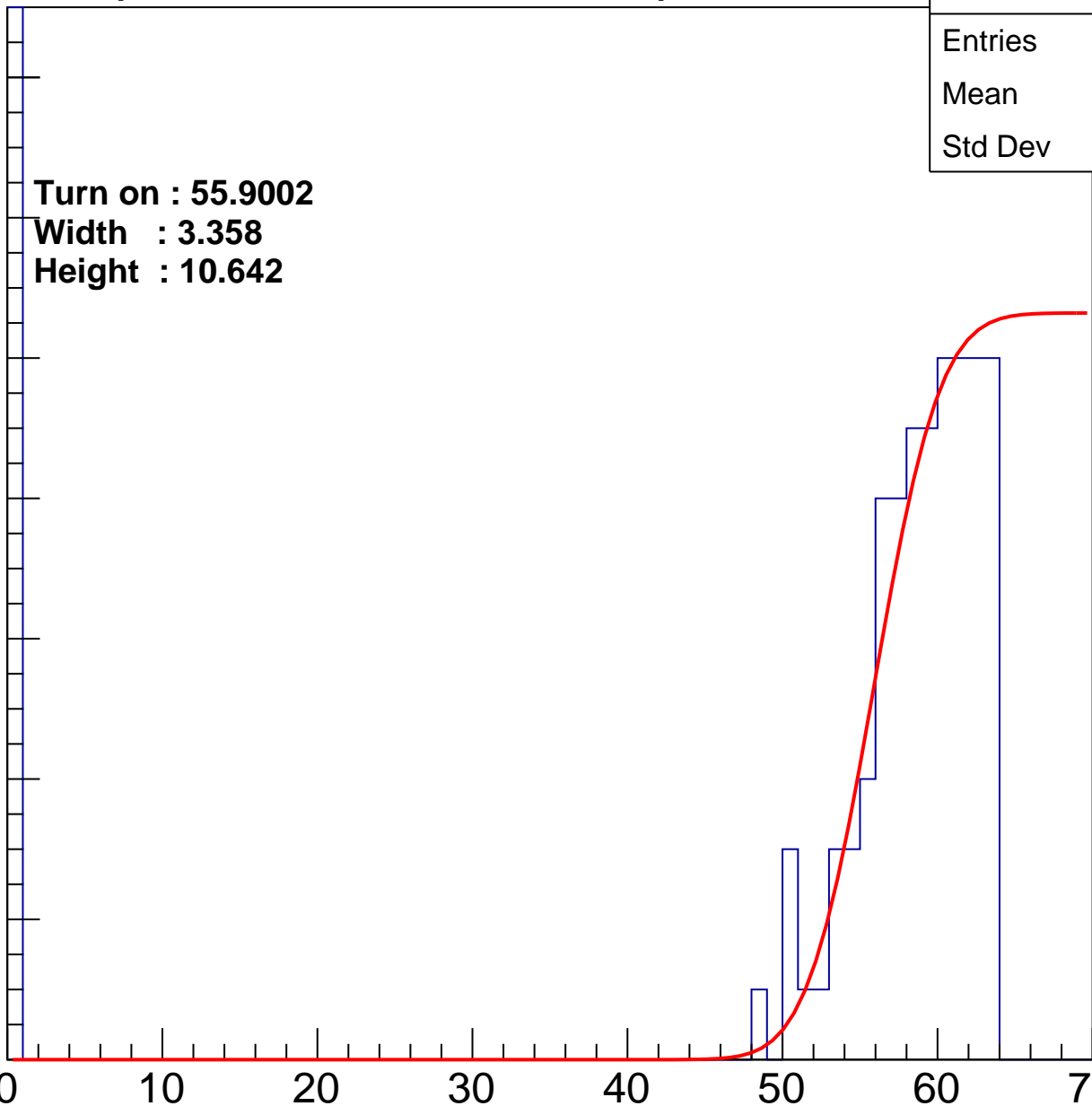
Entry

14
12
10
8
6
4
2
0

Turn on : 55.9002
Width : 3.358
Height : 10.642

Entries	180
Mean	29.22
Std Dev	29.32

ampl



B1L104S, U11-ch100

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	34.16
Std Dev	27.94

Turn on : 52.1070

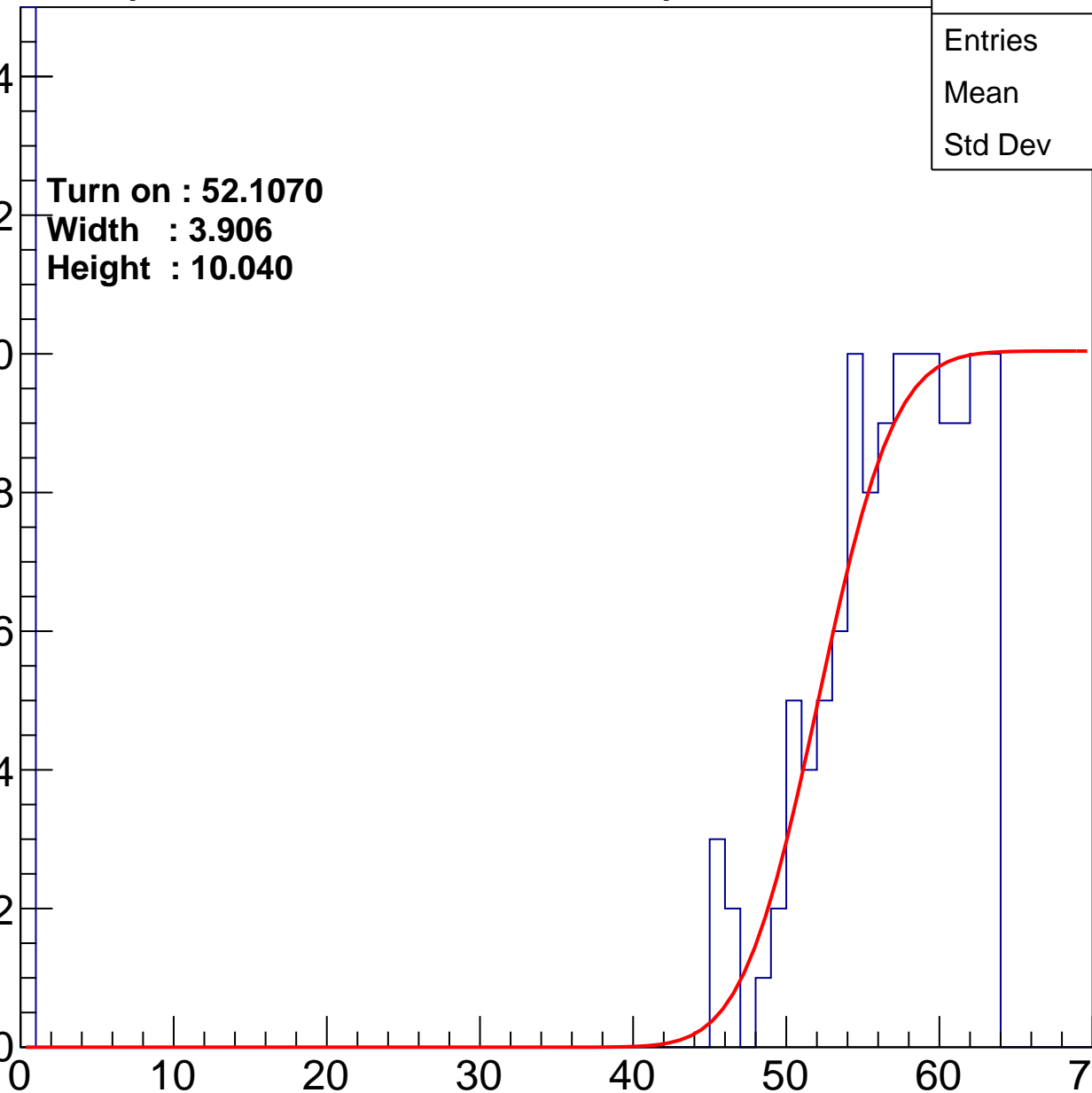
Width : 3.906

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	33.09
Std Dev	28.75

Turn on : 54.5447

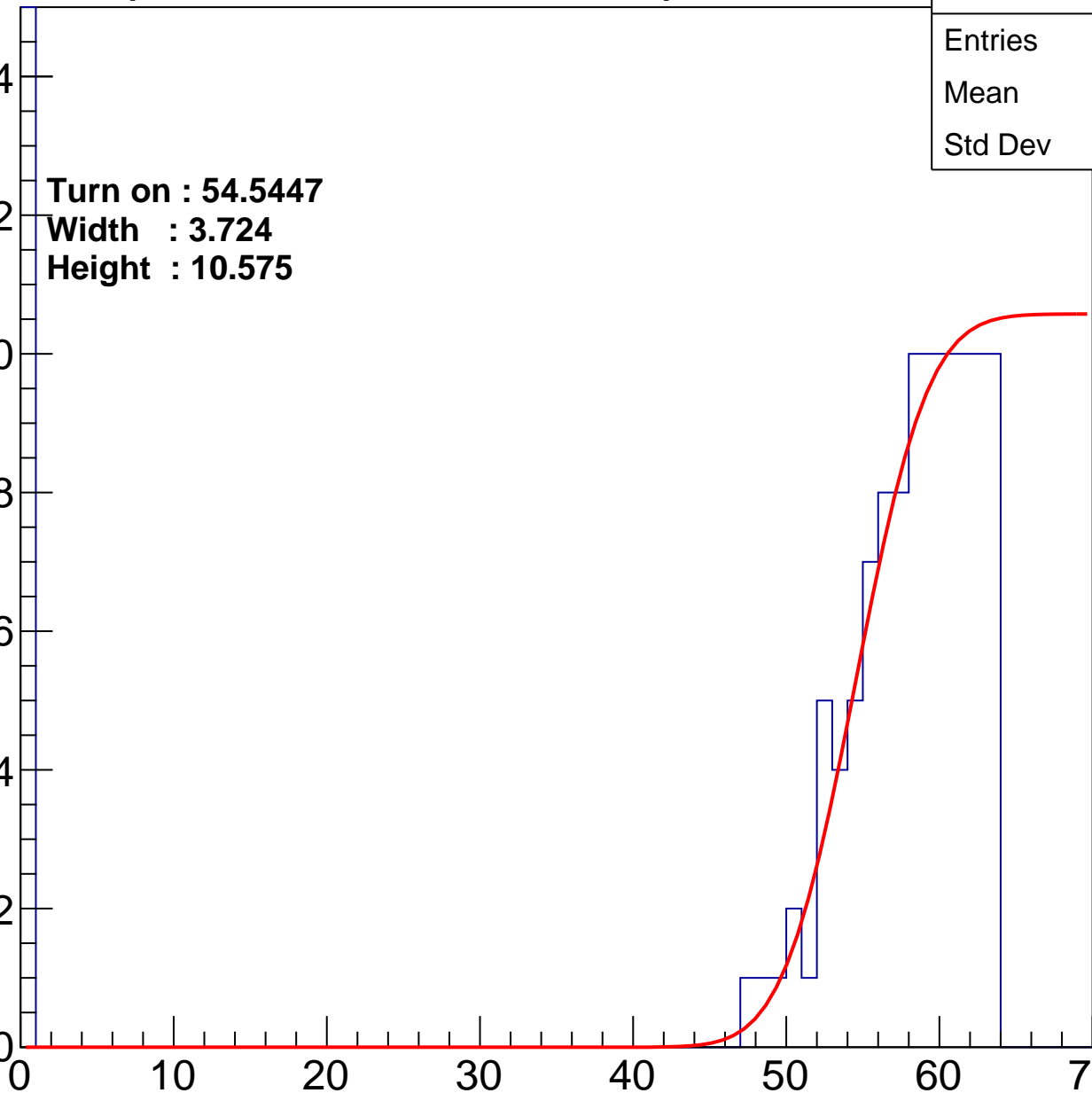
Width : 3.724

Height : 10.575

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	235
Mean	31.74
Std Dev	28.05

Turn on : 51.7148

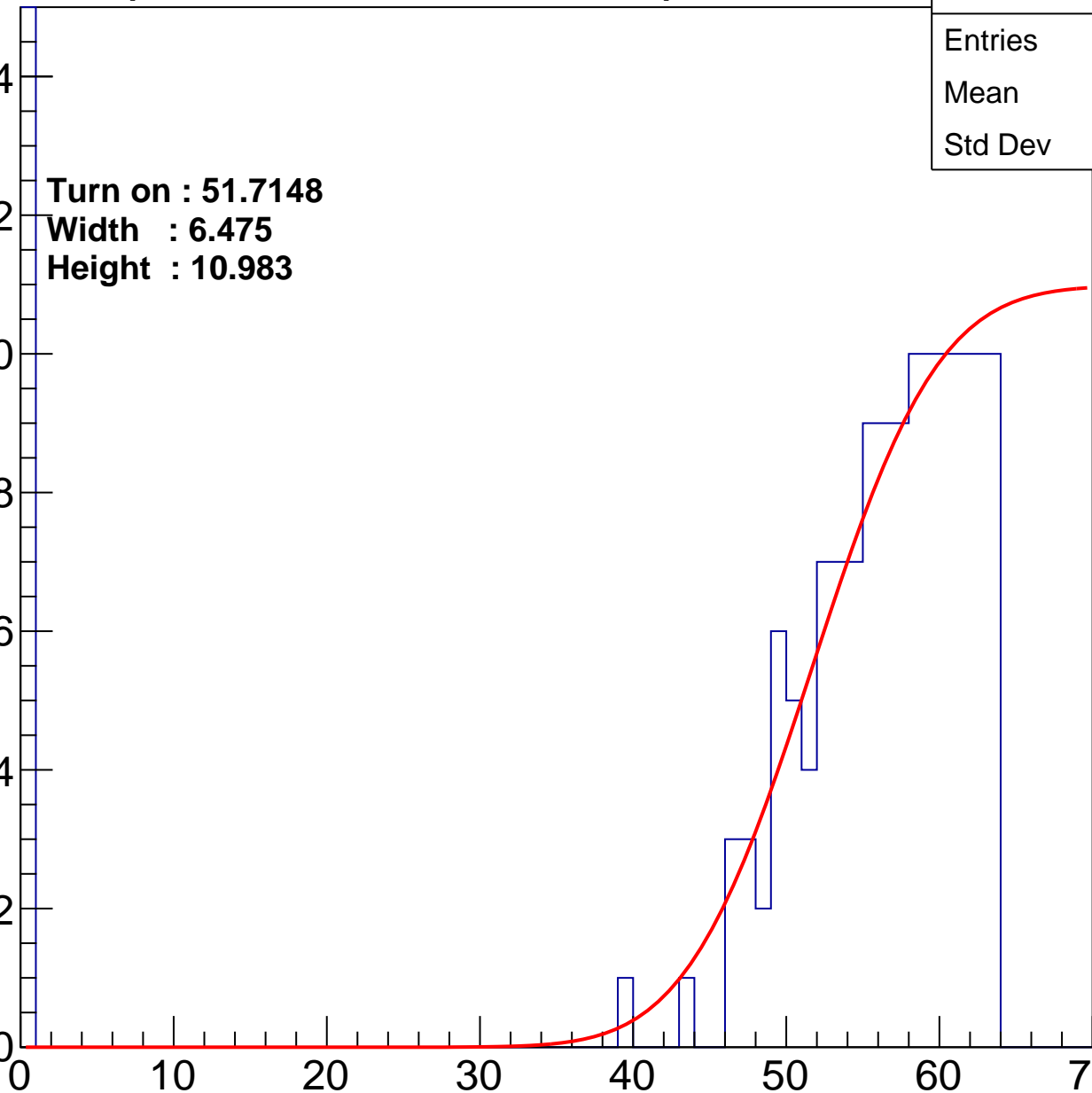
Width : 6.475

Height : 10.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	32.97
Std Dev	28.29

Turn on : 54.2296

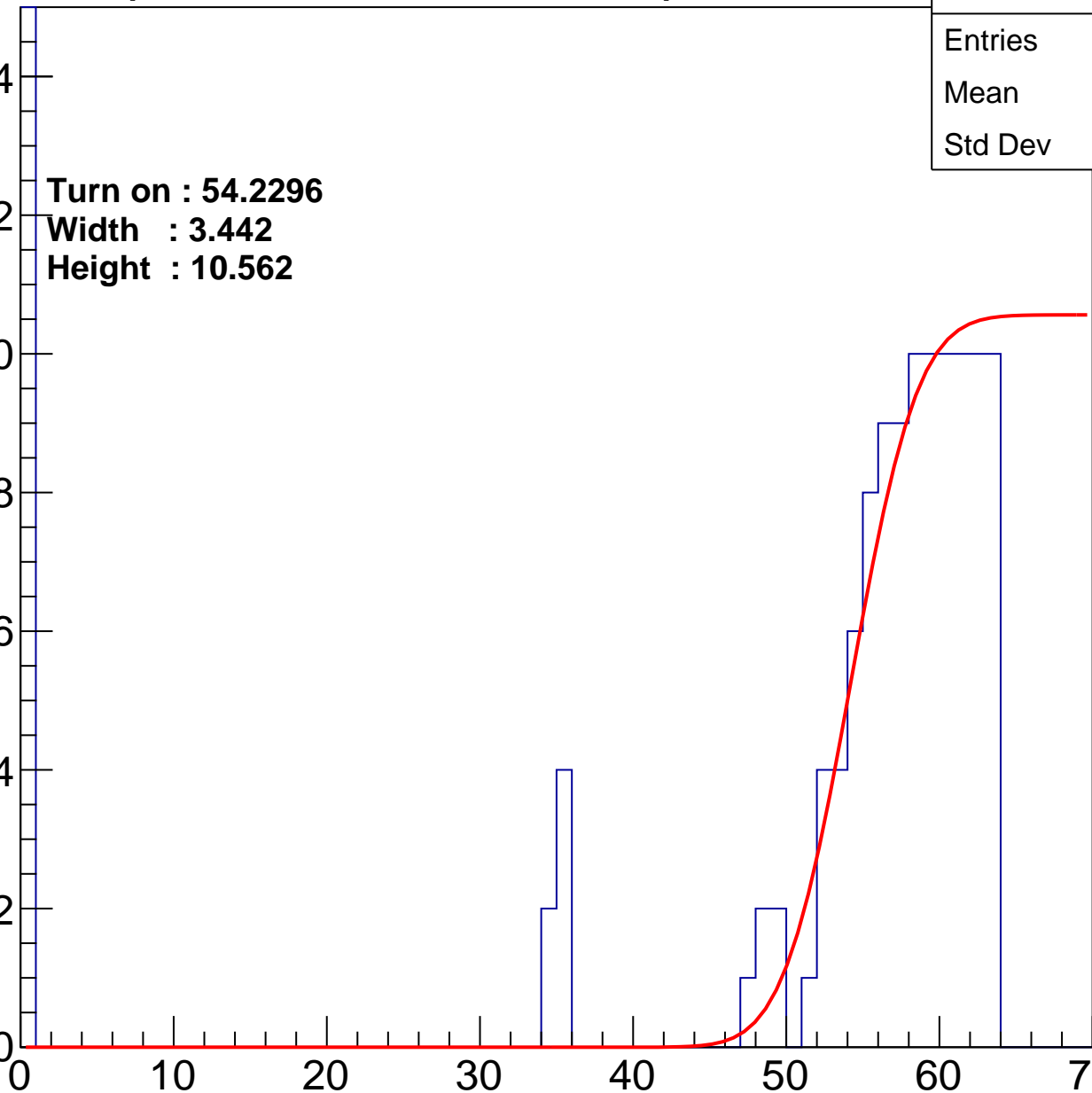
Width : 3.442

Height : 10.562

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	233
Mean	28.31
Std Dev	28.57

Turn on : 52.1490

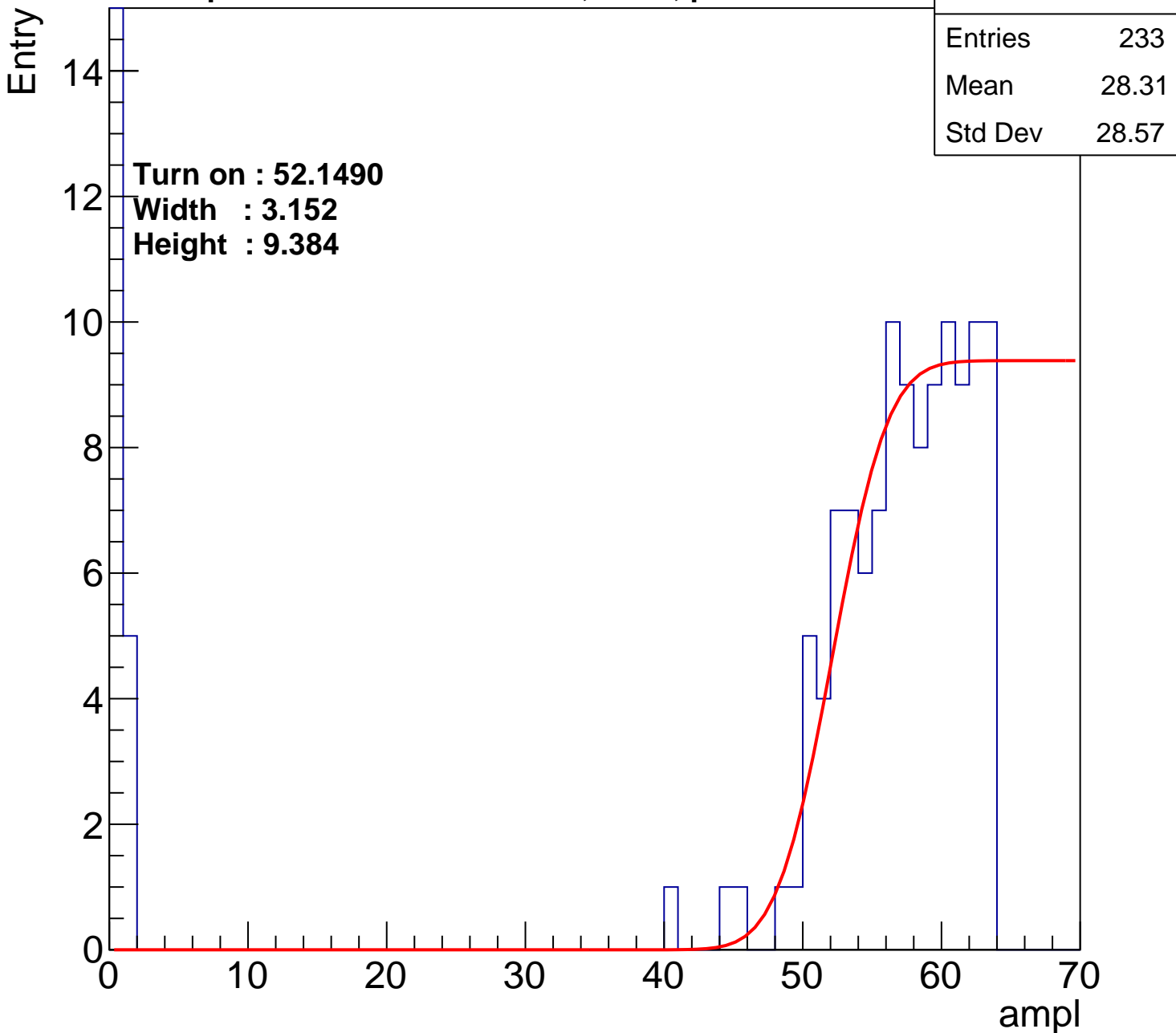
Width : 3.152

Height : 9.384

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.31
Std Dev	28.56

Turn on : 54.4162

Width : 2.490

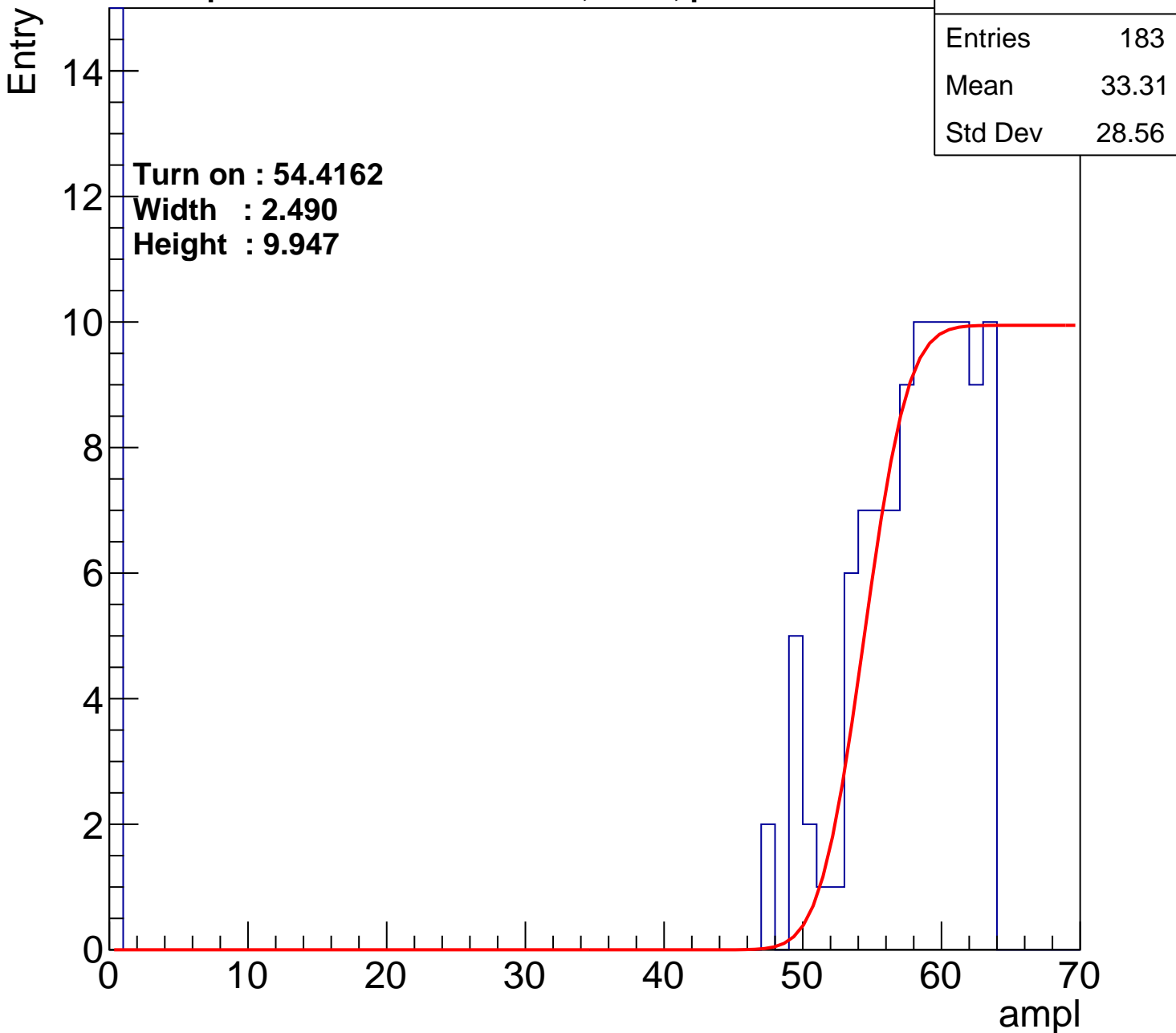
Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U11-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	238
Mean	27.43
Std Dev	28.75

Turn on : 53.2422

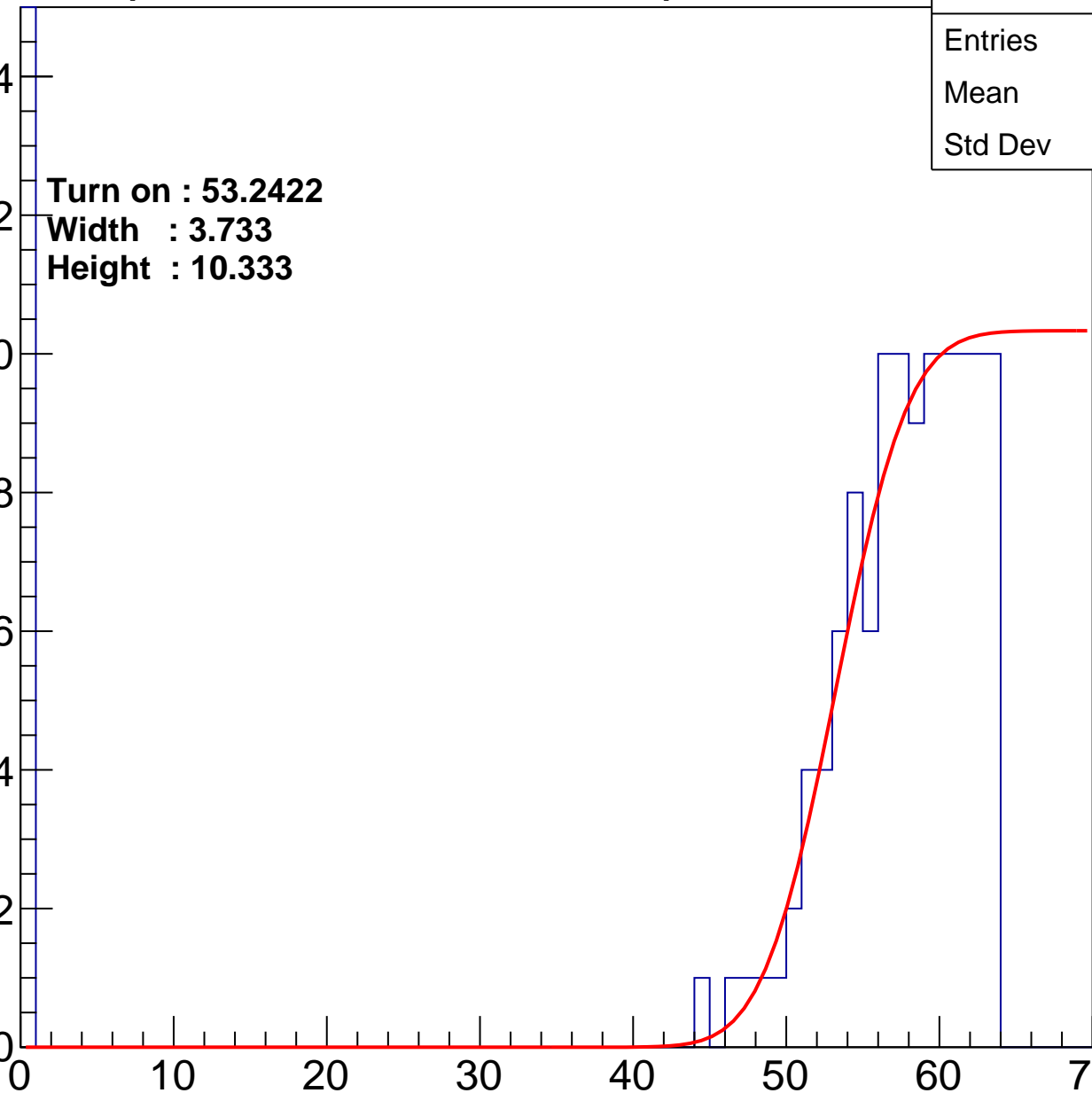
Width : 3.733

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	28.56
Std Dev	29.13

Turn on : 54.7038

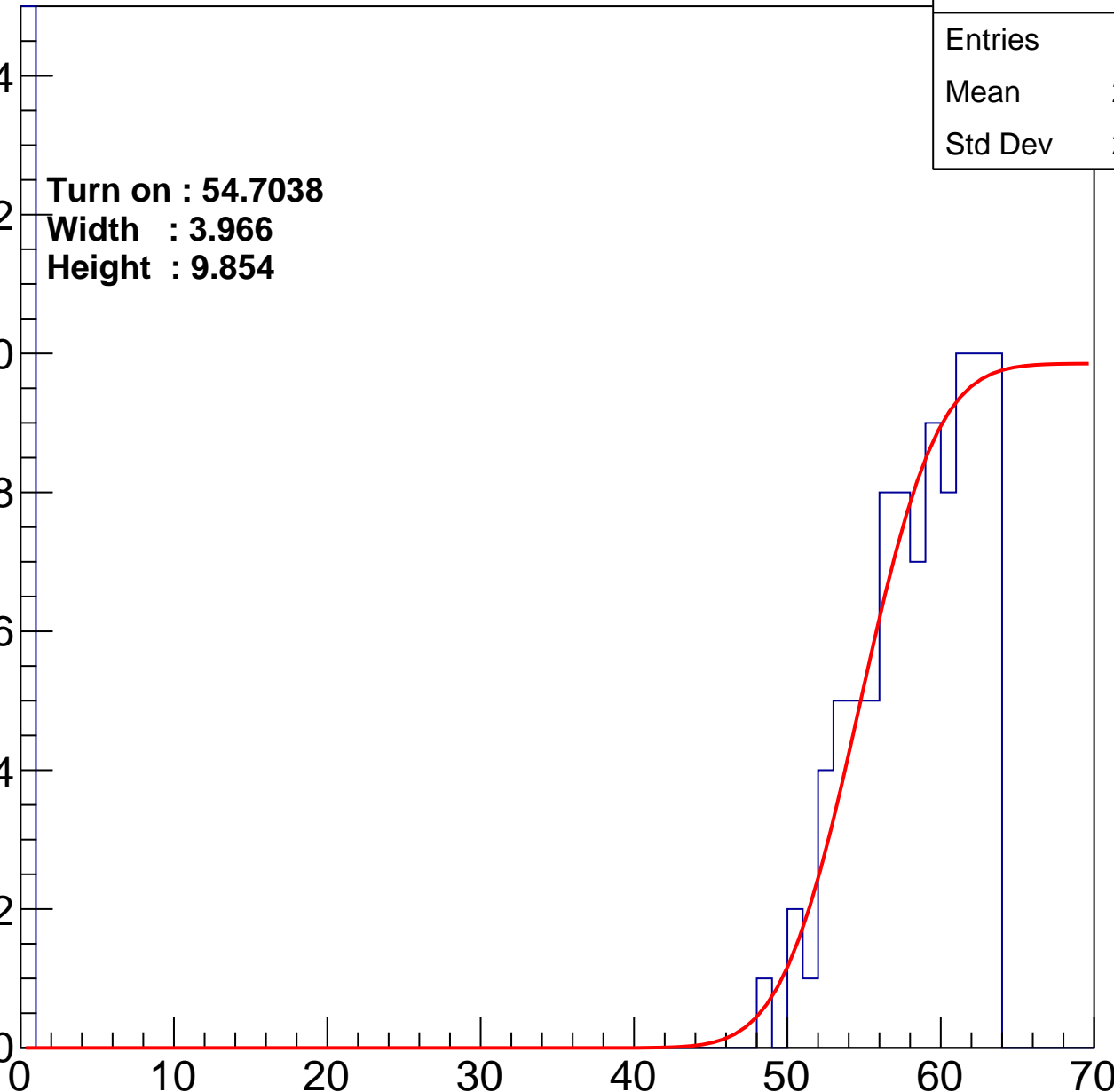
Width : 3.966

Height : 9.854

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	217
Mean	28.88
Std Dev	28.89

Turn on : 53.9881

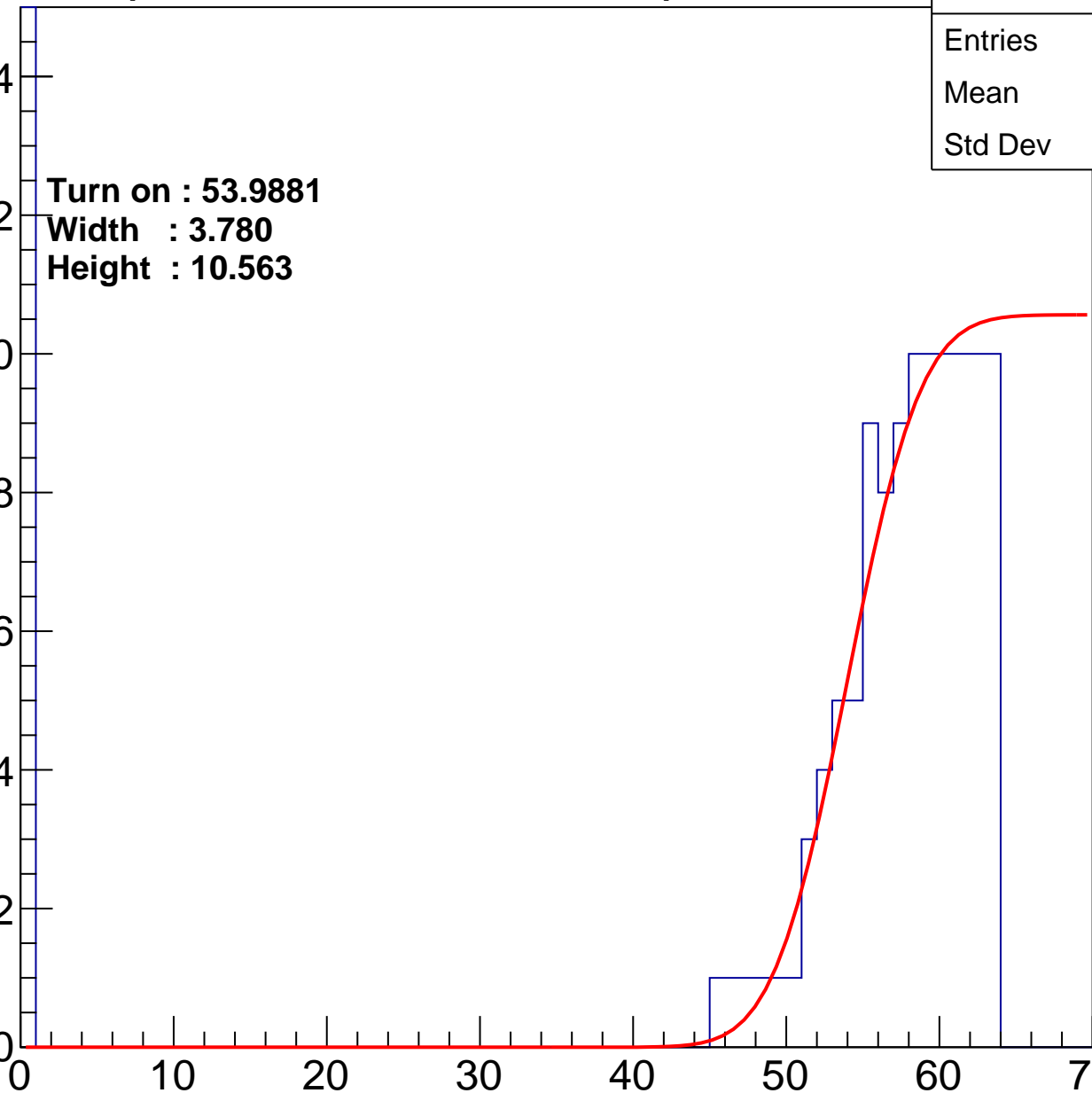
Width : 3.780

Height : 10.563

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch109

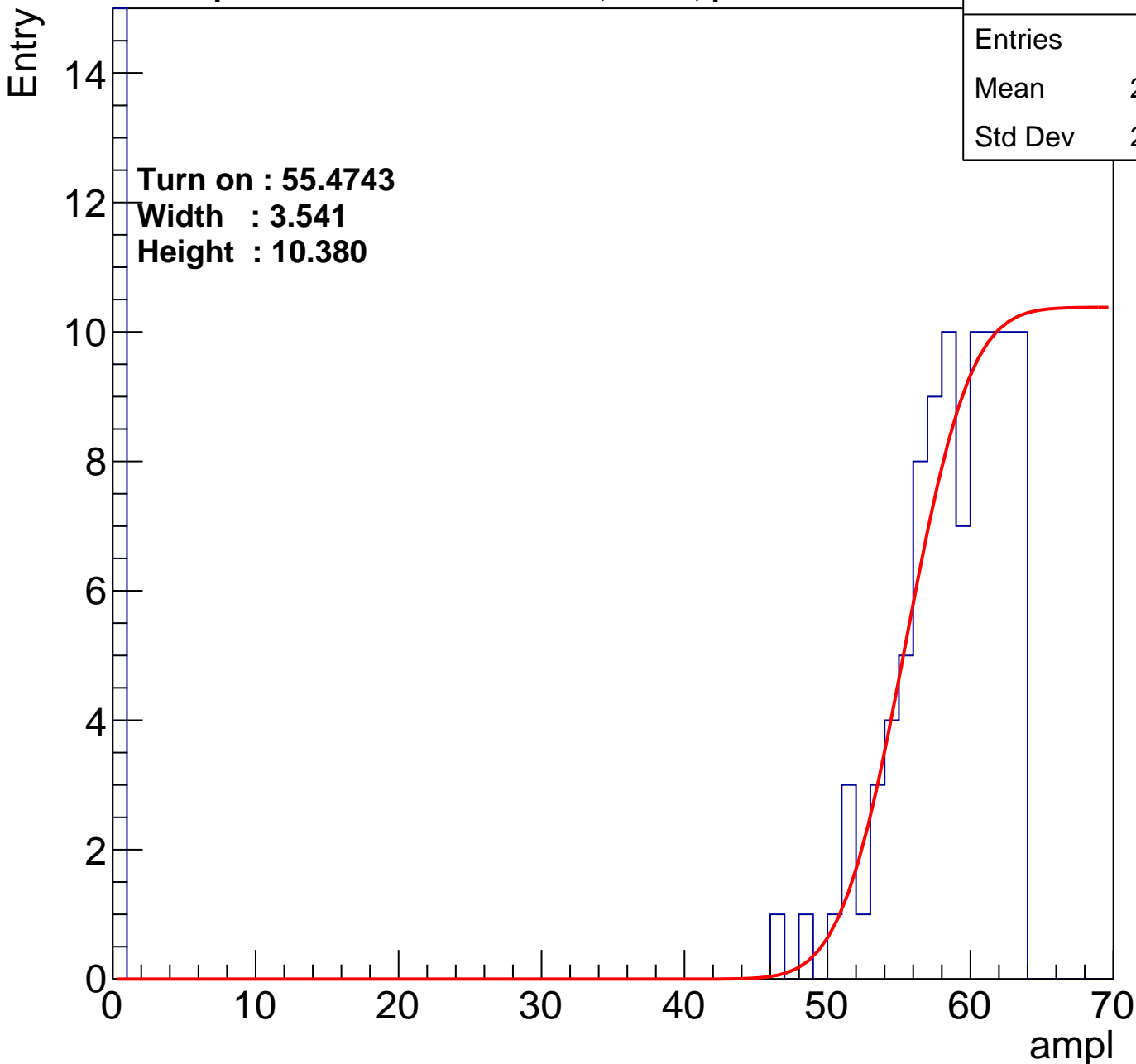
calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	28.79
Std Dev	29.22

Turn on : 55.4743

Width : 3.541

Height : 10.380



B1L104S, U11-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	197
Mean	30.8
Std Dev	28.72

Turn on : 54.4878

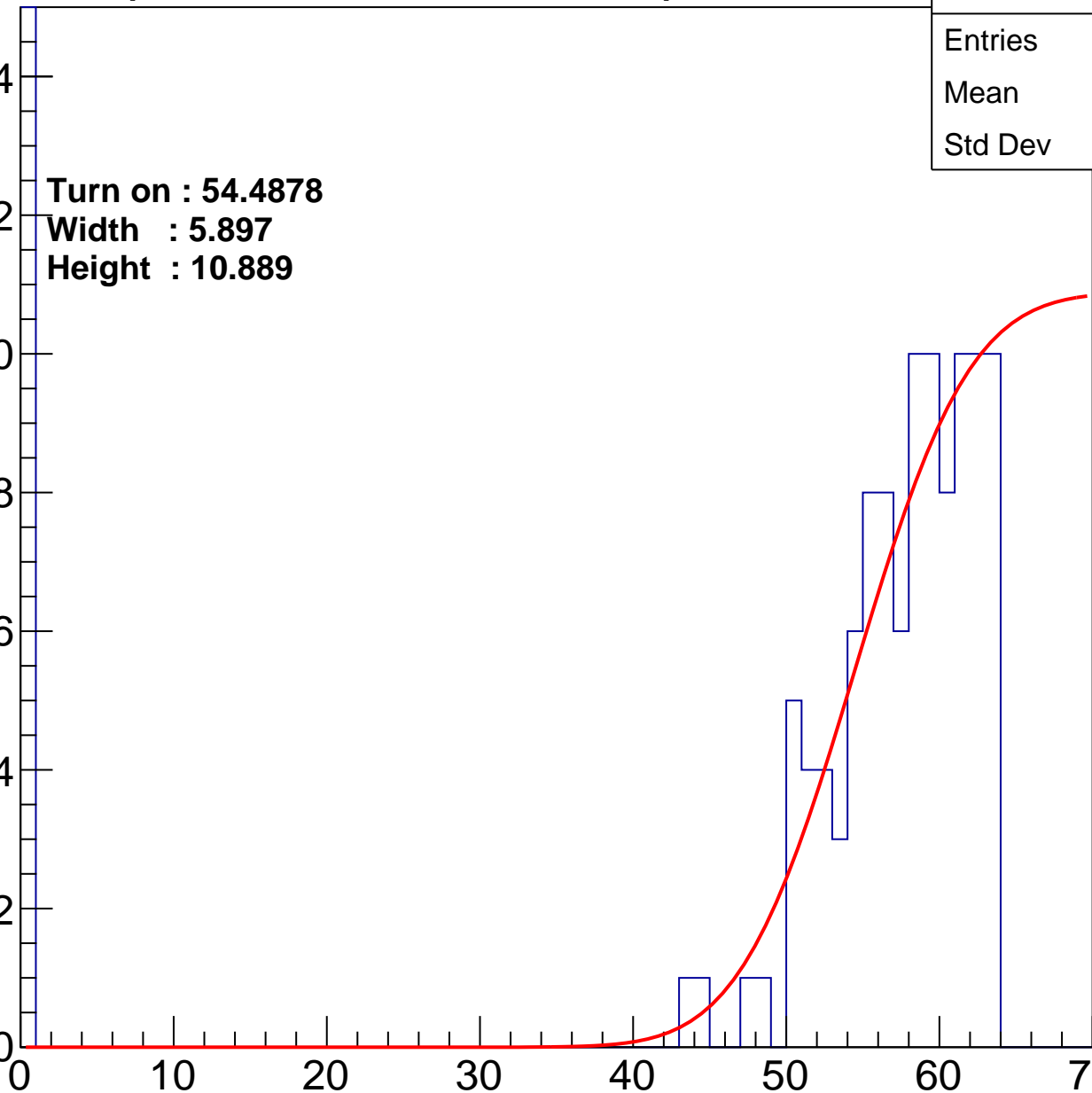
Width : 5.897

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	31.31
Std Dev	29.02

Turn on : 54.4191

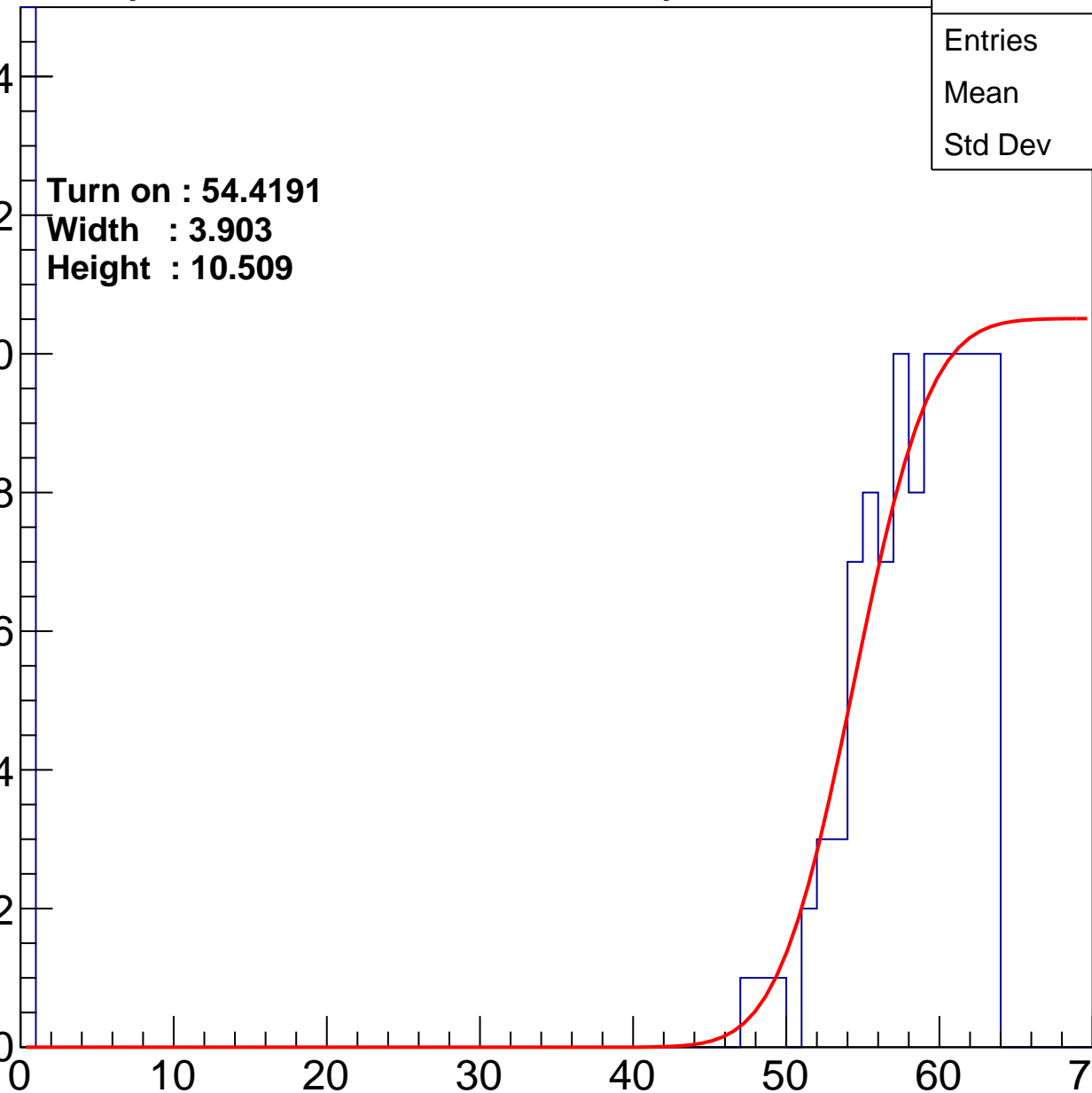
Width : 3.903

Height : 10.509

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	208
Mean	34.44
Std Dev	27.55

Turn on : 53.2428

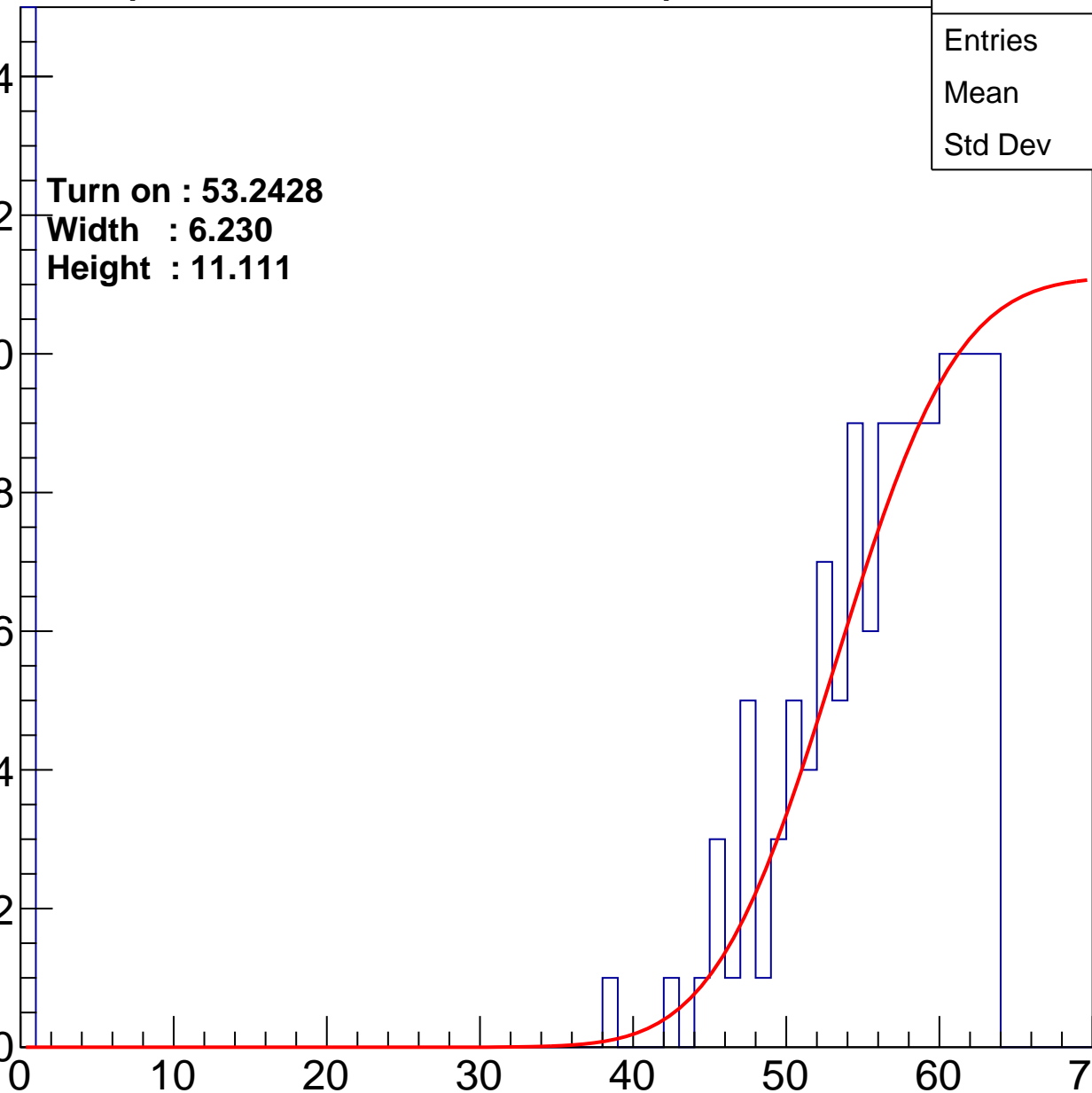
Width : 6.230

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	29.92
Std Dev	28.88

Turn on : 55.2201

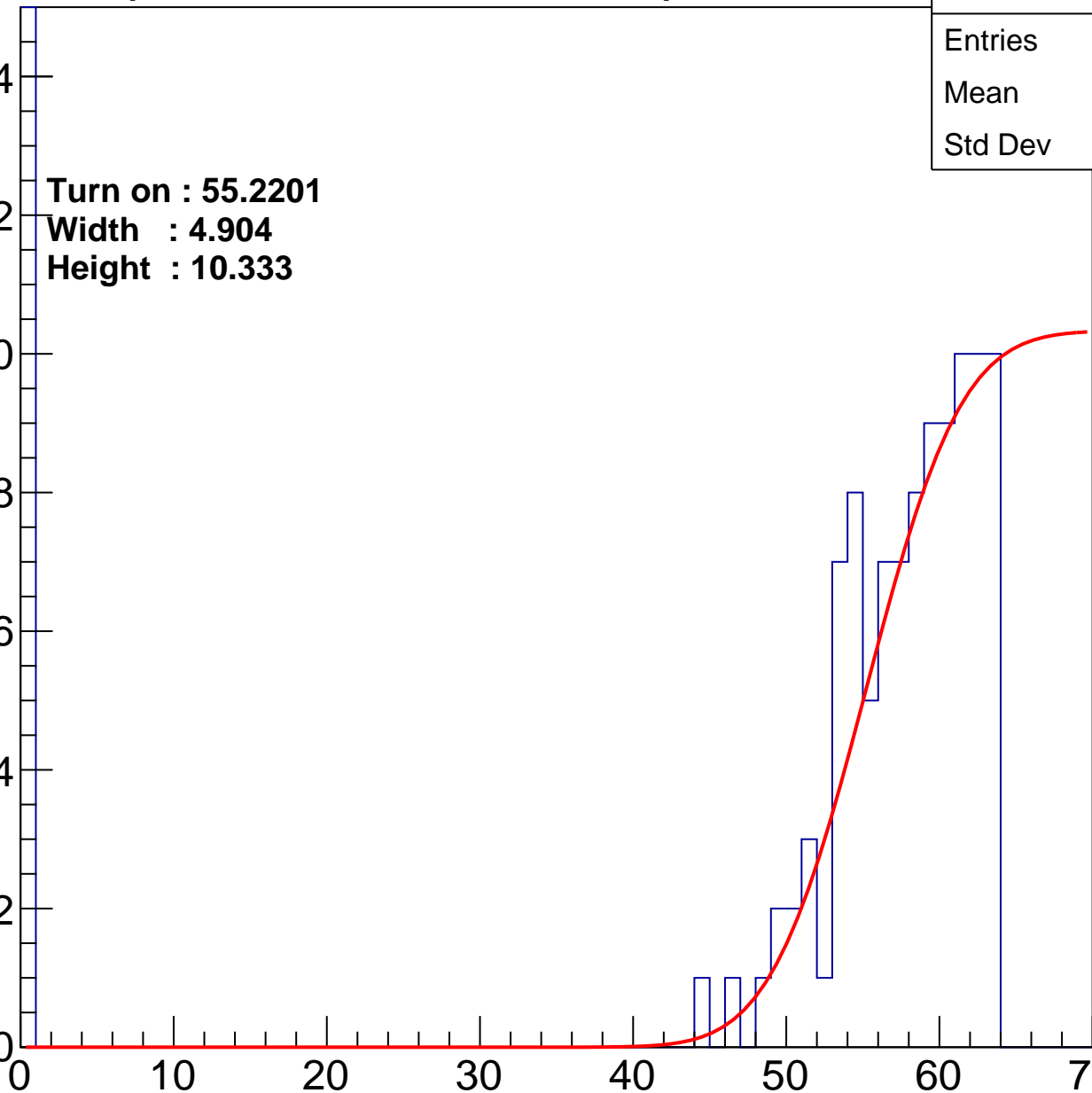
Width : 4.904

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch114

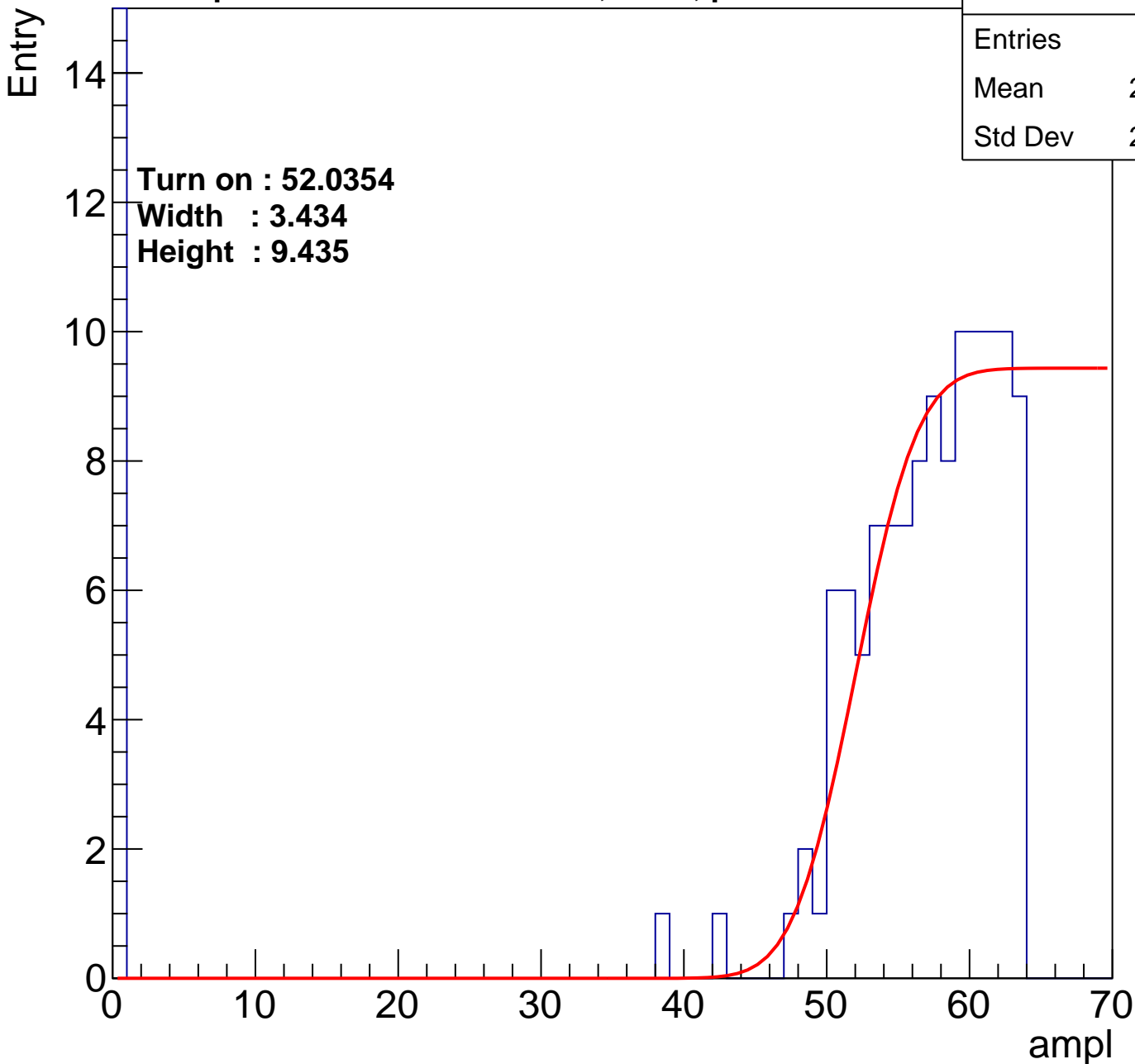
calib_packv5_033123_0516.root, FC#4, port A1

Entries	224
Mean	29.84
Std Dev	28.49

Turn on : 52.0354

Width : 3.434

Height : 9.435



B1L104S, U11-ch115

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	32.79
Std Dev	28.89

Turn on : 54.1850

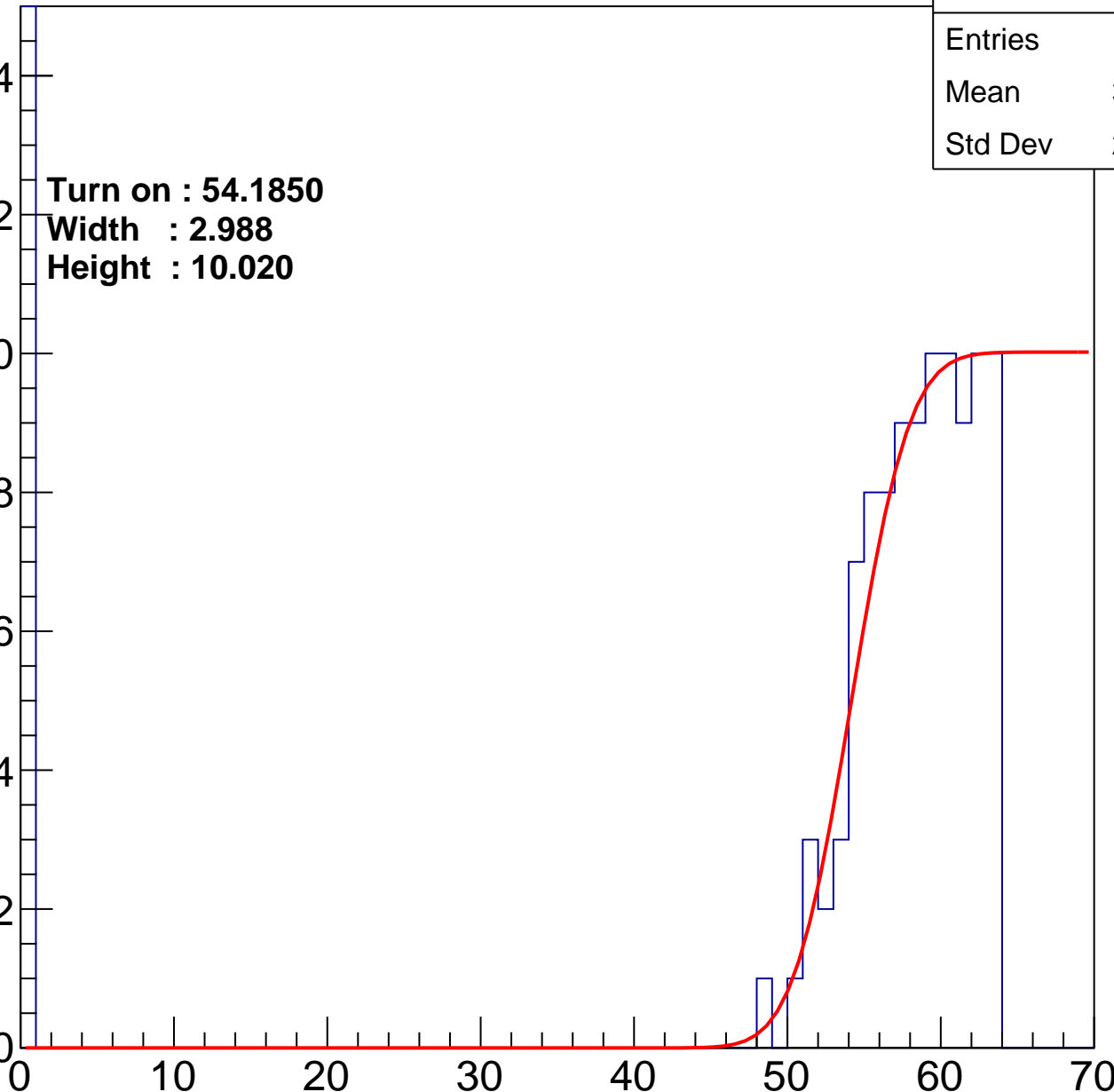
Width : 2.988

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch116

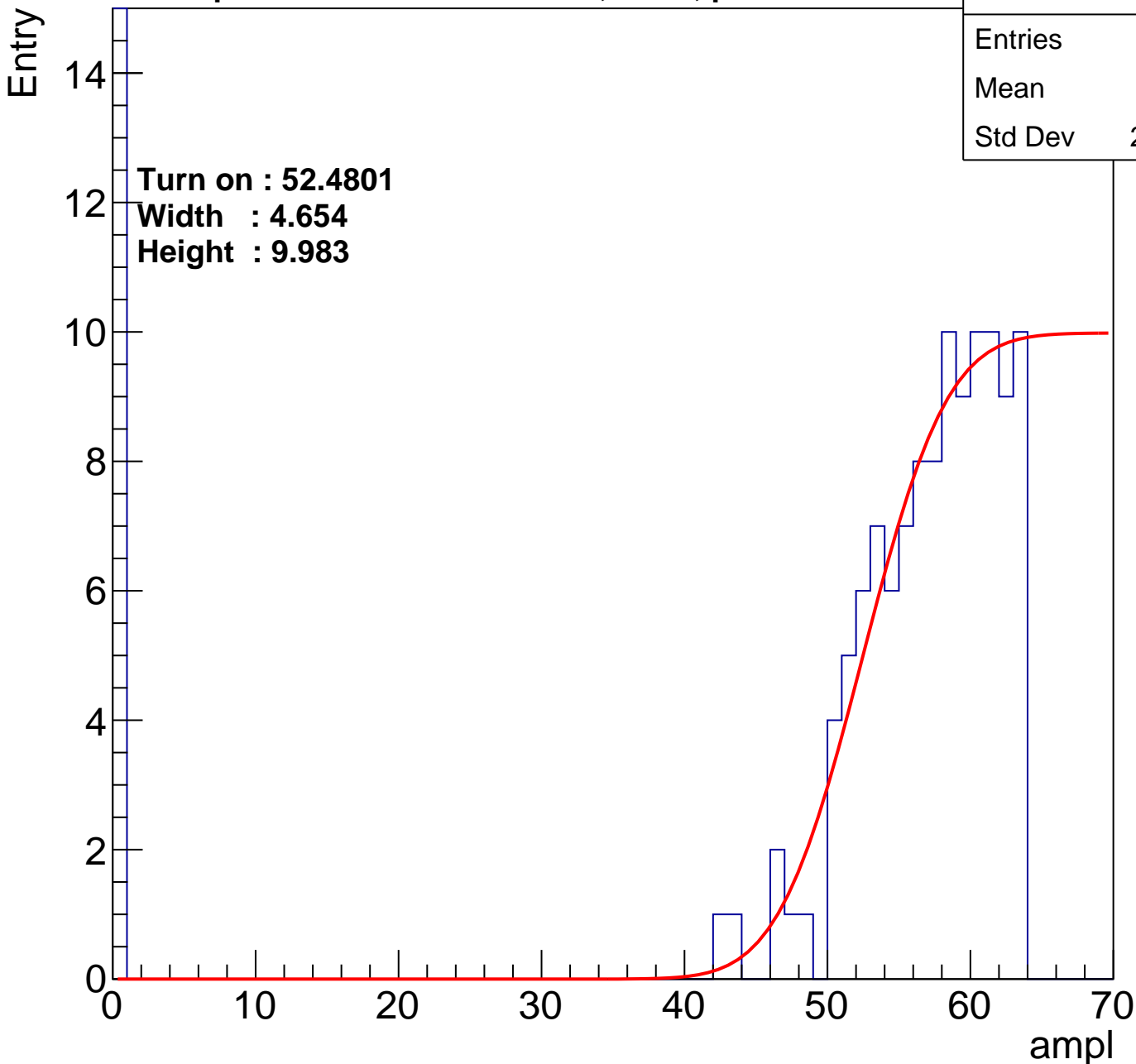
calib_packv5_033123_0516.root, FC#4, port A1

Entries	195
Mean	33.5
Std Dev	28.16

Turn on : 52.4801

Width : 4.654

Height : 9.983



B1L104S, U11-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	29.46
Std Dev	28.91

Turn on : 55.0264

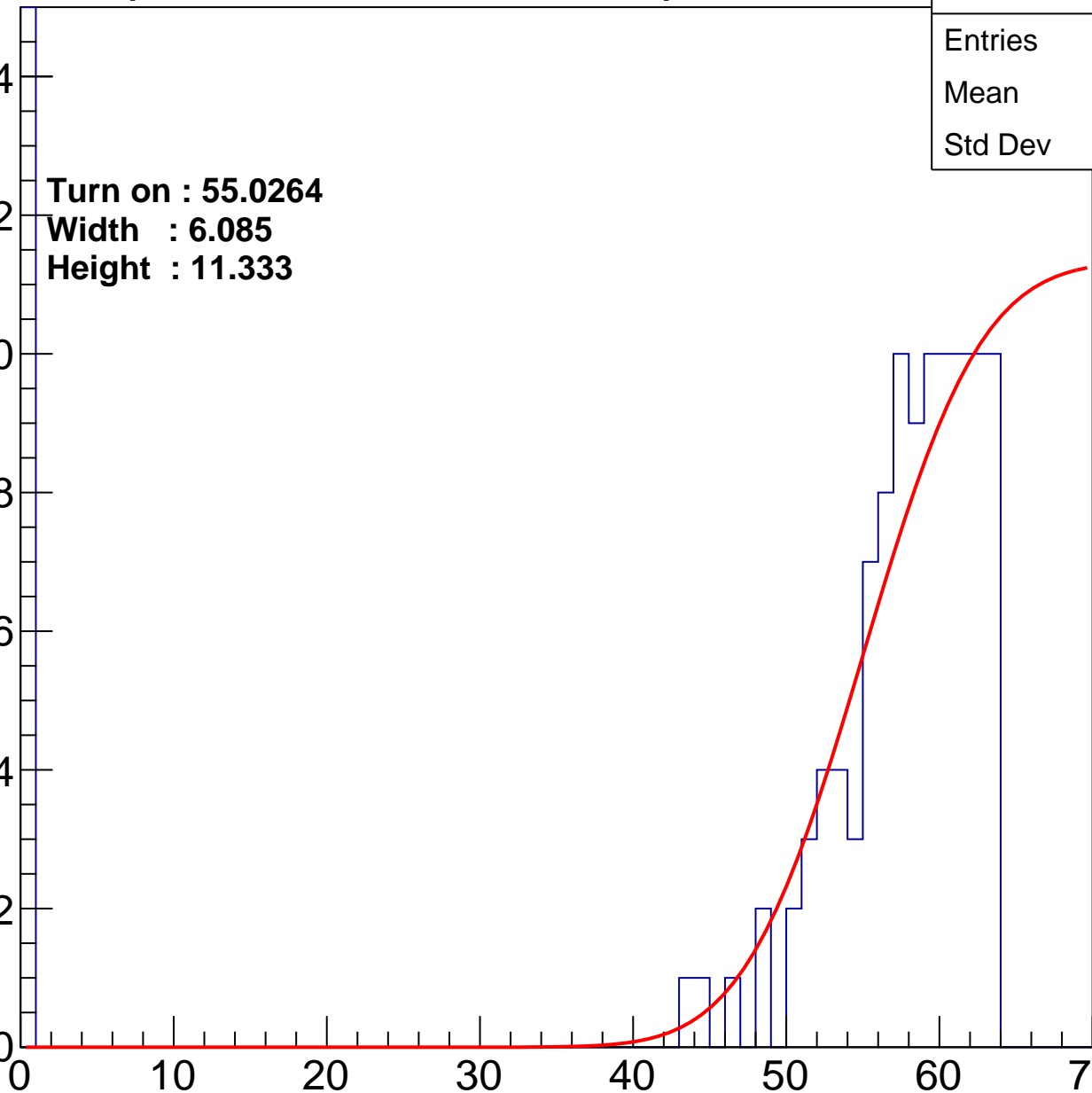
Width : 6.085

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch118

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	33.13
Std Dev	28.18

Turn on : 54.1816

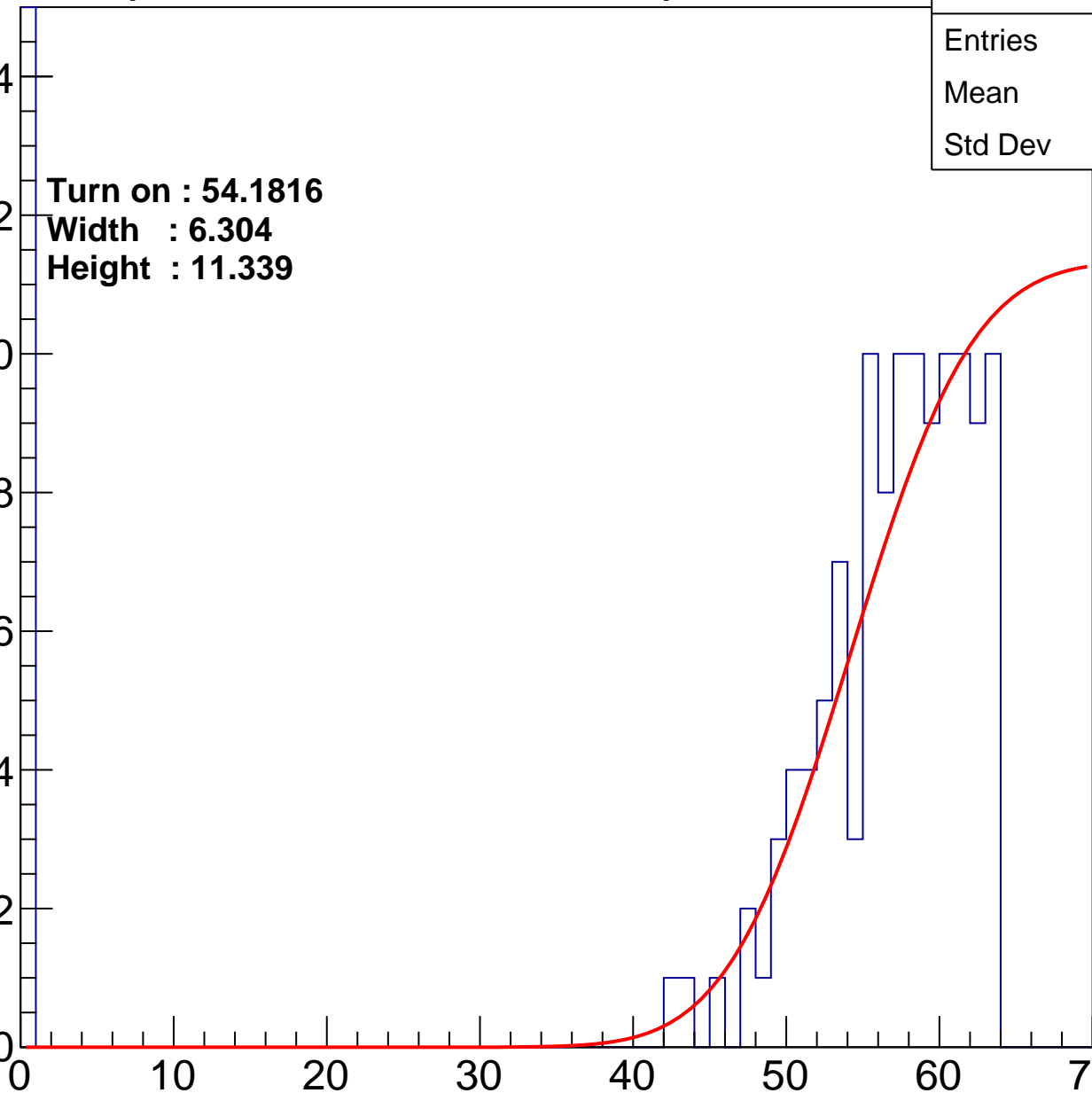
Width : 6.304

Height : 11.339

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	28.74
Std Dev	29.01

Turn on : 54.0192

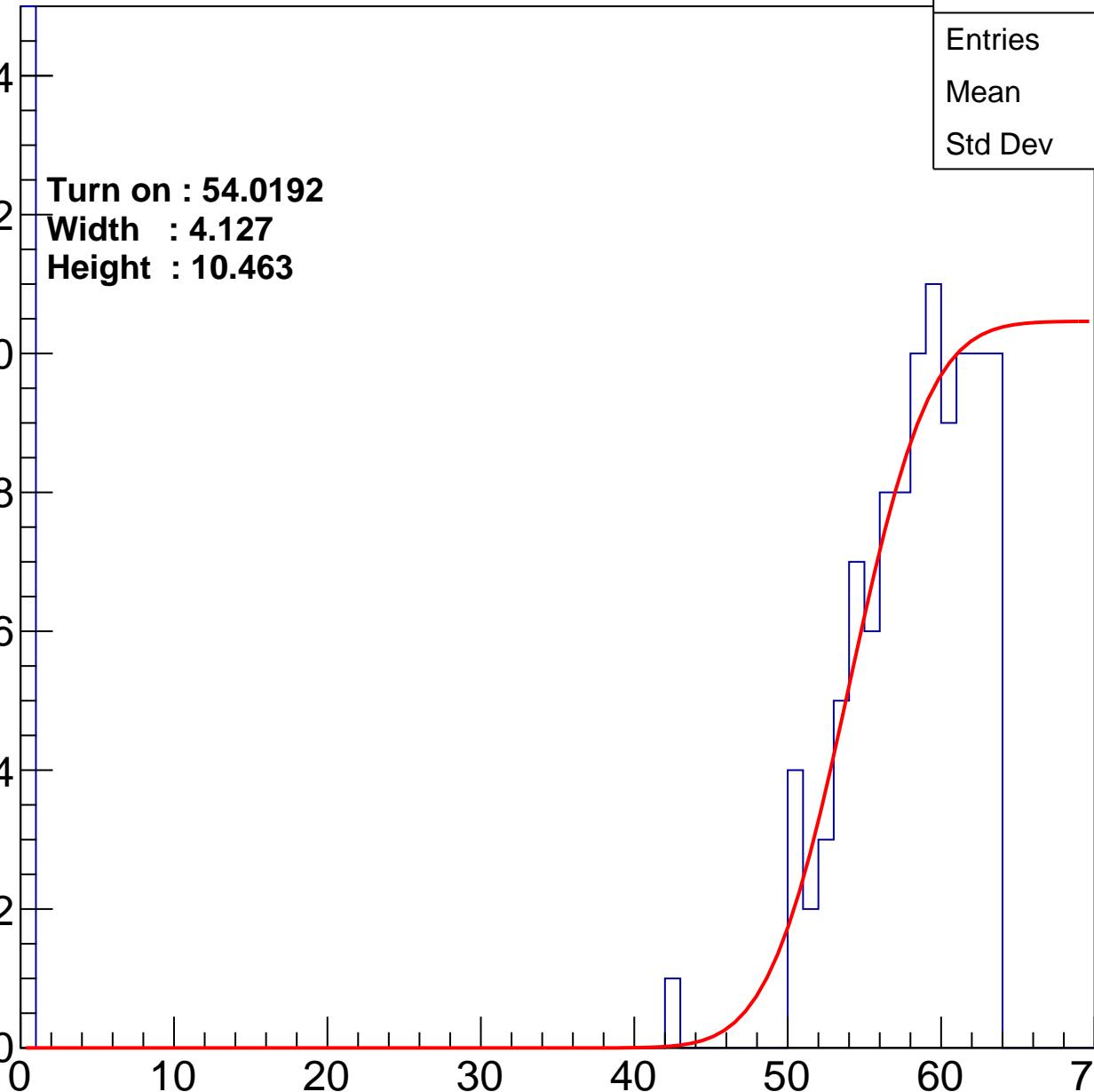
Width : 4.127

Height : 10.463

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch120

calib_packv5_033123_0516.root, FC#4, port A1

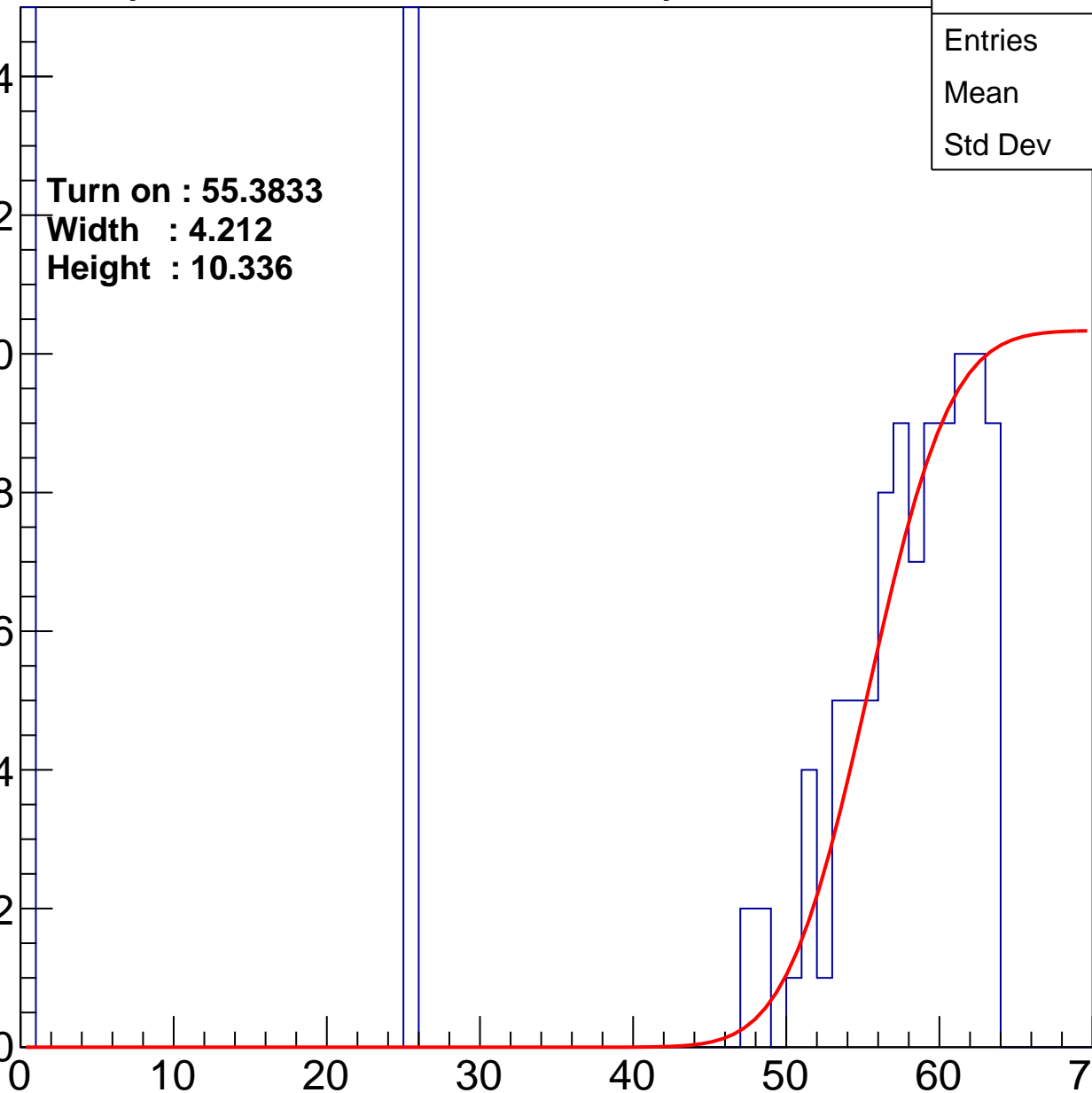
Entries	227
Mean	27.93
Std Dev	26.9

Turn on : 55.3833
Width : 4.212
Height : 10.336

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	29.73
Std Dev	29.33

Turn on : 55.1273

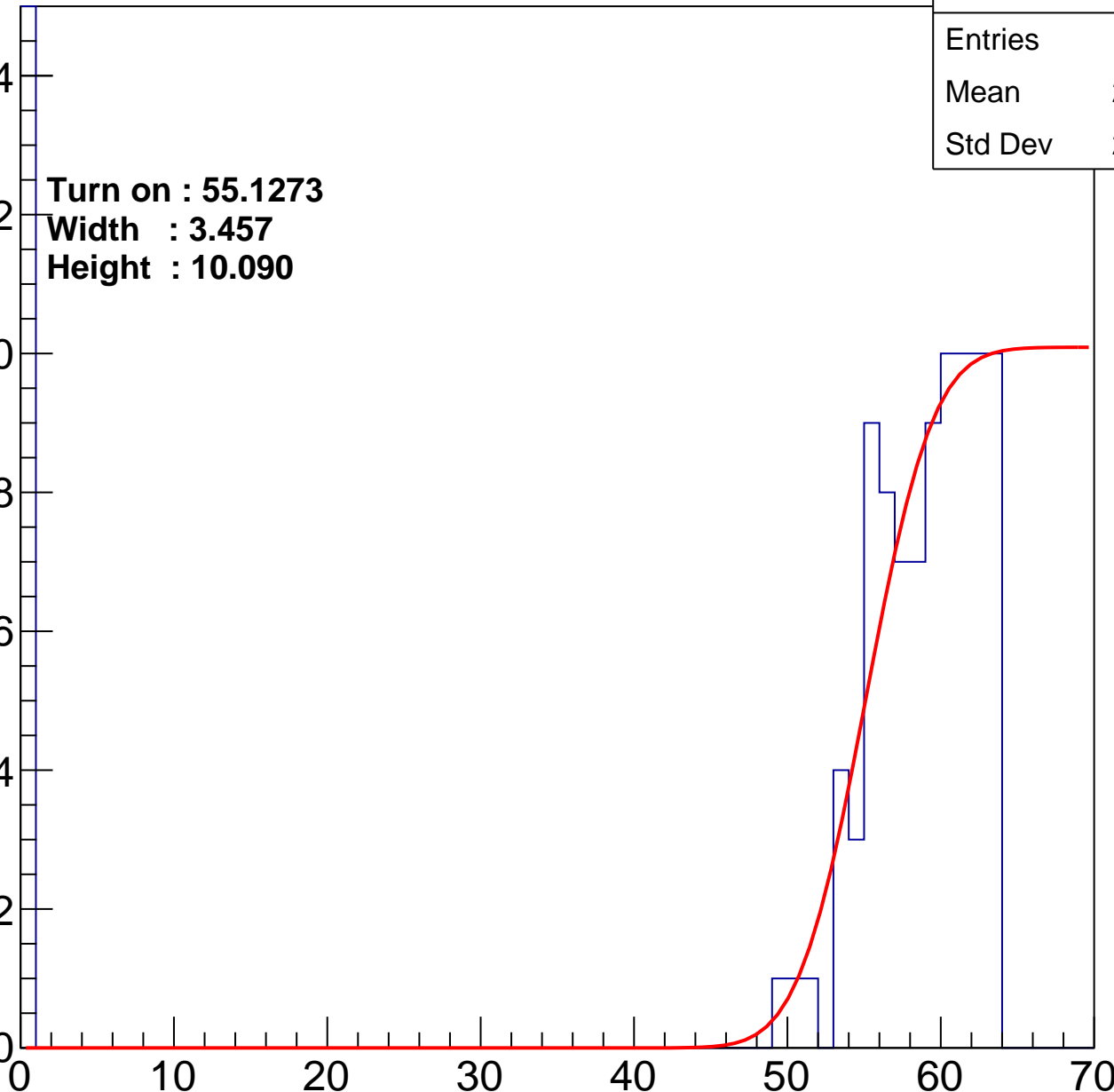
Width : 3.457

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	238
Mean	28.54
Std Dev	28.51

Turn on : 54.6370

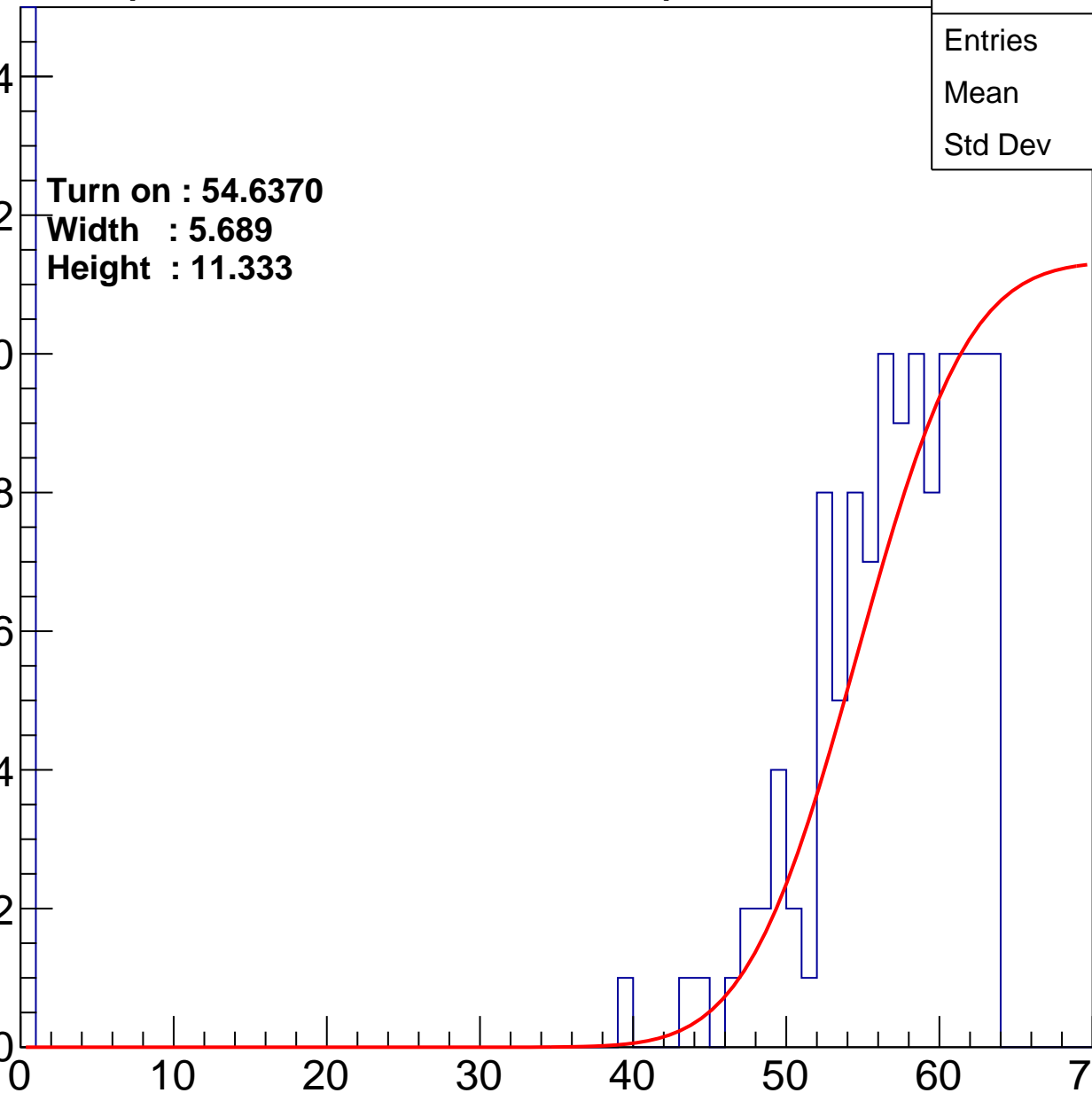
Width : 5.689

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	180
Mean	32.08
Std Dev	28.84

Turn on : 55.1767

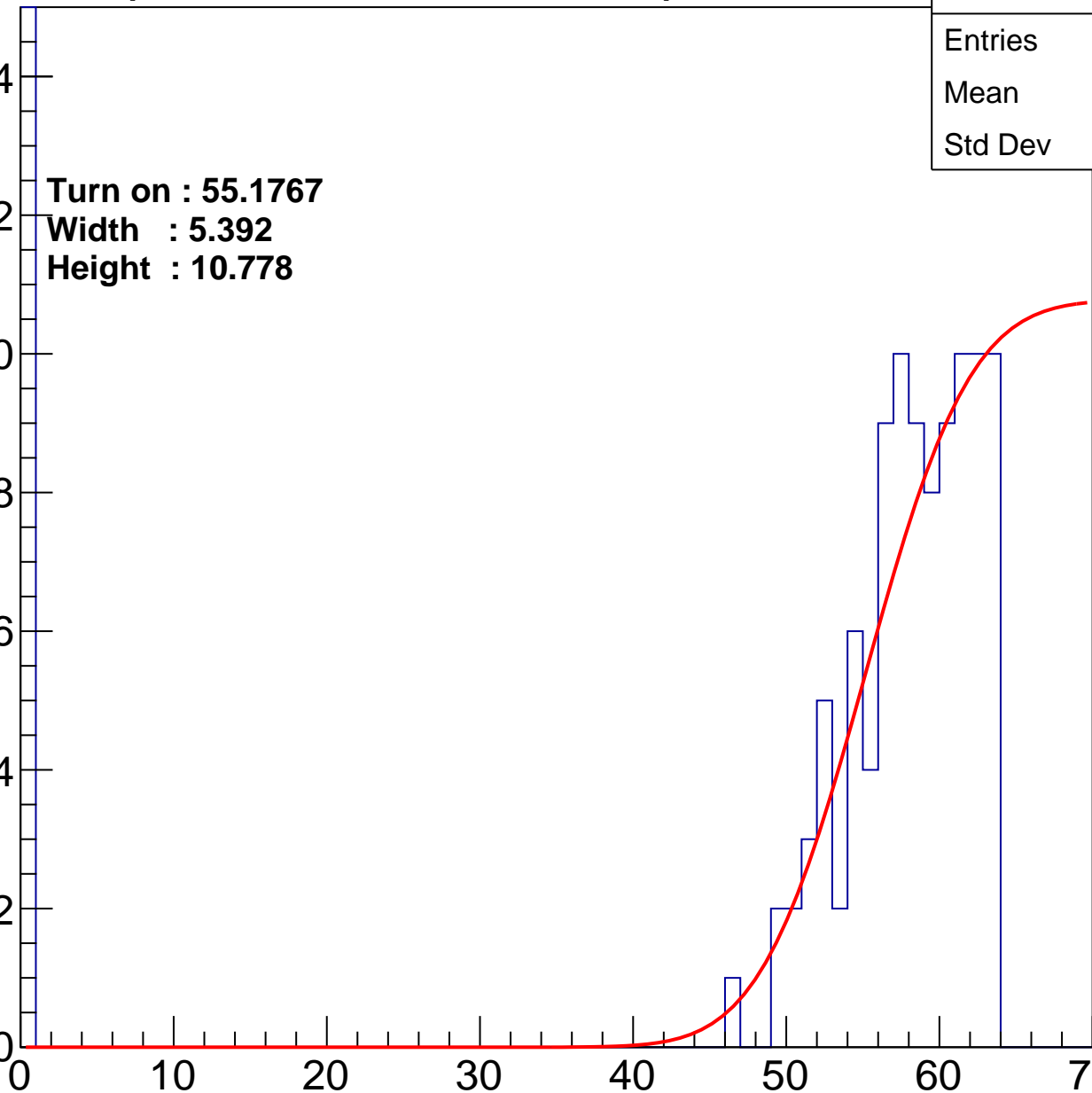
Width : 5.392

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch124

calib_packv5_033123_0516.root, FC#4, port A1

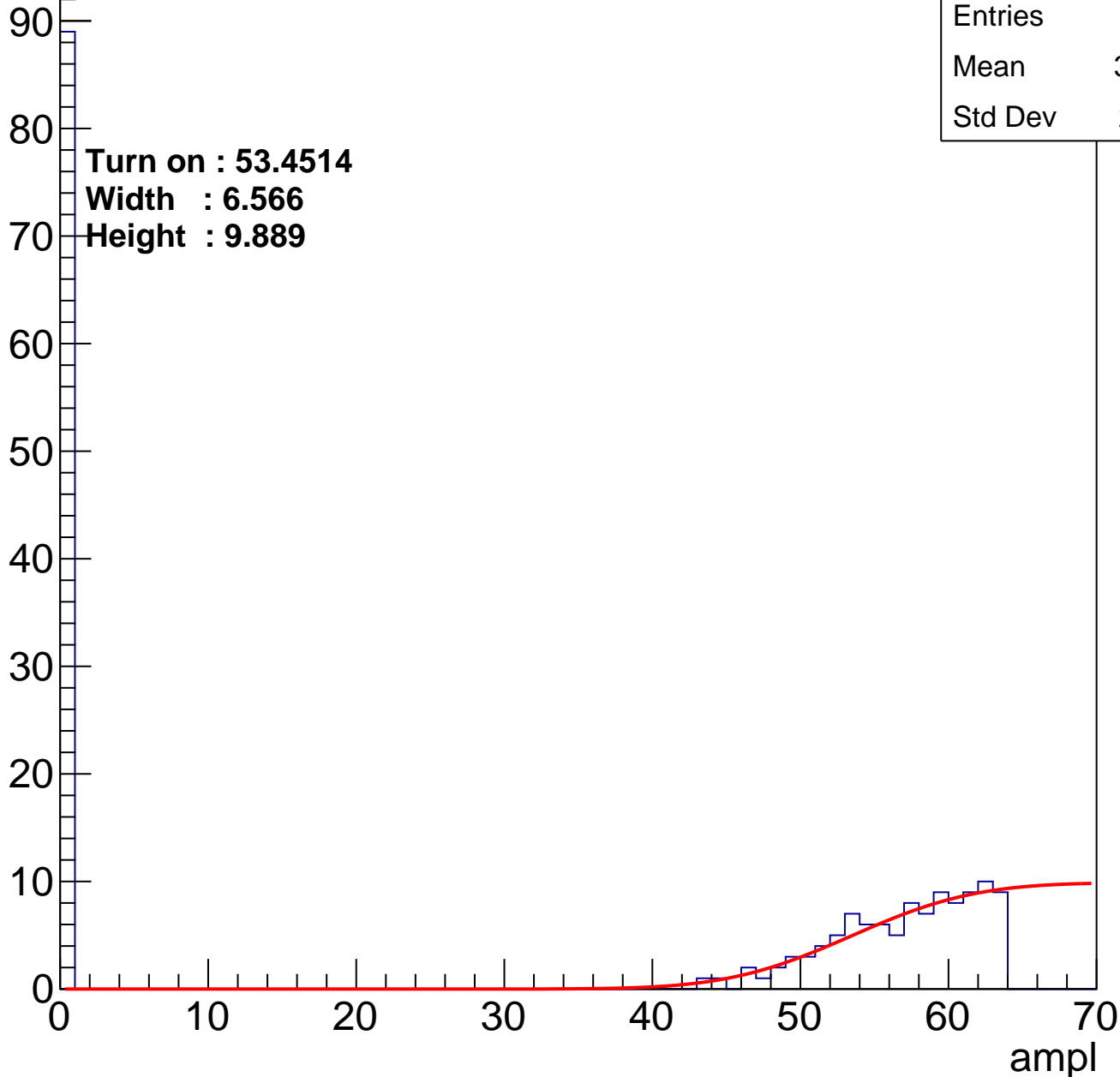
Entries	195
Mean	30.75
Std Dev	28.41

Turn on : 53.4514

Width : 6.566

Height : 9.889

Entry



B1L104S, U11-ch125

calib_packv5_033123_0516.root, FC#4, port A1

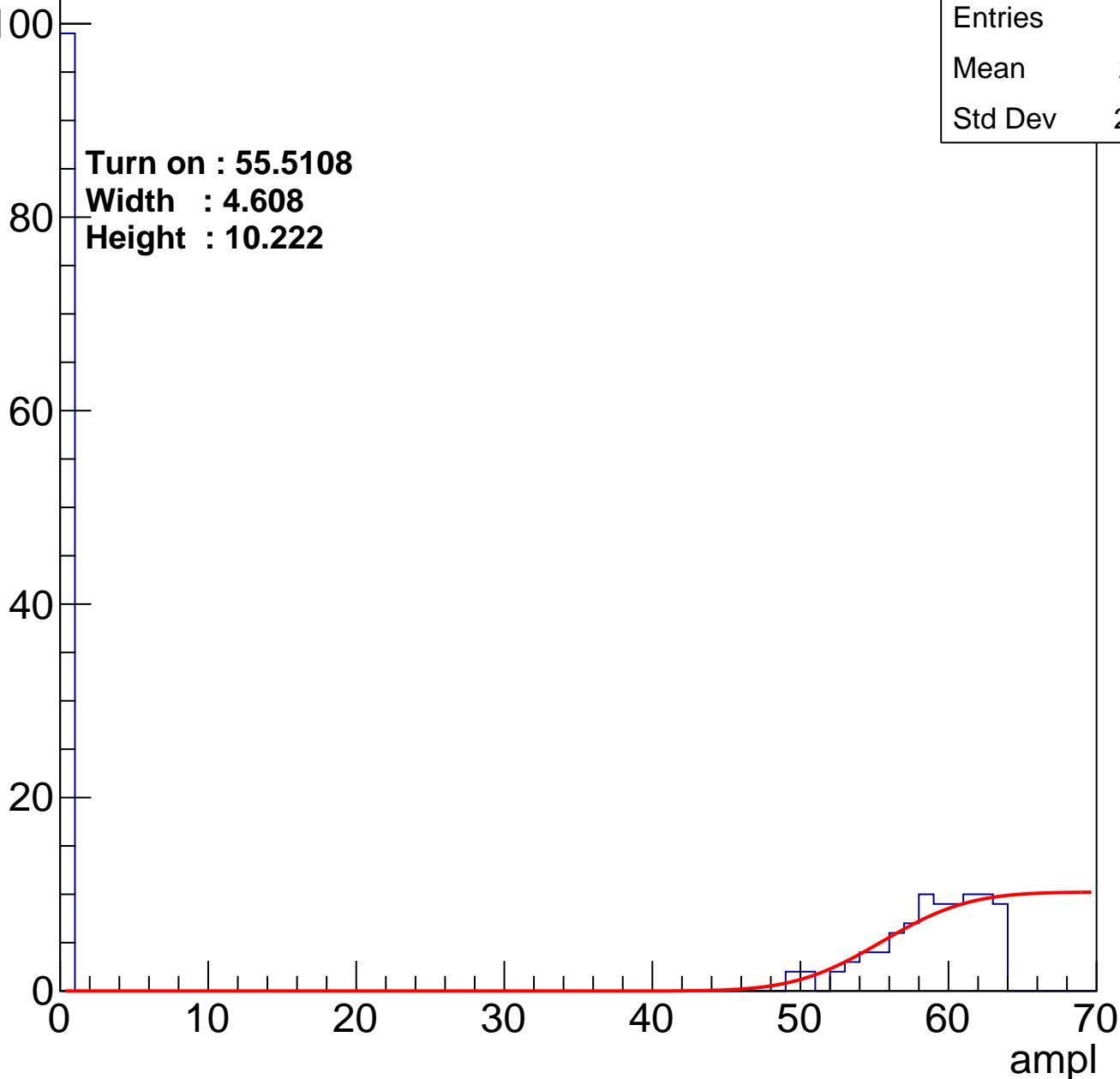
Entry

Entries	186
Mean	27.31
Std Dev	29.23

Turn on : 55.5108

Width : 4.608

Height : 10.222



B1L104S, U11-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	274
Mean	24.13
Std Dev	28.11

Turn on : 52.2684

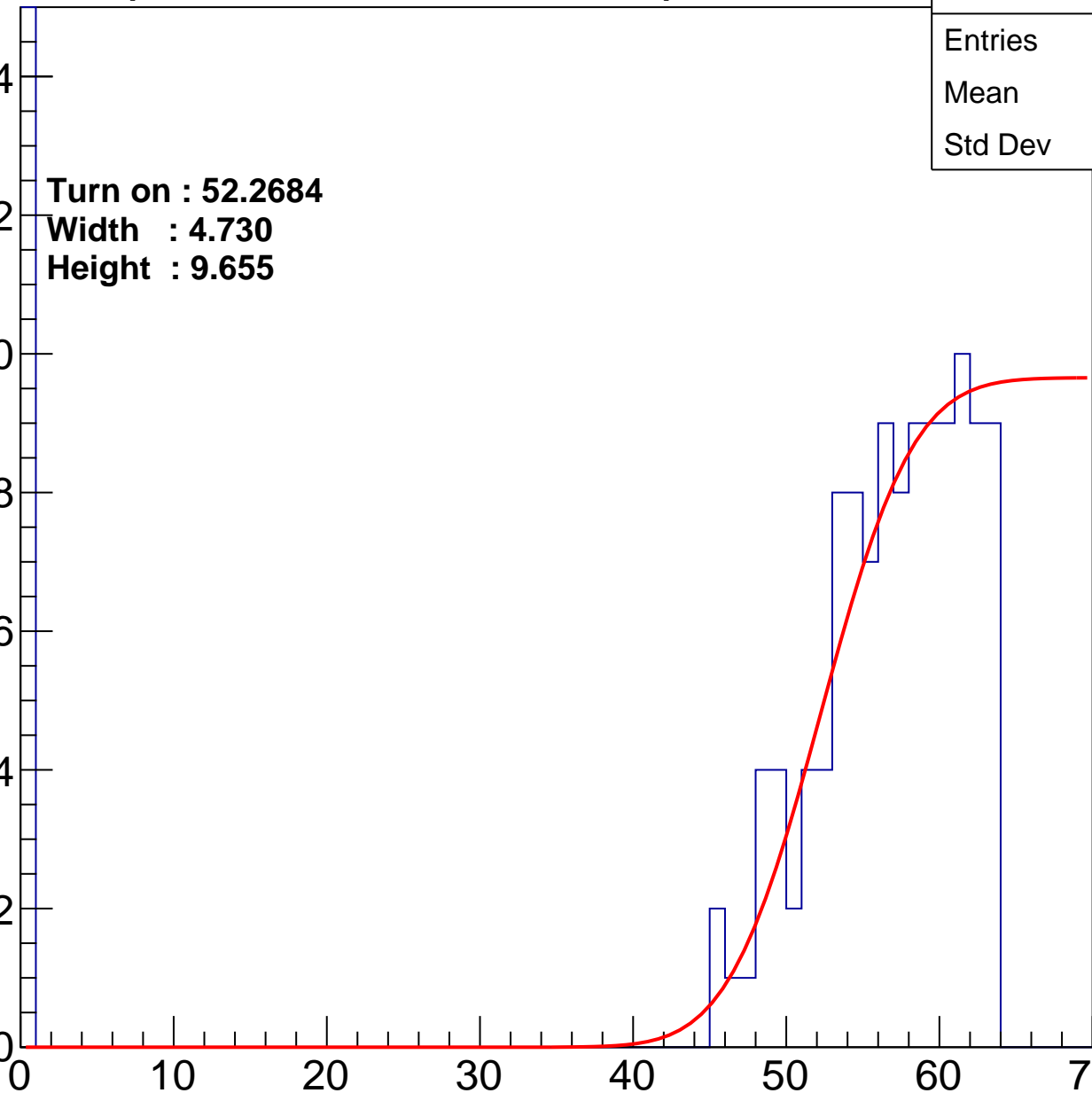
Width : 4.730

Height : 9.655

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	28.98
Std Dev	28.64

Turn on : 53.9568

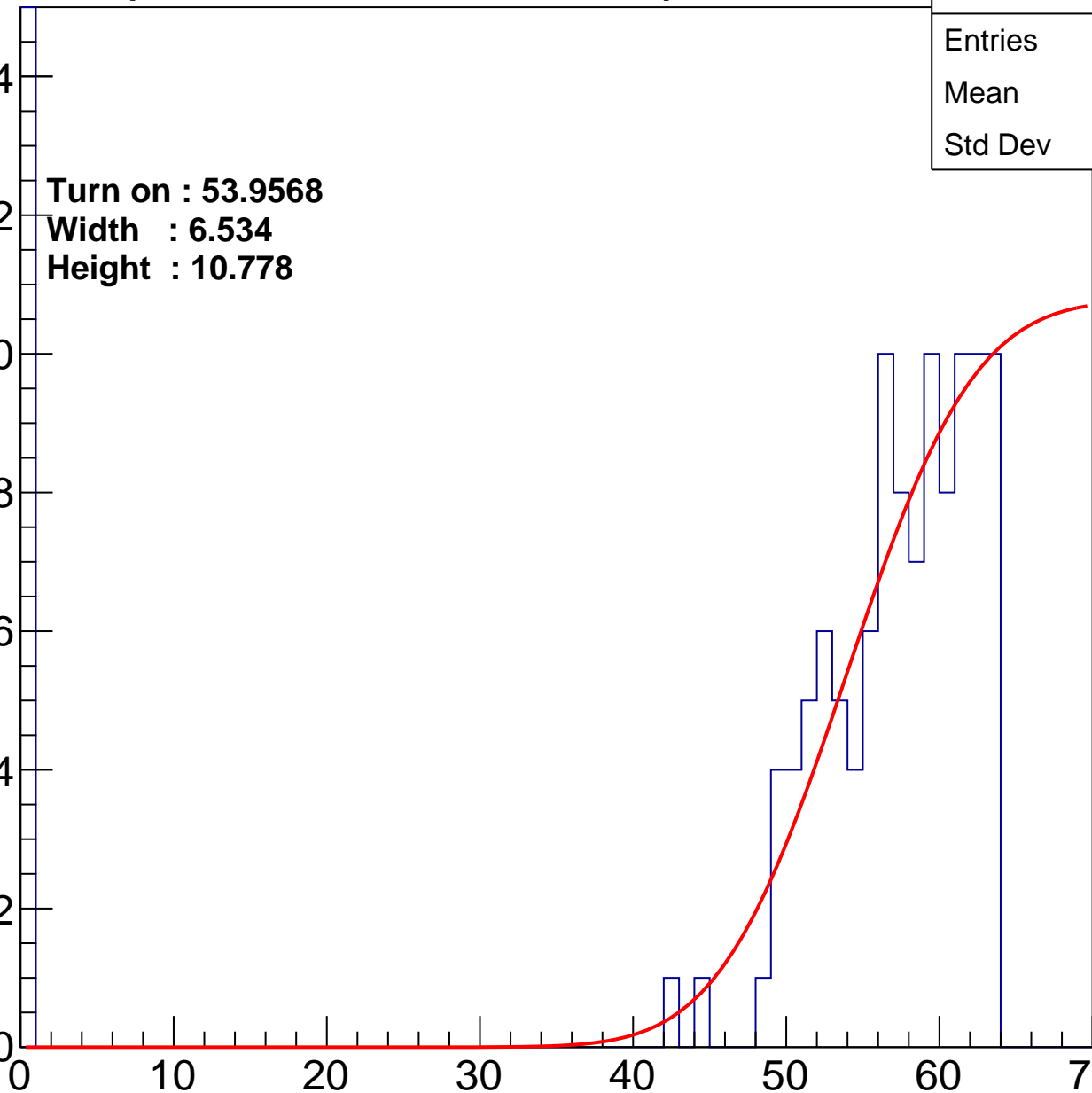
Width : 6.534

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U11-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	28.98
Std Dev	28.64

Turn on : 53.9568

Width : 6.534

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl

