

B1L002S, U19-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.44
Std Dev	11.77

Turn on : 24.1789

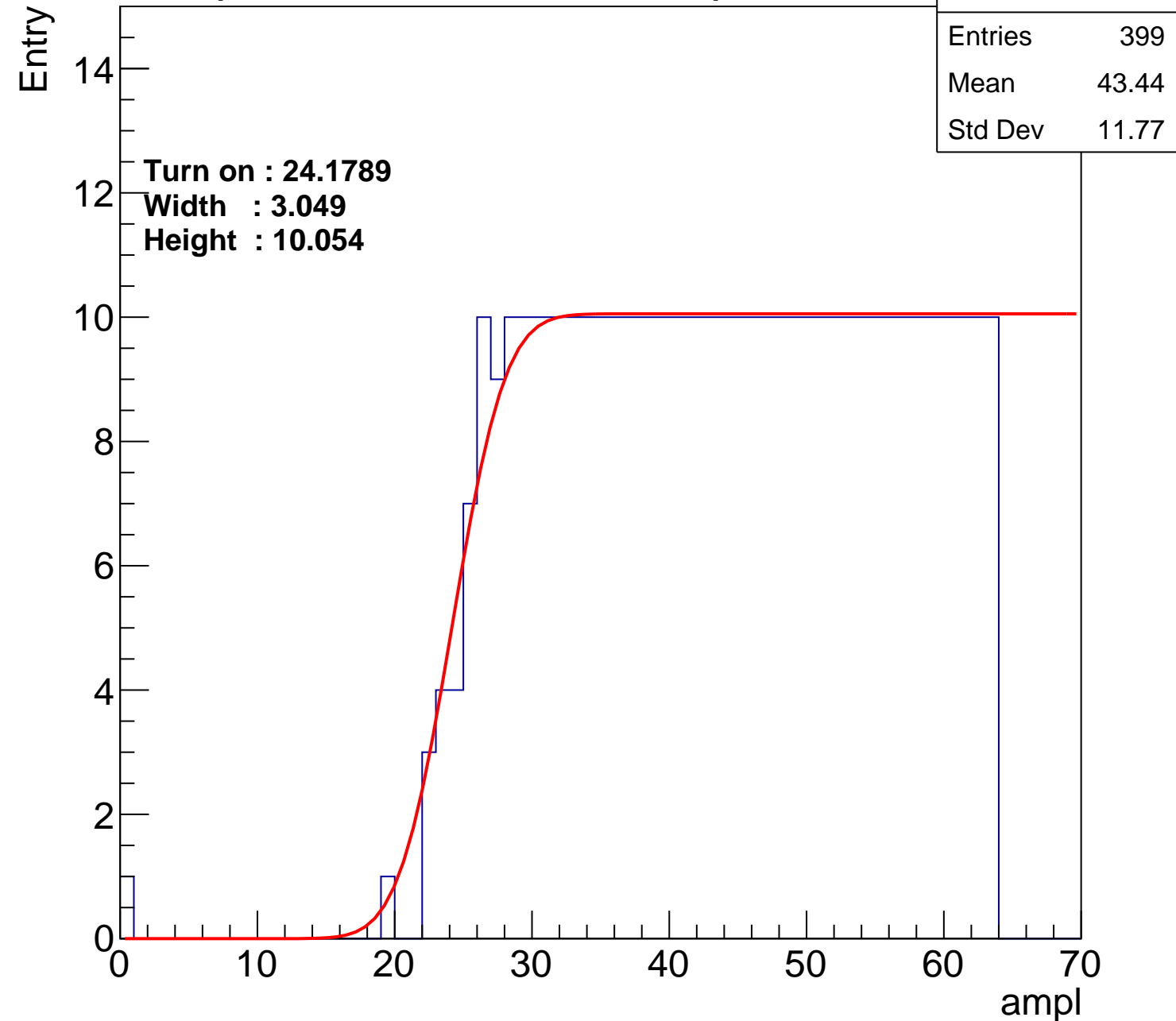
Width : 3.049

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.25
Std Dev	11.36

Turn on : 26.2133

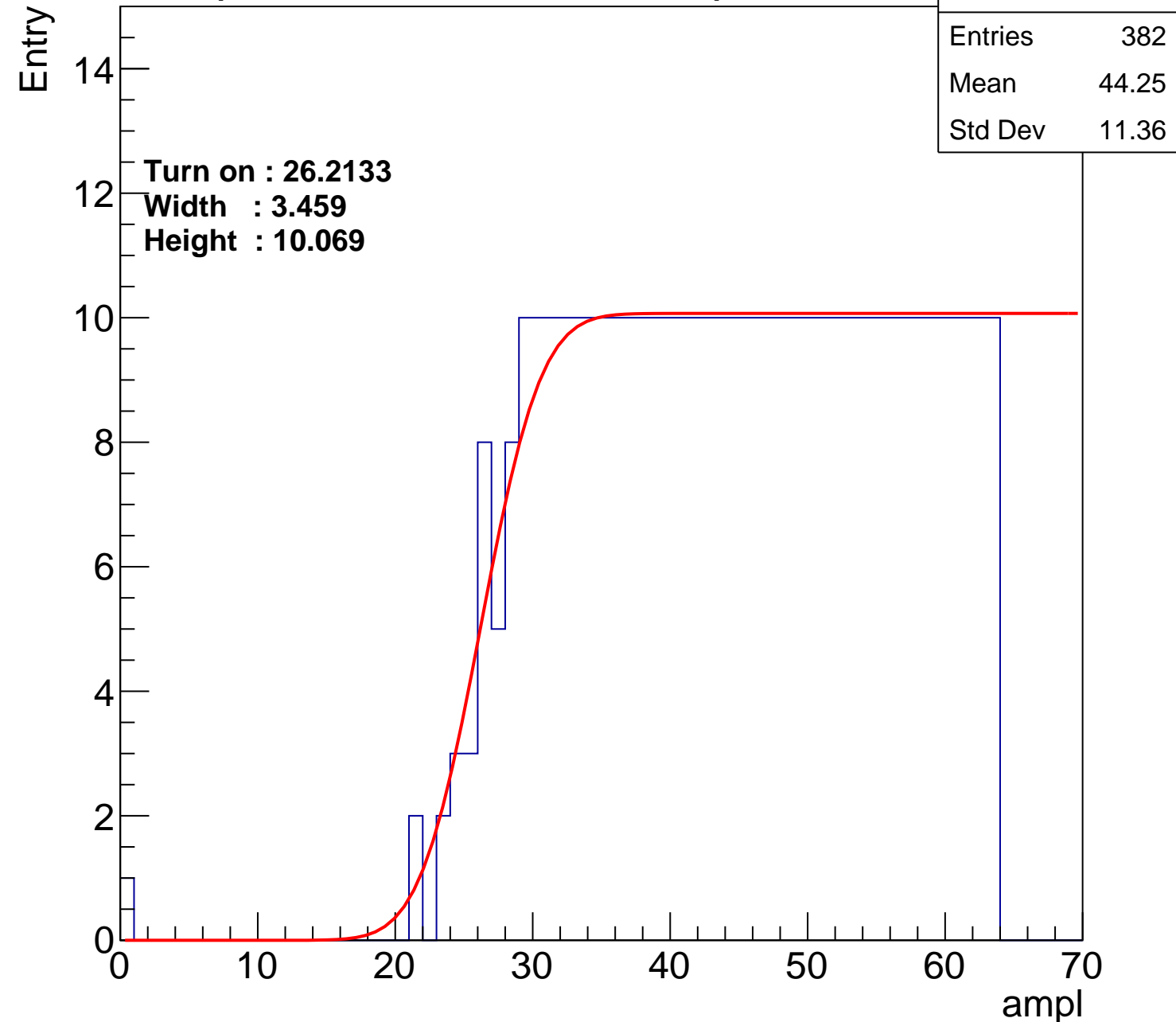
Width : 3.459

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.14
Std Dev	12

Turn on : 23.3952

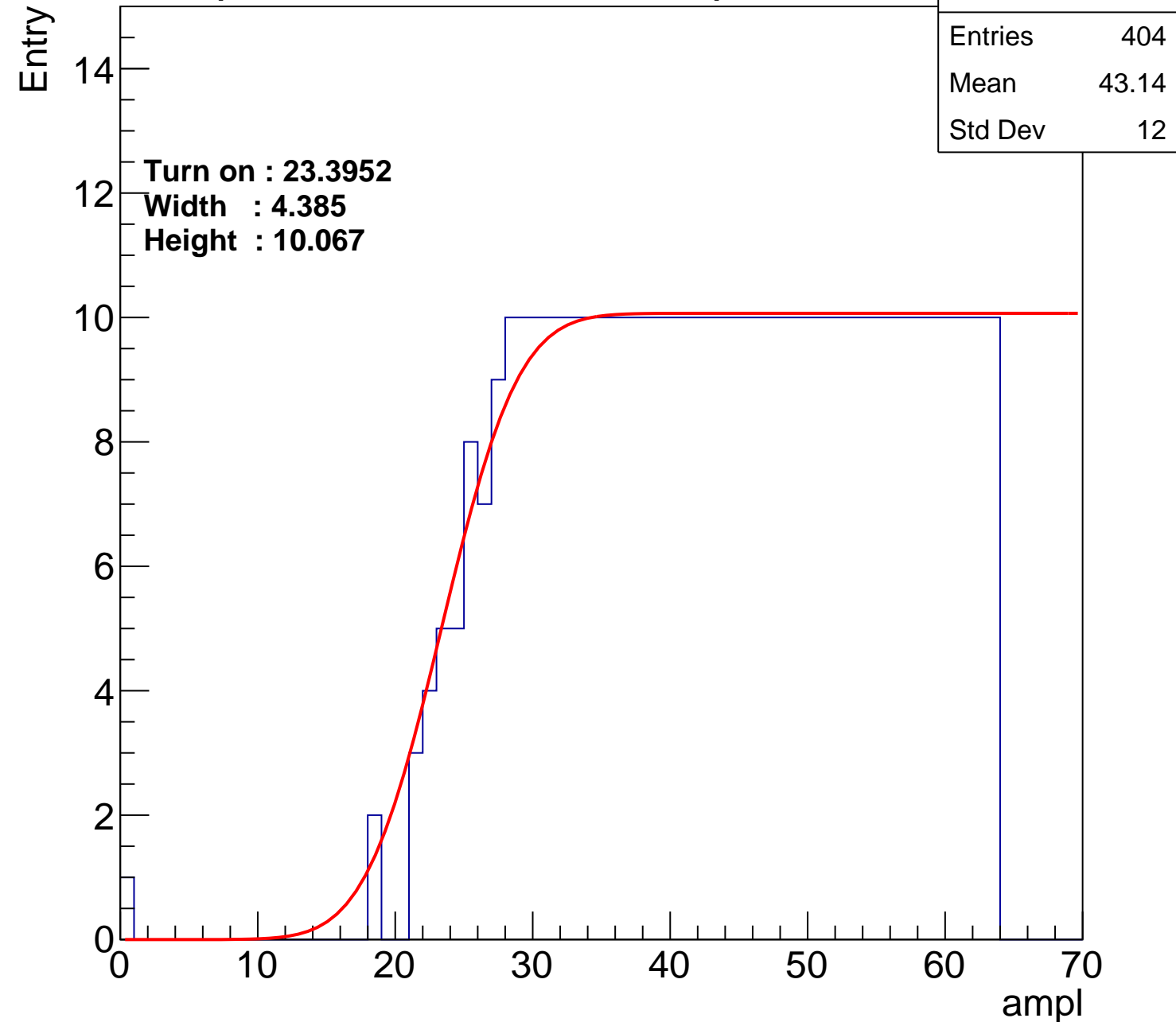
Width : 4.385

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 25.6967

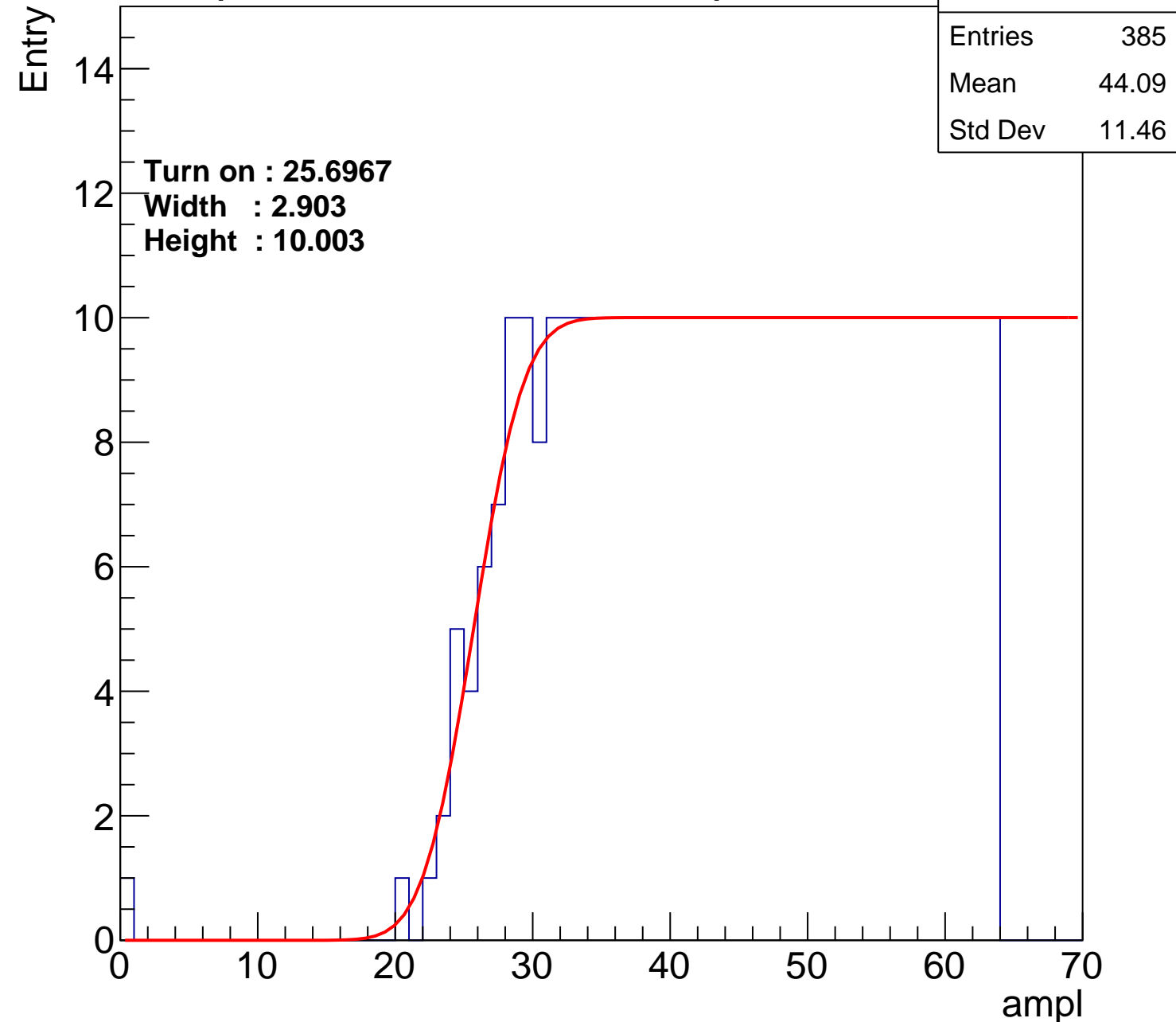
Width : 2.903

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch4

calib_packv5_042523_0143.root, FC#10, port B3

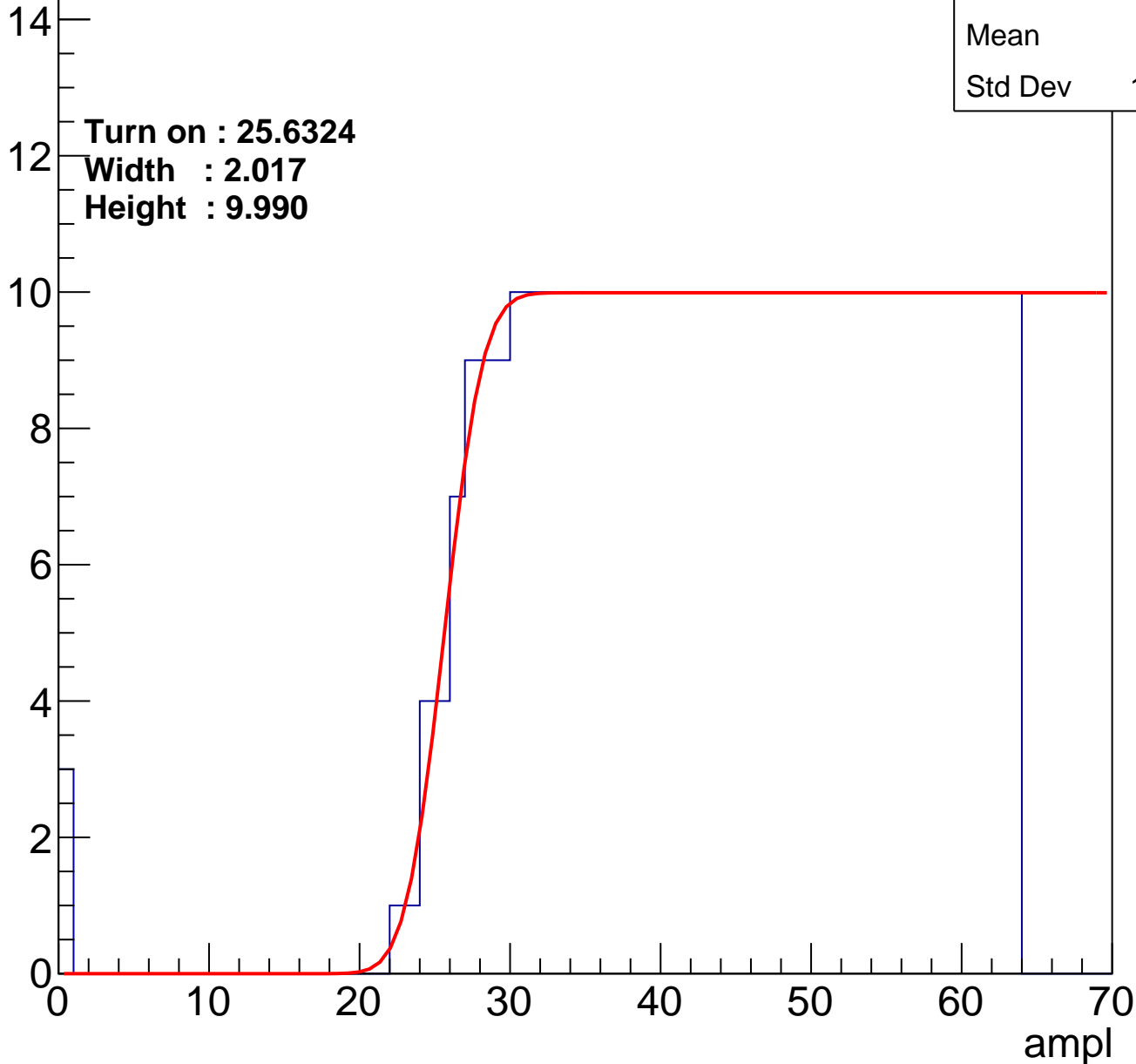
Entry

Entries	387
Mean	43.9
Std Dev	11.79

Turn on : 25.6324

Width : 2.017

Height : 9.990



B1L002S, U19-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 26.5508

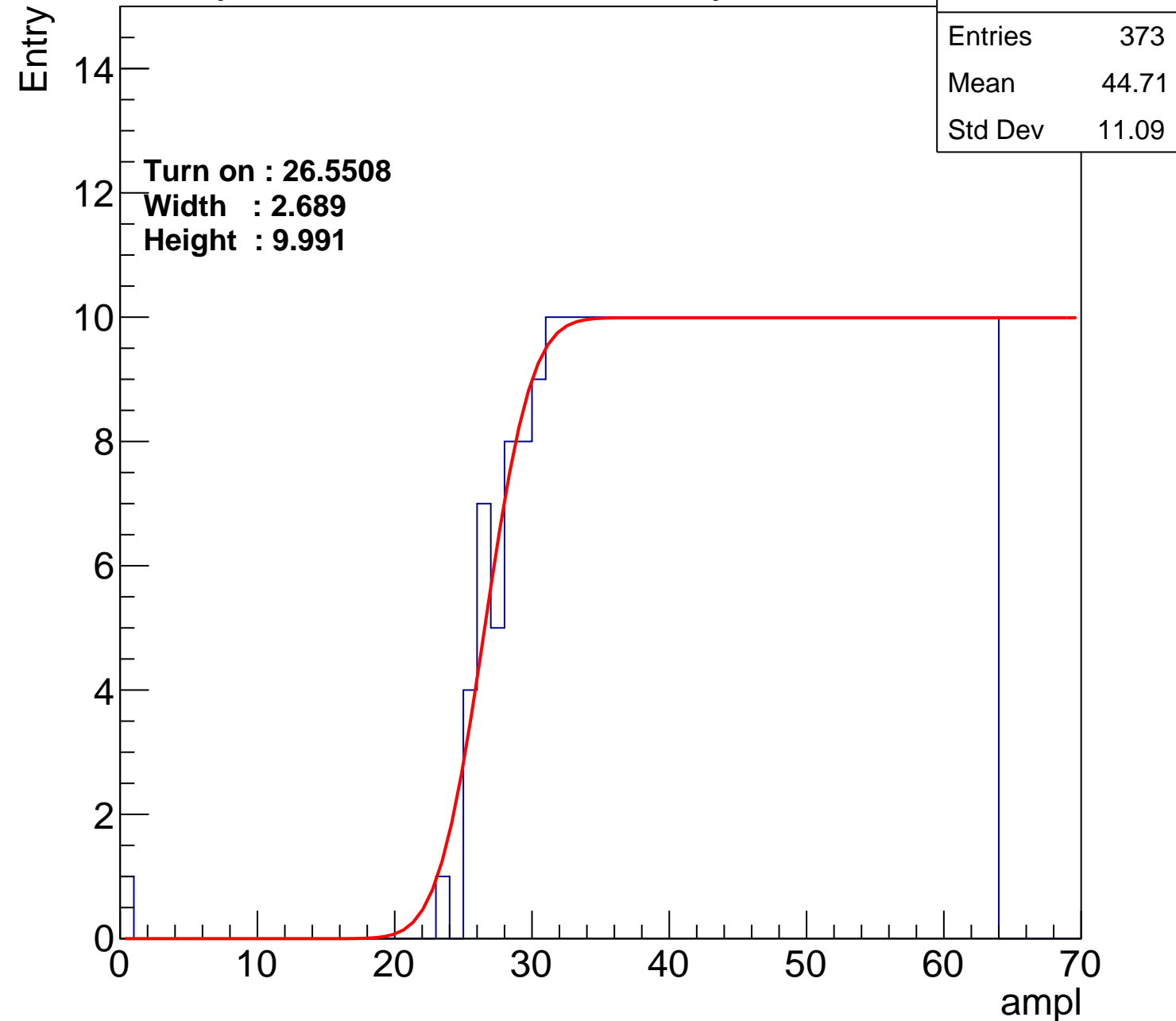
Width : 2.689

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch6

calib_packv5_042523_0143.root, FC#10, port B3

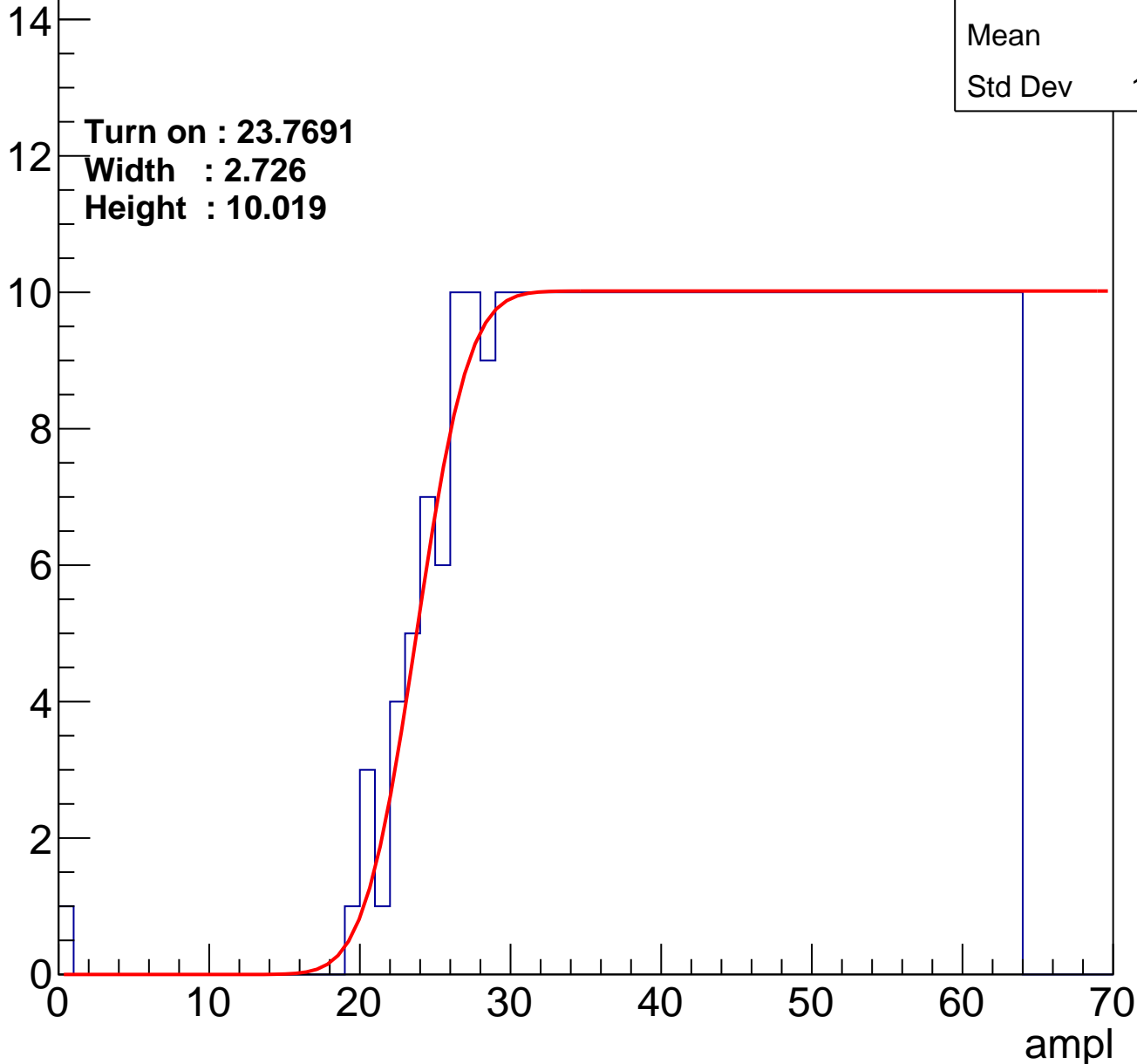
Entries	407
Mean	43
Std Dev	12.05

Turn on : 23.7691

Width : 2.726

Height : 10.019

Entry



B1L002S, U19-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.0511

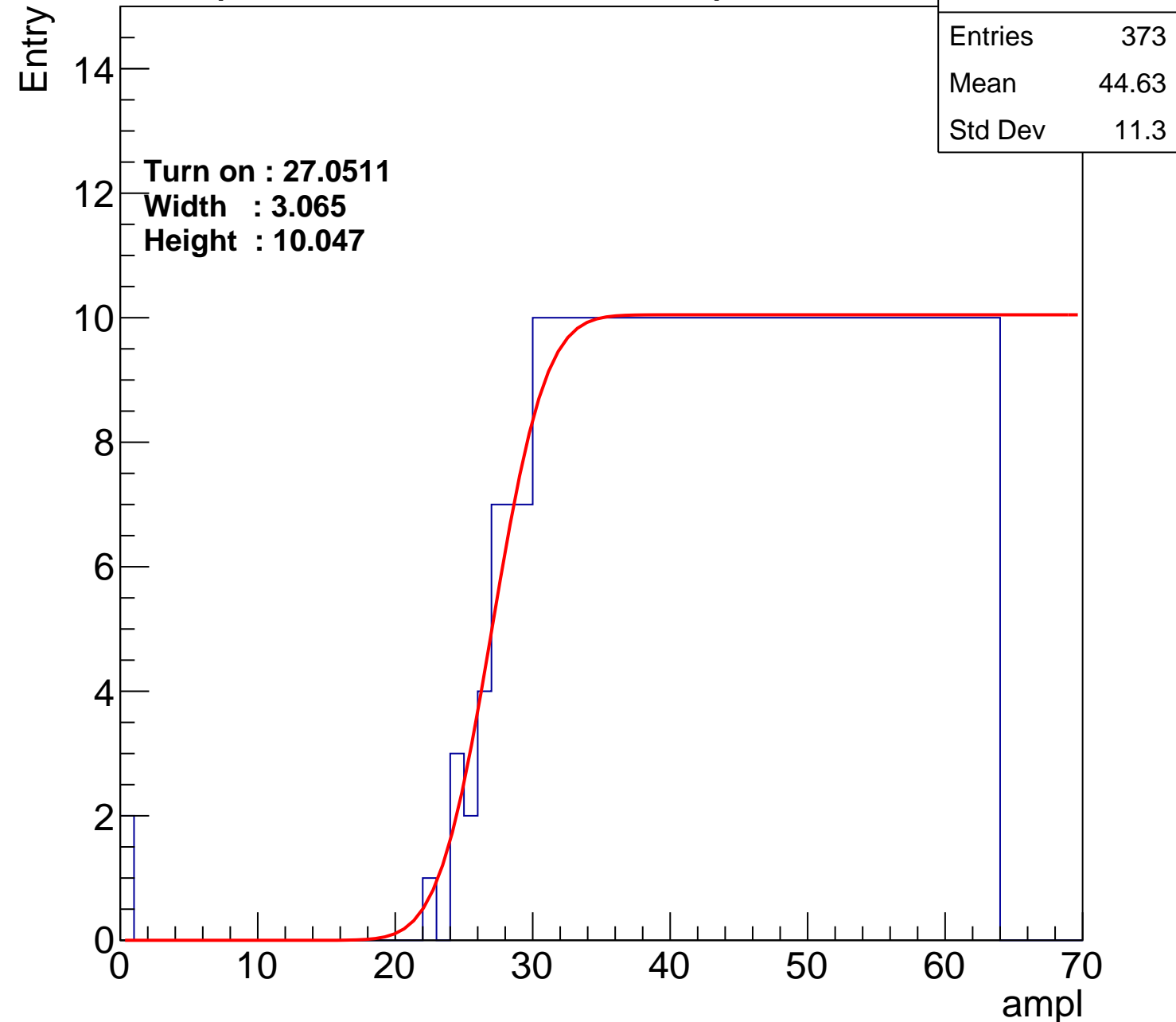
Width : 3.065

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.06
Std Dev	11.56

Turn on : 26.0347

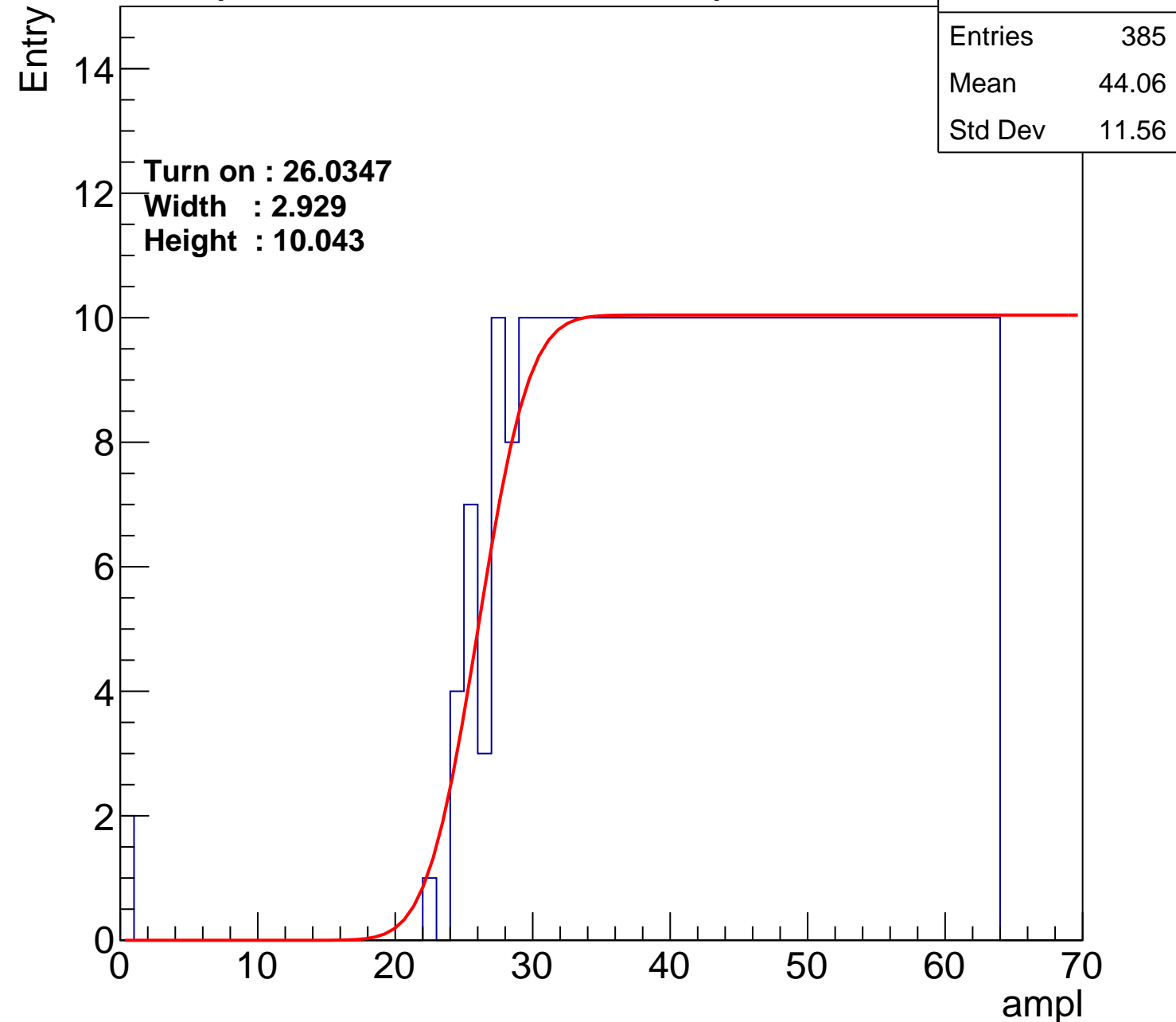
Width : 2.929

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.12
Std Dev	11.39

Turn on : 25.5497

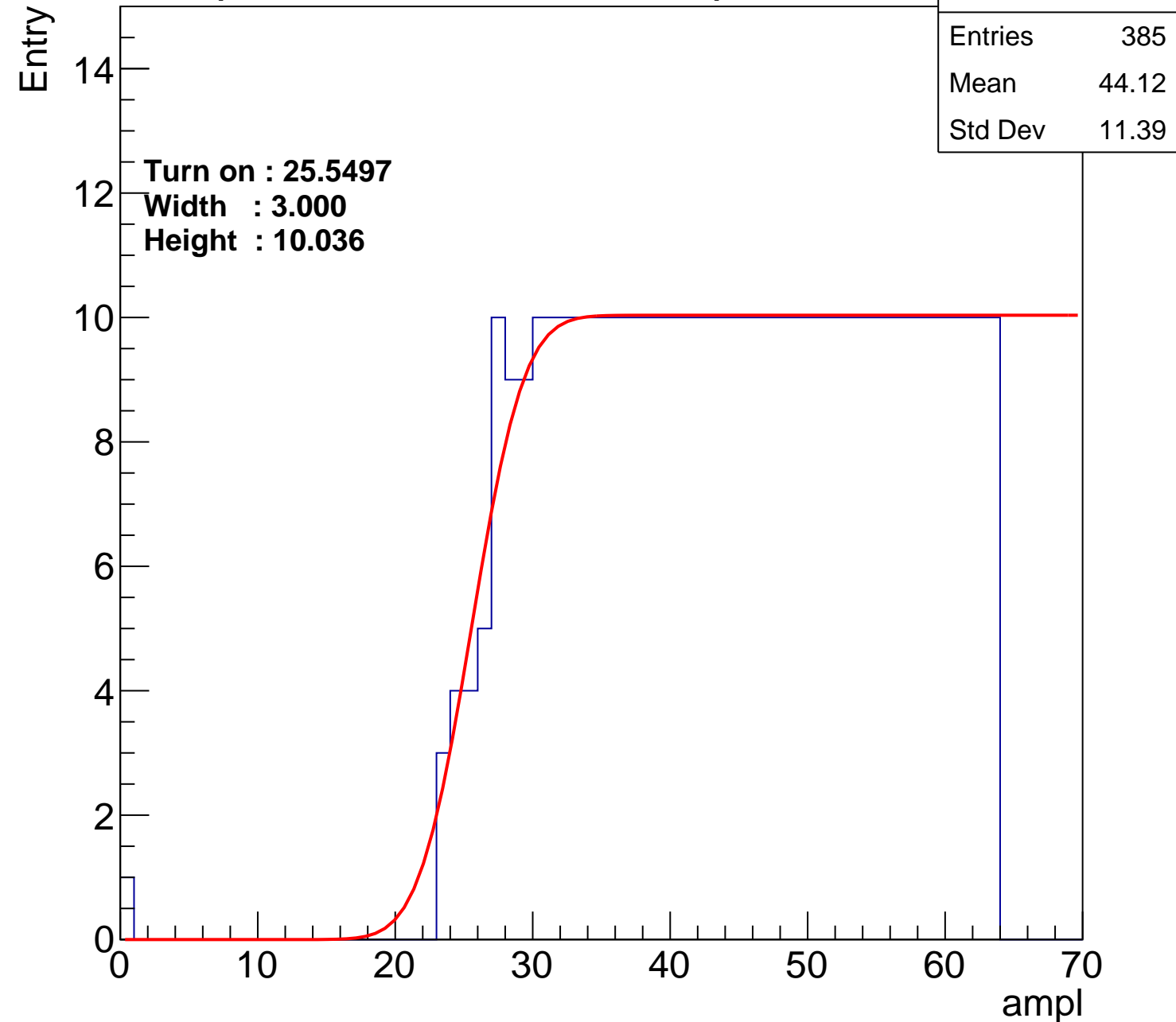
Width : 3.000

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 24.8520

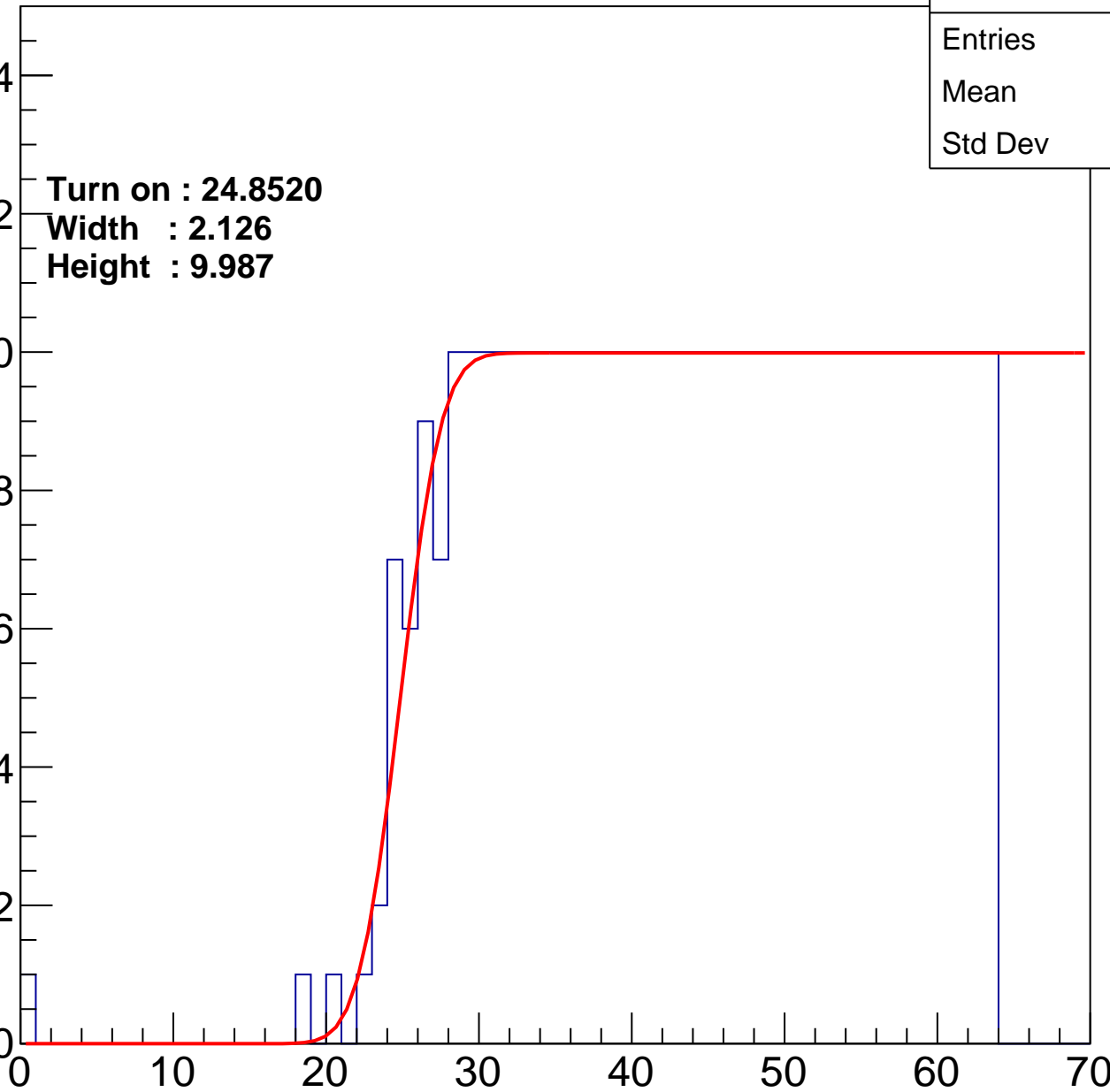
Width : 2.126

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.38
Std Dev	11.49

Turn on : 27.1980

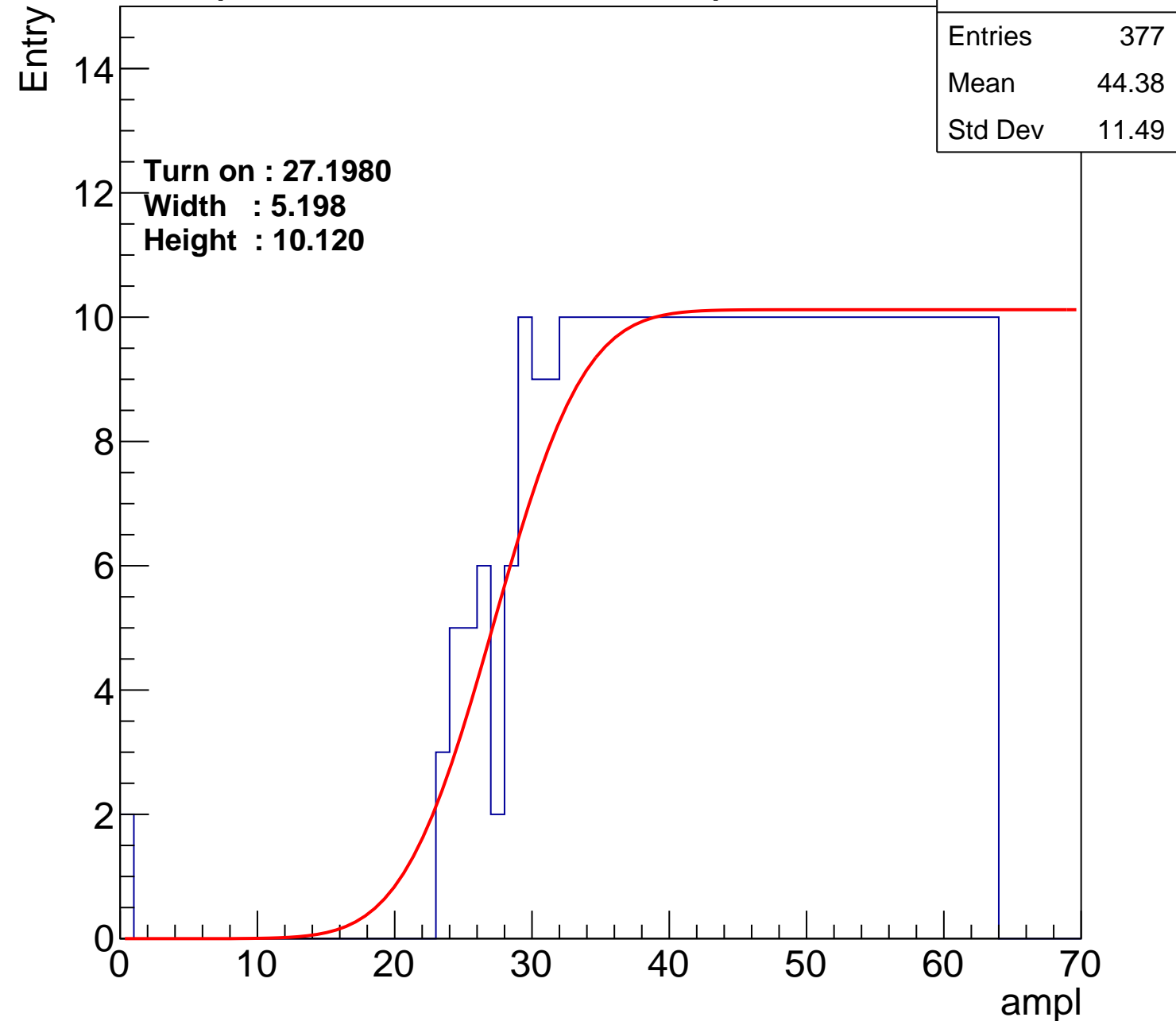
Width : 5.198

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.77
Std Dev	11.04

Turn on : 26.8913

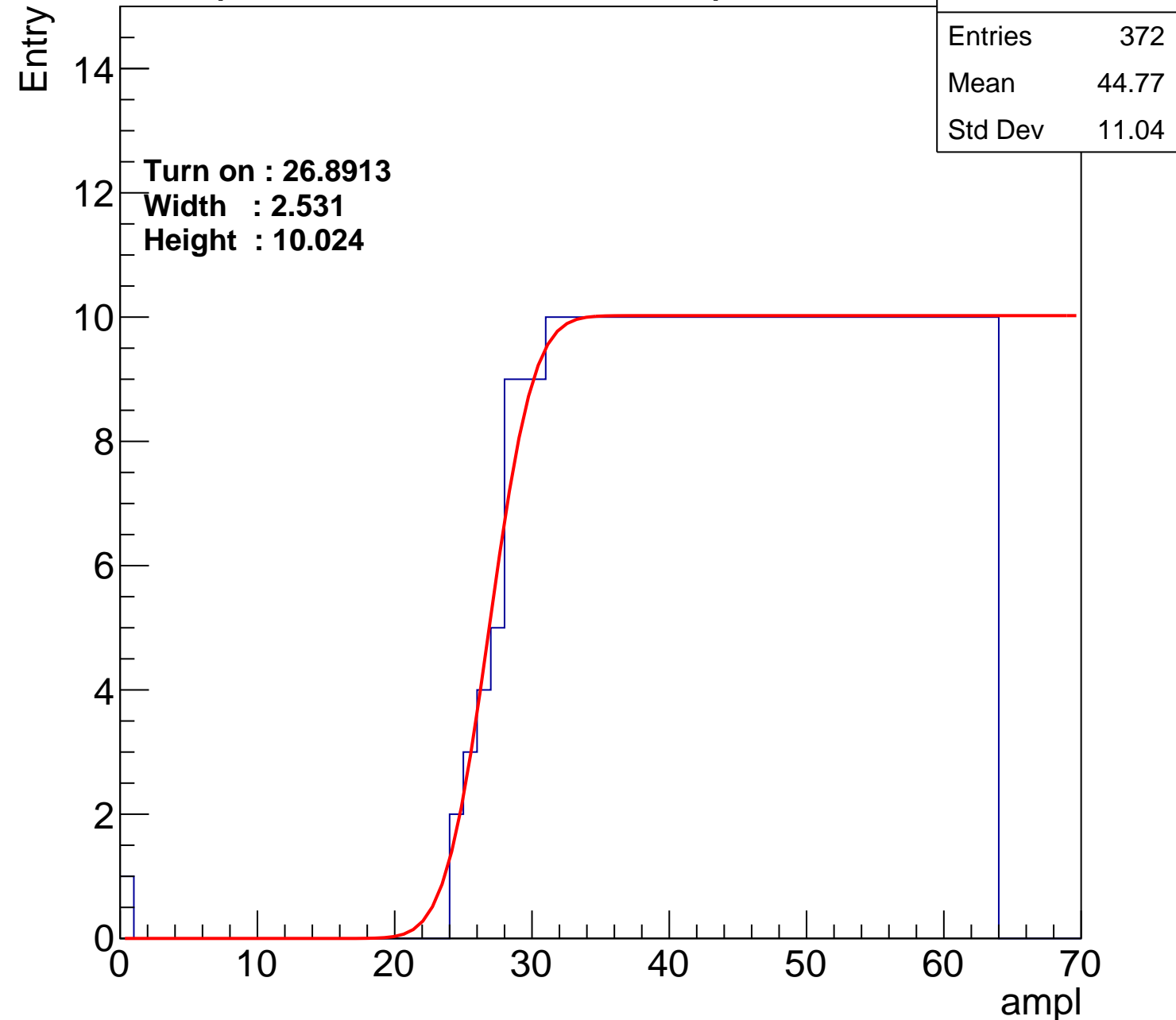
Width : 2.531

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch13

calib_packv5_042523_0143.root, FC#10, port B3

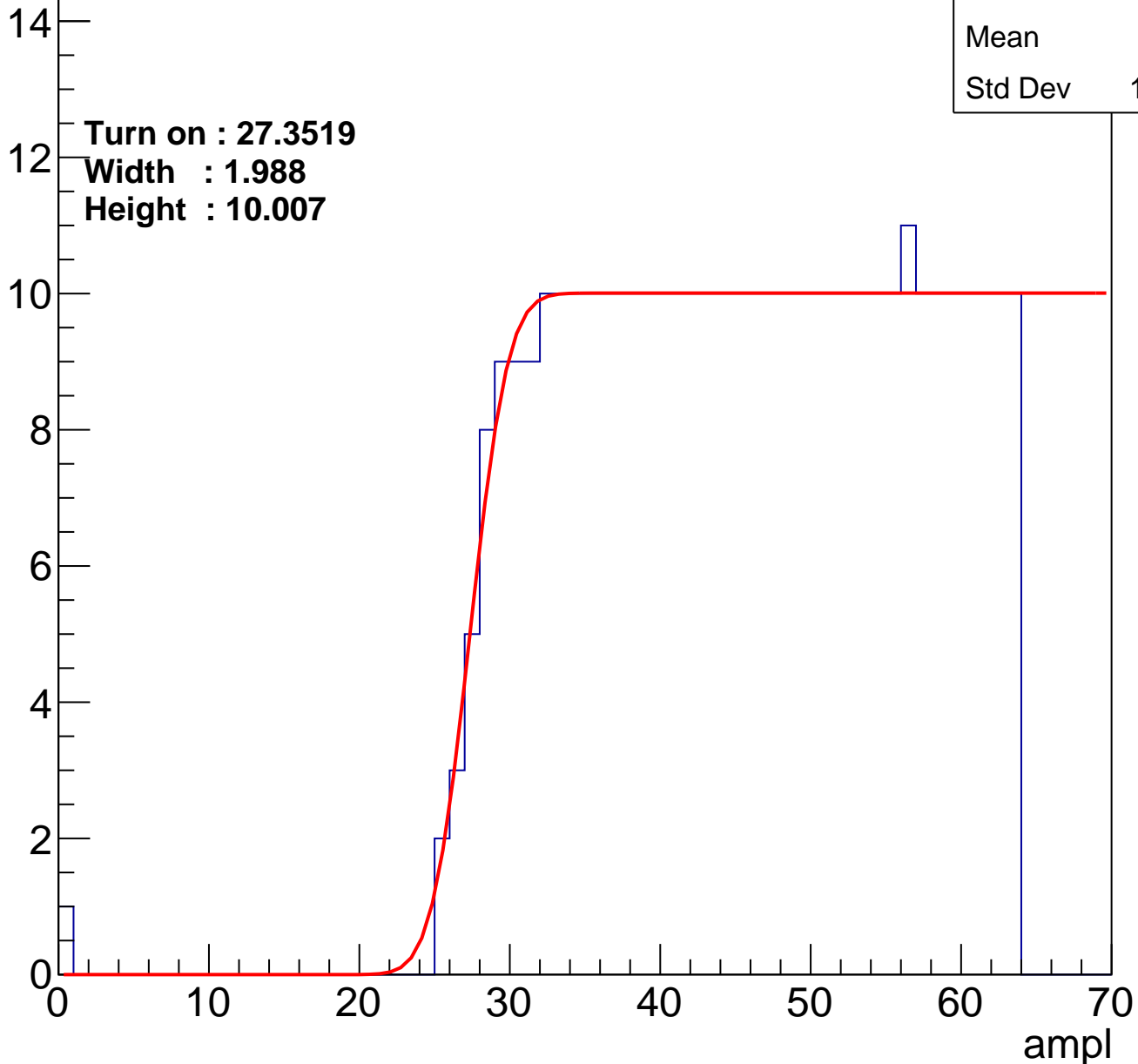
Entries	367
Mean	45.1
Std Dev	10.87

Turn on : 27.3519

Width : 1.988

Height : 10.007

Entry



B1L002S, U19-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.62
Std Dev	11.12

Turn on : 26.6818

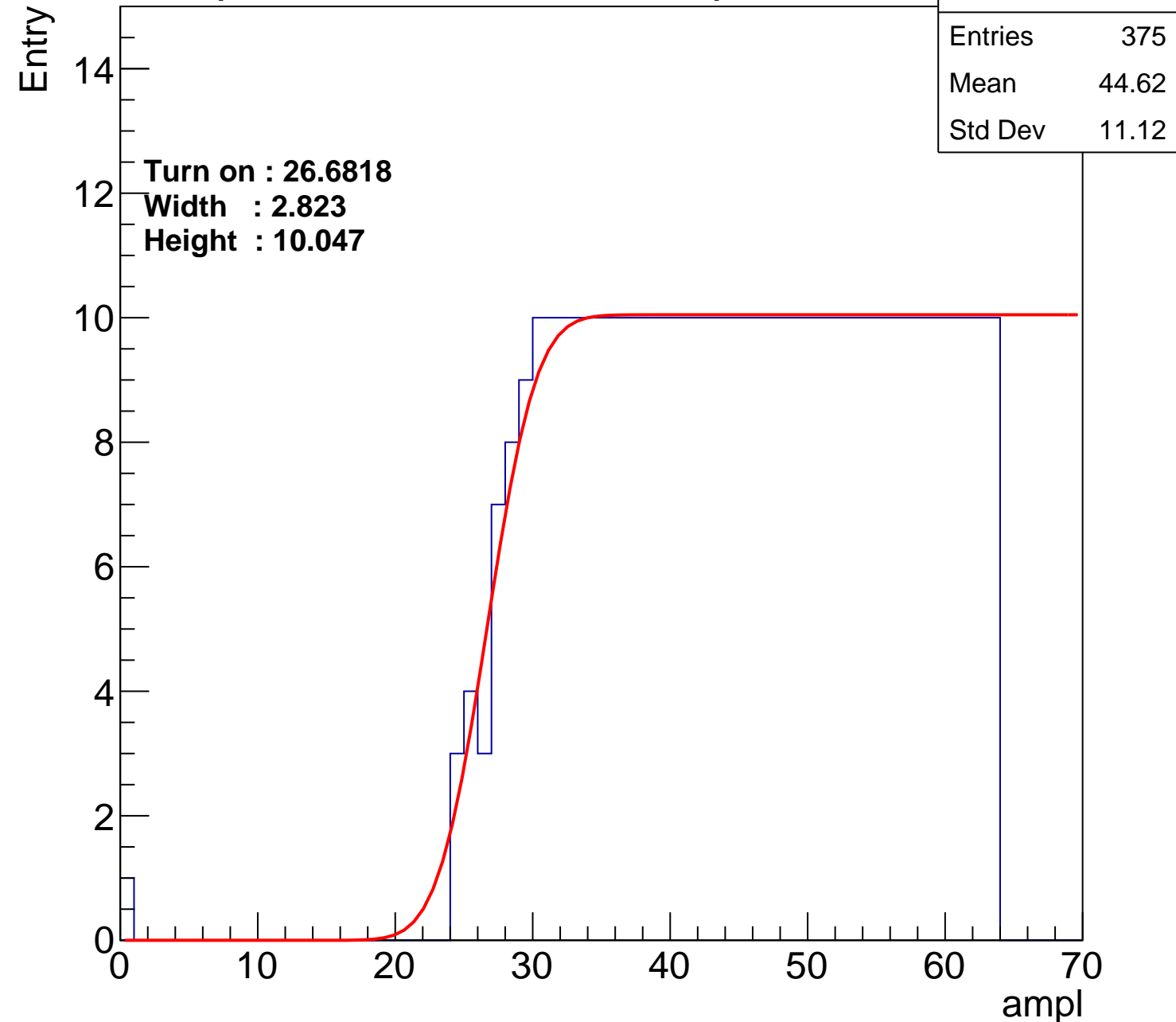
Width : 2.823

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch15

calib_packv5_042523_0143.root, FC#10, port B3

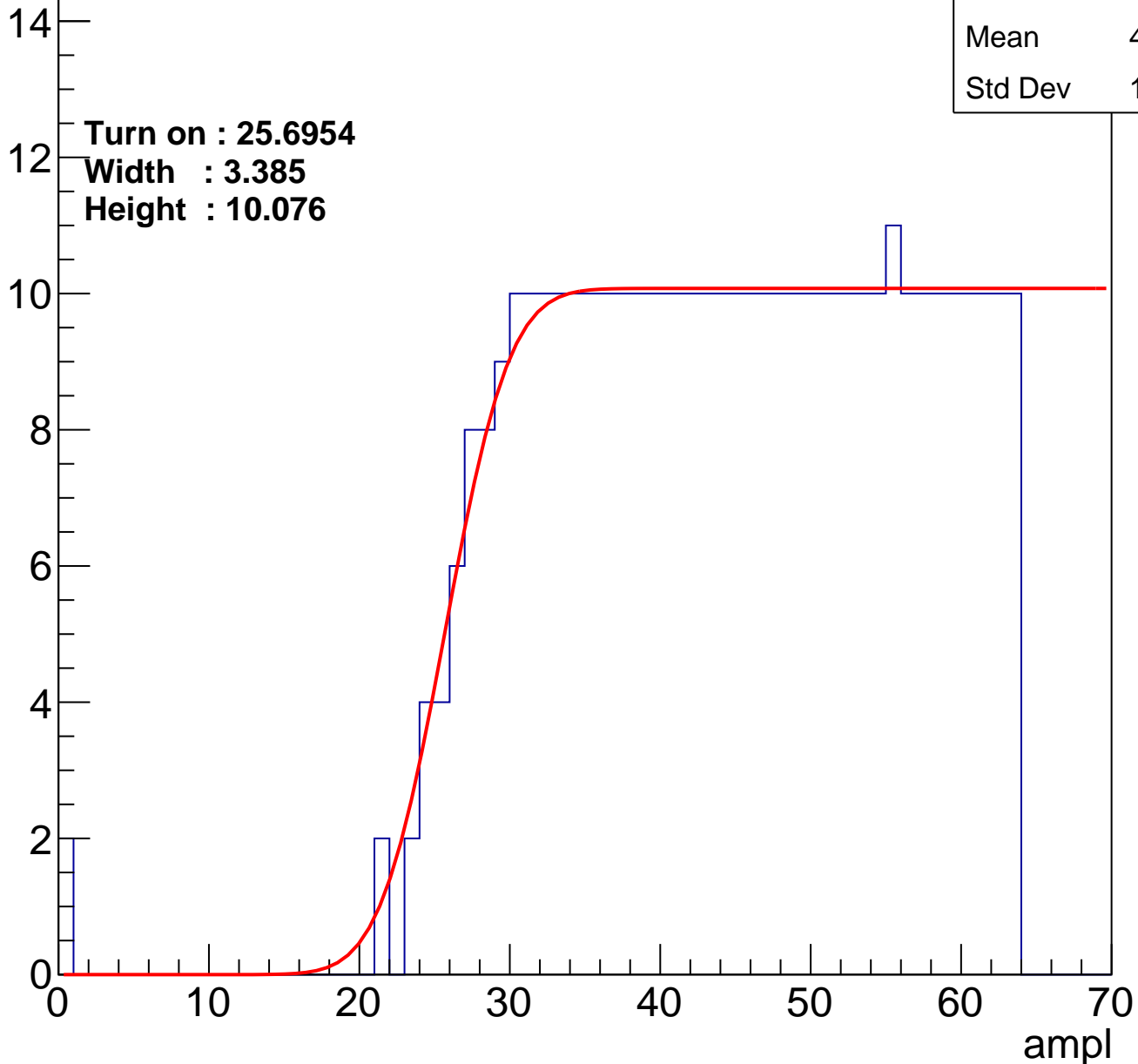
Entries	386
Mean	44.06
Std Dev	11.62

Turn on : 25.6954

Width : 3.385

Height : 10.076

Entry



B1L002S, U19-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.07
Std Dev	11.56

Turn on : 25.6237

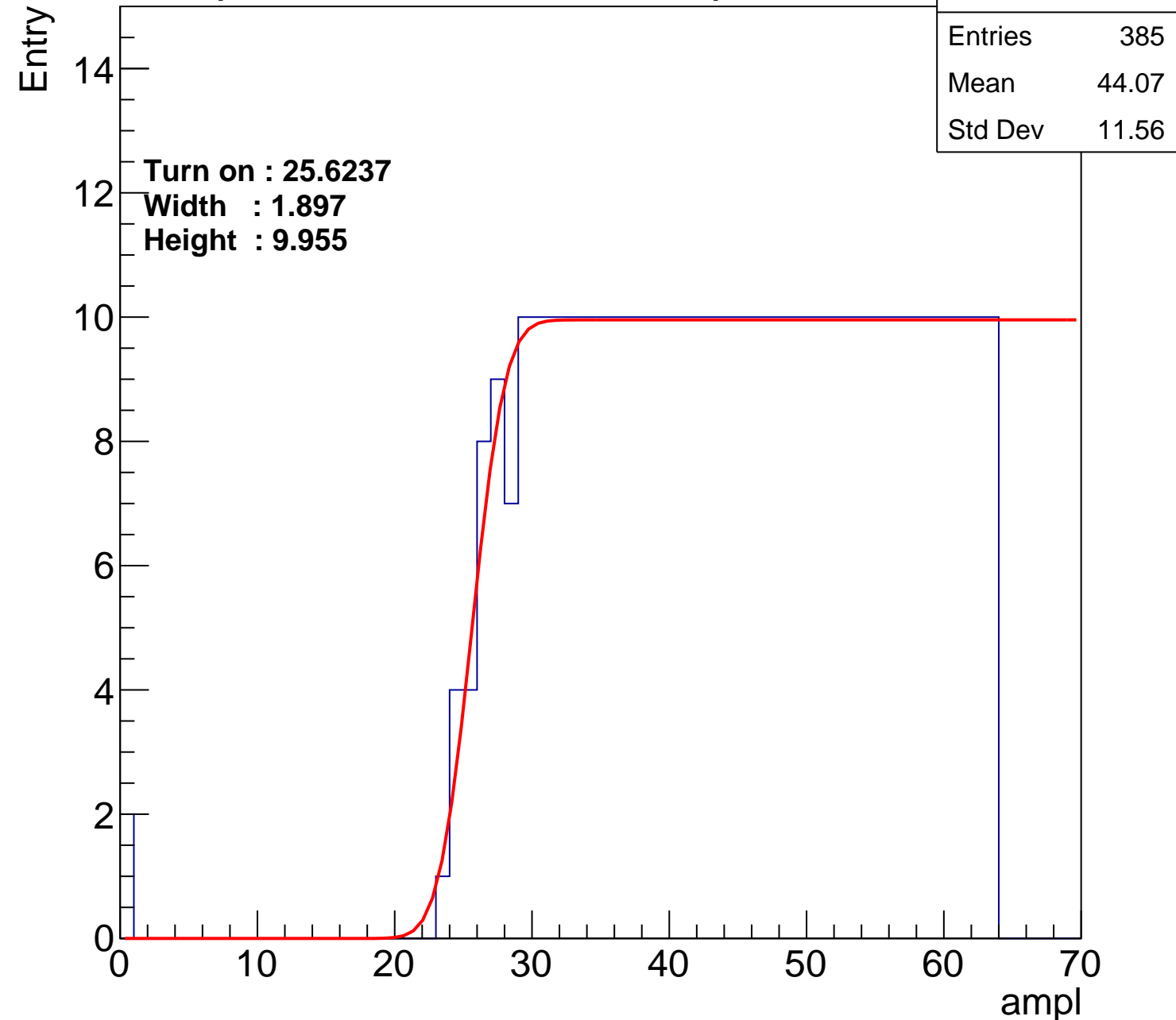
Width : 1.897

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.84
Std Dev	11.72

Turn on : 25.5928

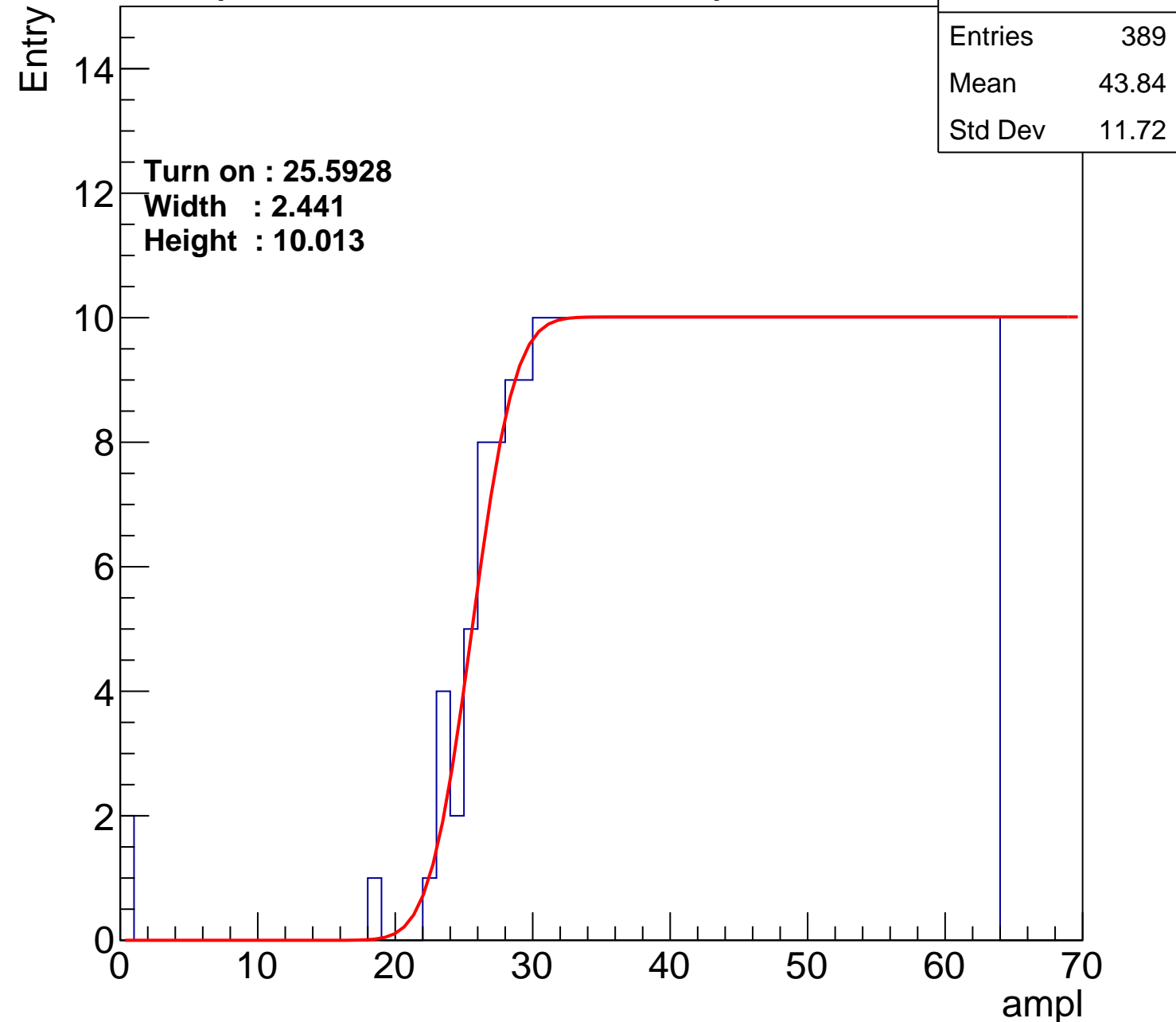
Width : 2.441

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.65
Std Dev	12.3

Turn on : 22.7856

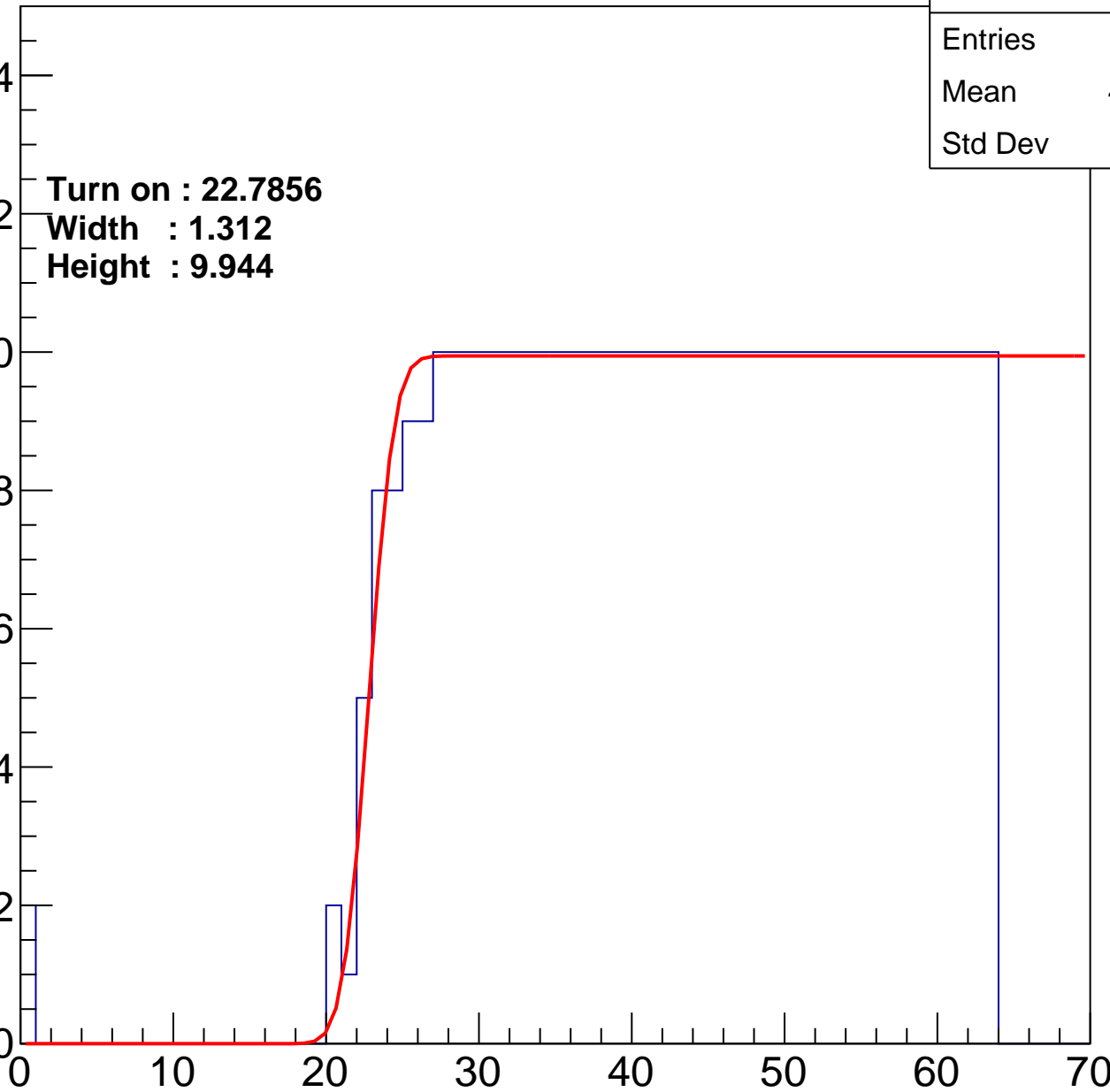
Width : 1.312

Height : 9.944

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch19

calib_packv5_042523_0143.root, FC#10, port B3

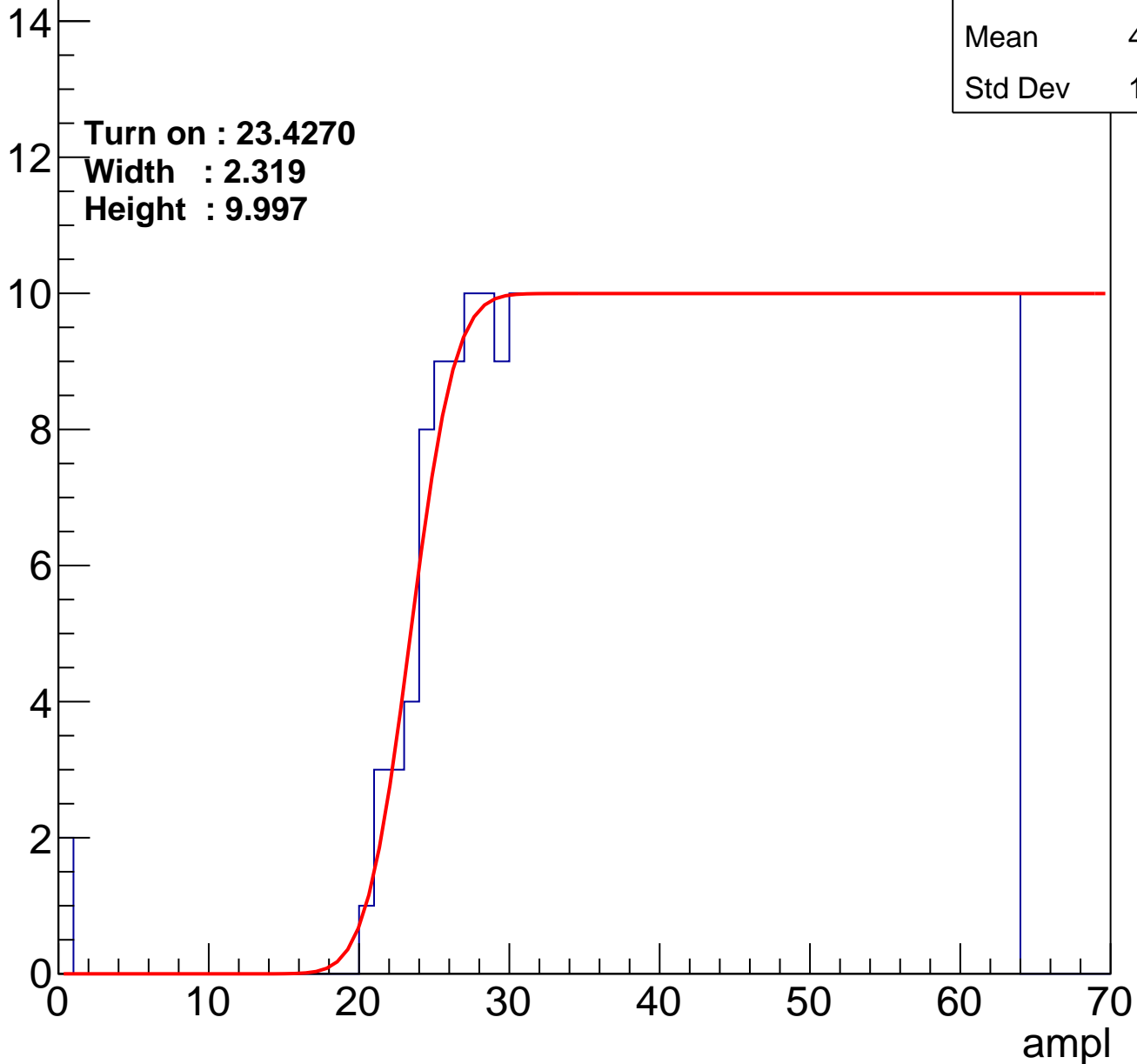
Entries	408
Mean	42.92
Std Dev	12.17

Turn on : 23.4270

Width : 2.319

Height : 9.997

Entry



B1L002S, U19-ch20

calib_packv5_042523_0143.root, FC#10, port B3

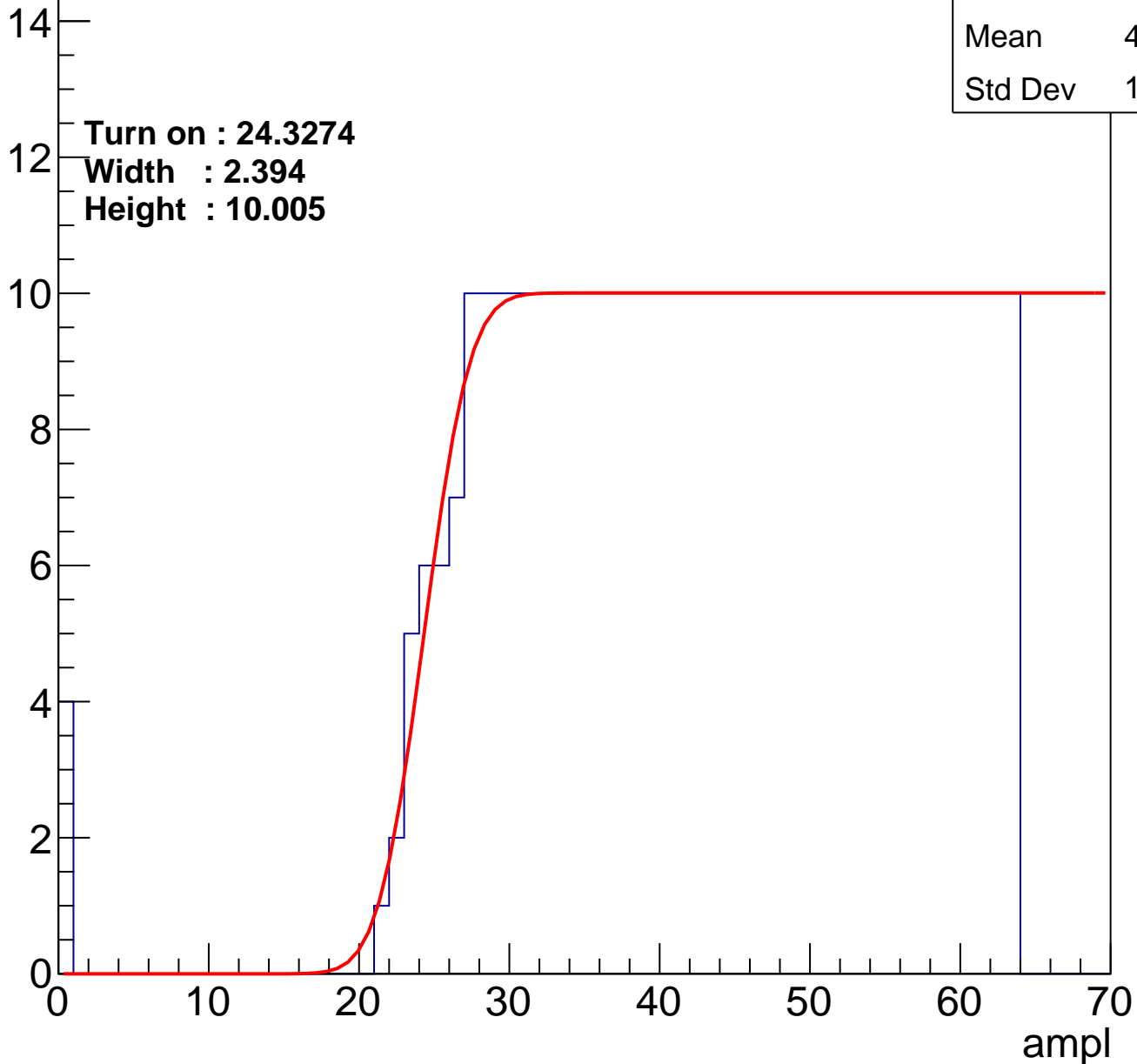
Entries	401
Mean	43.16
Std Dev	12.29

Turn on : 24.3274

Width : 2.394

Height : 10.005

Entry



B1L002S, U19-ch21

calib_packv5_042523_0143.root, FC#10, port B3

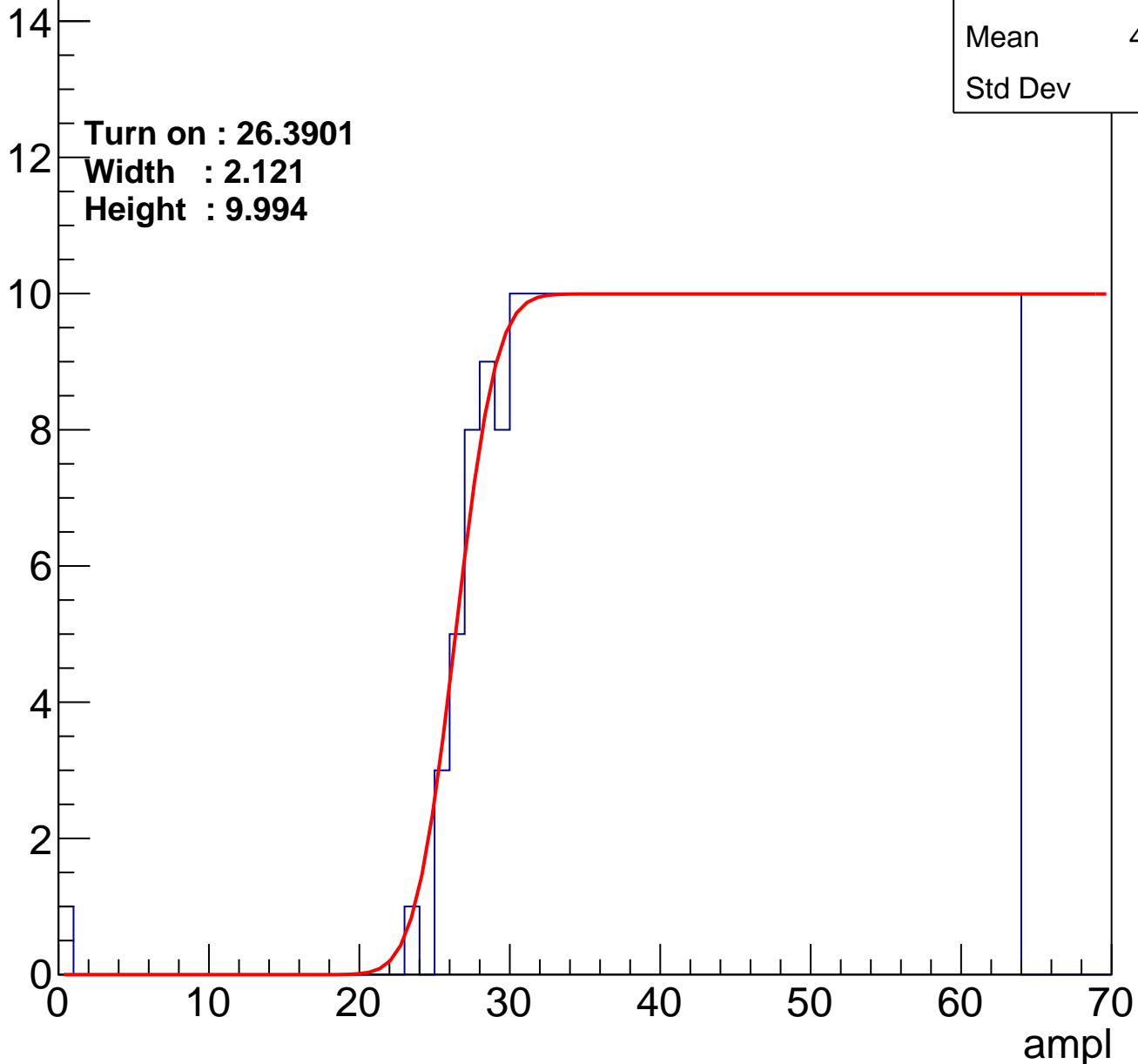
Entries	375
Mean	44.63
Std Dev	11.1

Turn on : 26.3901

Width : 2.121

Height : 9.994

Entry



B1L002S, U19-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.52
Std Dev	11.69

Turn on : 24.3036

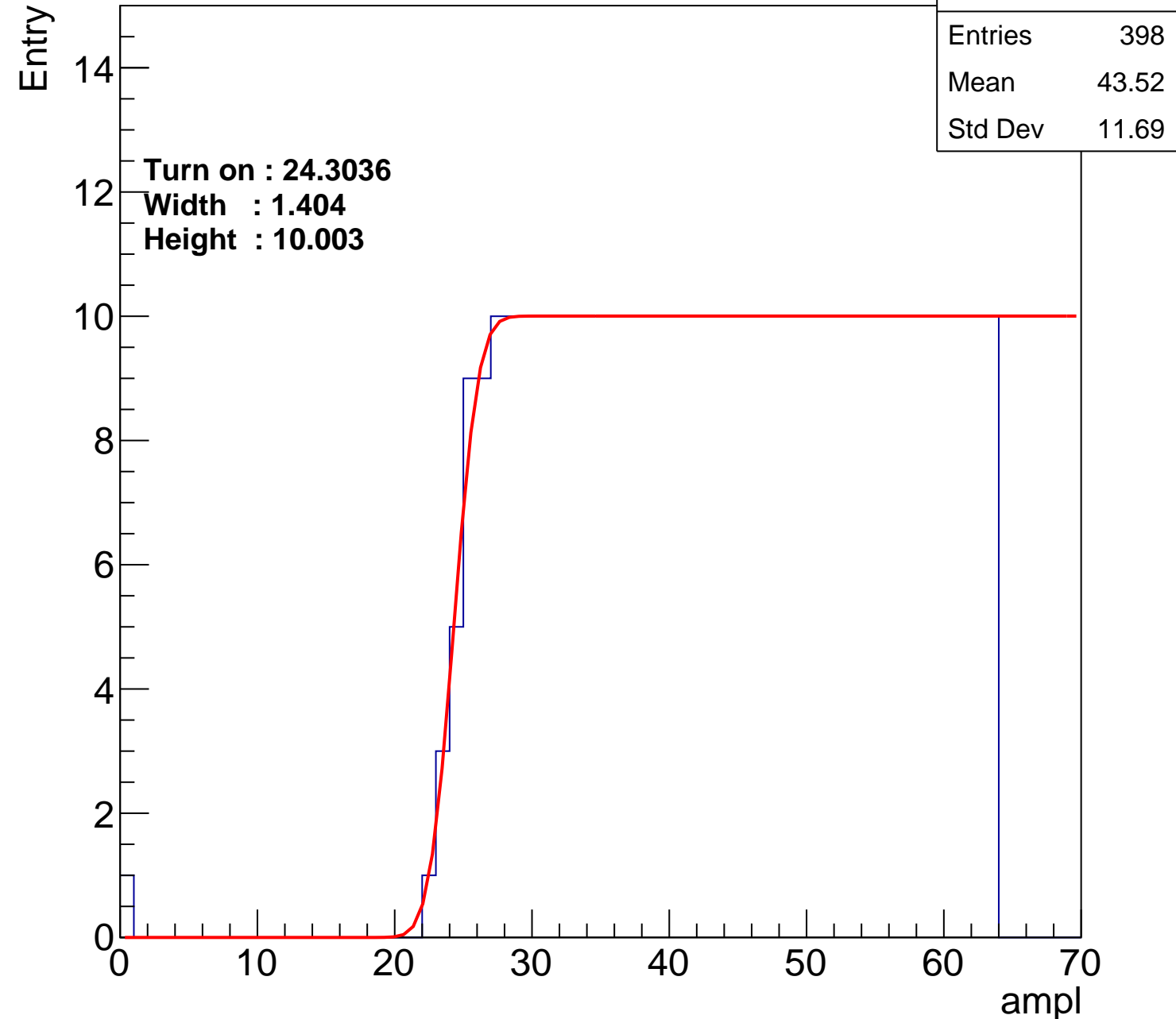
Width : 1.404

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch23

calib_packv5_042523_0143.root, FC#10, port B3

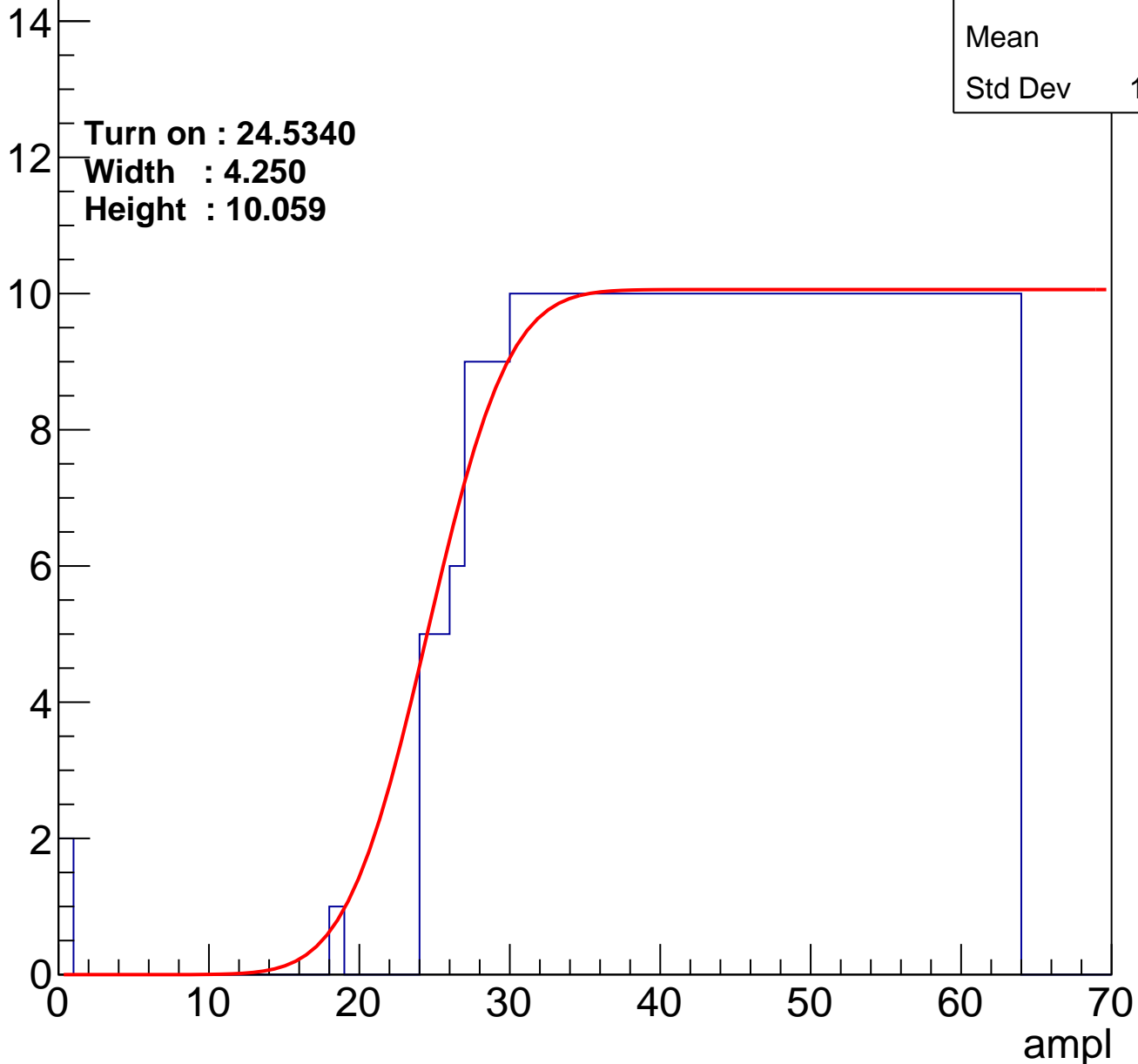
Entries	386
Mean	44
Std Dev	11.62

Turn on : 24.5340

Width : 4.250

Height : 10.059

Entry



B1L002S, U19-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.88
Std Dev	12.25

Turn on : 23.6408

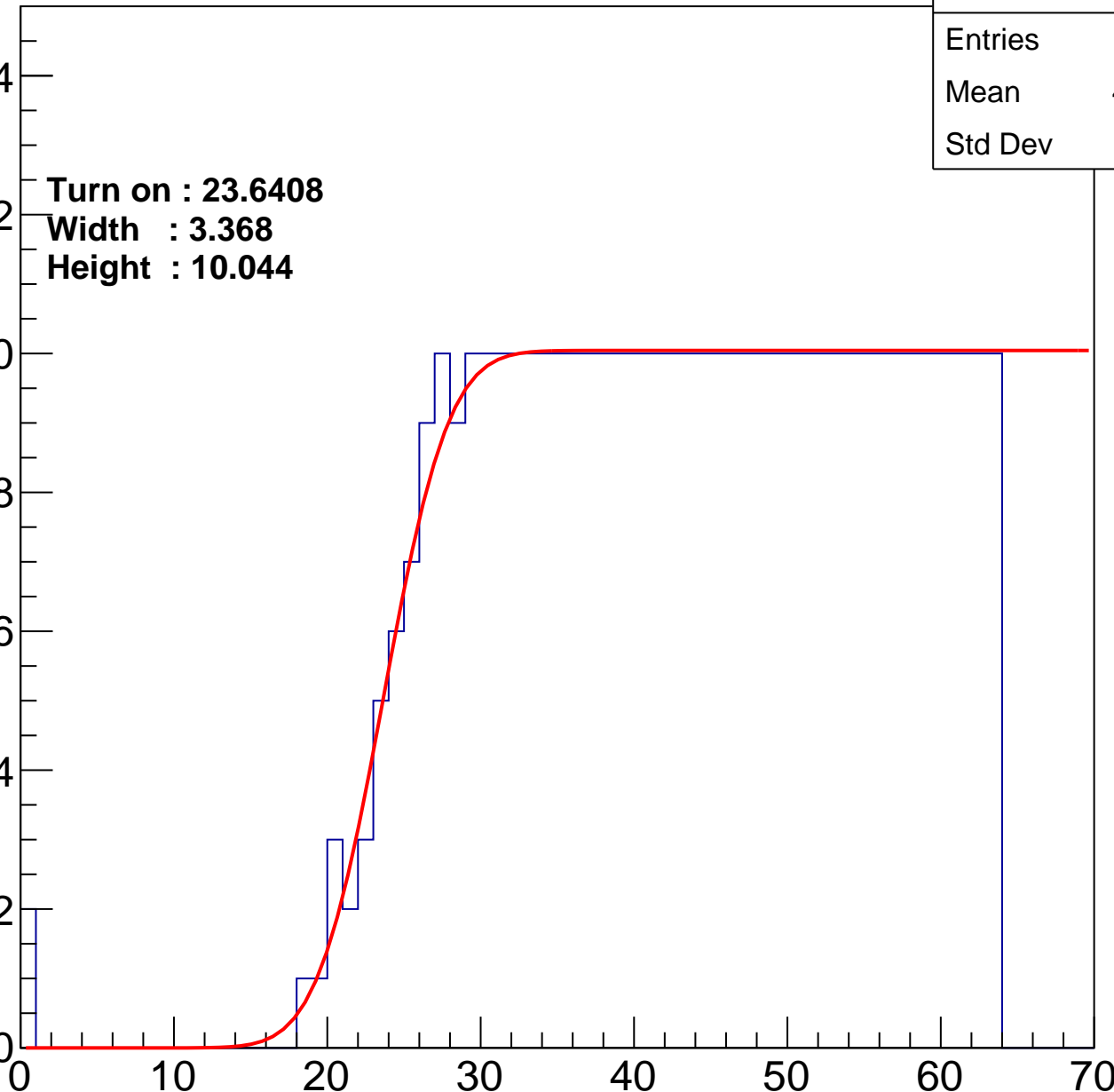
Width : 3.368

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.62
Std Dev	11.79

Turn on : 24.6847

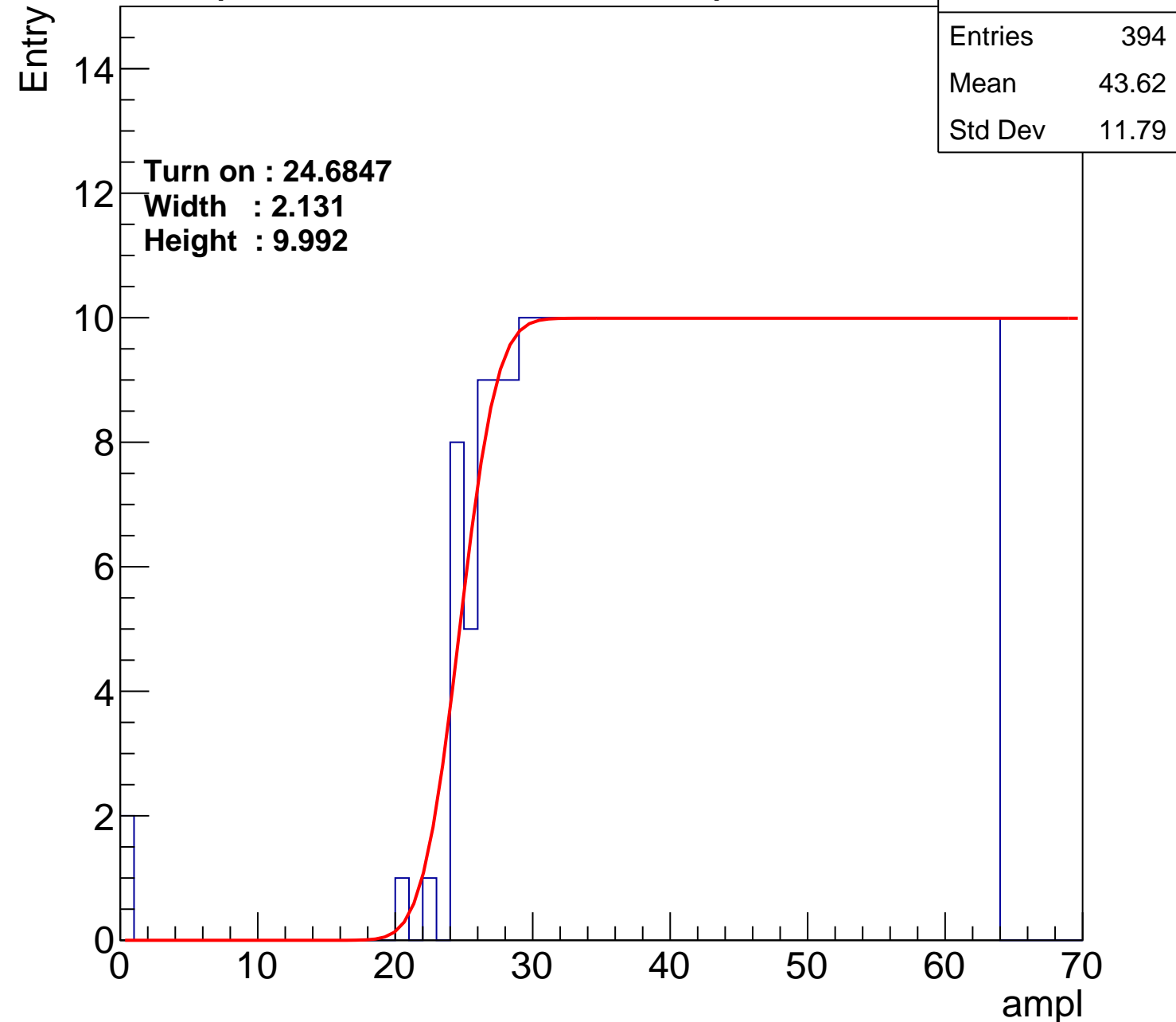
Width : 2.131

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.21
Std Dev	11.5

Turn on : 26.2682

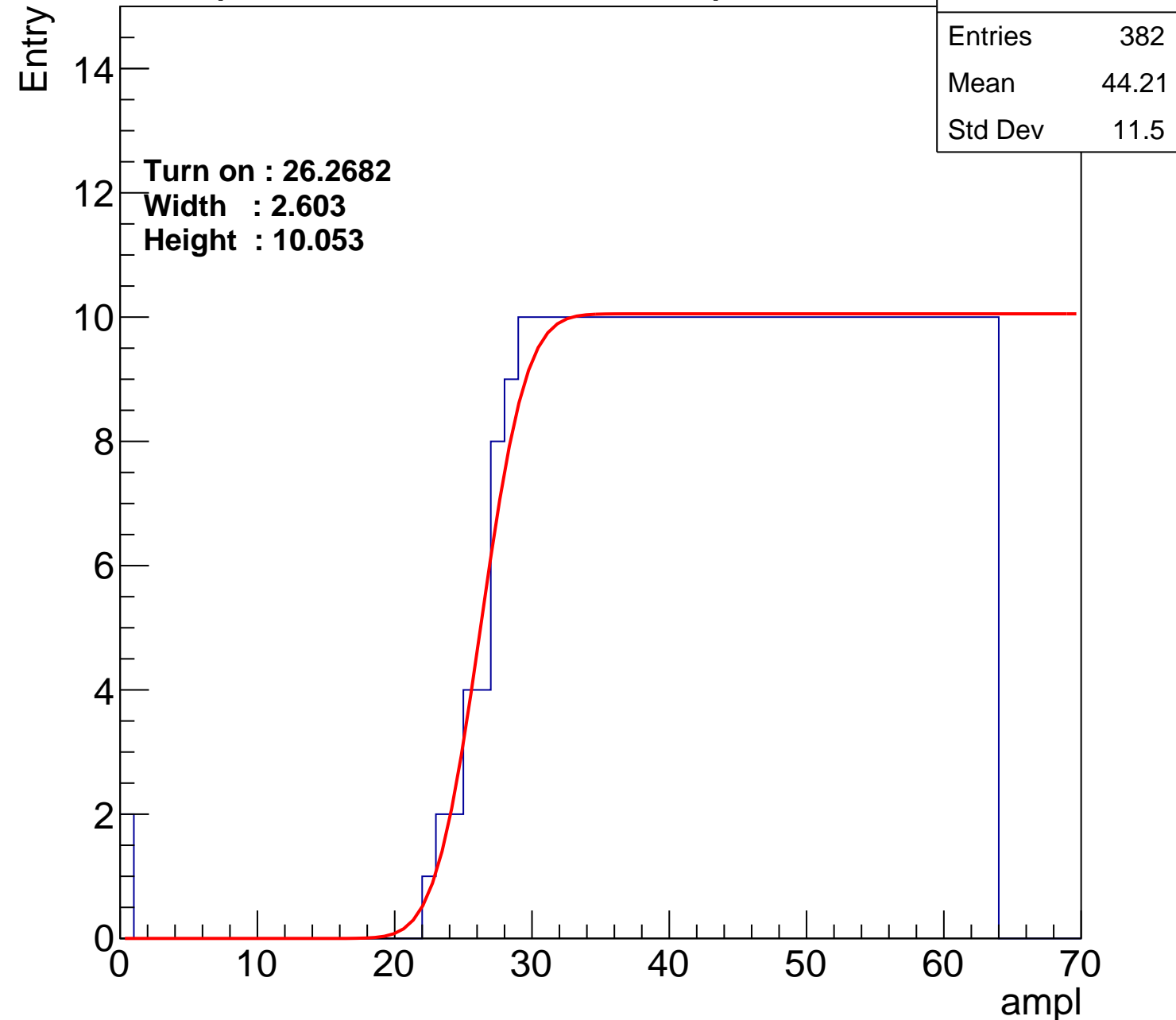
Width : 2.603

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	415
Mean	42.55
Std Dev	12.41

Turn on : 23.0663

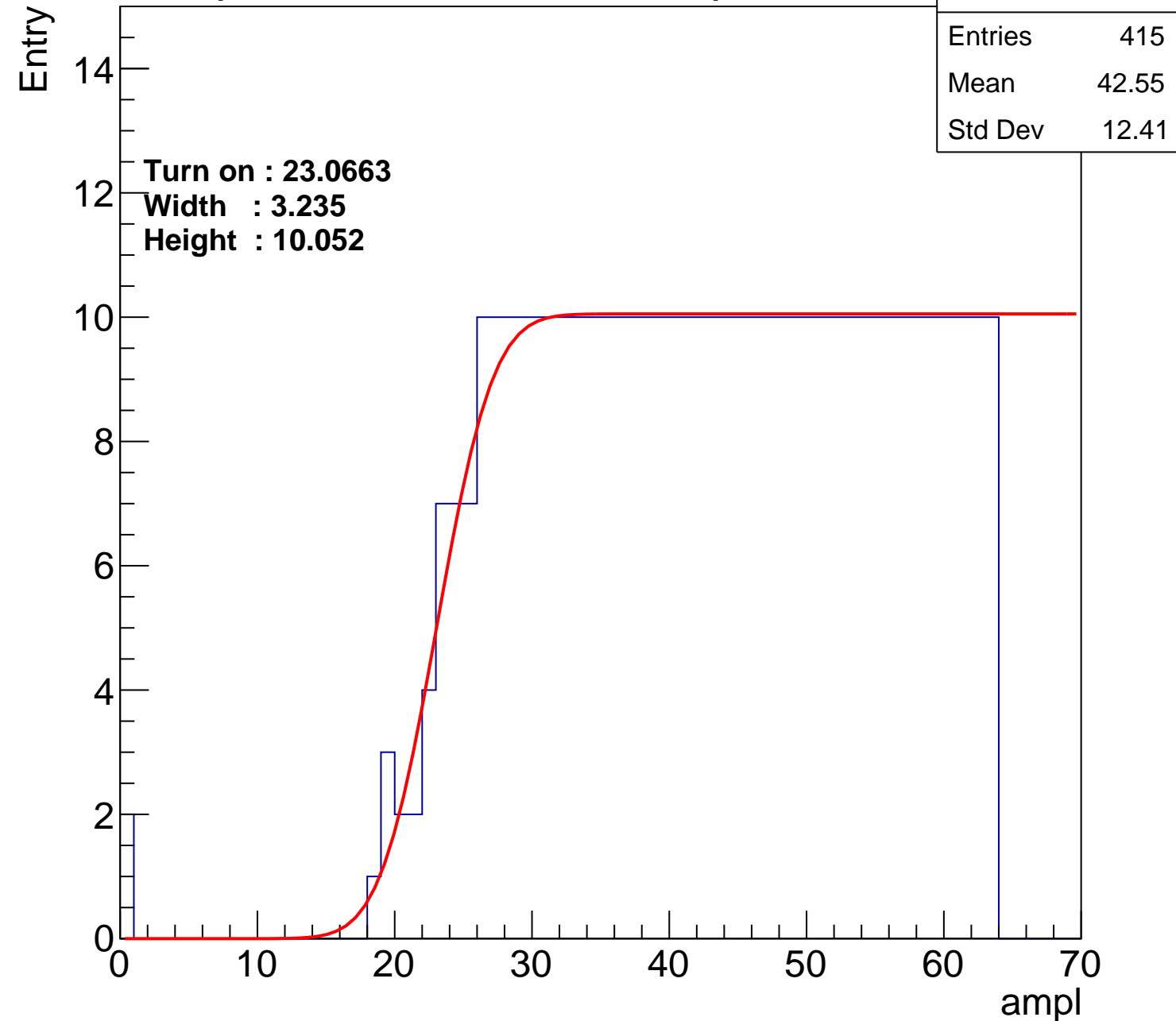
Width : 3.235

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch28

calib_packv5_042523_0143.root, FC#10, port B3

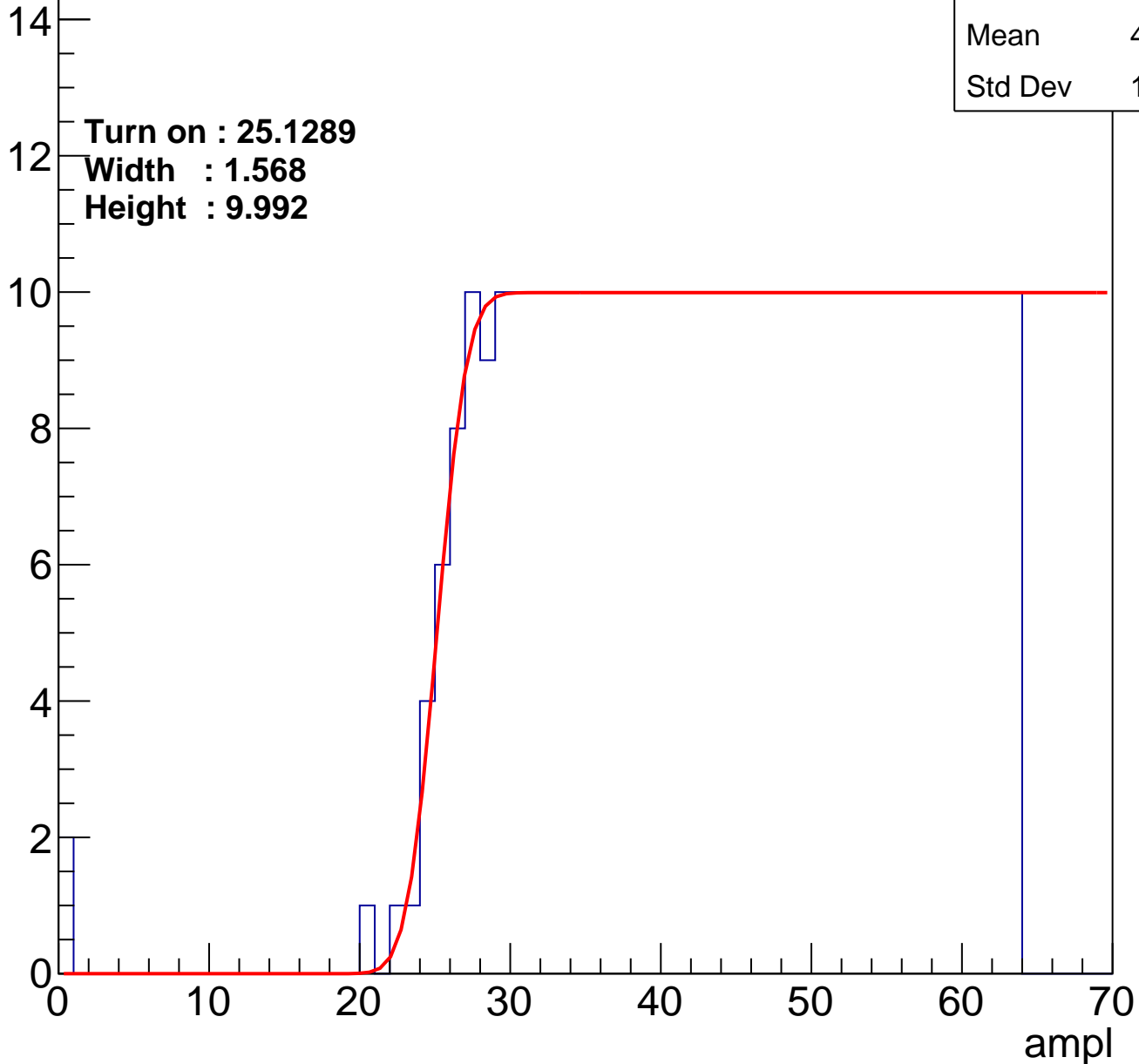
Entries	392
Mean	43.73
Std Dev	11.74

Turn on : 25.1289

Width : 1.568

Height : 9.992

Entry



B1L002S, U19-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.57
Std Dev	11.22

Turn on : 27.5286

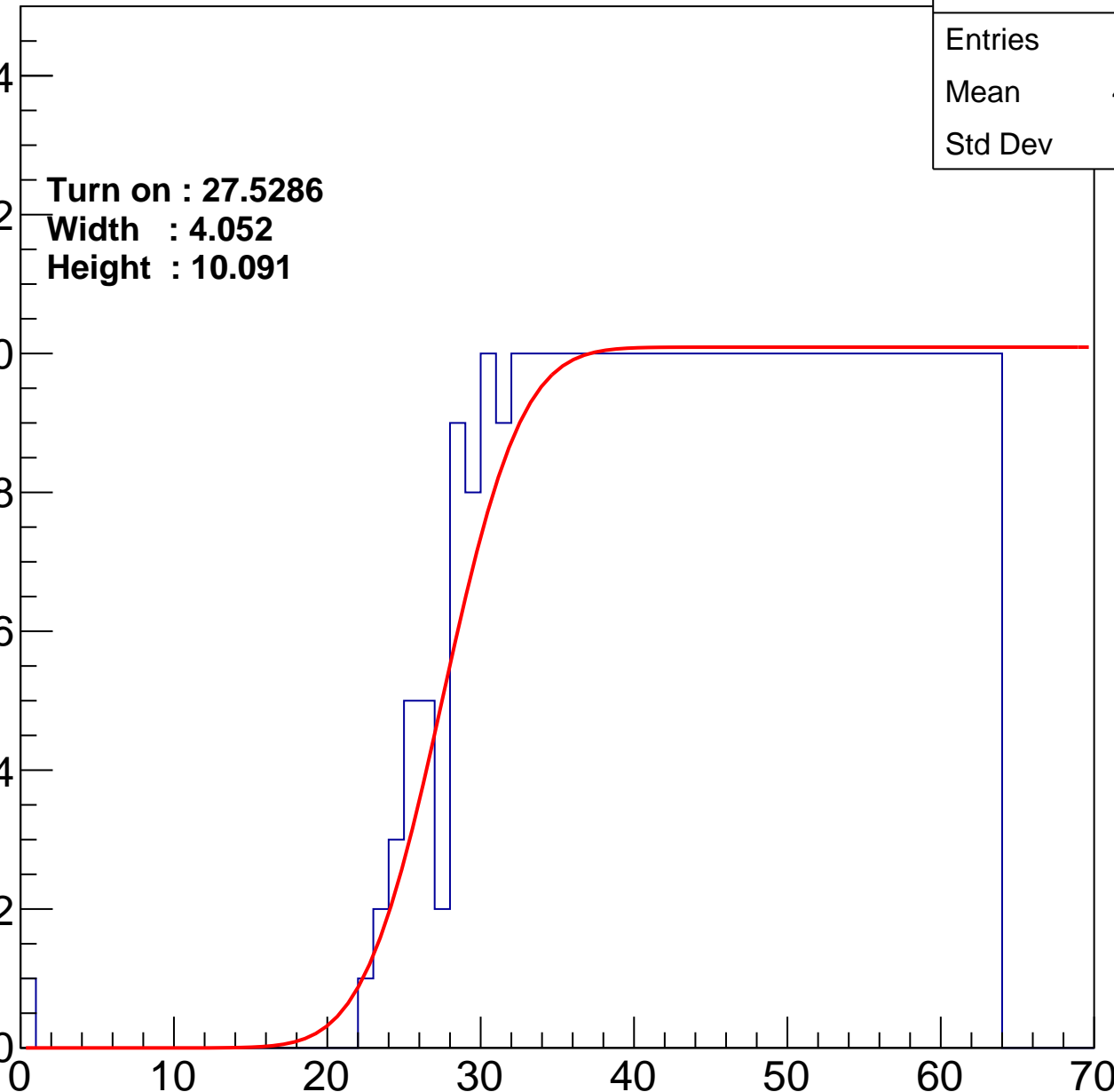
Width : 4.052

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch30

calib_packv5_042523_0143.root, FC#10, port B3

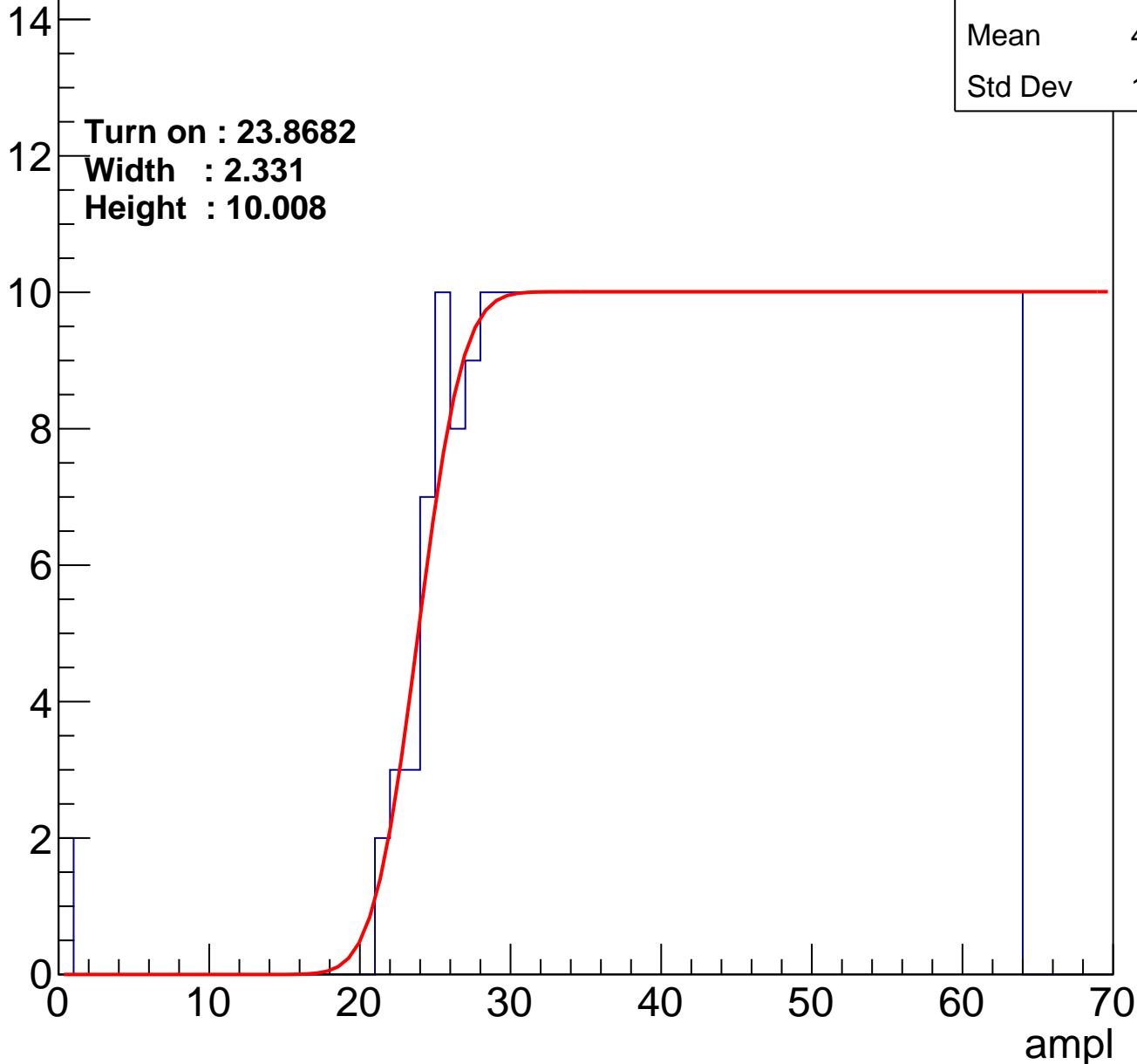
Entries	404
Mean	43.13
Std Dev	12.05

Turn on : 23.8682

Width : 2.331

Height : 10.008

Entry



B1L002S, U19-ch31

calib_packv5_042523_0143.root, FC#10, port B3

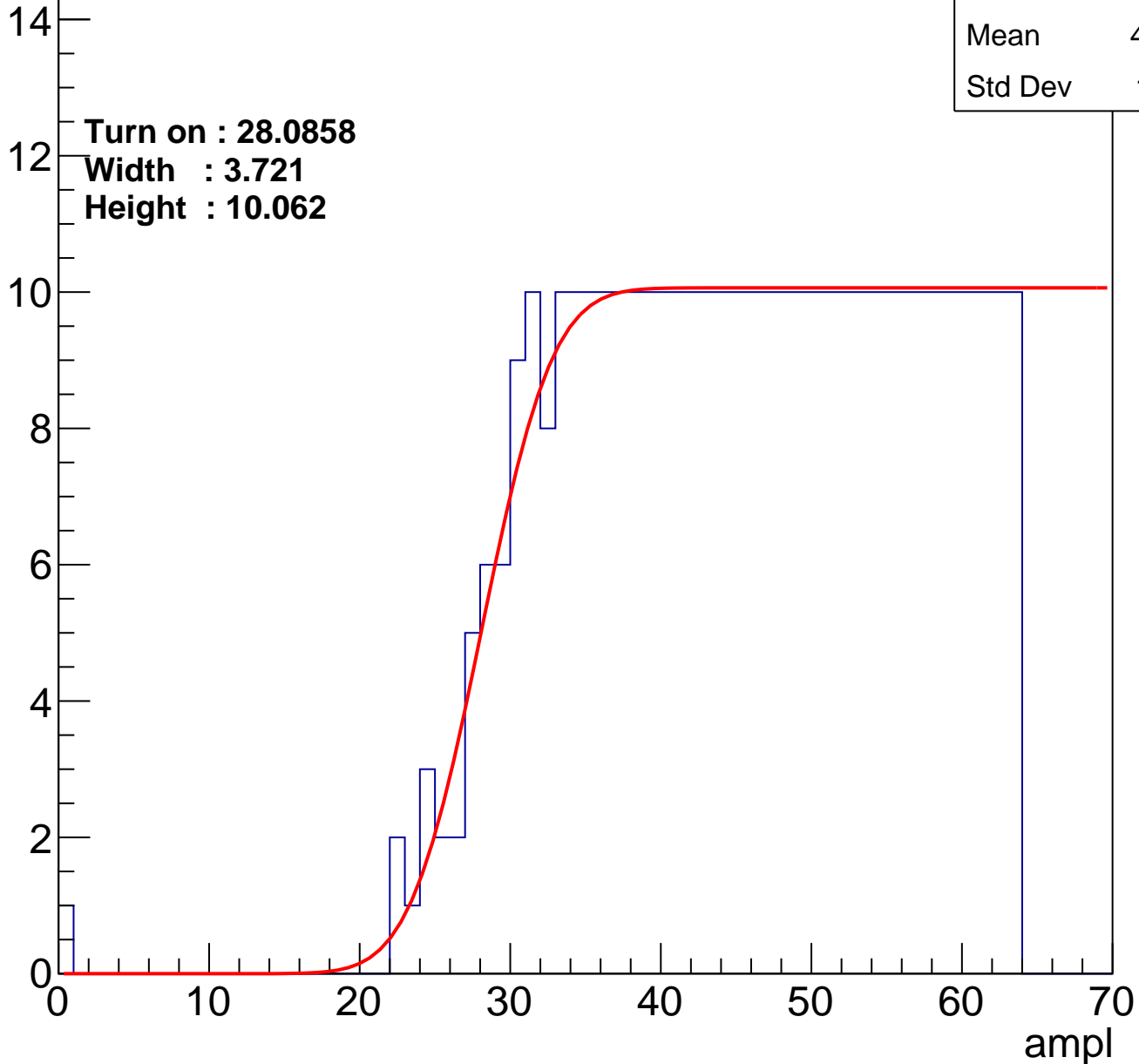
Entries	365
Mean	45.02
Std Dev	11.01

Turn on : 28.0858

Width : 3.721

Height : 10.062

Entry



B1L002S, U19-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.4
Std Dev	11.83

Turn on : 24.5753

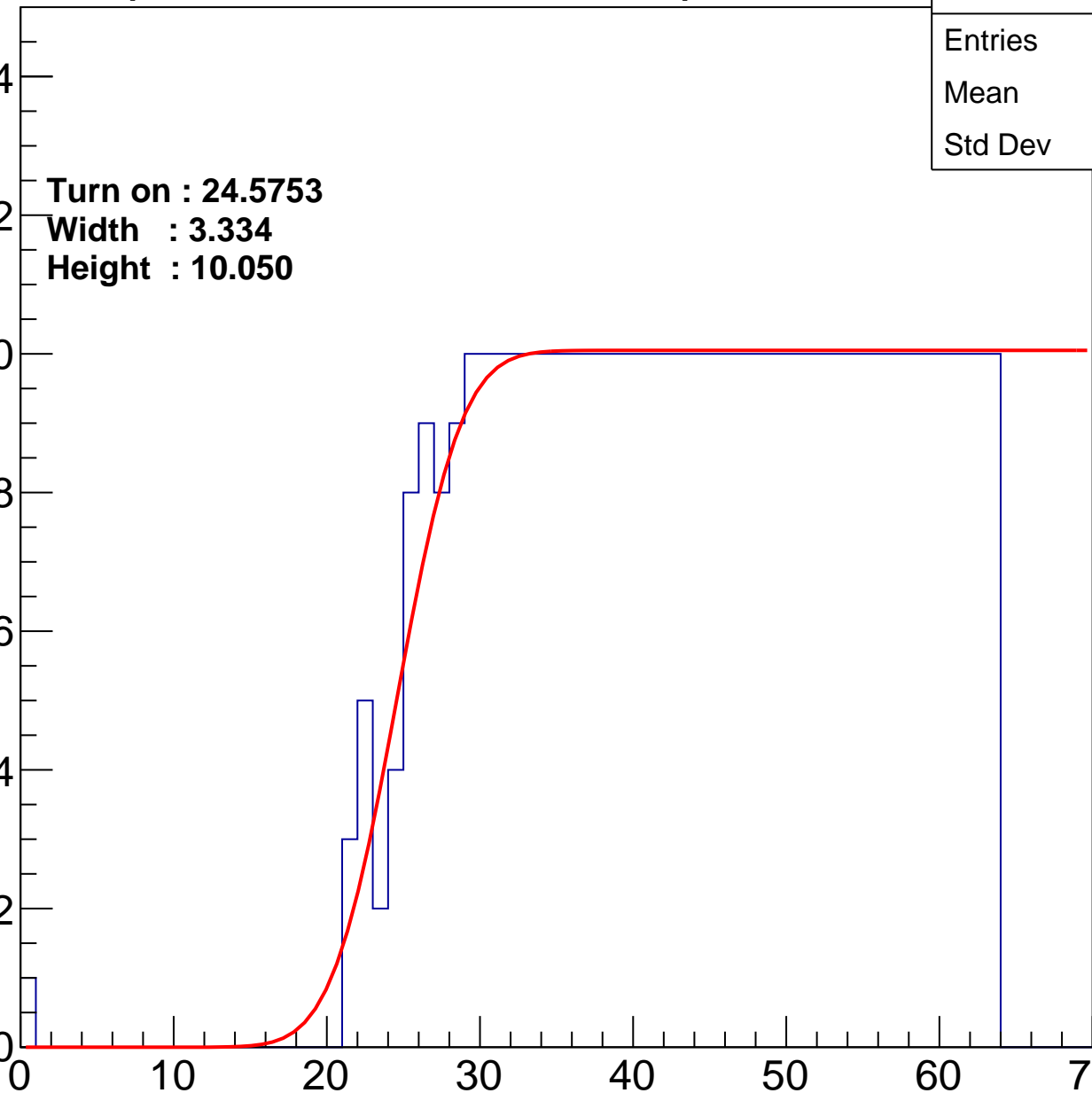
Width : 3.334

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.37
Std Dev	11.31

Turn on : 26.4015

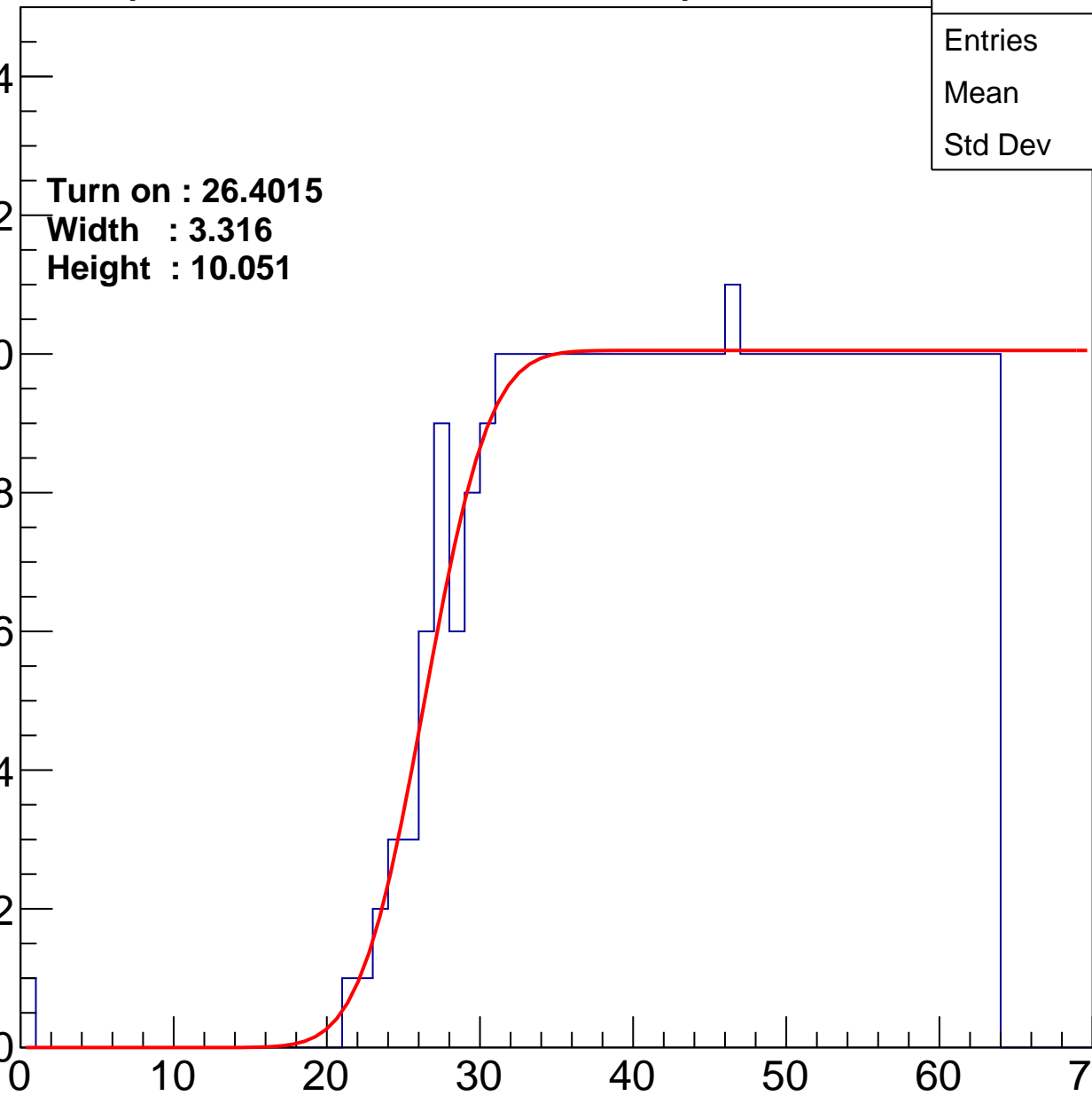
Width : 3.316

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch34

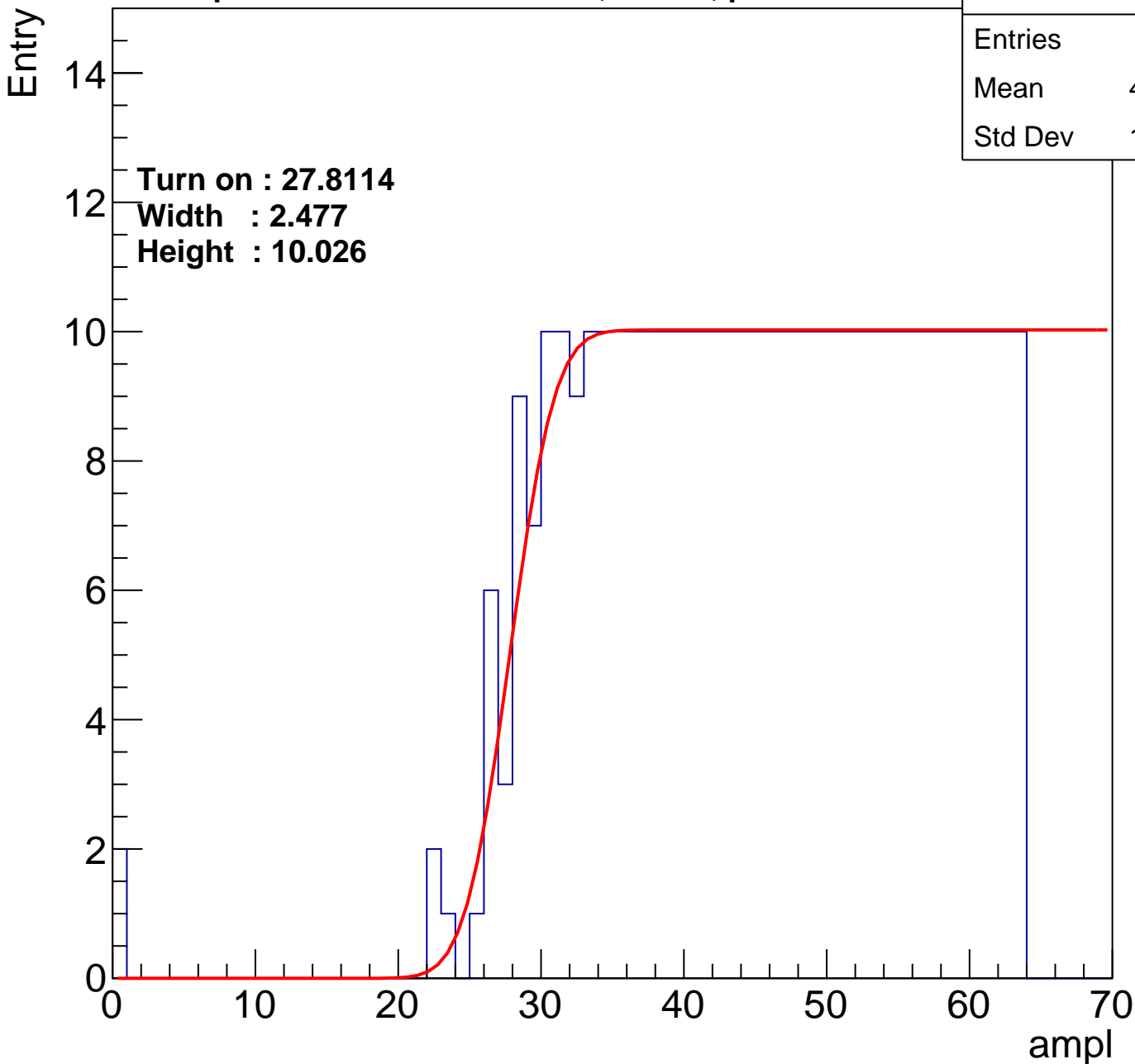
calib_packv5_042523_0143.root, FC#10, port B3

Entries	370
Mean	44.76
Std Dev	11.25

Turn on : 27.8114

Width : 2.477

Height : 10.026



B1L002S, U19-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43
Std Dev	12.27

Turn on : 24.1780

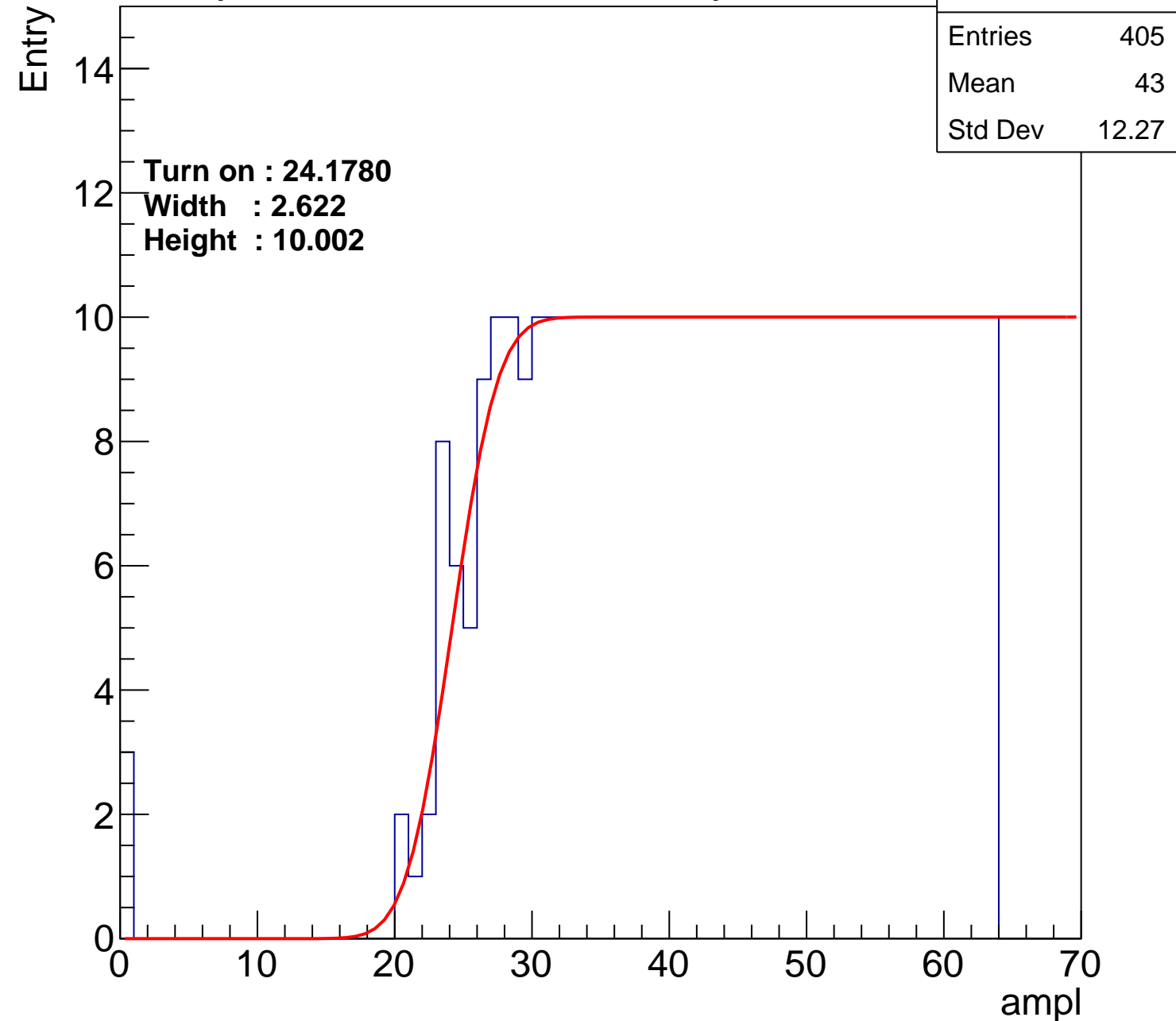
Width : 2.622

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch36

calib_packv5_042523_0143.root, FC#10, port B3

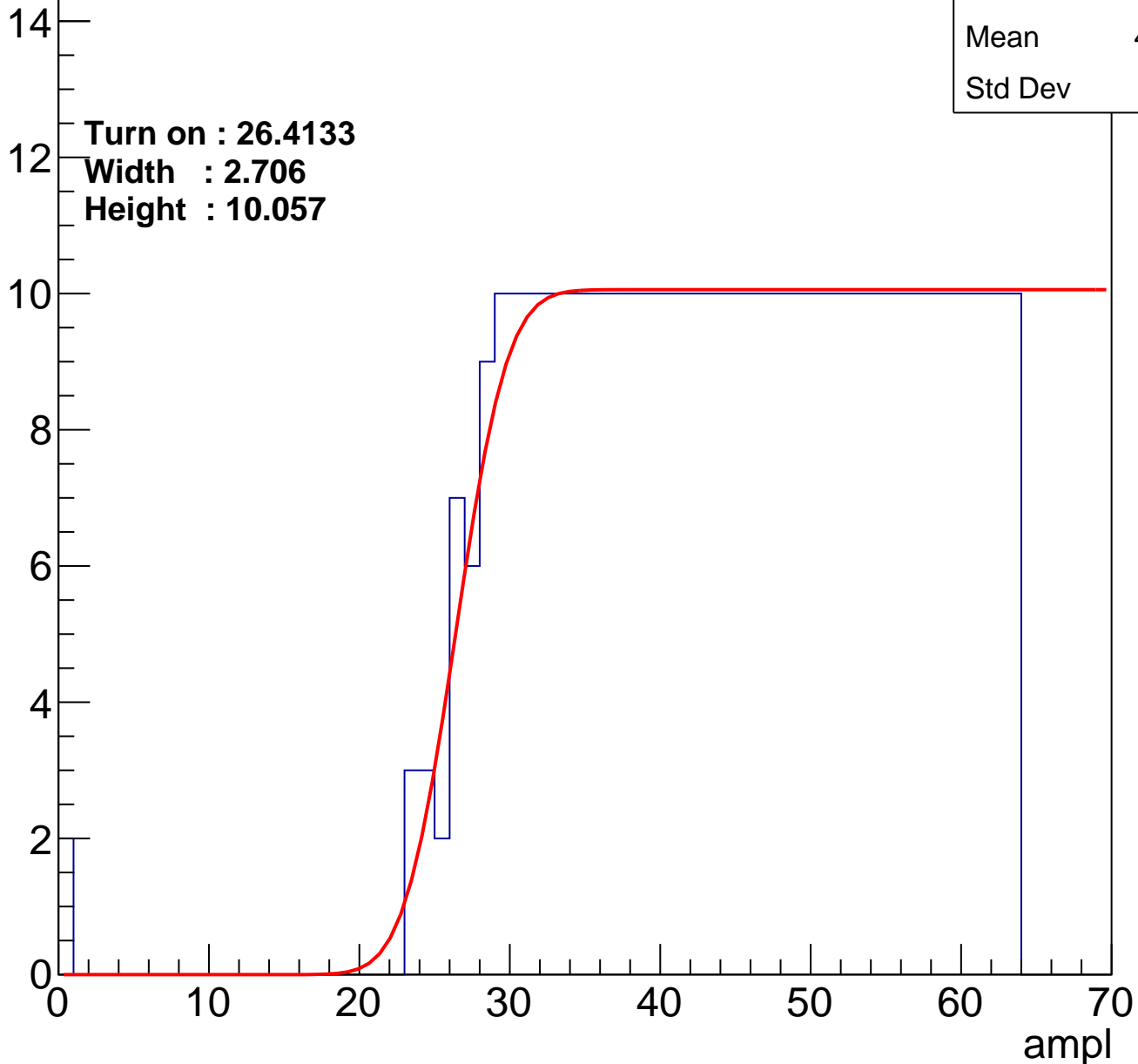
Entries	382
Mean	44.21
Std Dev	11.5

Turn on : 26.4133

Width : 2.706

Height : 10.057

Entry



B1L002S, U19-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.02
Std Dev	11.76

Turn on : 26.2378

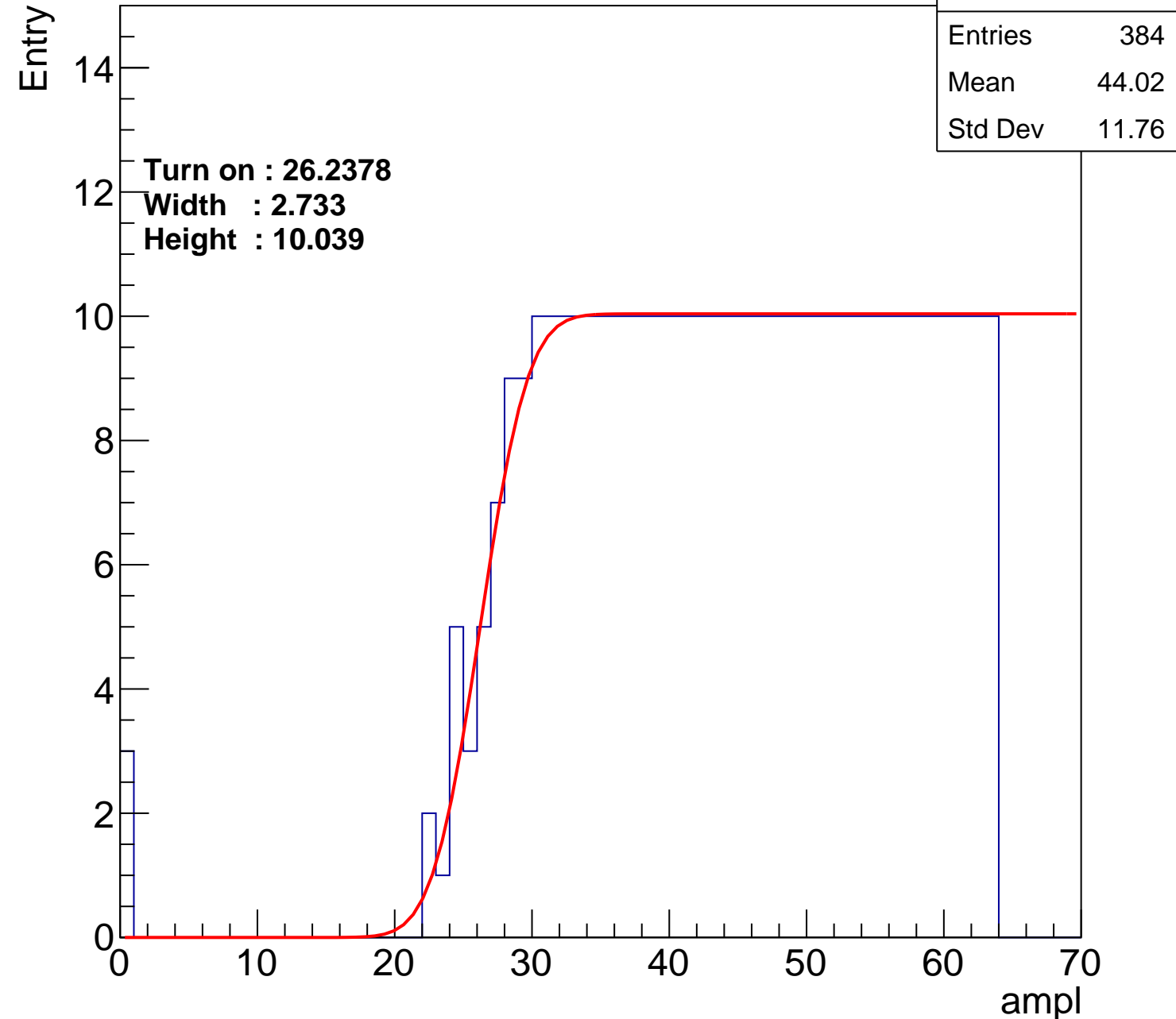
Width : 2.733

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.97
Std Dev	12.04

Turn on : 23.3608

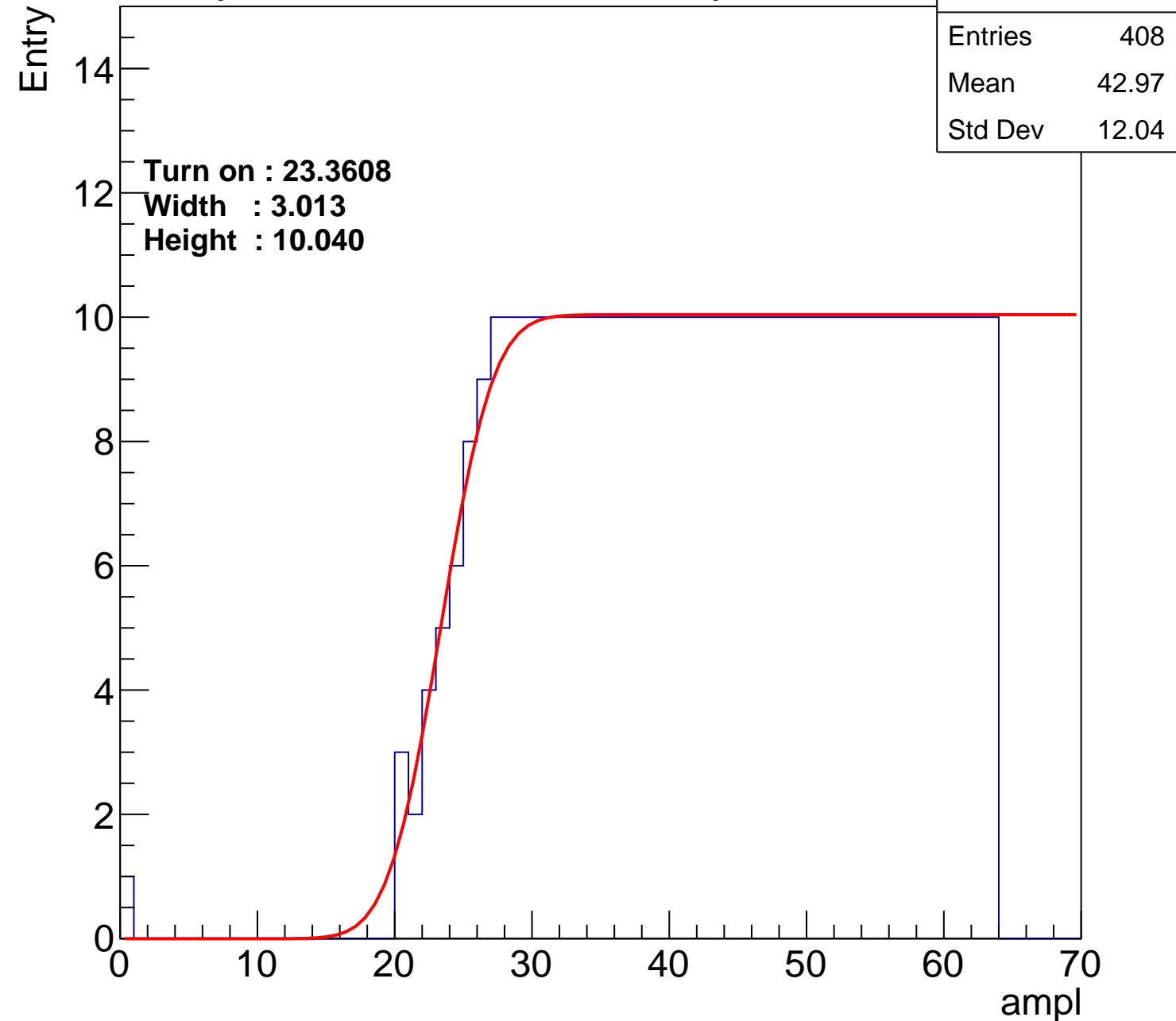
Width : 3.013

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.19
Std Dev	11.34

Turn on : 25.9174

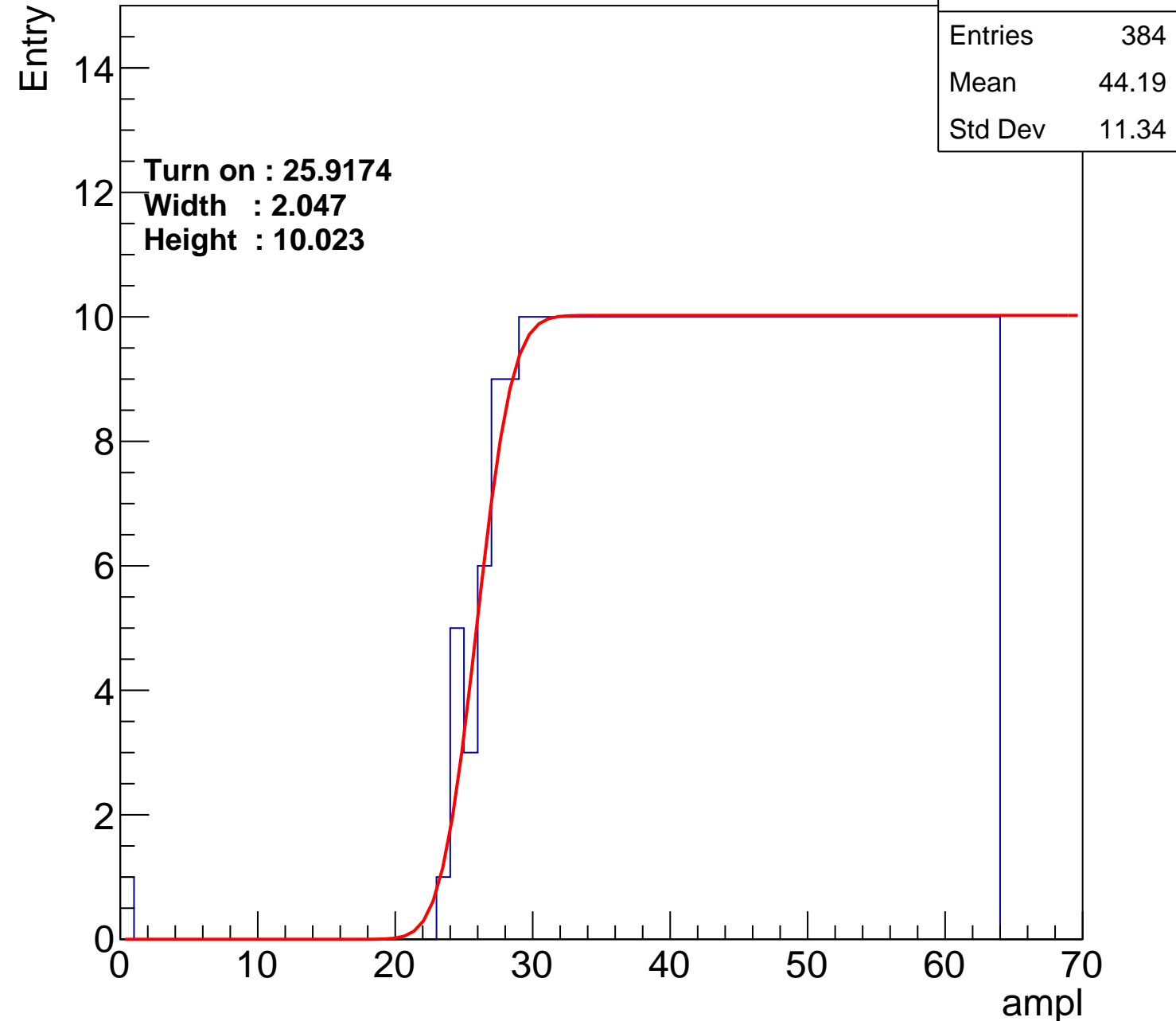
Width : 2.047

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.67

Turn on : 25.4792

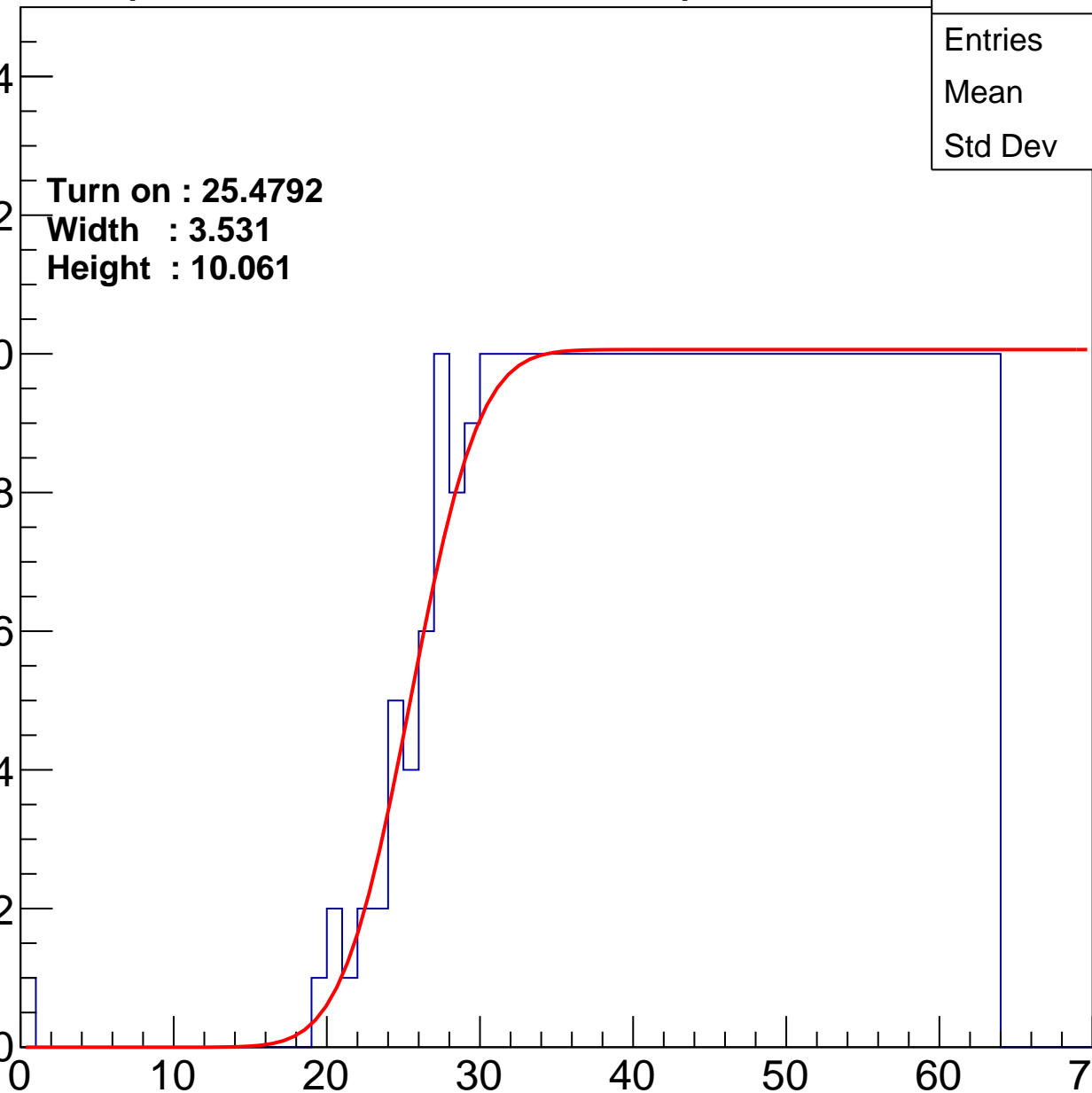
Width : 3.531

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.74
Std Dev	11.24

Turn on : 26.9719

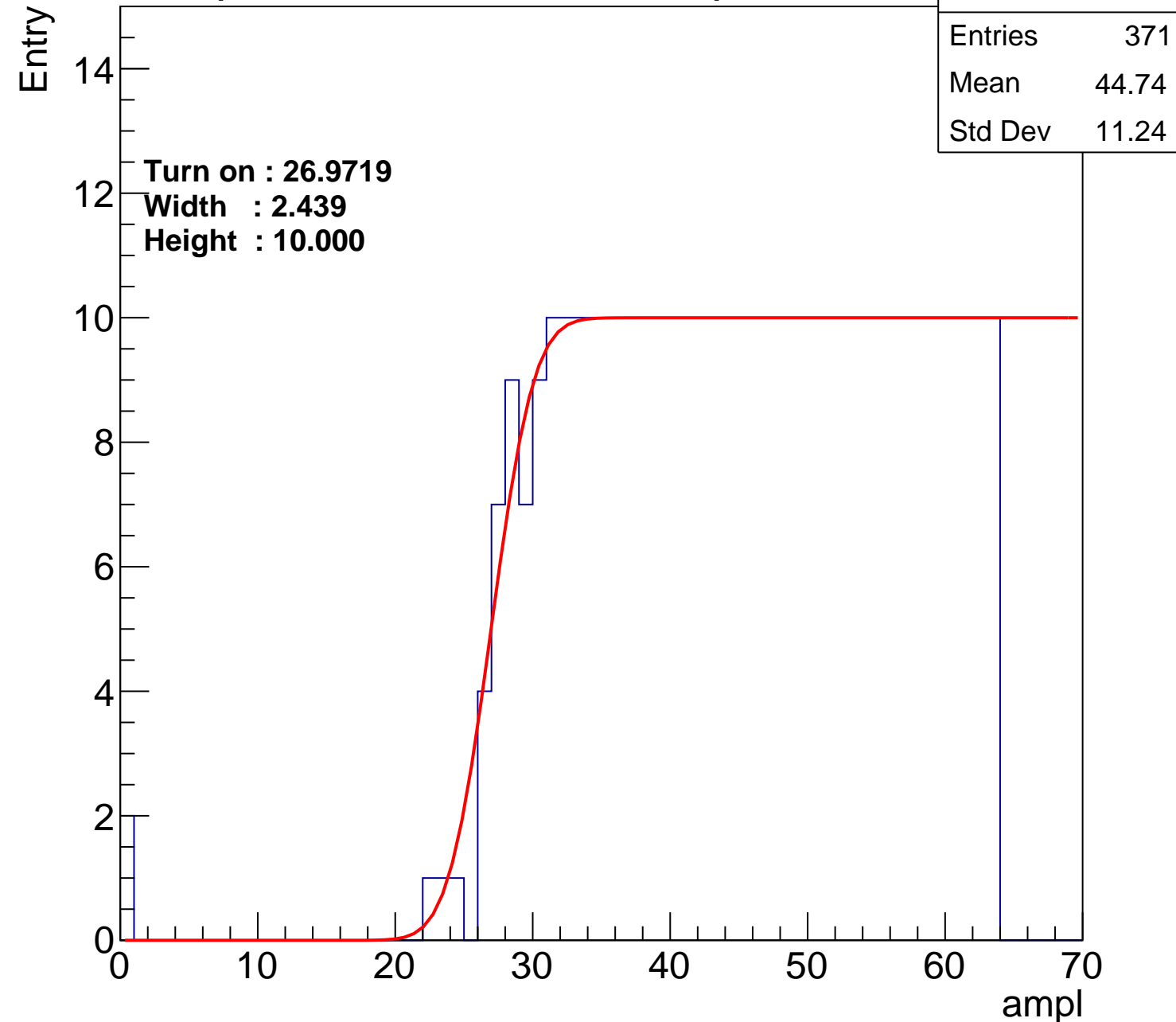
Width : 2.439

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.95
Std Dev	11.47

Turn on : 25.3579

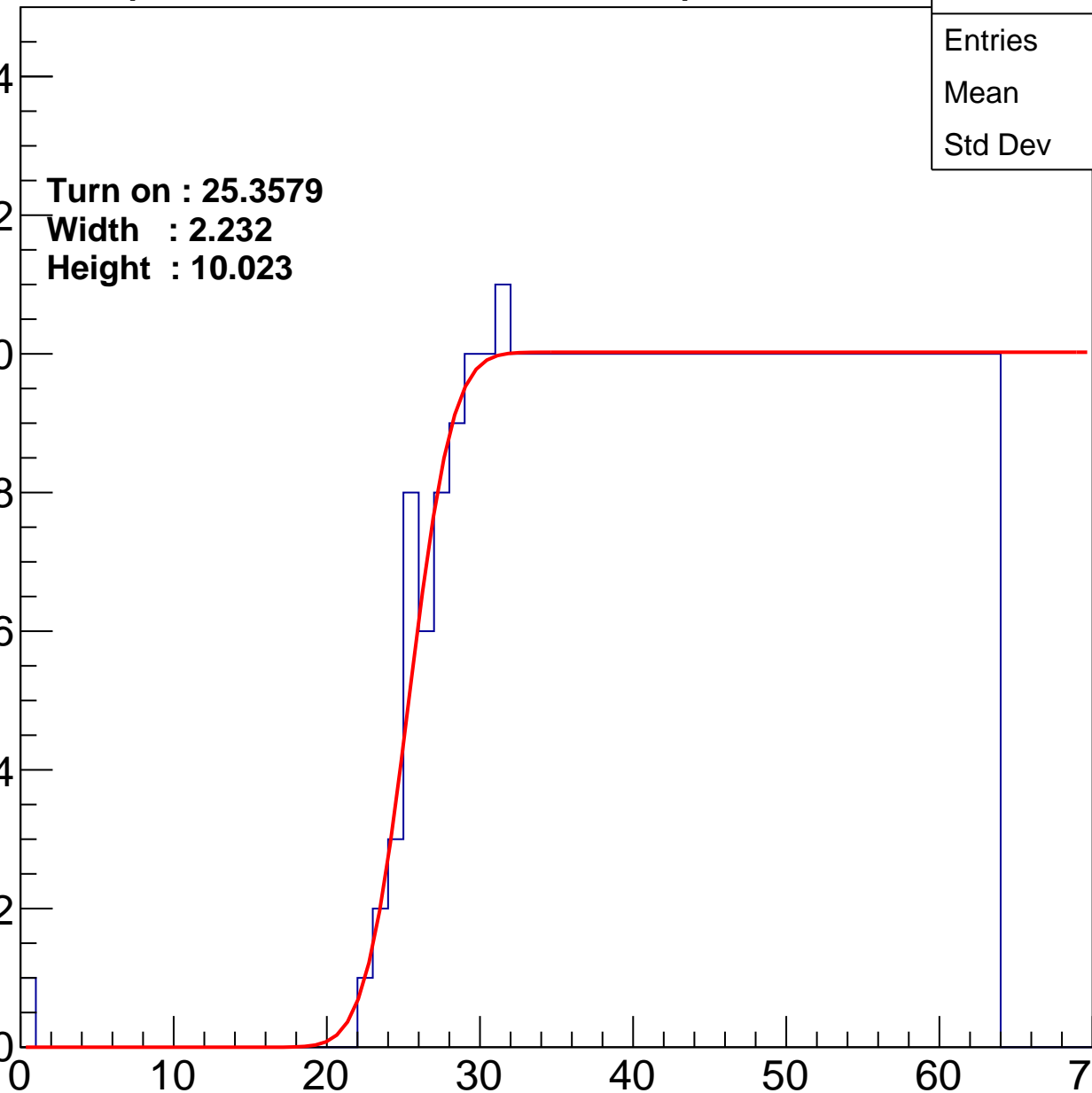
Width : 2.232

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.52
Std Dev	12.26

Turn on : 25.4197

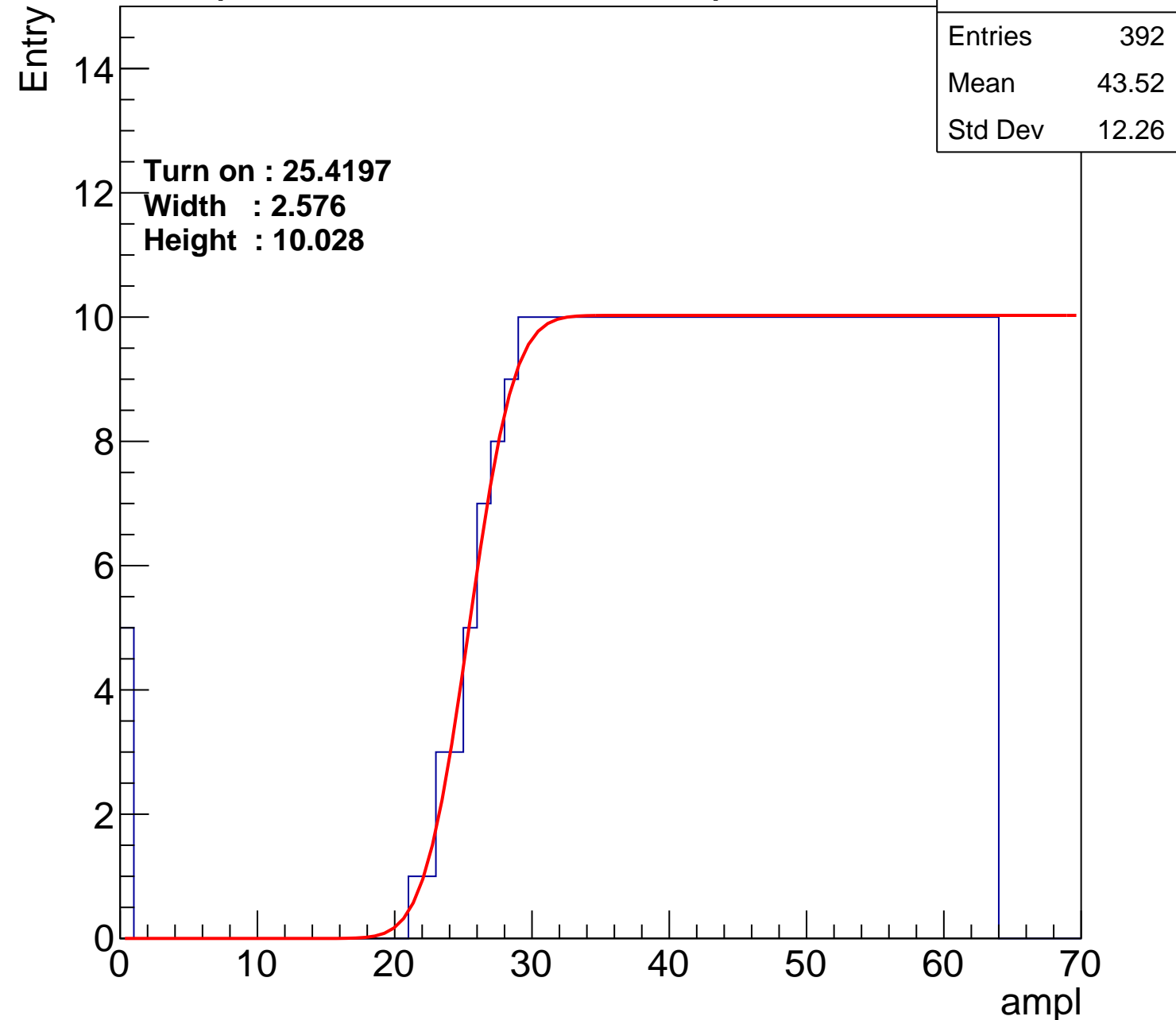
Width : 2.576

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch44

calib_packv5_042523_0143.root, FC#10, port B3

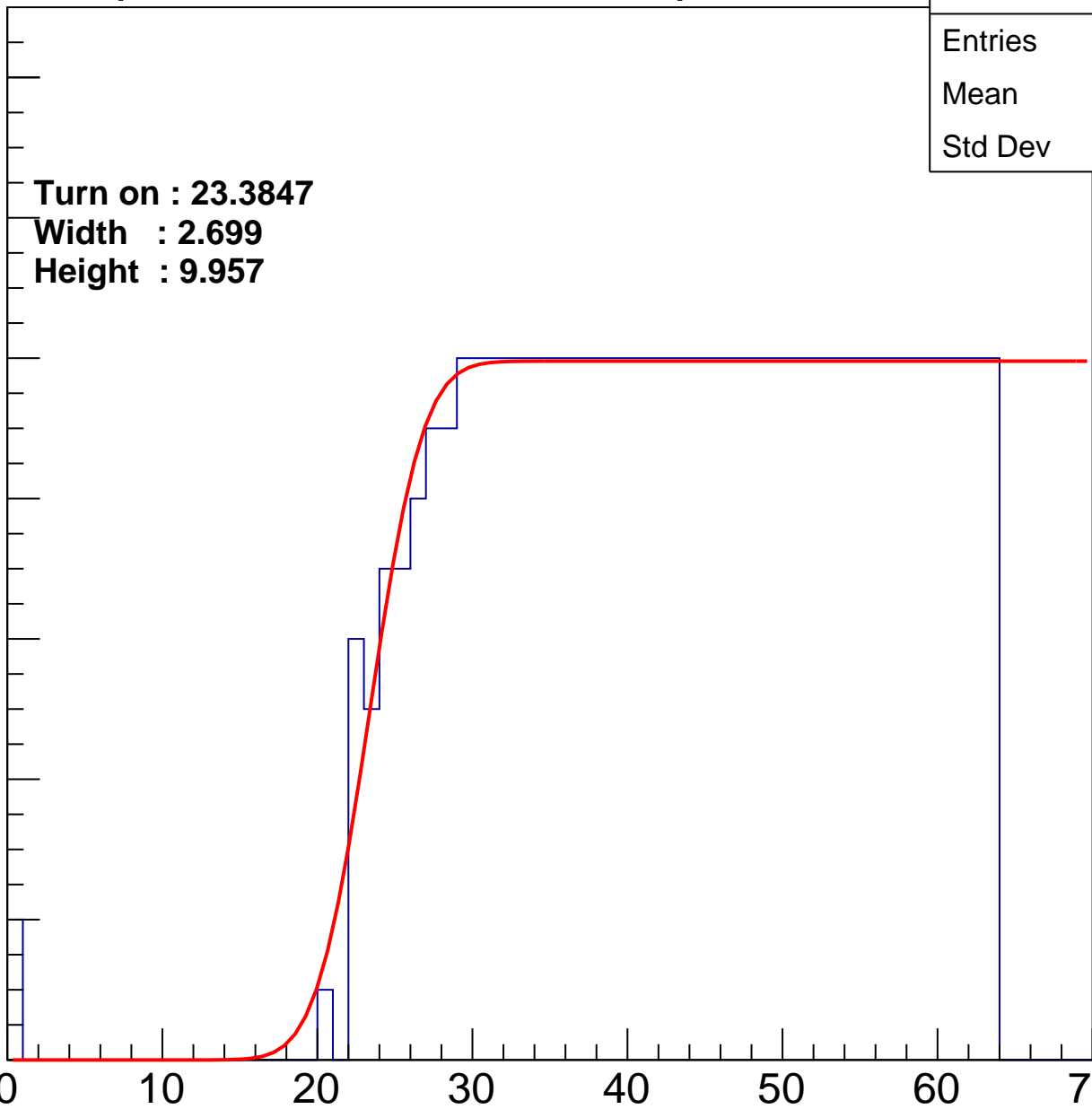
Entry

14
12
10
8
6
4
2
0

Turn on : 23.3847
Width : 2.699
Height : 9.957

Entries	404
Mean	43.1
Std Dev	12.1

ampl



B1L002S, U19-ch45

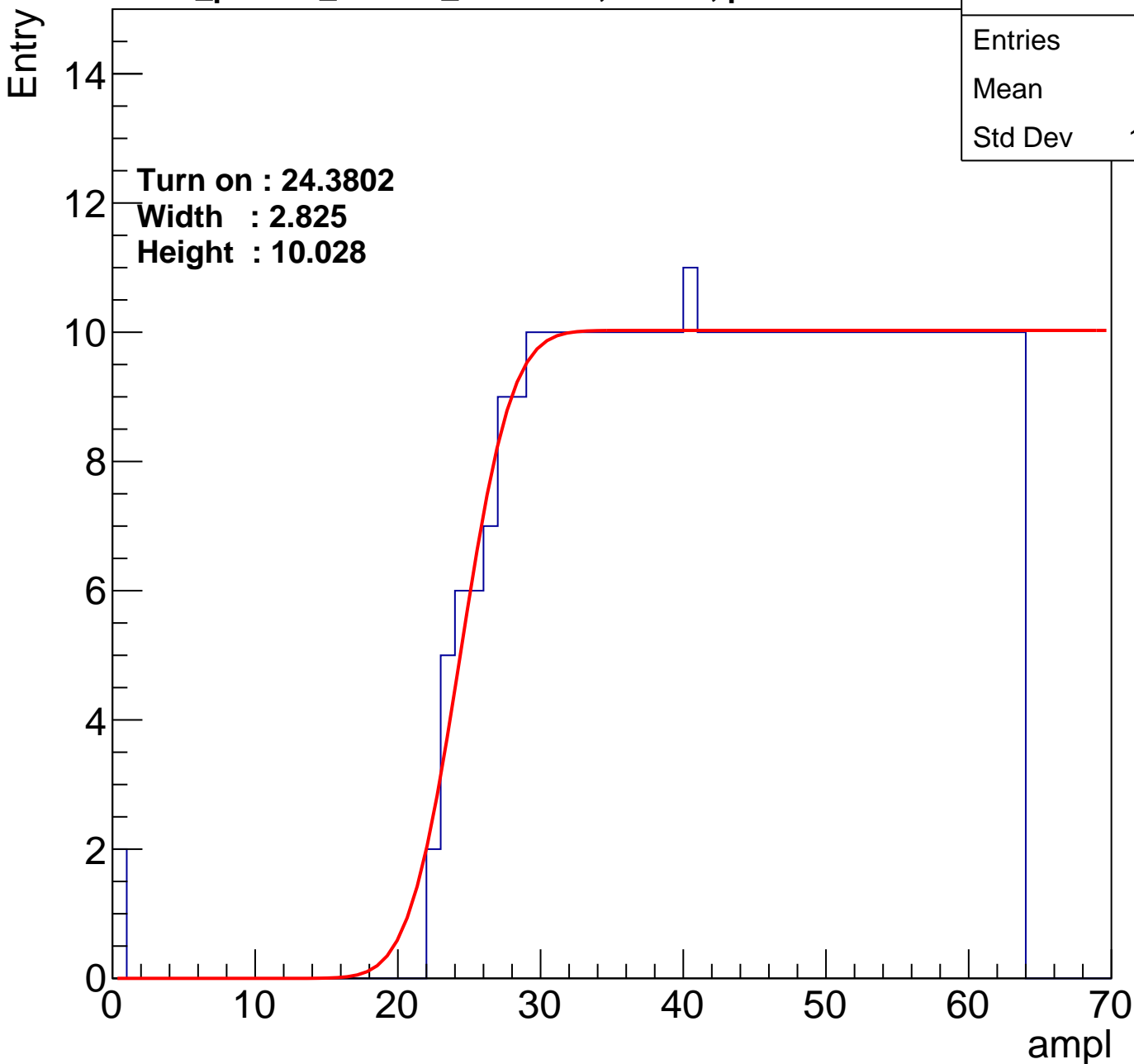
calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.86

Turn on : 24.3802

Width : 2.825

Height : 10.028



B1L002S, U19-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.6789

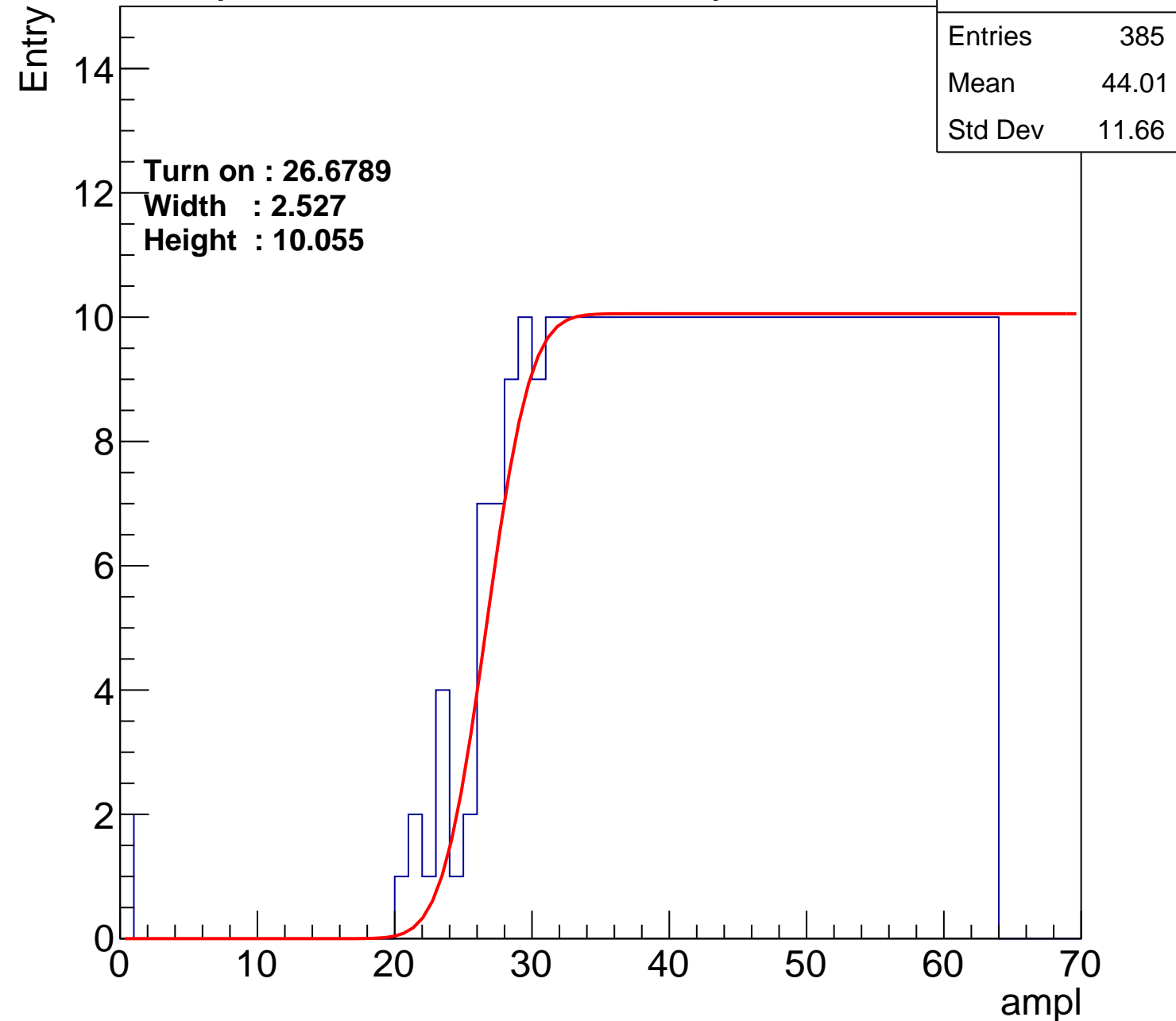
Width : 2.527

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.85
Std Dev	11.71

Turn on : 25.5265

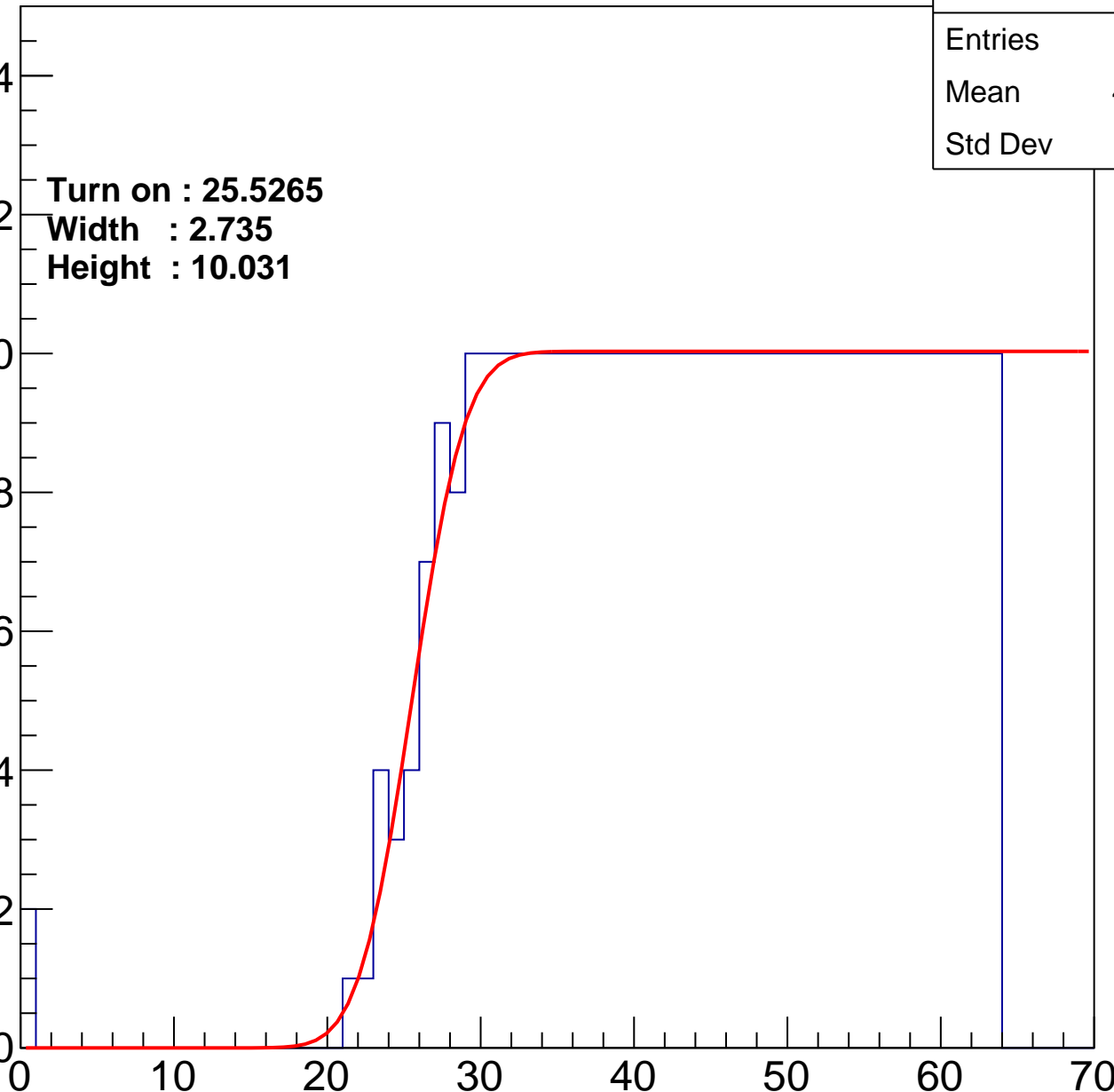
Width : 2.735

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch48

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.03
Std Dev	11.45

Turn on : 25.6404

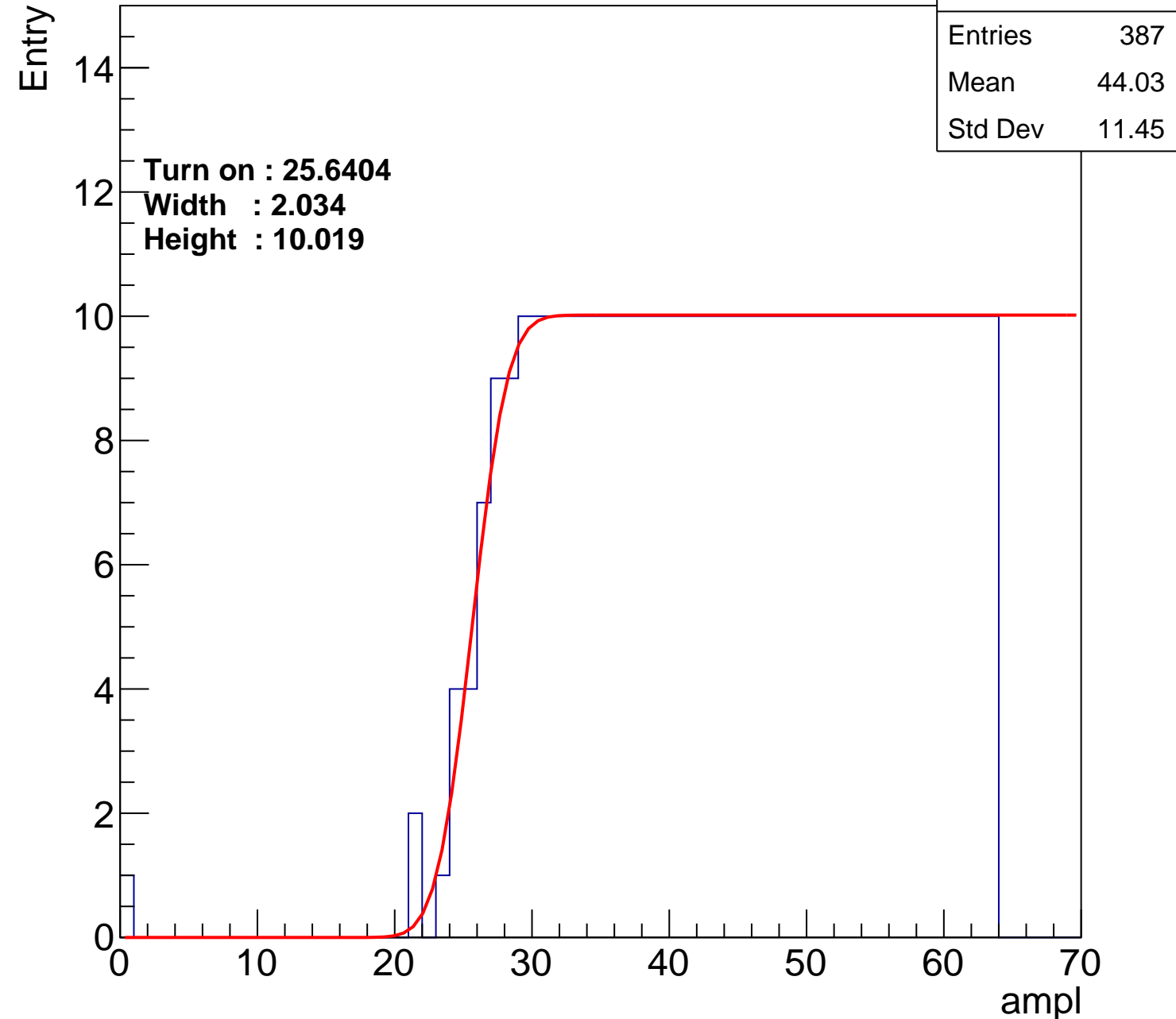
Width : 2.034

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.37
Std Dev	11.26

Turn on : 26.2927

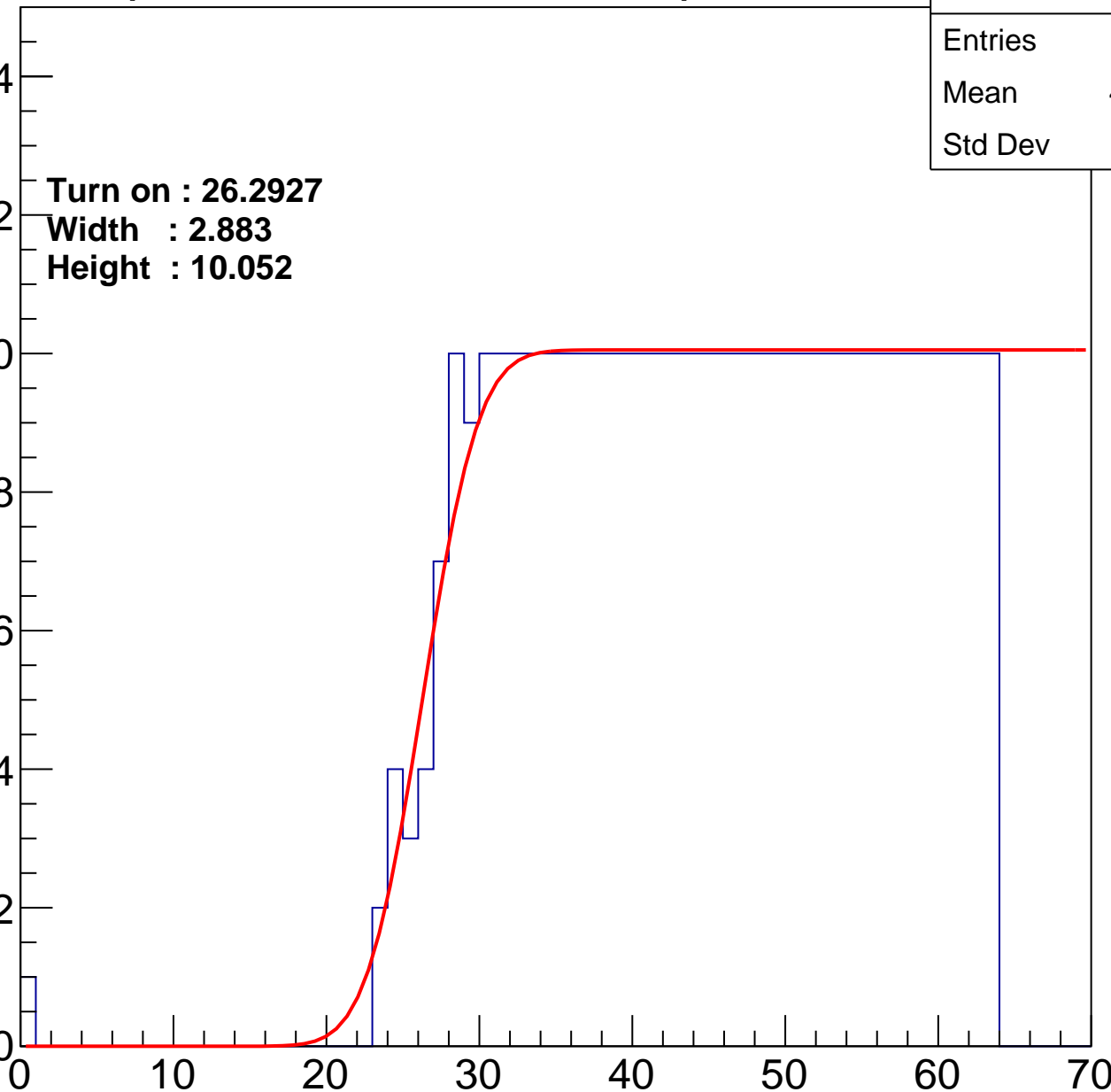
Width : 2.883

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.61
Std Dev	11.71

Turn on : 24.6967

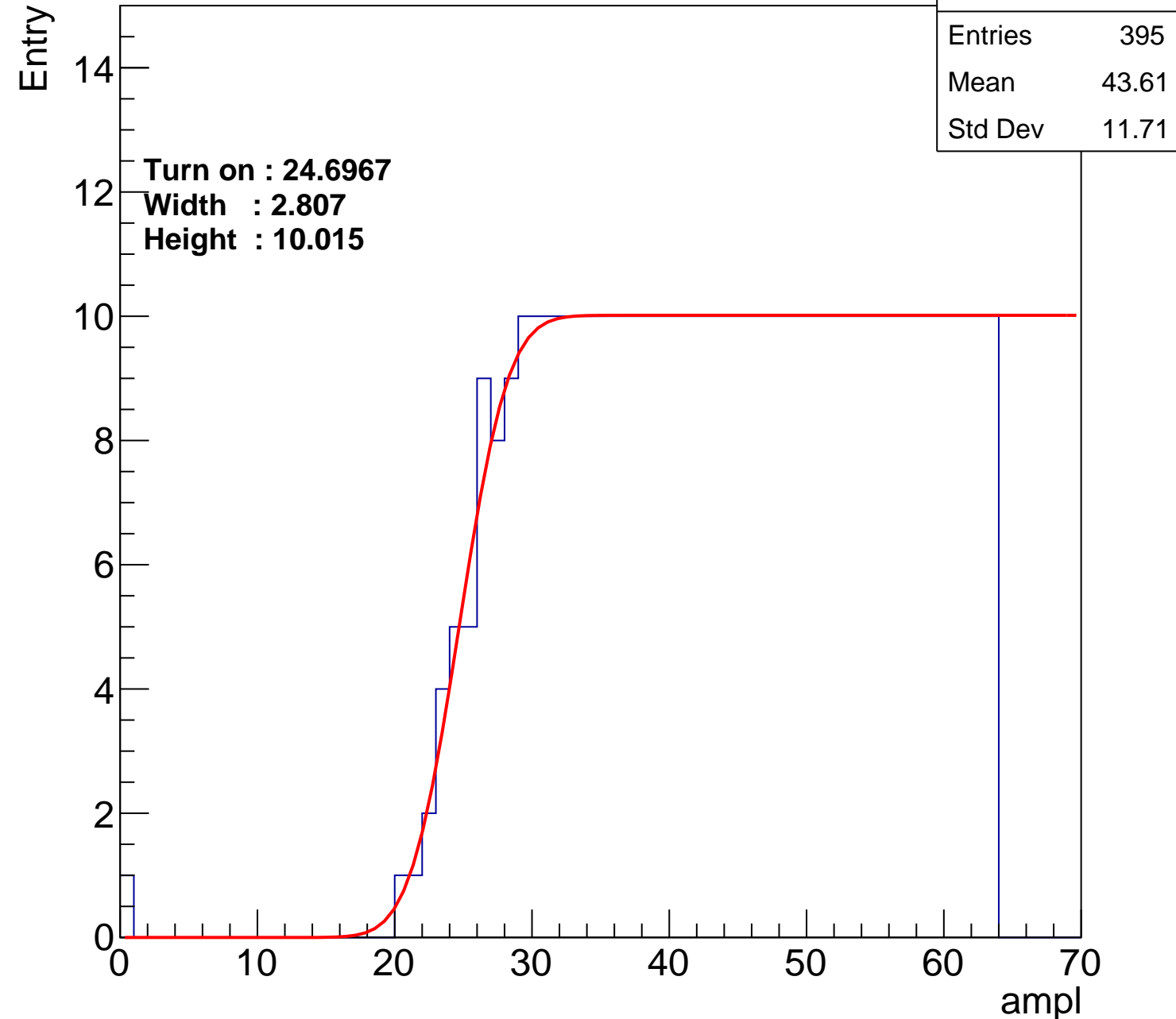
Width : 2.807

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch51

calib_packv5_042523_0143.root, FC#10, port B3

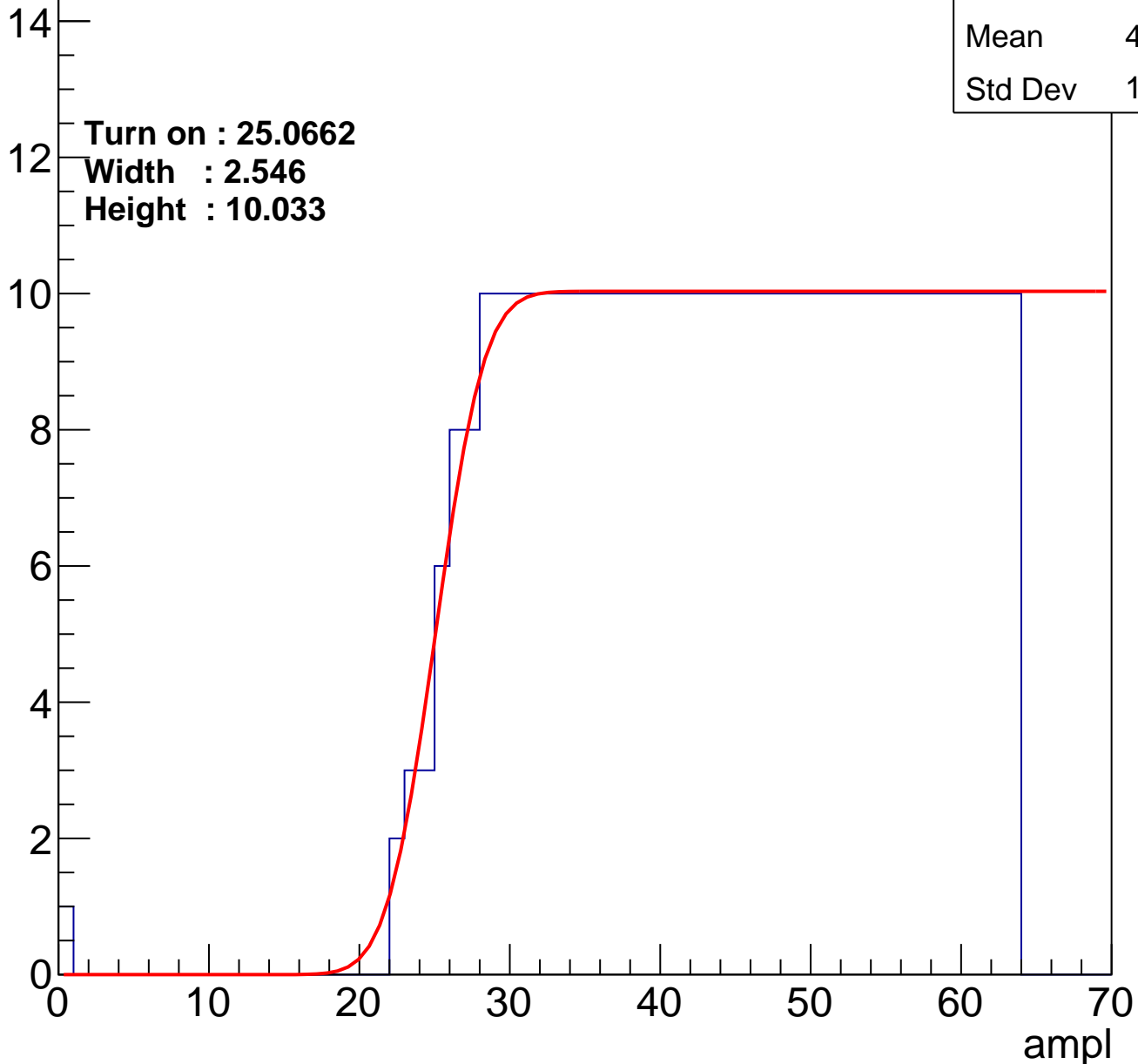
Entries	391
Mean	43.83
Std Dev	11.55

Turn on : 25.0662

Width : 2.546

Height : 10.033

Entry



B1L002S, U19-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.3185

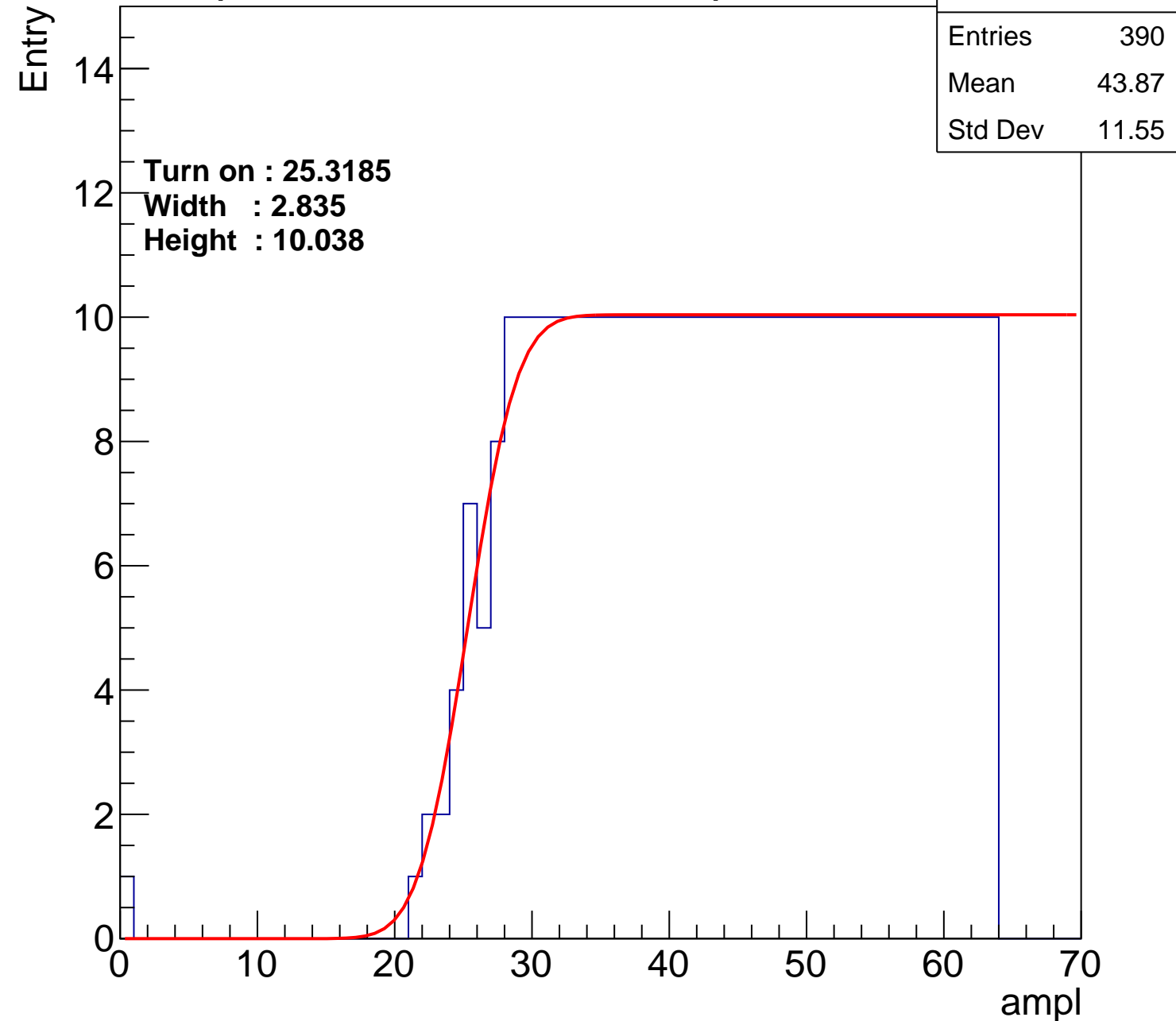
Width : 2.835

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 26.6213

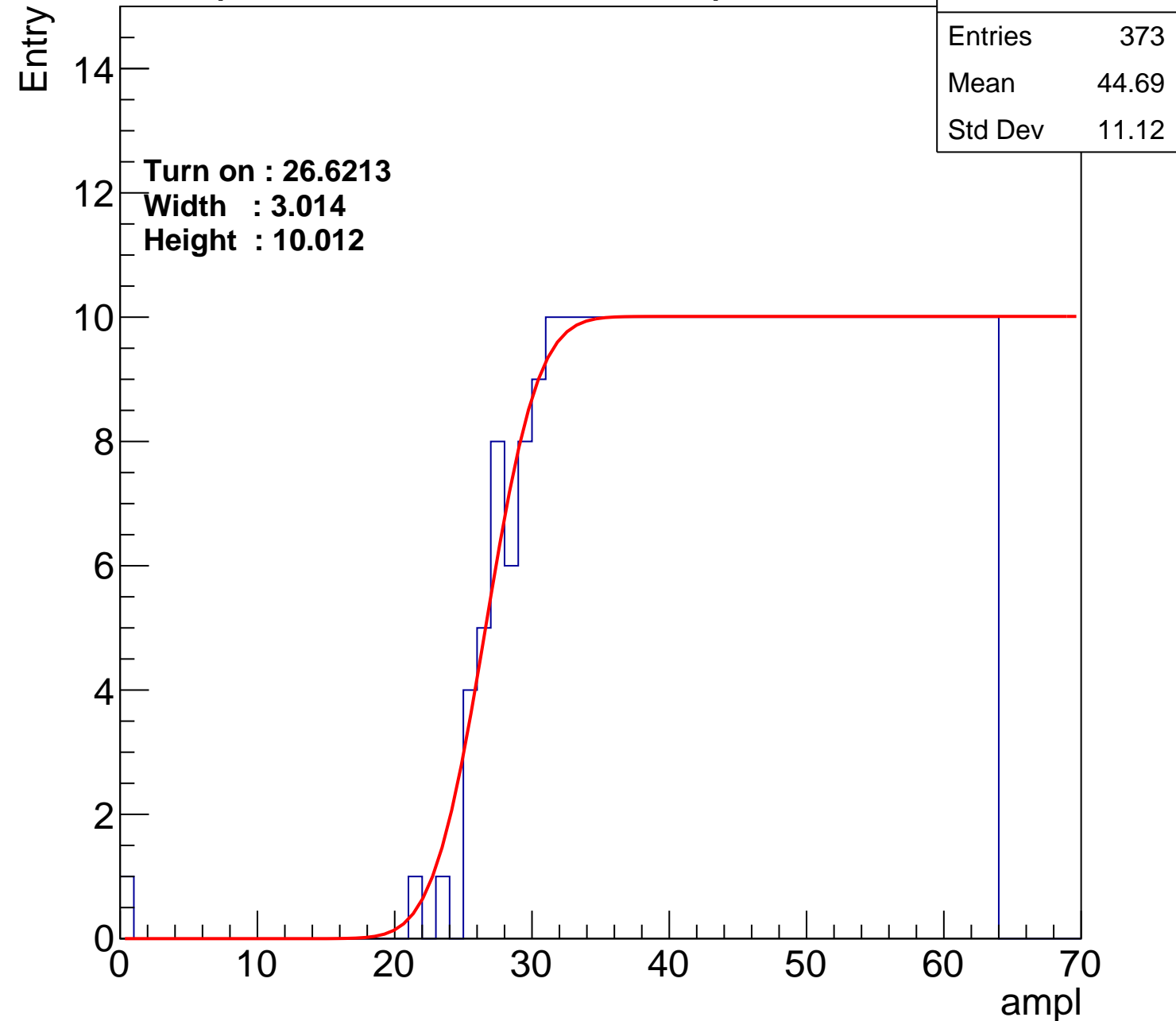
Width : 3.014

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.26
Std Dev	11.3

Turn on : 26.1153

Width : 1.649

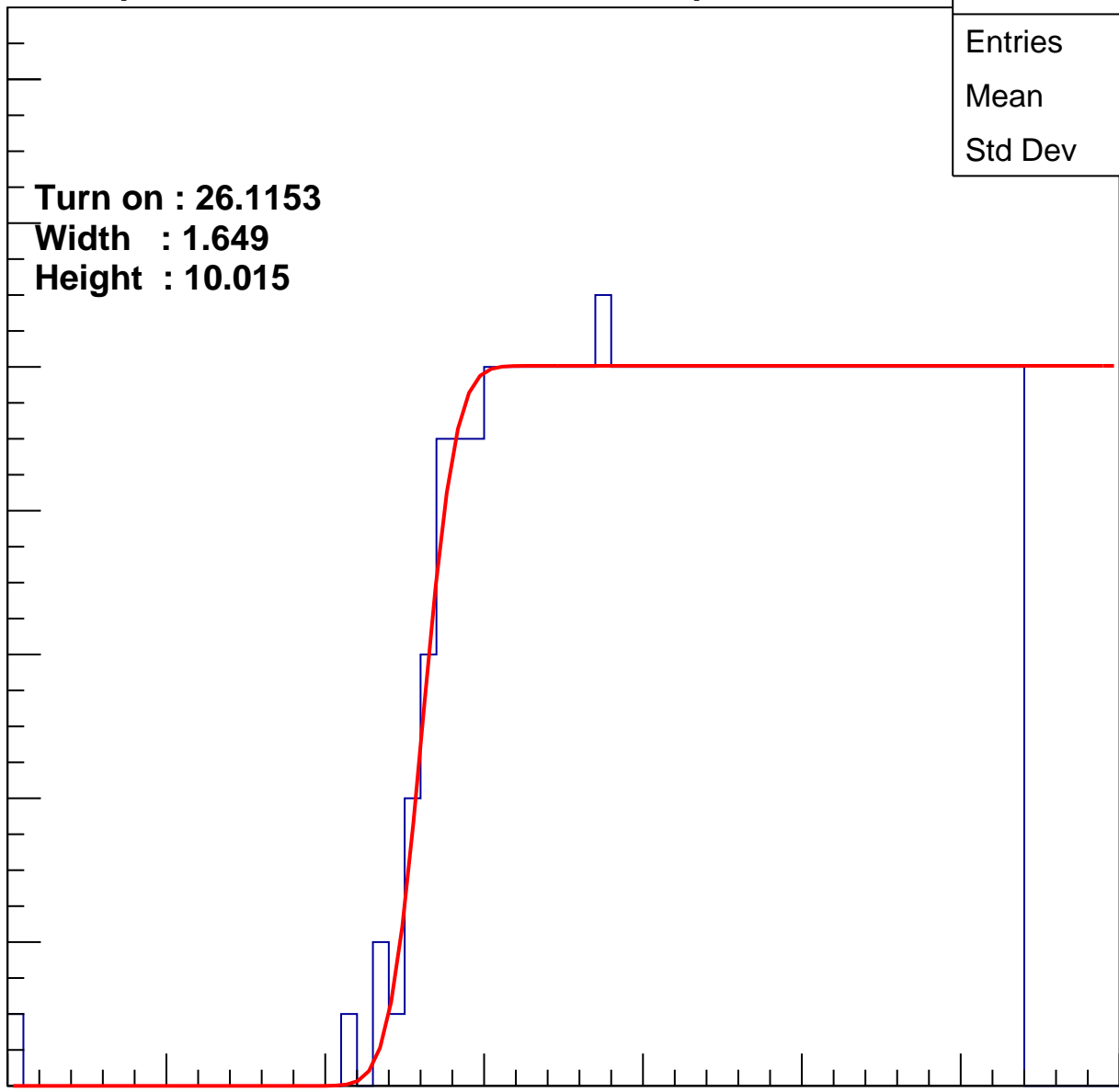
Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U19-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.72
Std Dev	11.63

Turn on : 24.7750

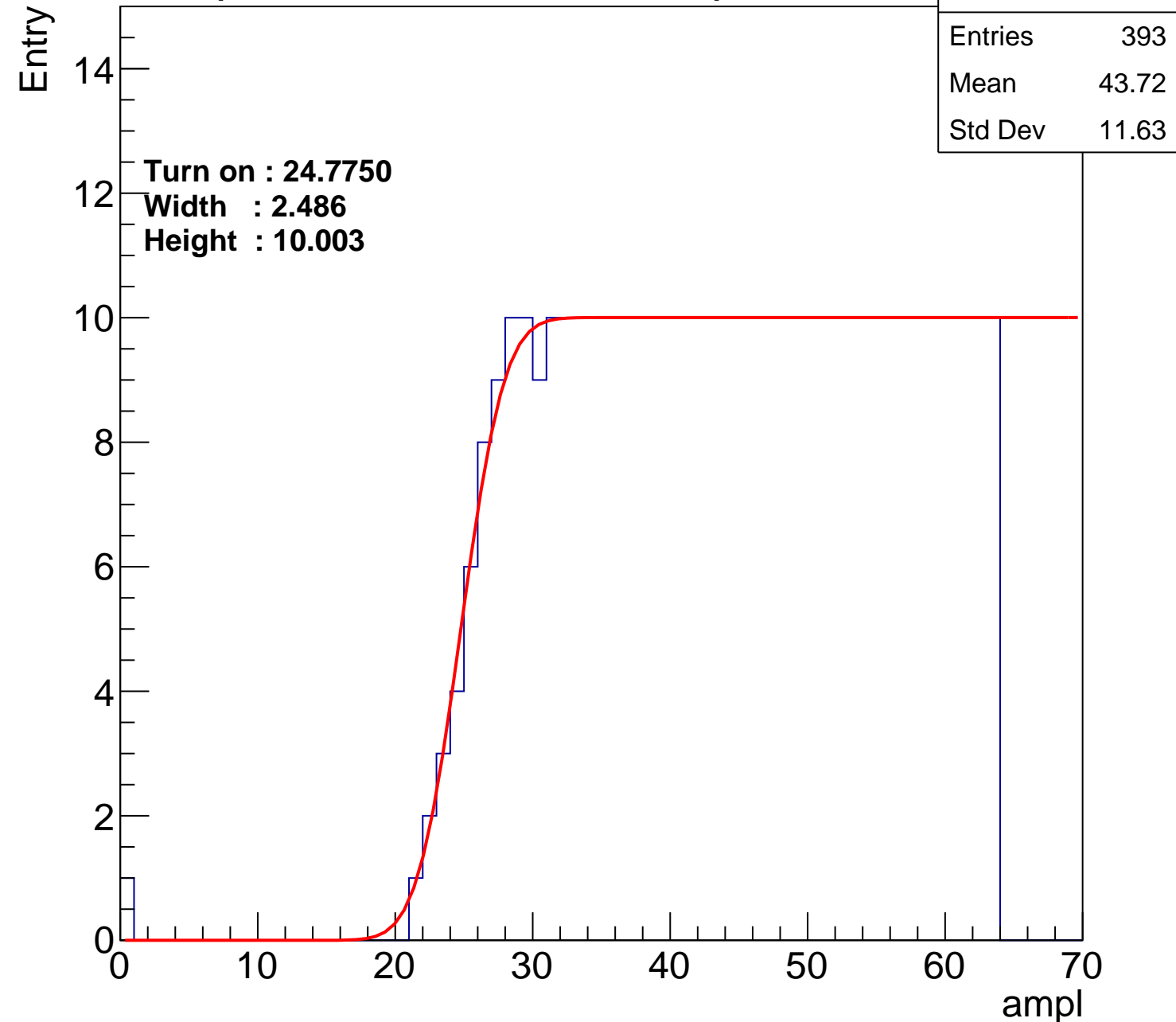
Width : 2.486

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch56

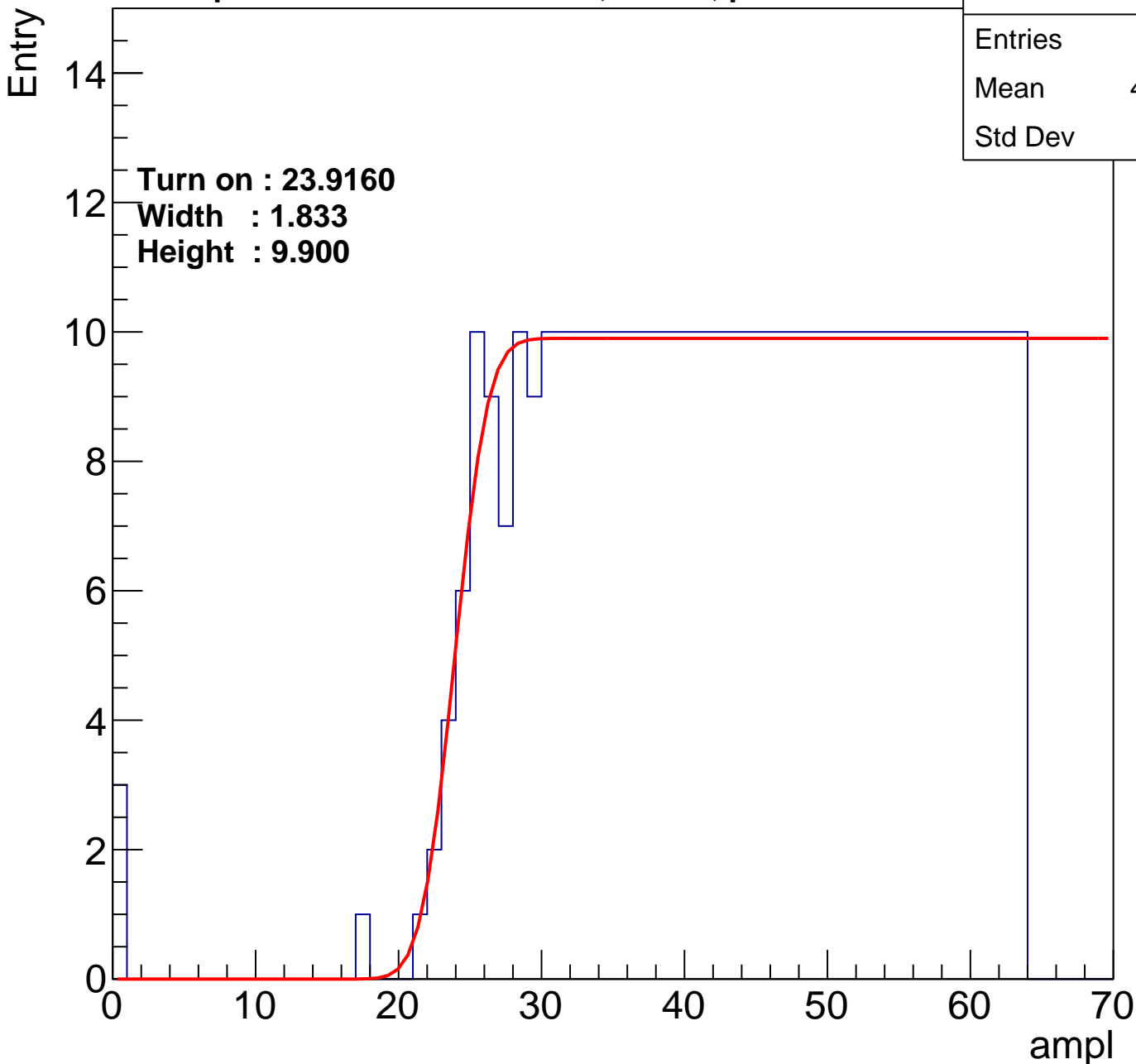
calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.14
Std Dev	12.21

Turn on : 23.9160

Width : 1.833

Height : 9.900



B1L002S, U19-ch57

calib_packv5_042523_0143.root, FC#10, port B3

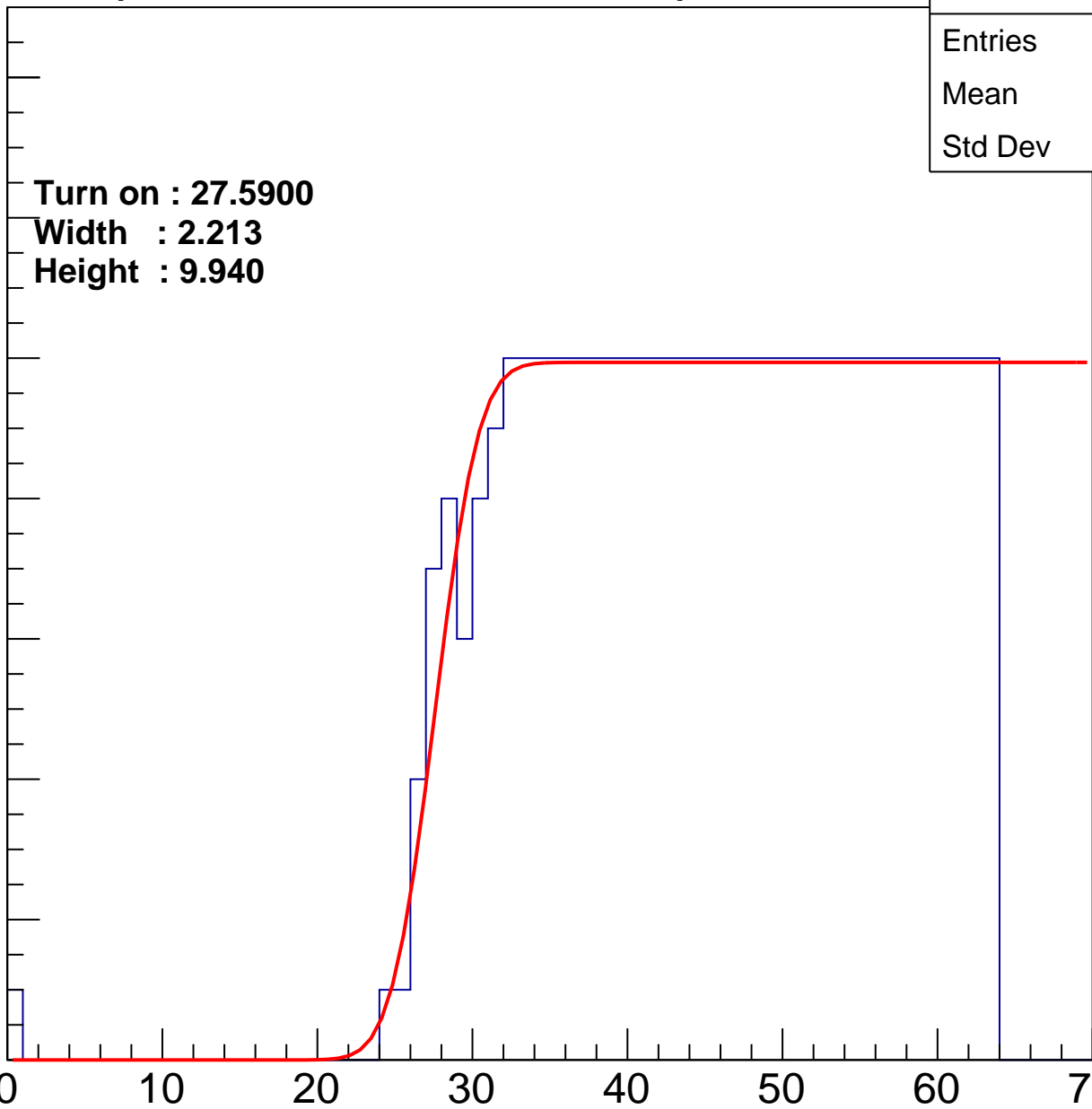
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5900
Width : 2.213
Height : 9.940

Entries	365
Mean	45.09
Std Dev	10.89

ampl



B1L002S, U19-ch58

calib_packv5_042523_0143.root, FC#10, port B3

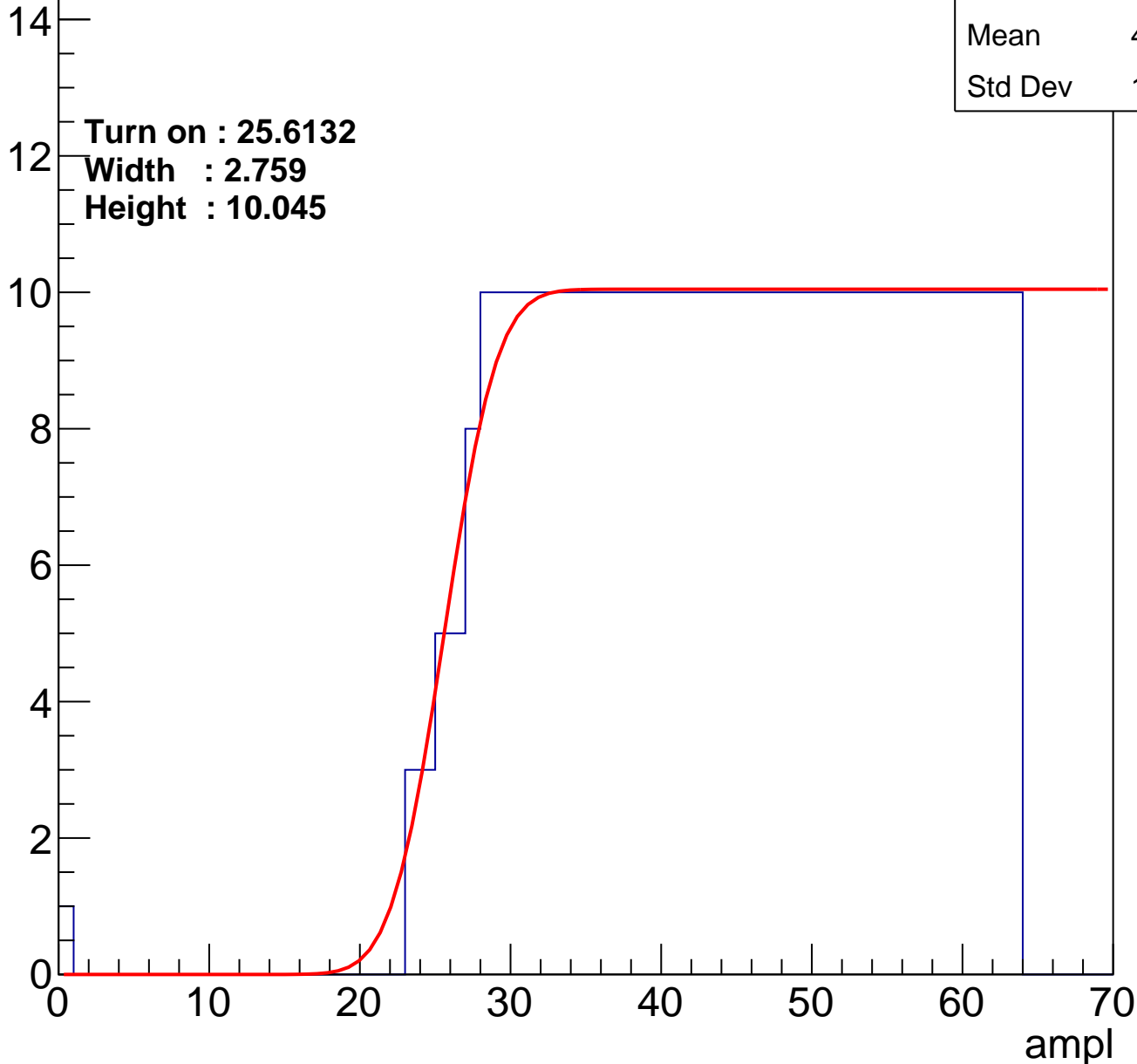
Entries	385
Mean	44.14
Std Dev	11.38

Turn on : 25.6132

Width : 2.759

Height : 10.045

Entry



B1L002S, U19-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.6567

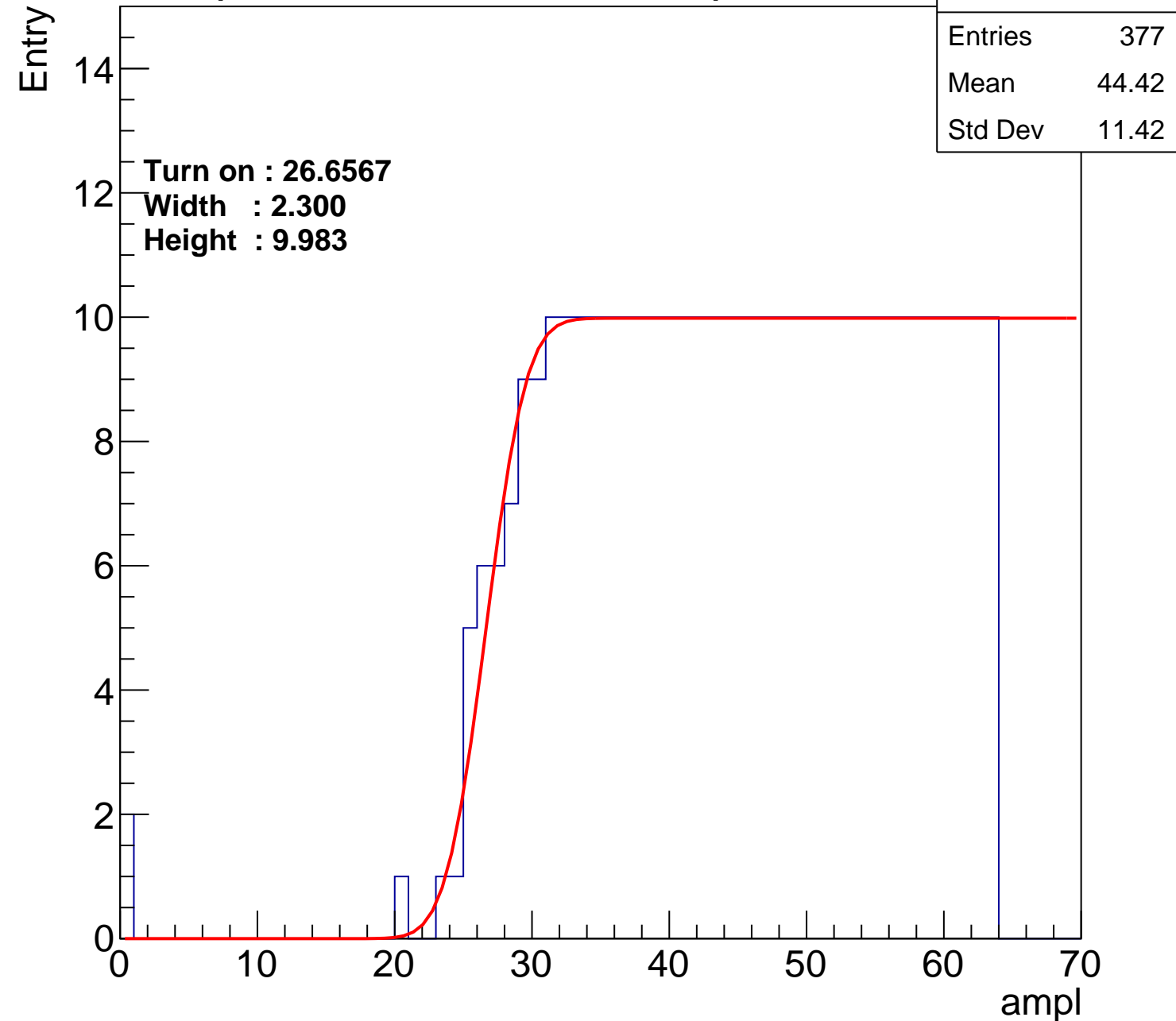
Width : 2.300

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.2901

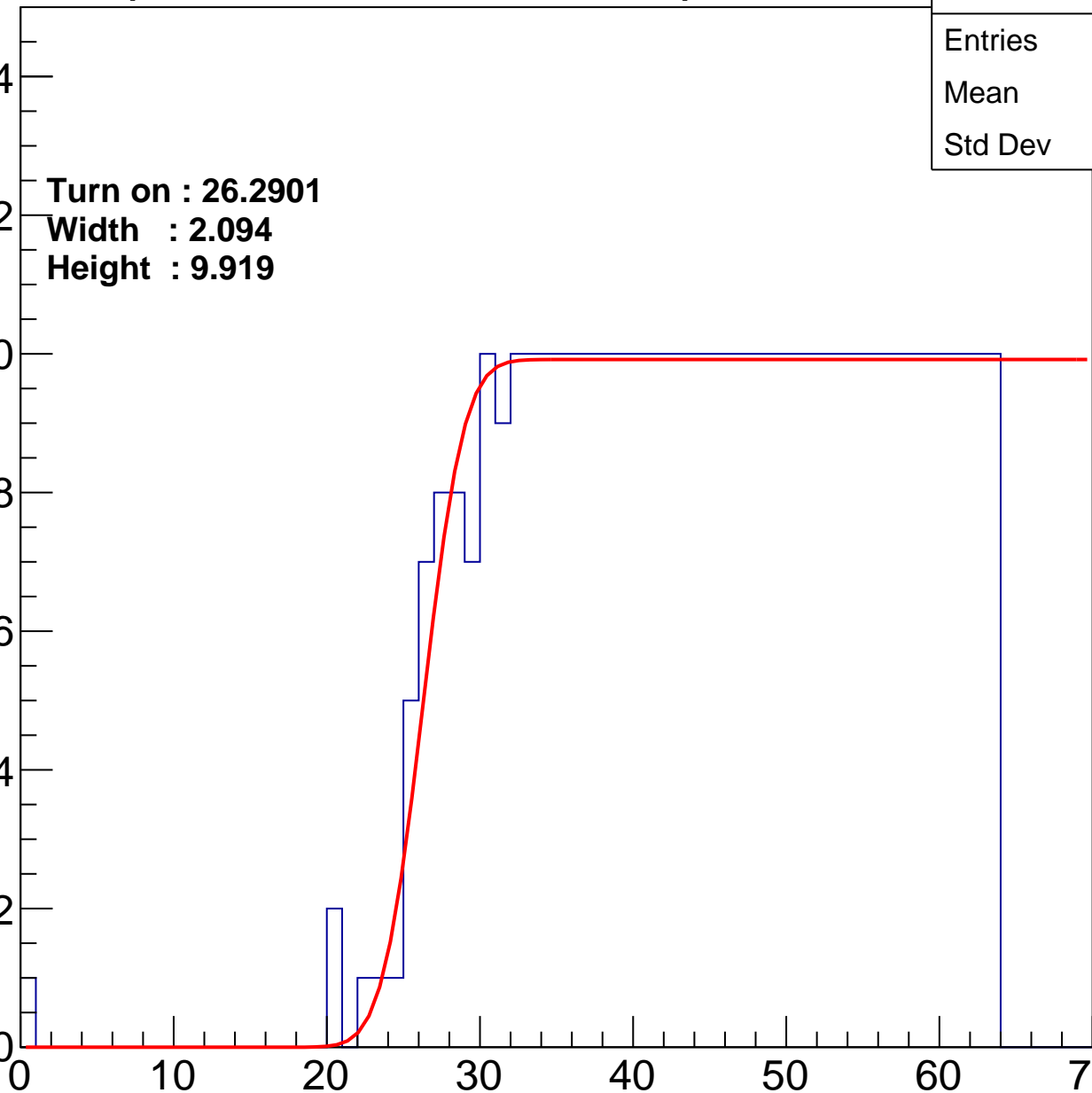
Width : 2.094

Height : 9.919

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.43
Std Dev	12.18

Turn on : 25.1810

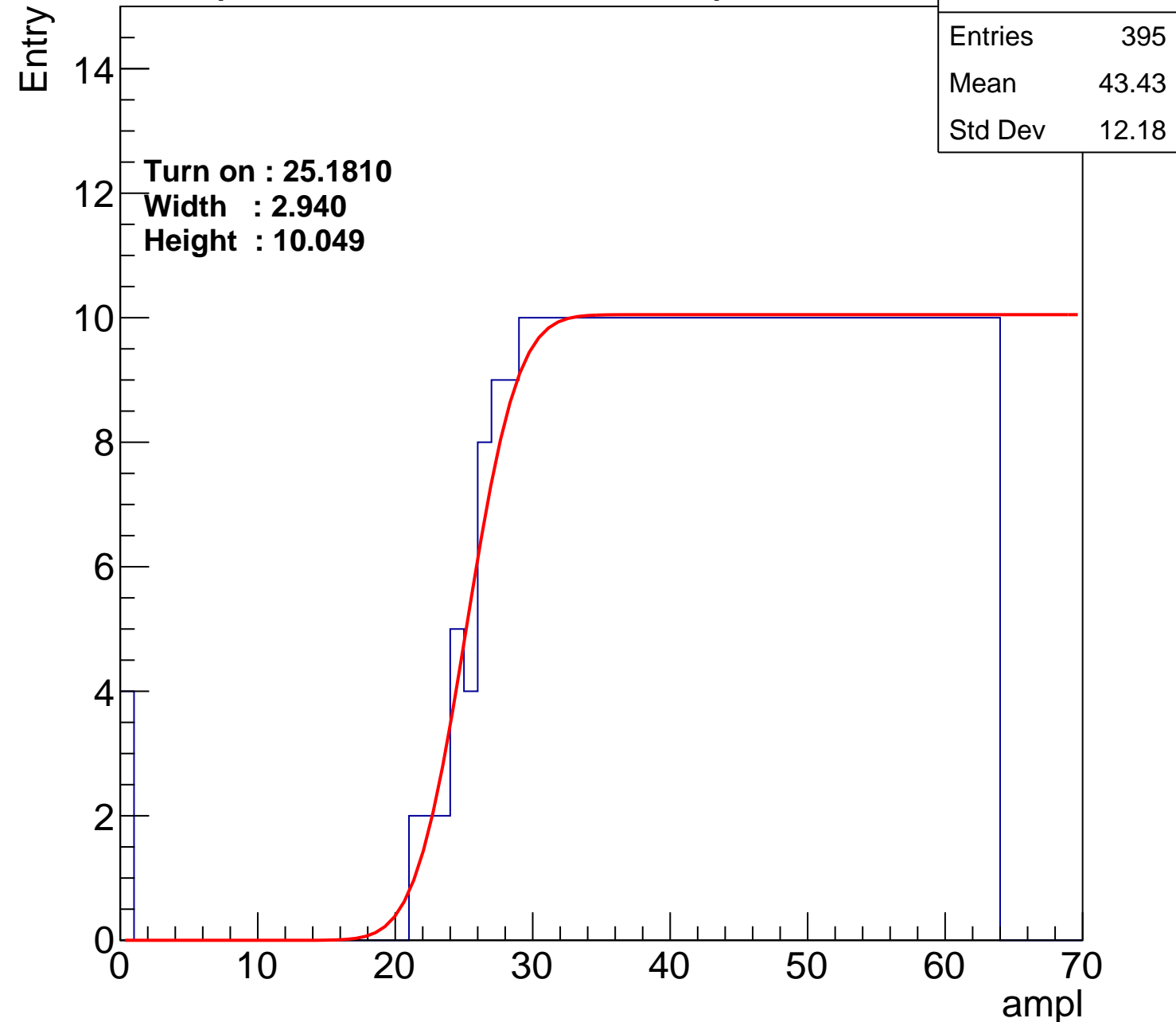
Width : 2.940

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch62

calib_packv5_042523_0143.root, FC#10, port B3

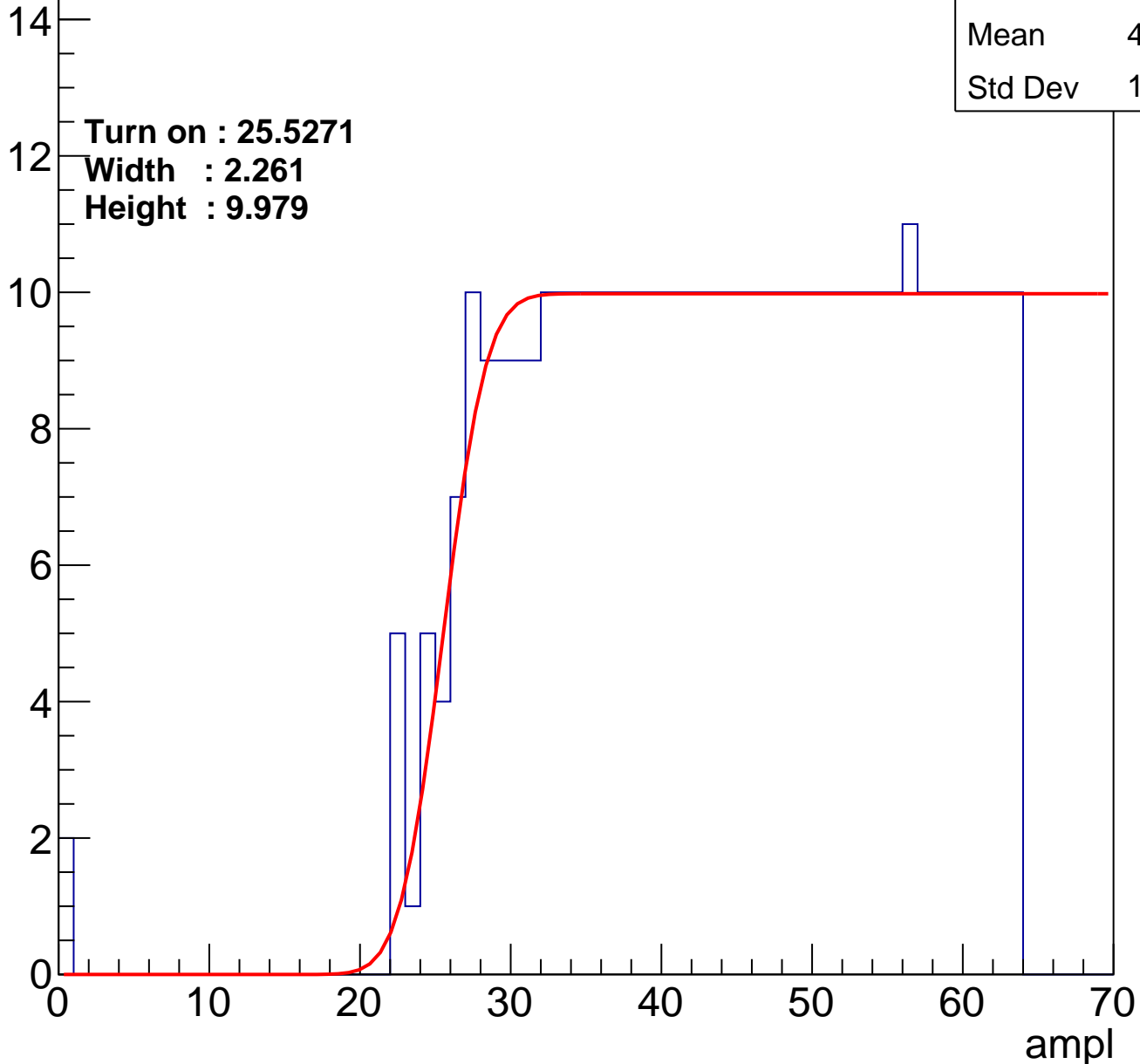
Entries	391
Mean	43.79
Std Dev	11.78

Turn on : 25.5271

Width : 2.261

Height : 9.979

Entry



B1L002S, U19-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.38
Std Dev	11.25

Turn on : 25.8337

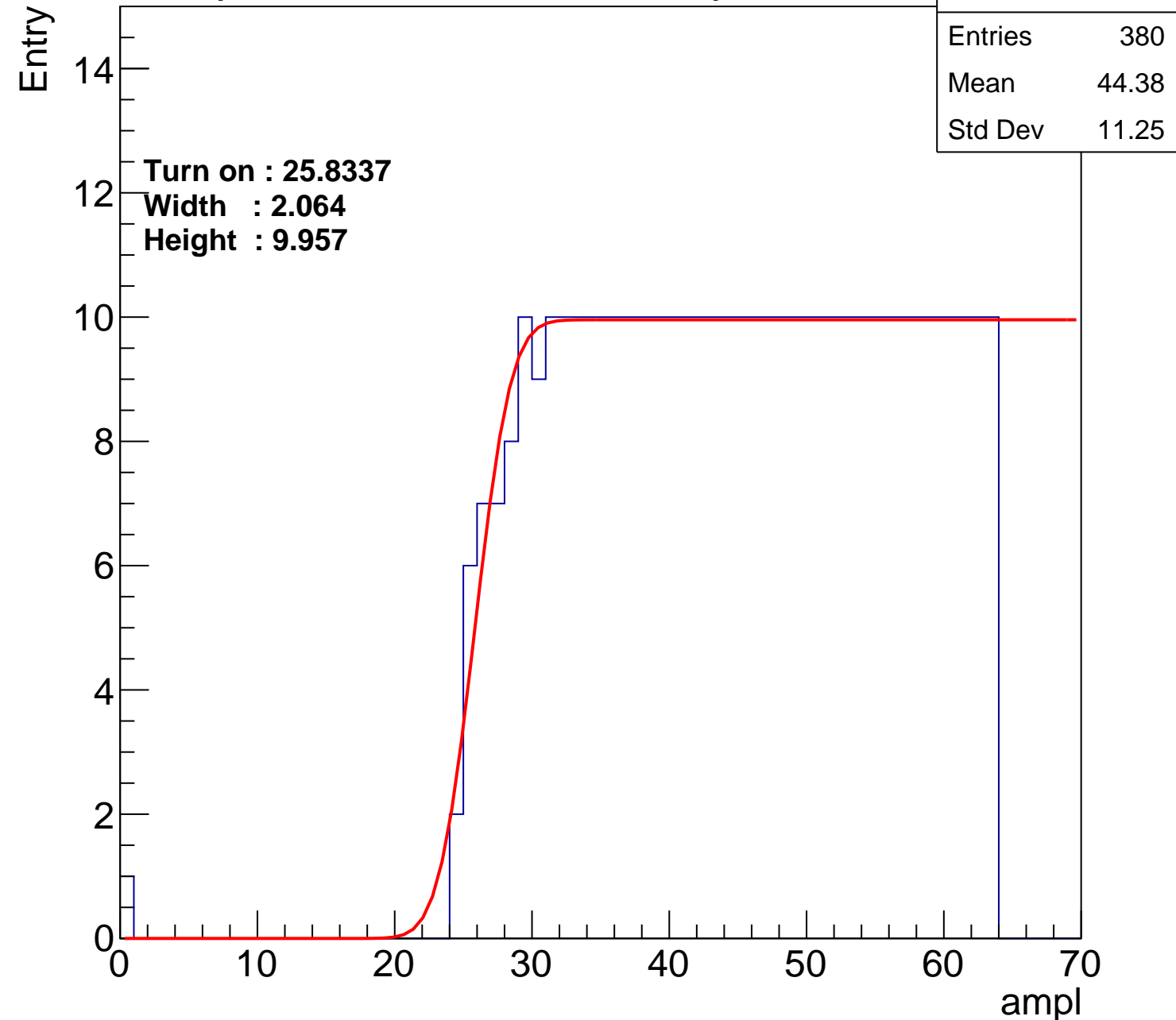
Width : 2.064

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.44
Std Dev	11.94

Turn on : 24.7669

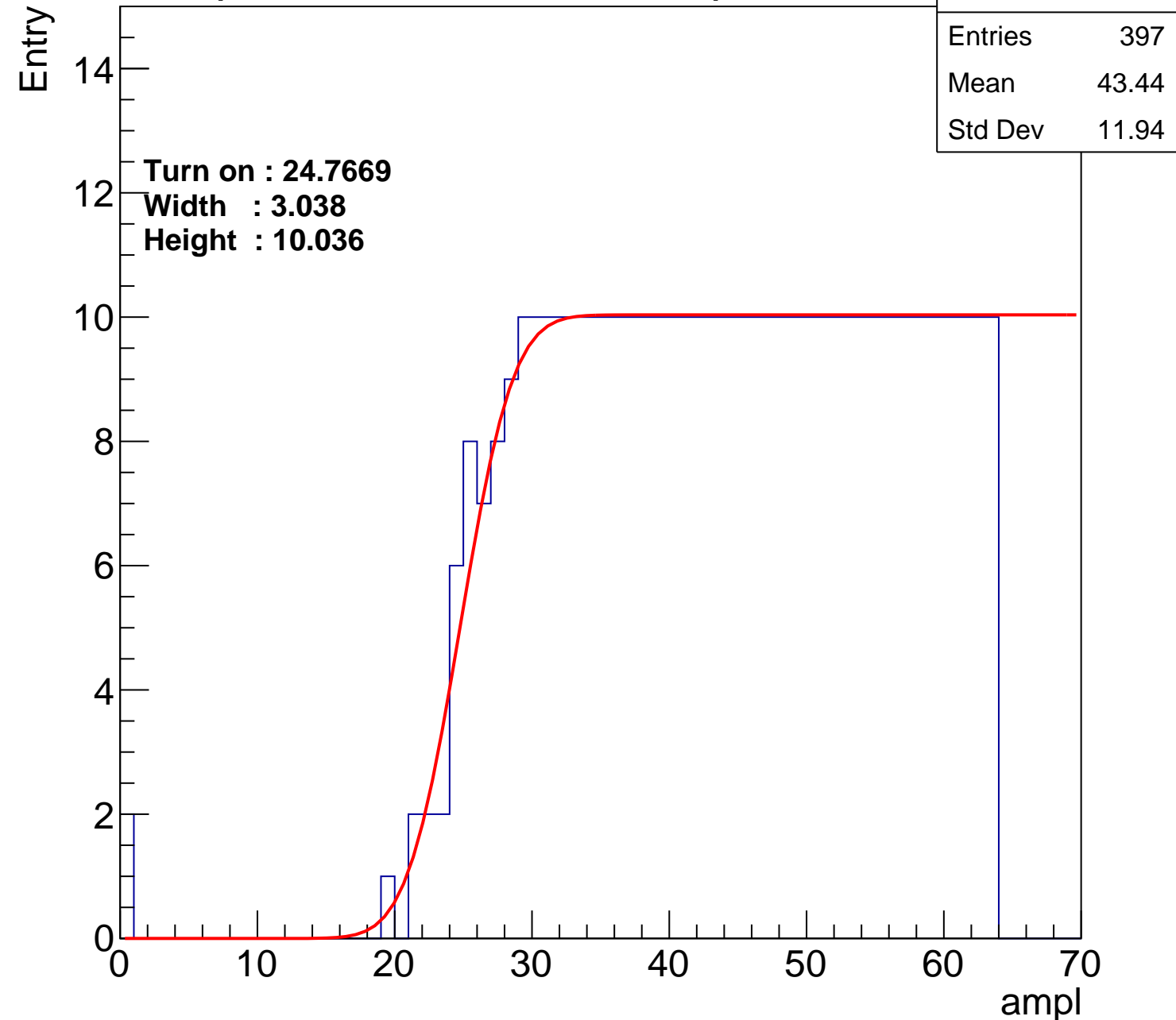
Width : 3.038

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.9767

Width : 2.315

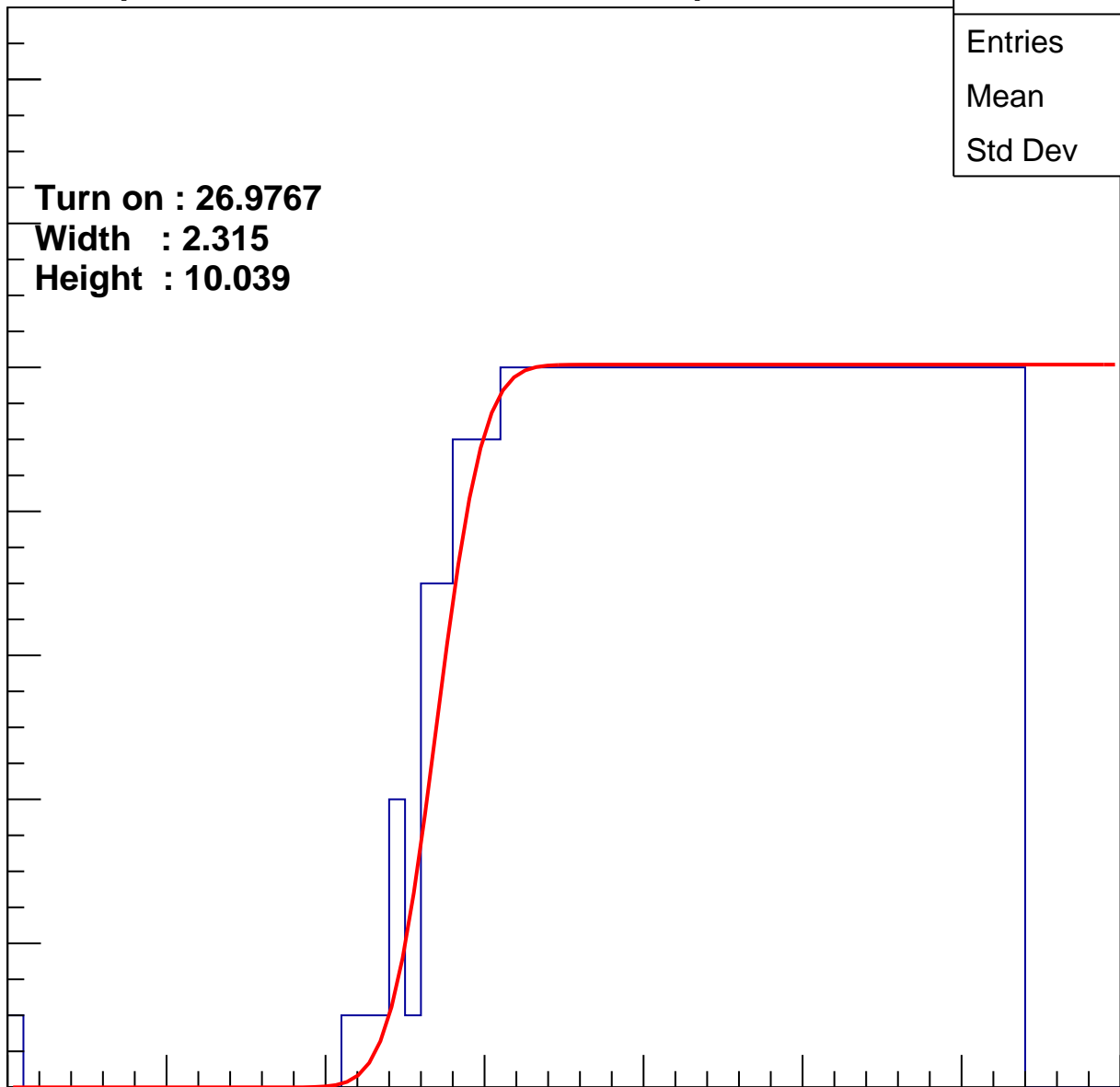
Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U19-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.06
Std Dev	11.45

Turn on : 25.7157

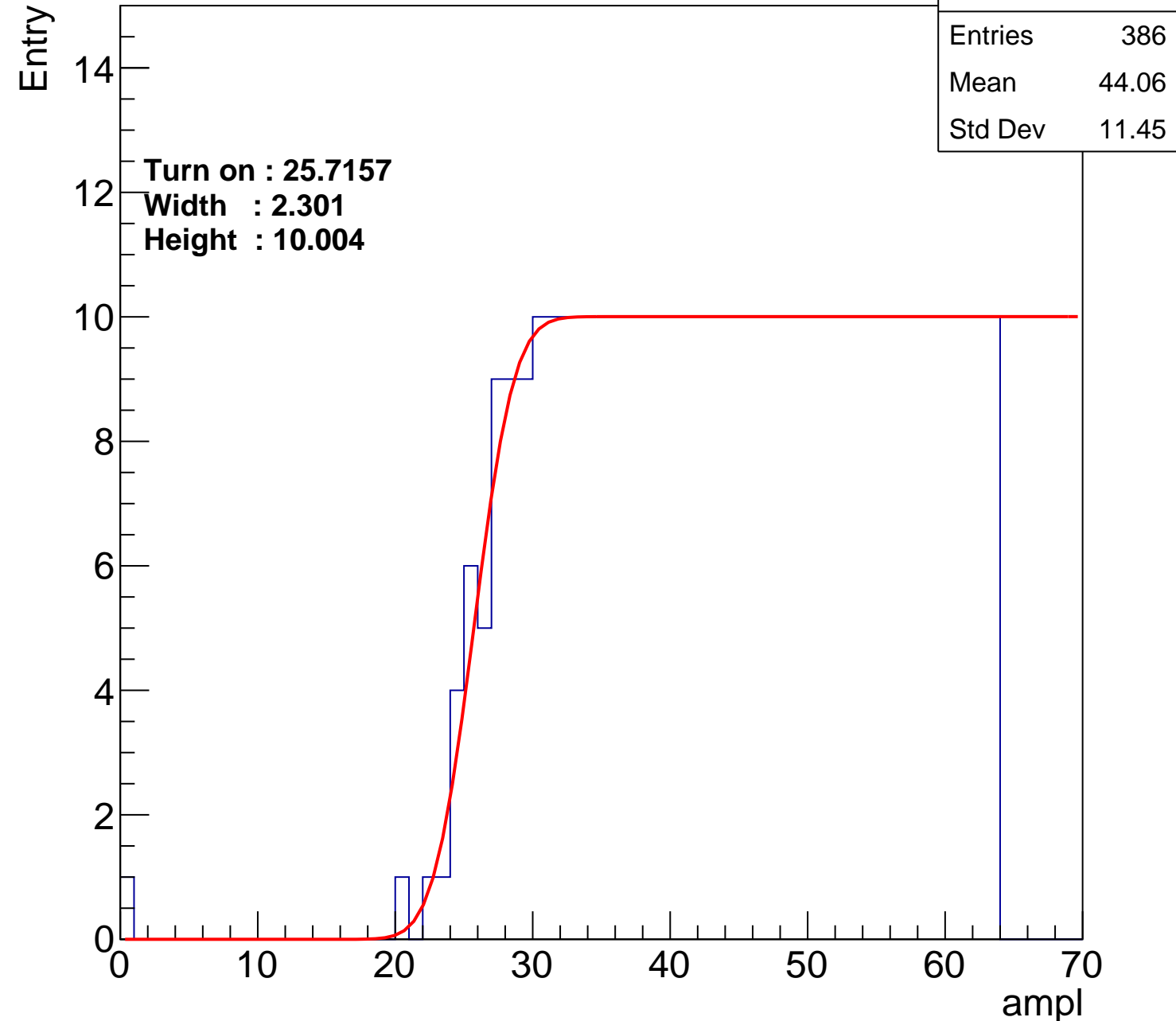
Width : 2.301

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.77
Std Dev	11.63

Turn on : 25.2719

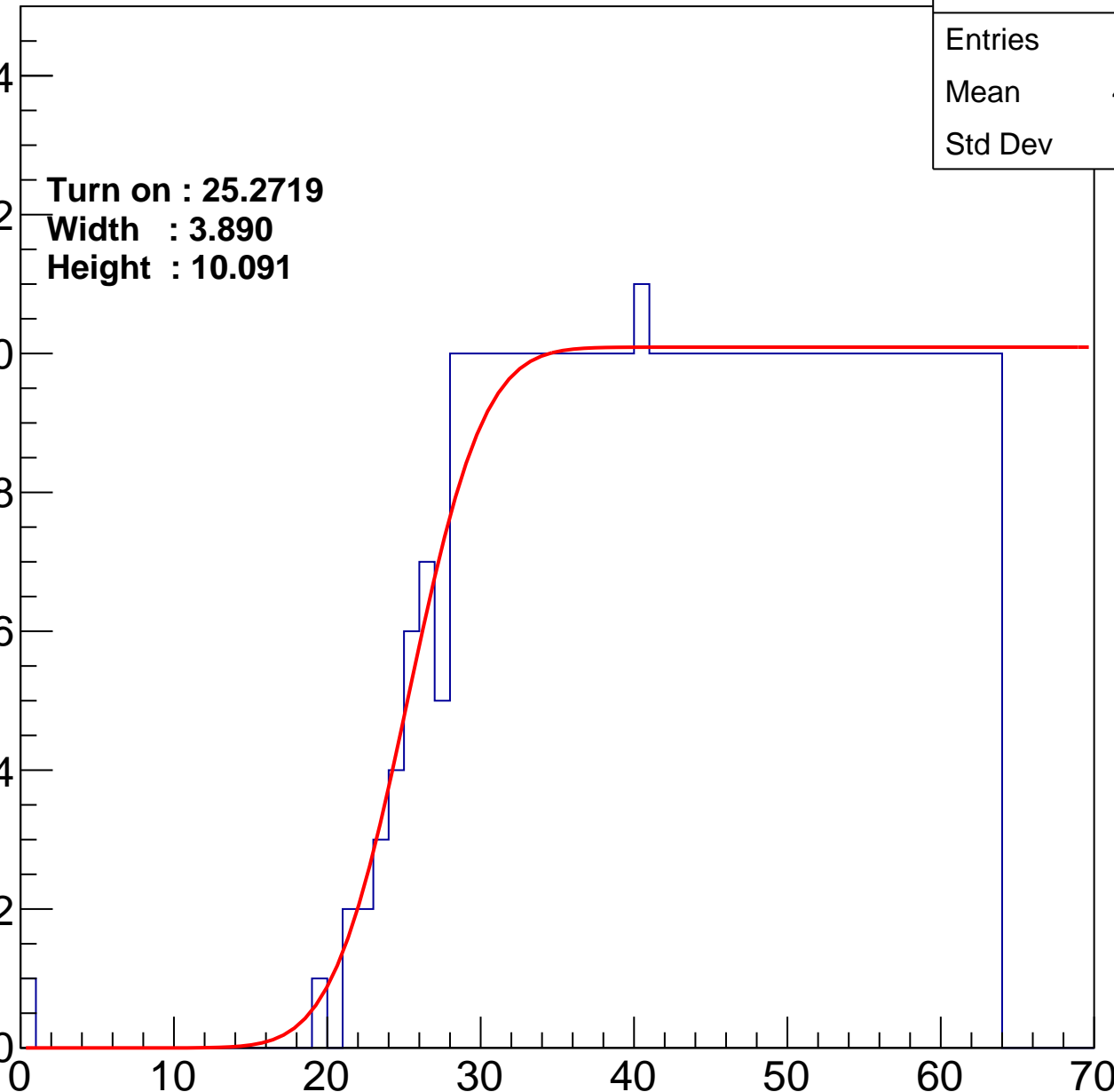
Width : 3.890

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.56
Std Dev	12.02

Turn on : 25.8733

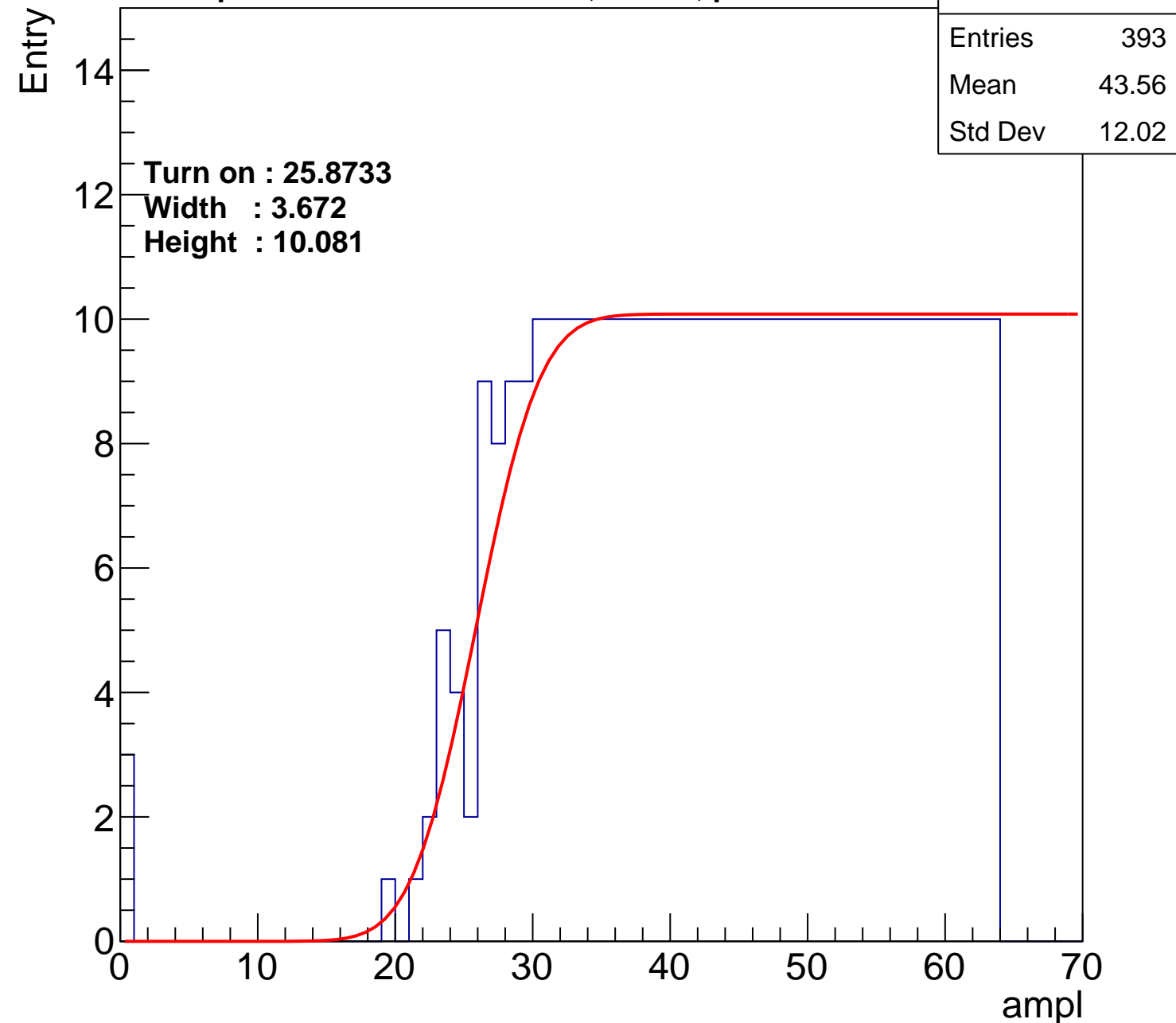
Width : 3.672

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.15
Std Dev	11.71

Turn on : 26.6219

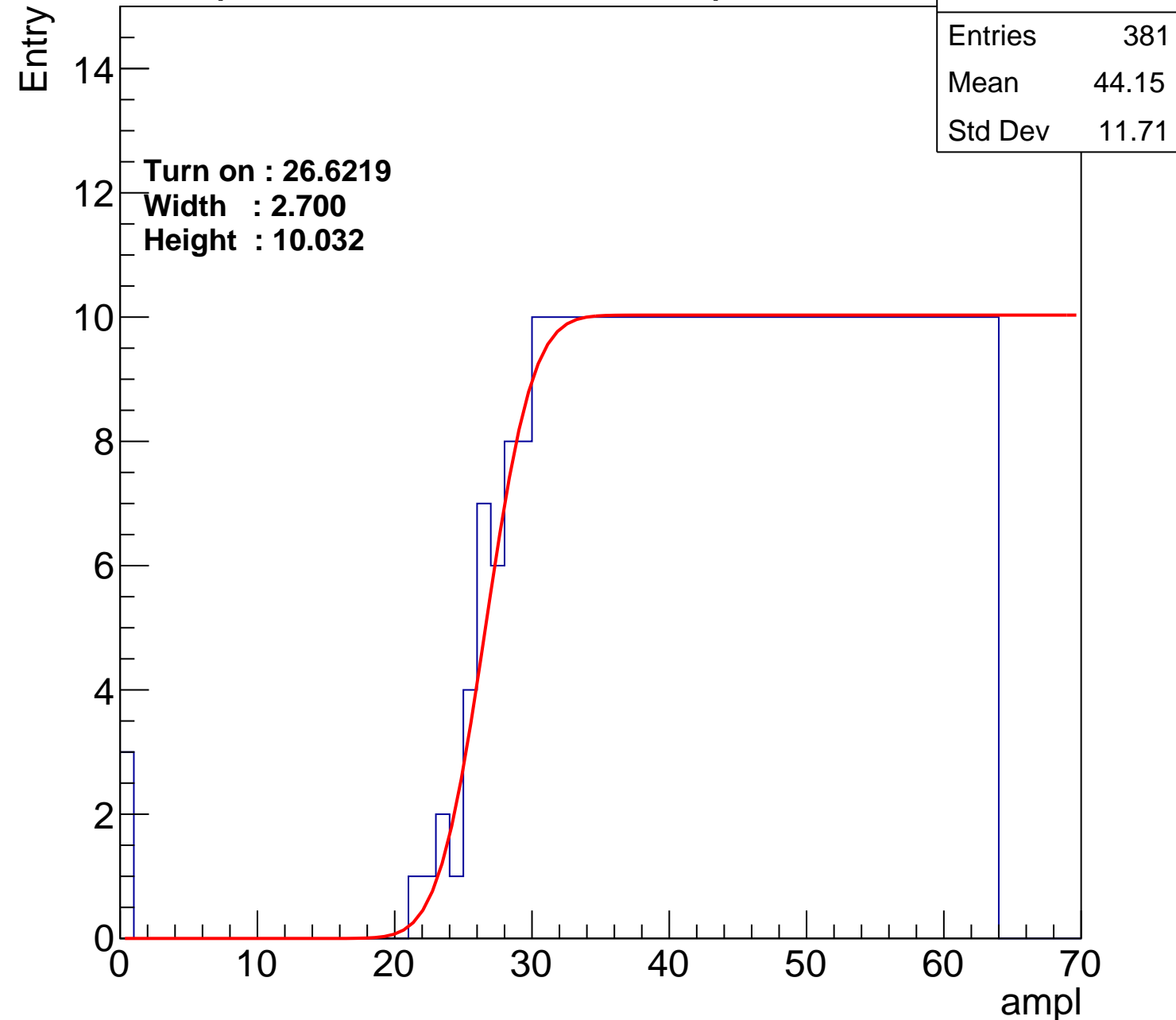
Width : 2.700

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.39
Std Dev	11.91

Turn on : 24.3822

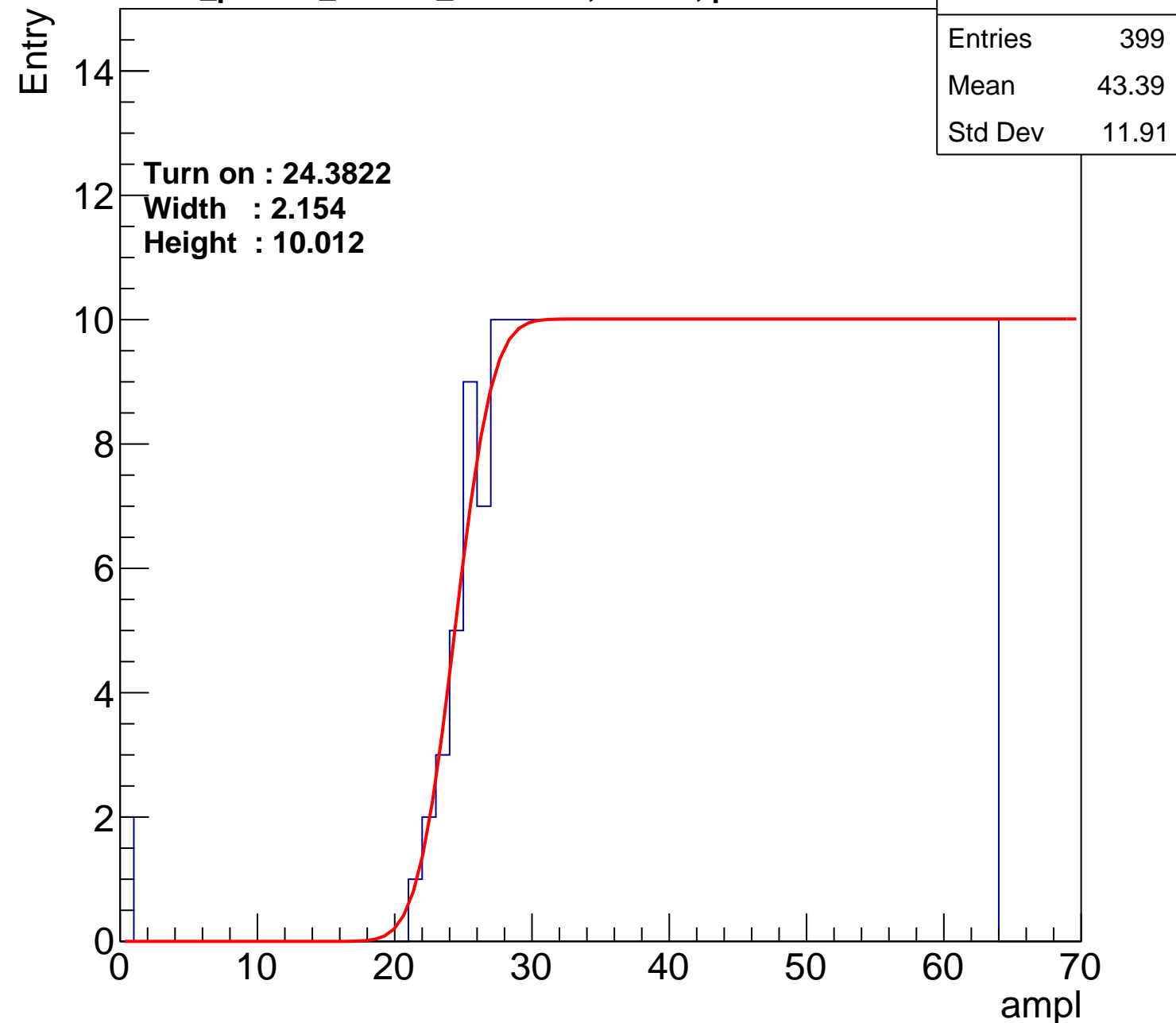
Width : 2.154

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.64
Std Dev	11.14

Turn on : 26.9770

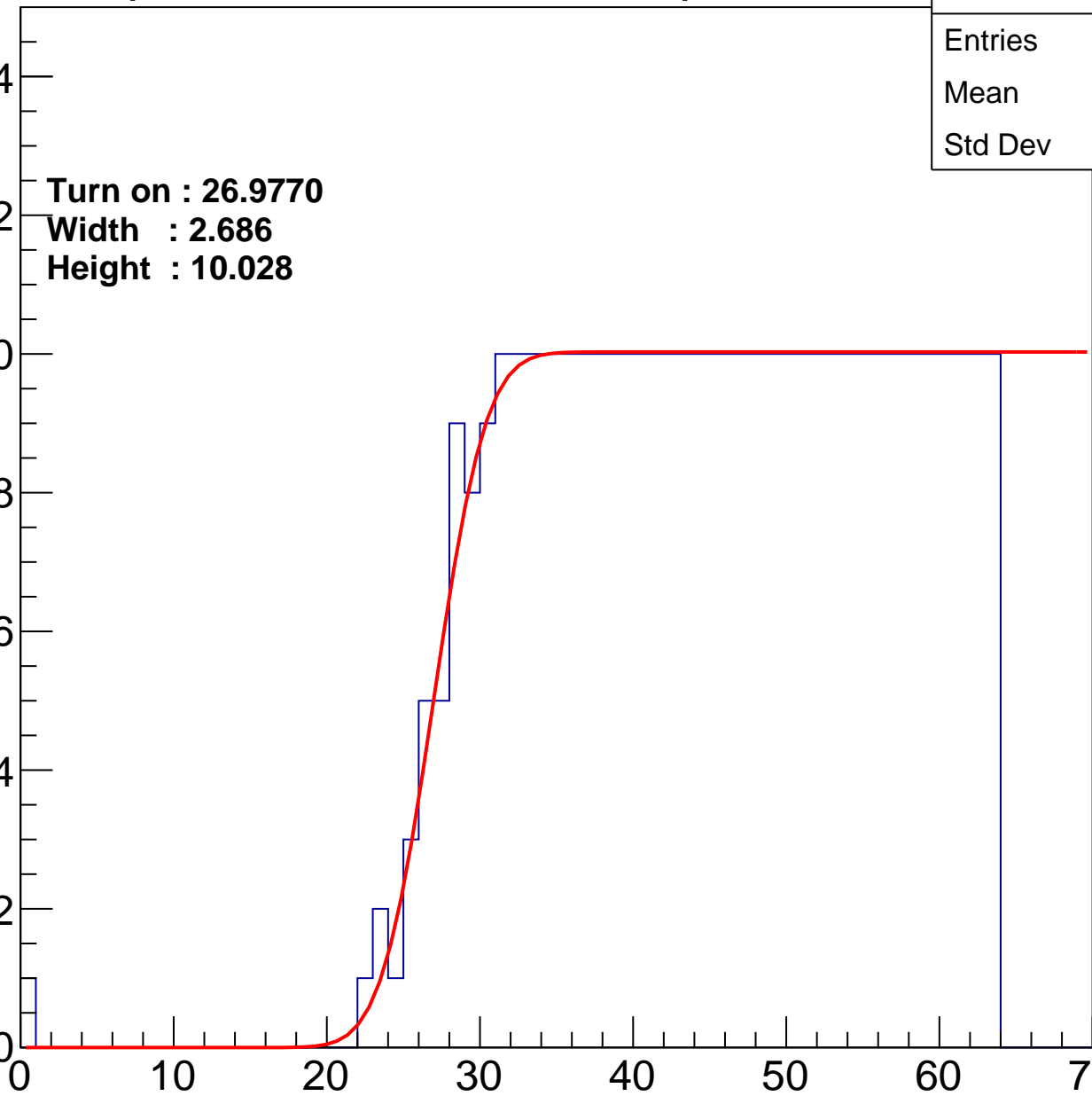
Width : 2.686

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.41
Std Dev	12.04

Turn on : 24.7926

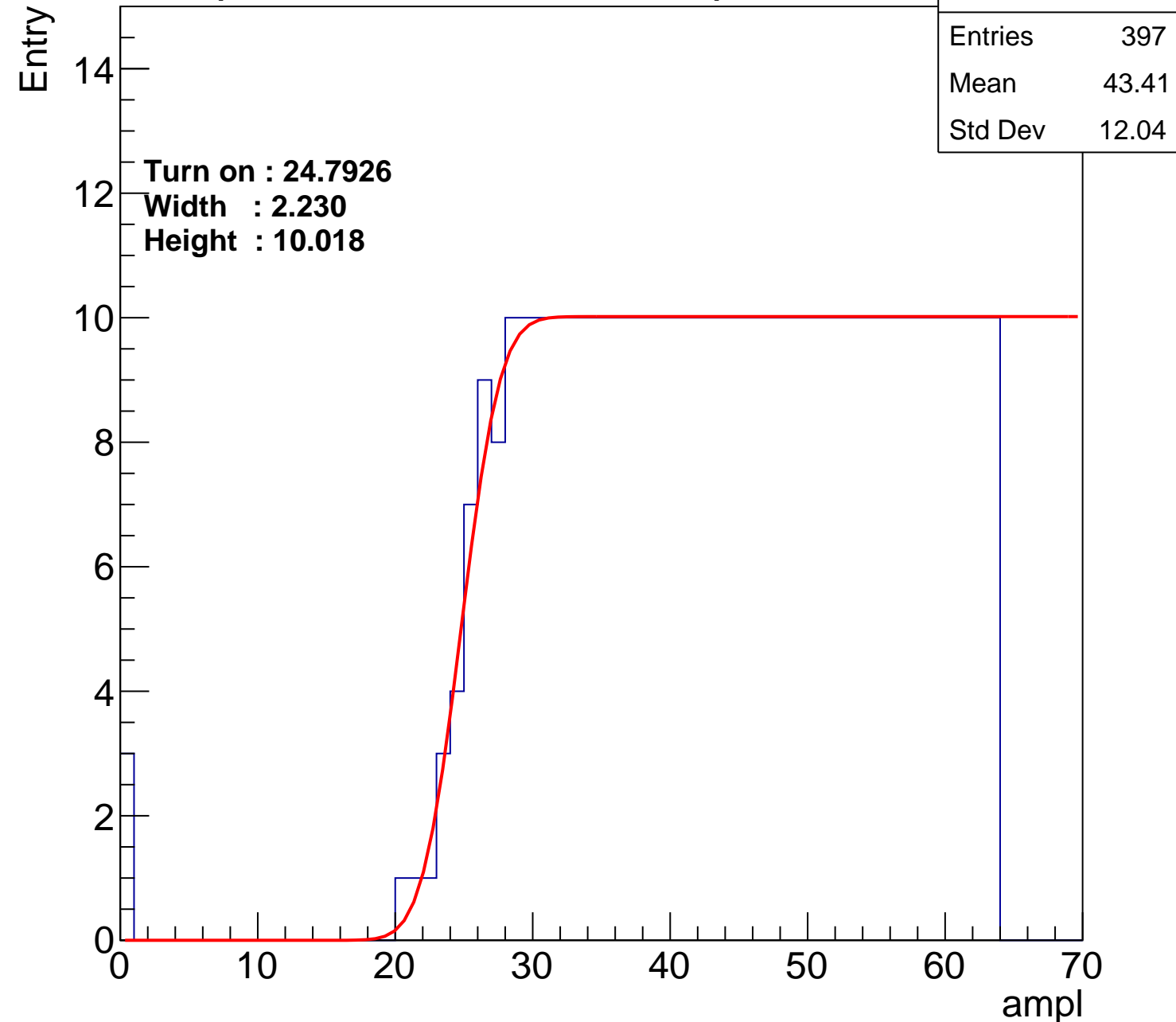
Width : 2.230

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.84

Turn on : 24.7092

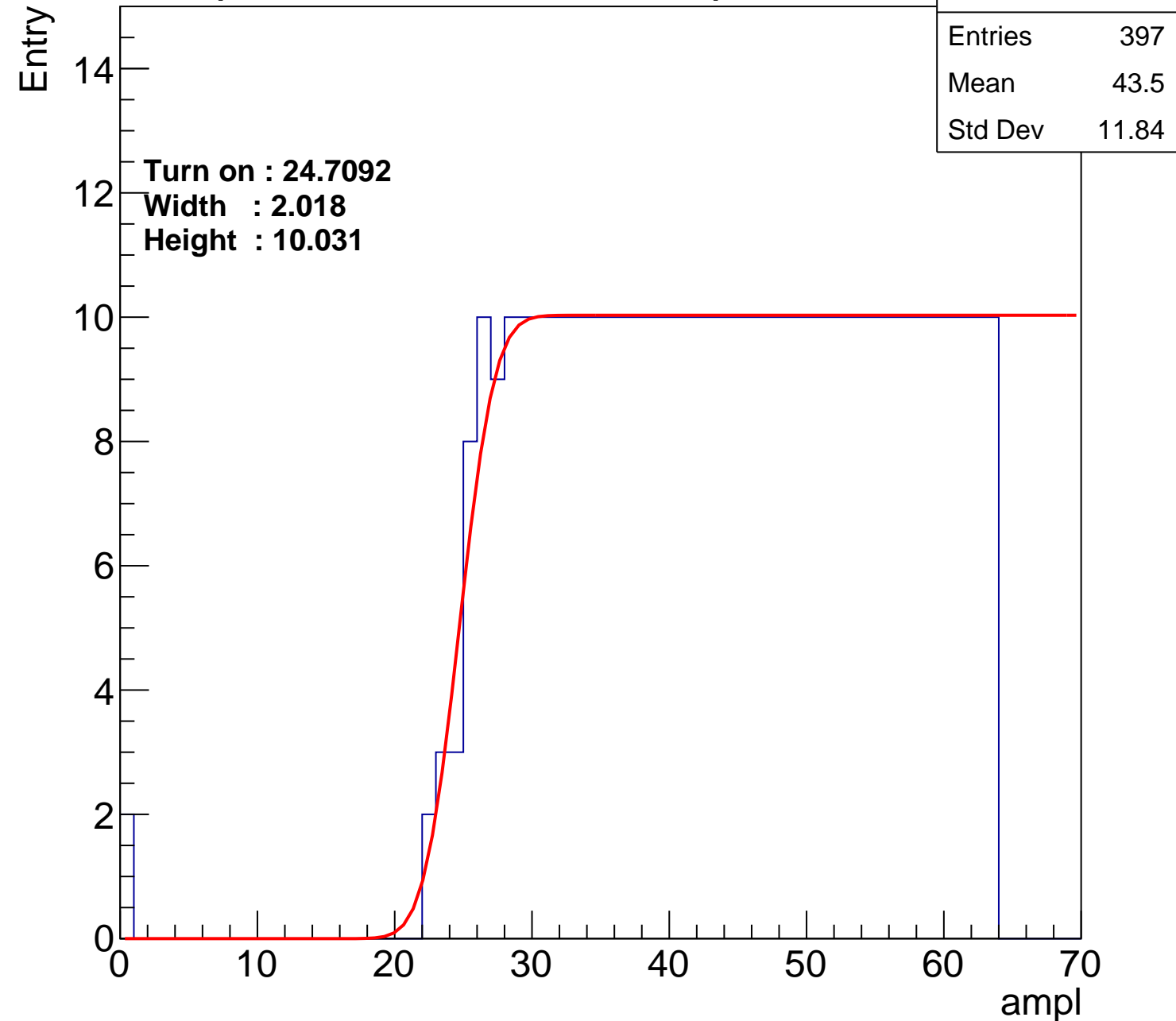
Width : 2.018

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.07
Std Dev	11.61

Turn on : 25.8348

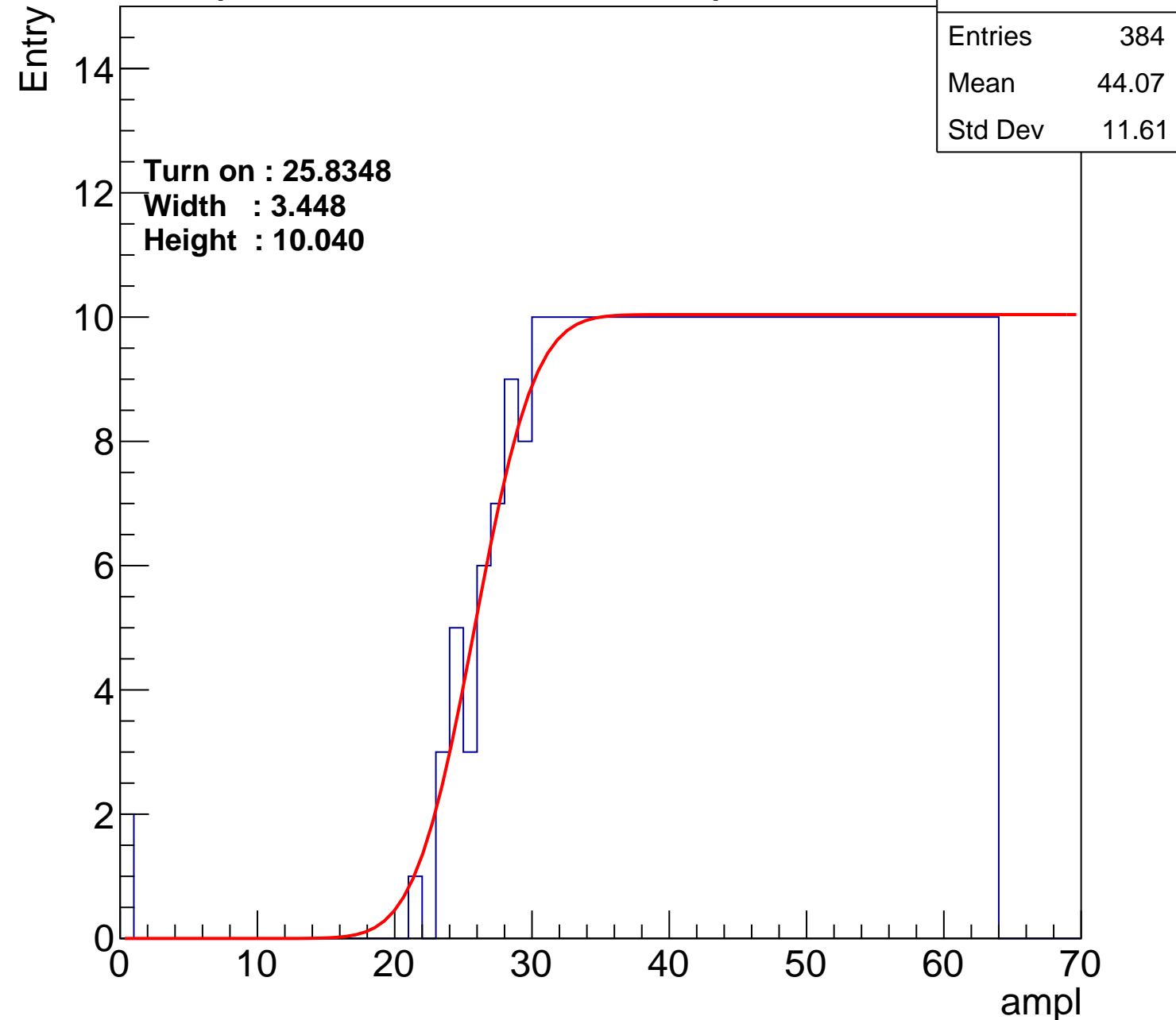
Width : 3.448

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.16
Std Dev	11.7

Turn on : 26.3292

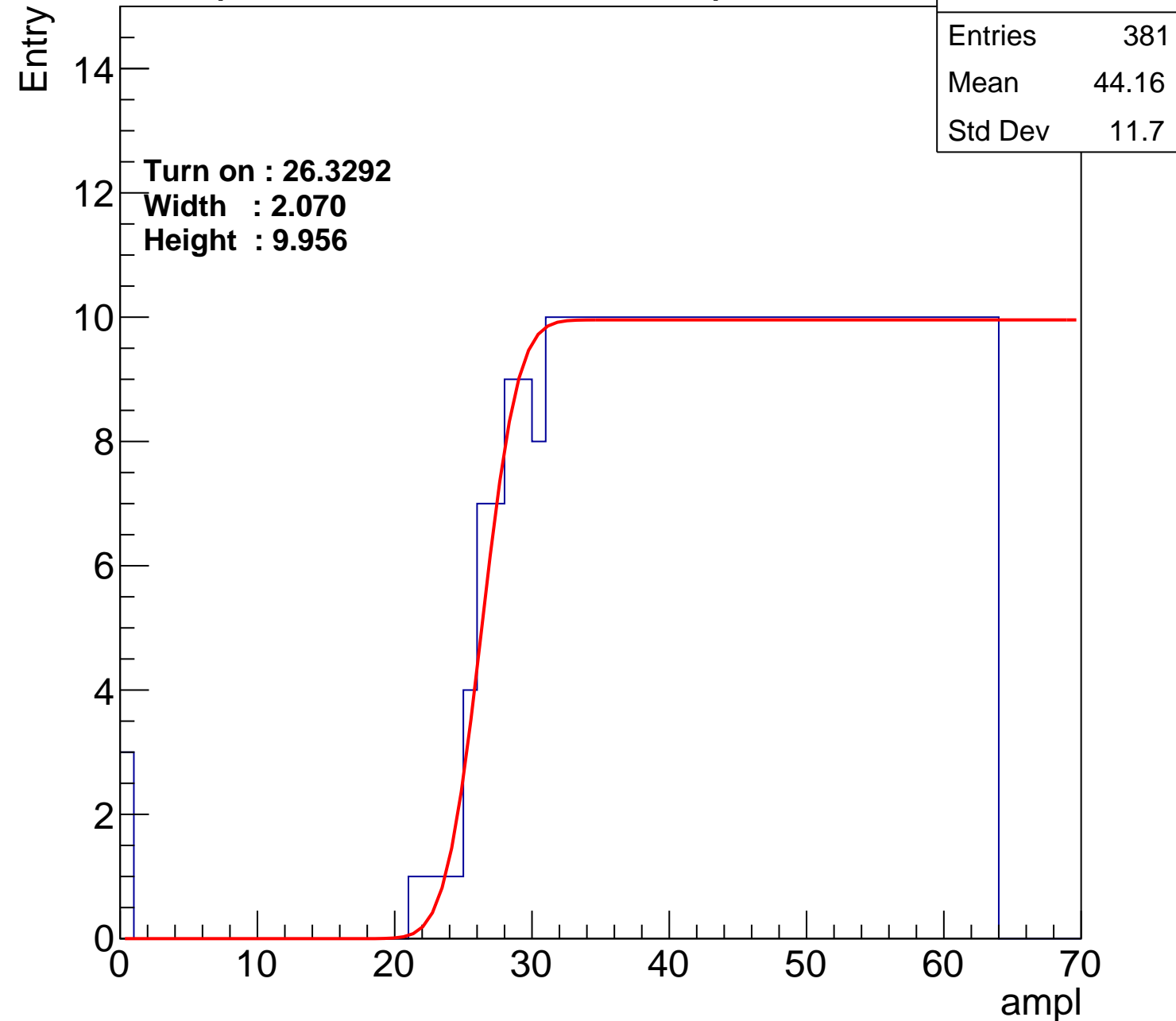
Width : 2.070

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.58
Std Dev	12.46

Turn on : 22.6953

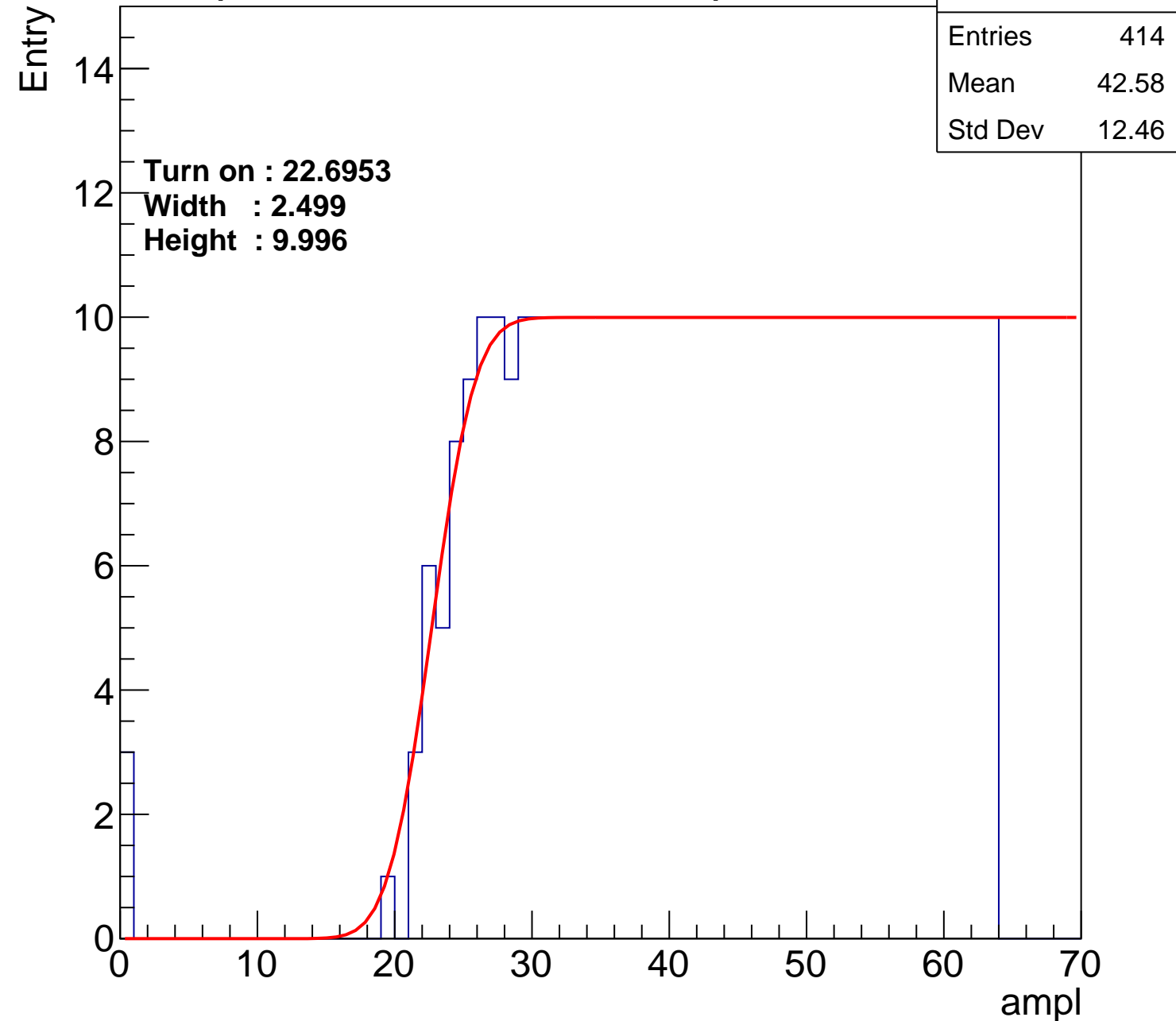
Width : 2.499

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.99
Std Dev	12.28

Turn on : 24.1267

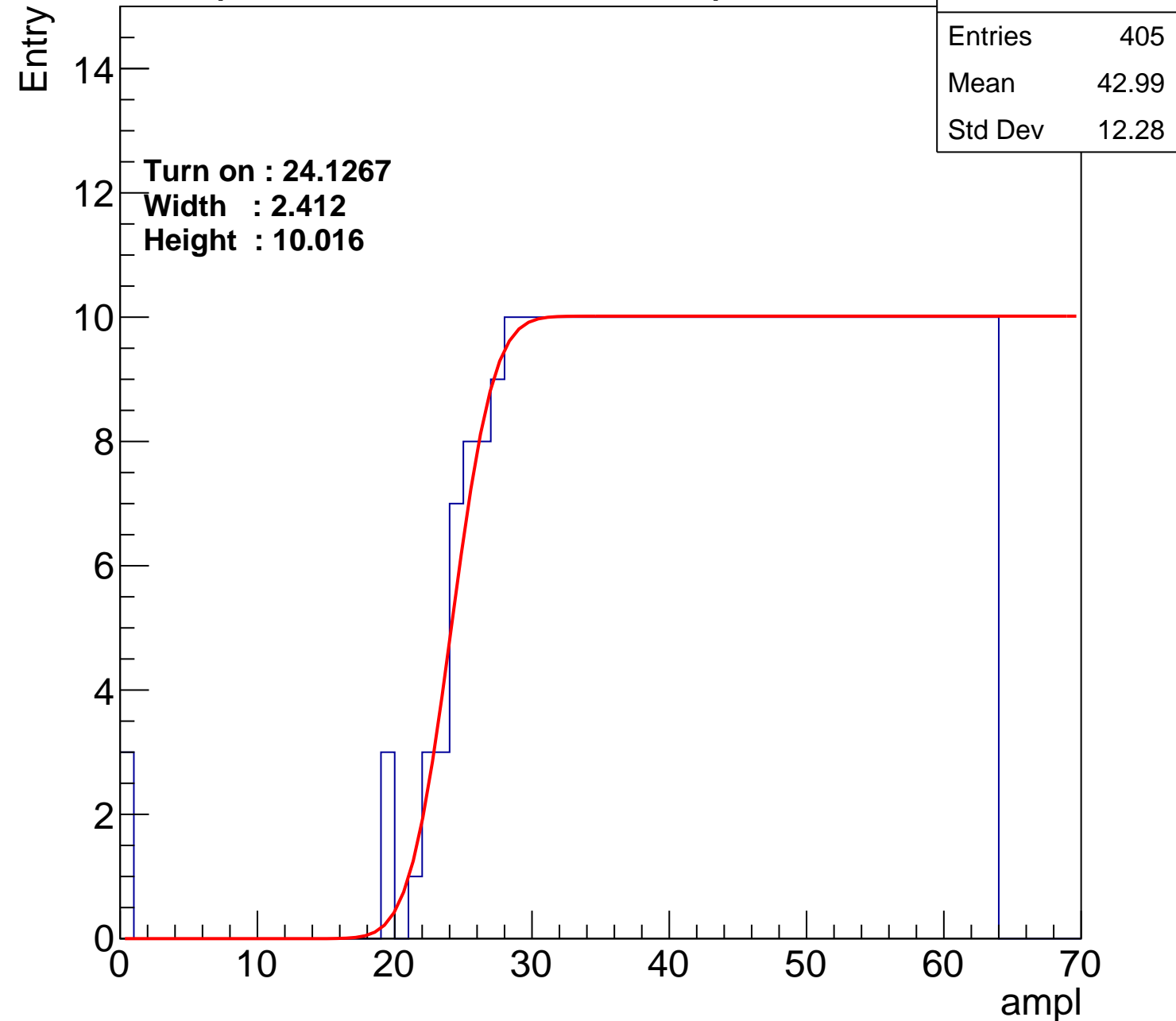
Width : 2.412

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch78

calib_packv5_042523_0143.root, FC#10, port B3

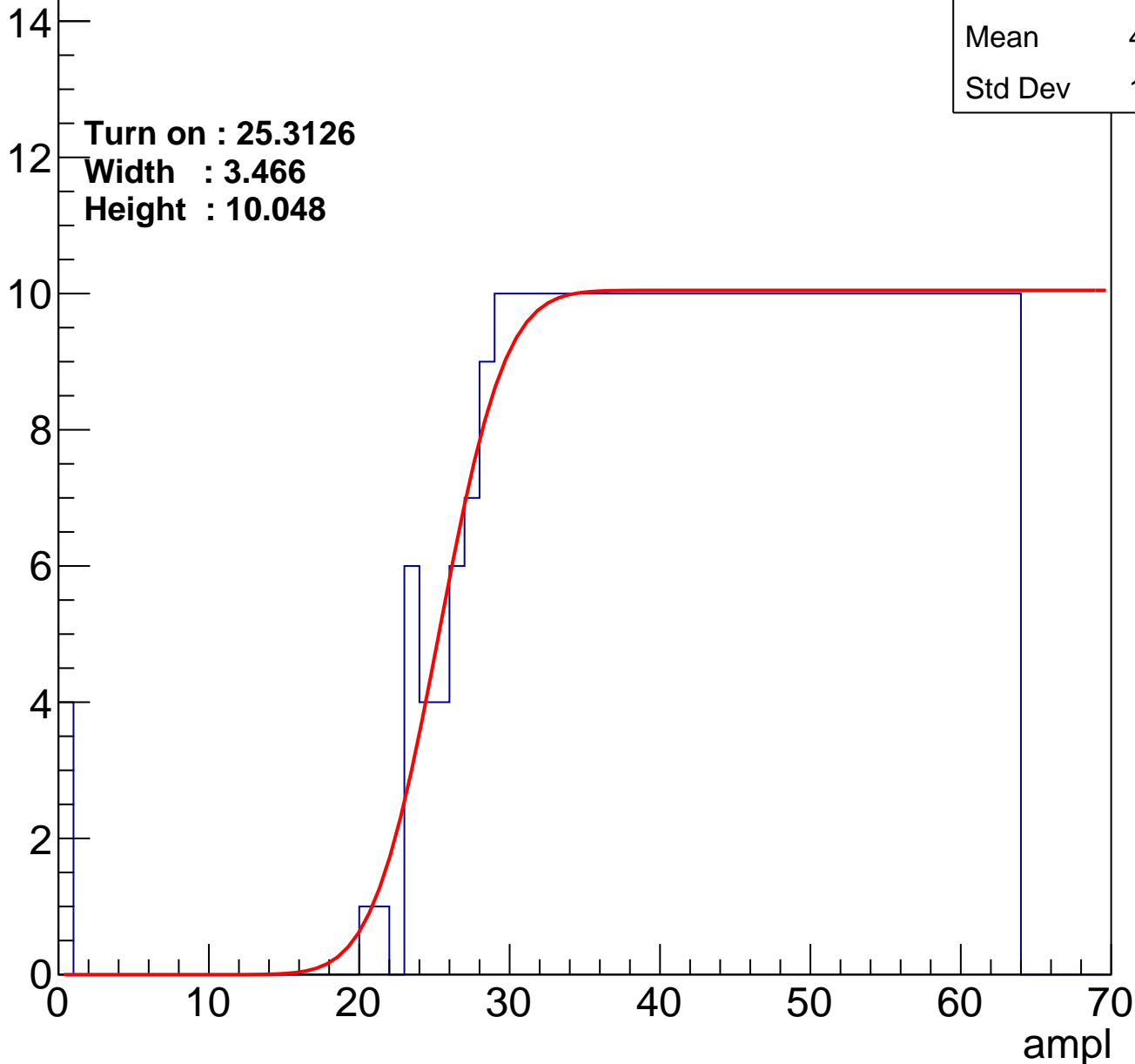
Entries	392
Mean	43.55
Std Dev	12.15

Turn on : 25.3126

Width : 3.466

Height : 10.048

Entry



B1L002S, U19-ch79

calib_packv5_042523_0143.root, FC#10, port B3

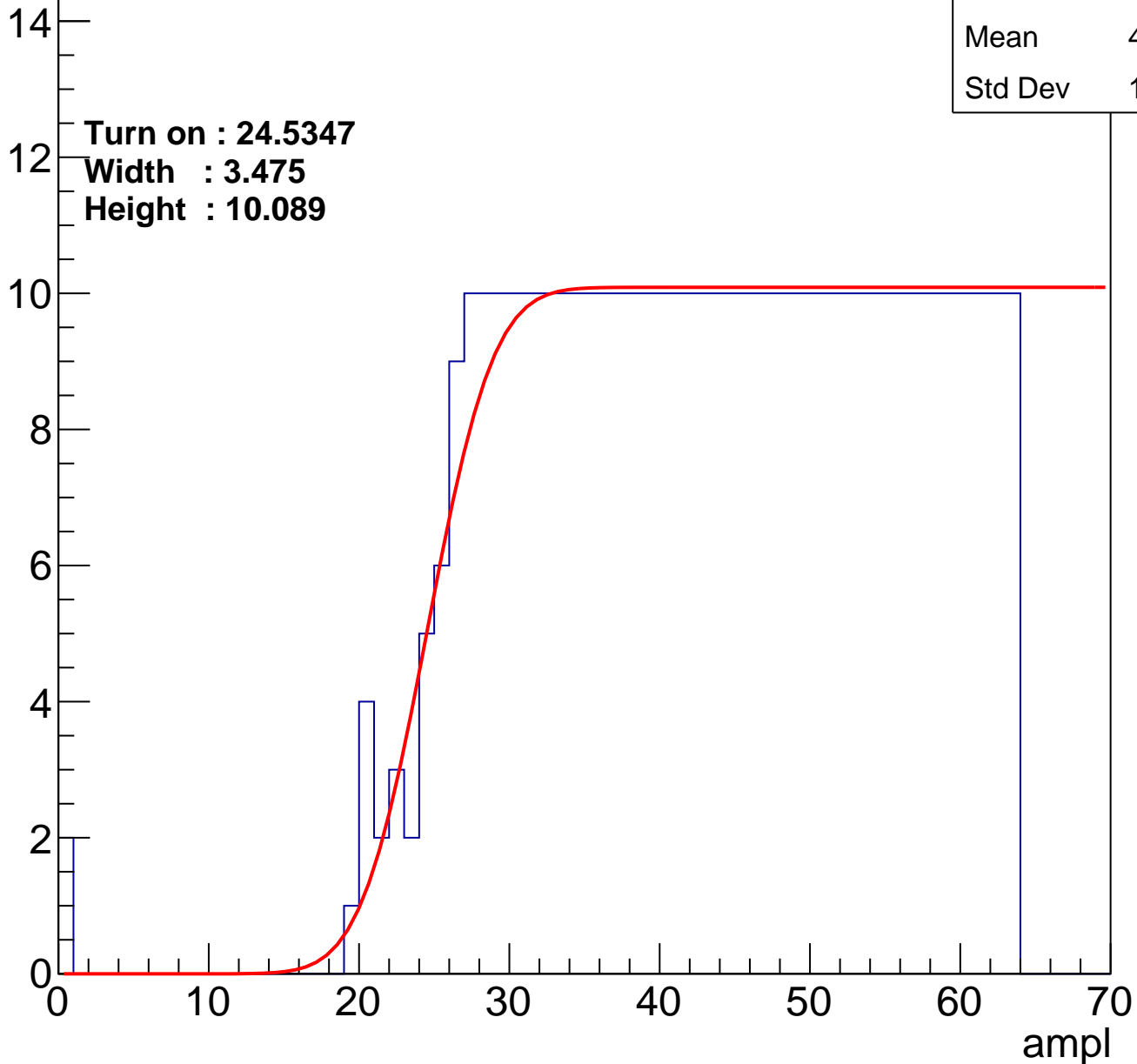
Entries	404
Mean	43.09
Std Dev	12.13

Turn on : 24.5347

Width : 3.475

Height : 10.089

Entry



B1L002S, U19-ch80

calib_packv5_042523_0143.root, FC#10, port B3

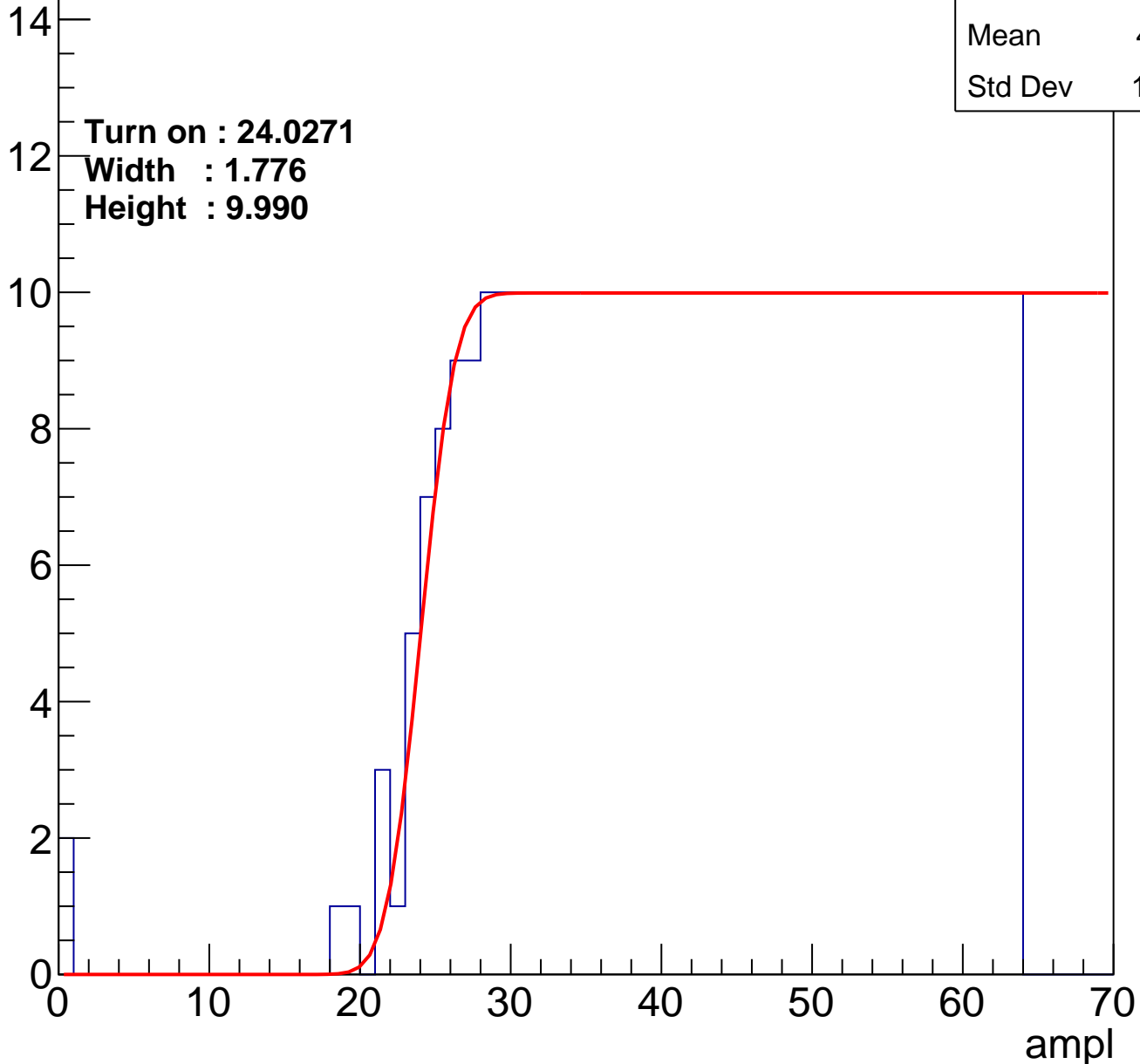
Entries	406
Mean	43.01
Std Dev	12.15

Turn on : 24.0271

Width : 1.776

Height : 9.990

Entry



B1L002S, U19-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.31
Std Dev	11.37

Turn on : 26.4380

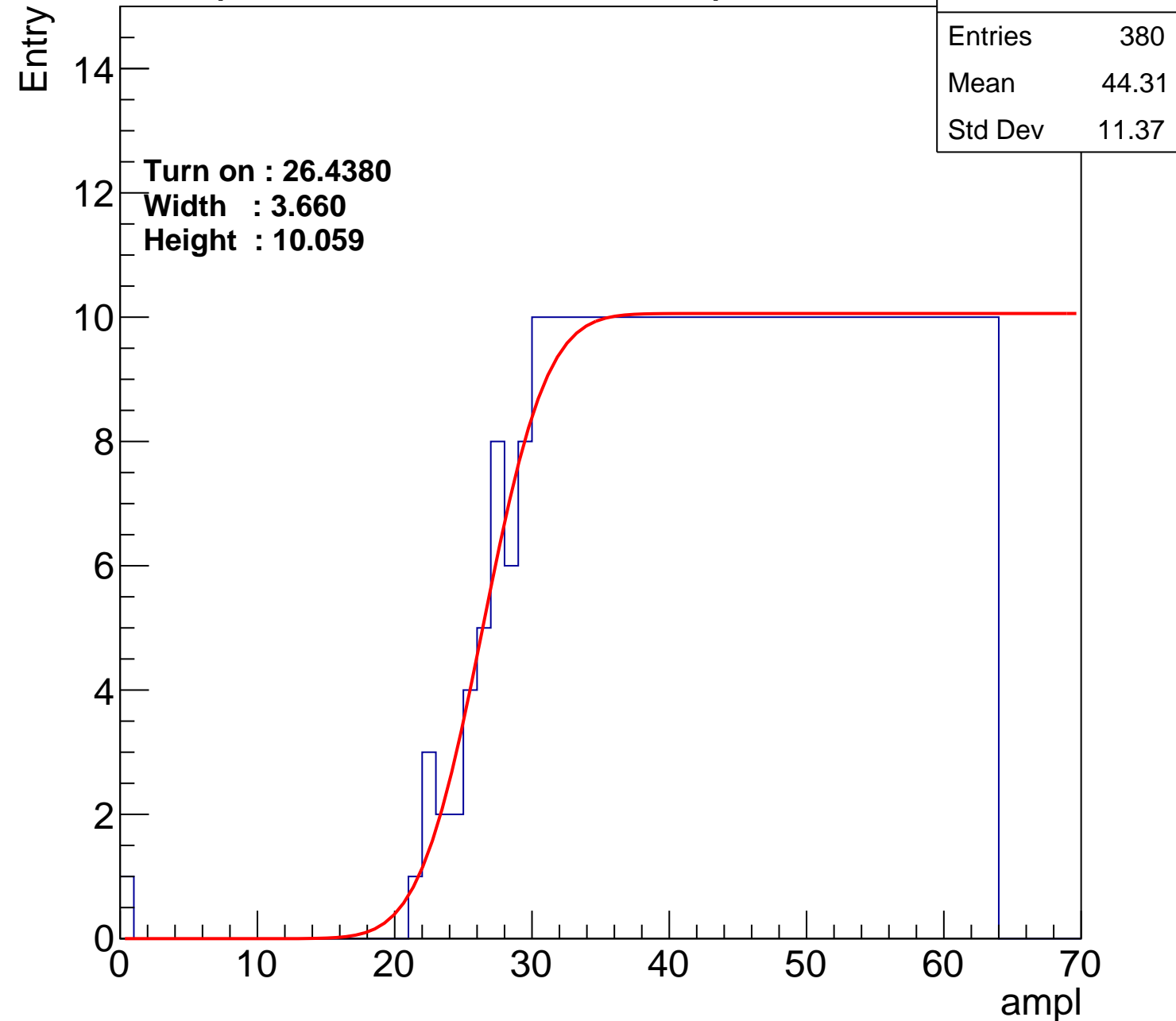
Width : 3.660

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.2
Std Dev	12.37

Turn on : 25.0511

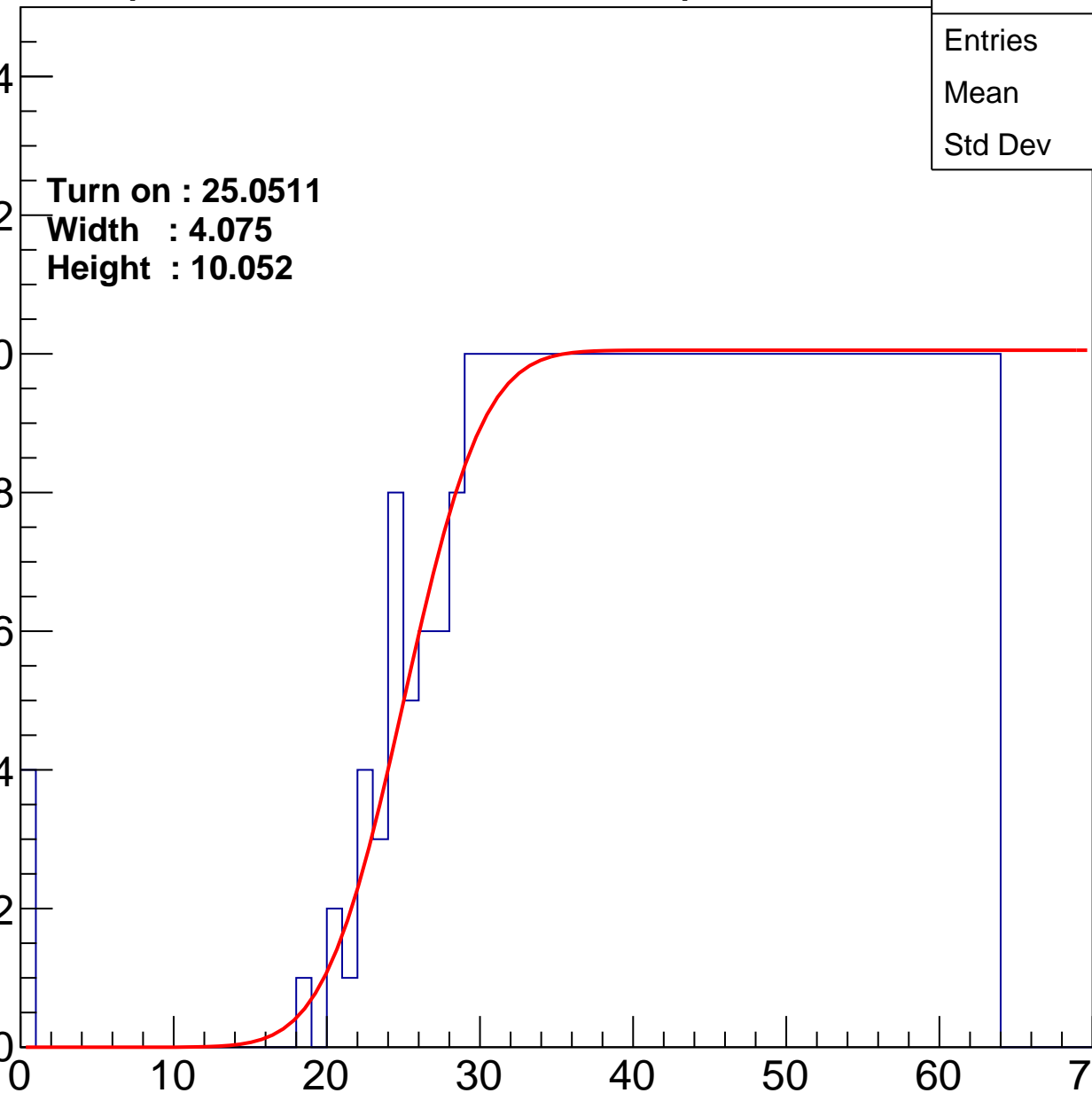
Width : 4.075

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch83

calib_packv5_042523_0143.root, FC#10, port B3

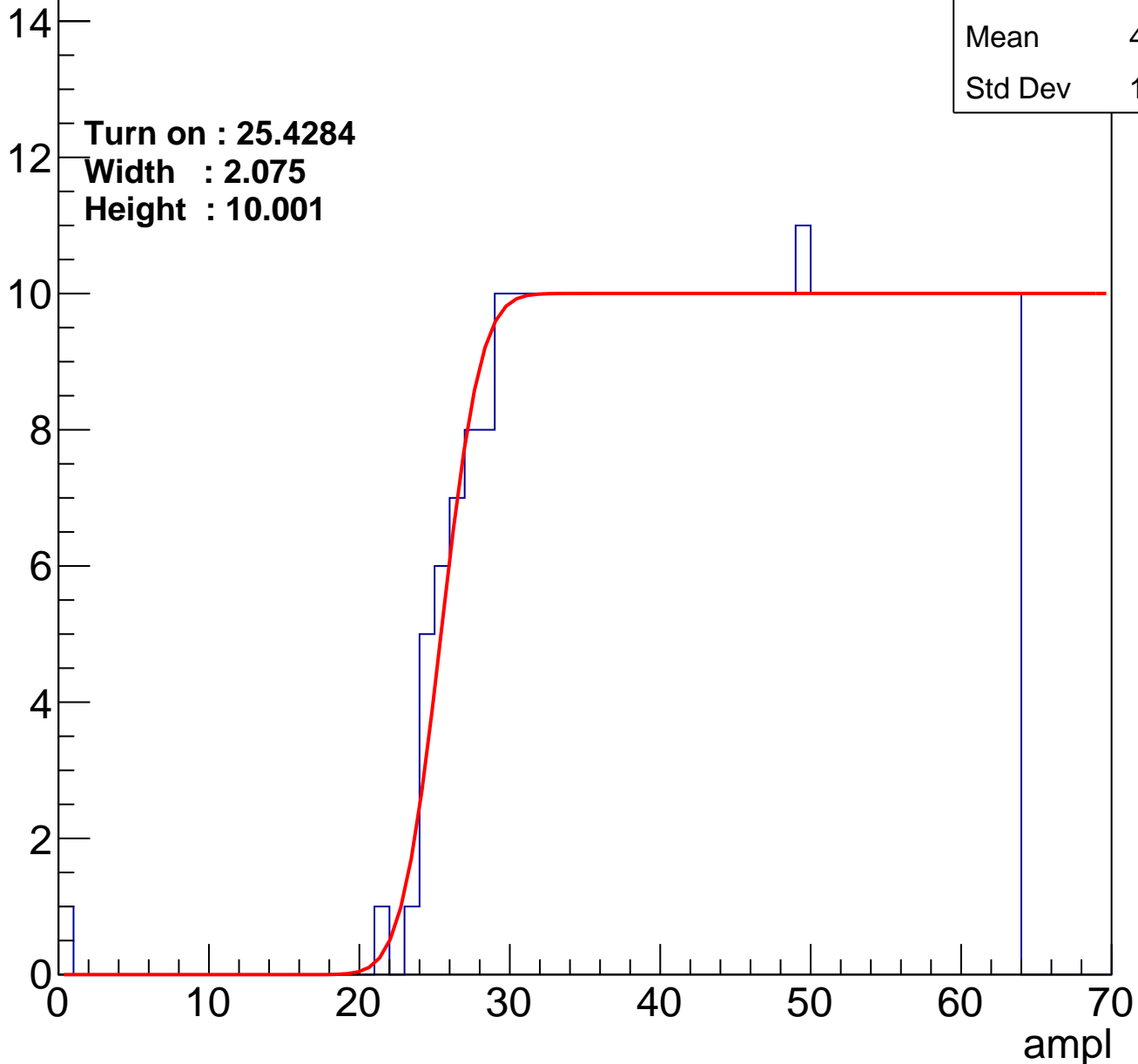
Entries	388
Mean	44.03
Std Dev	11.45

Turn on : 25.4284

Width : 2.075

Height : 10.001

Entry



B1L002S, U19-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 26.2955

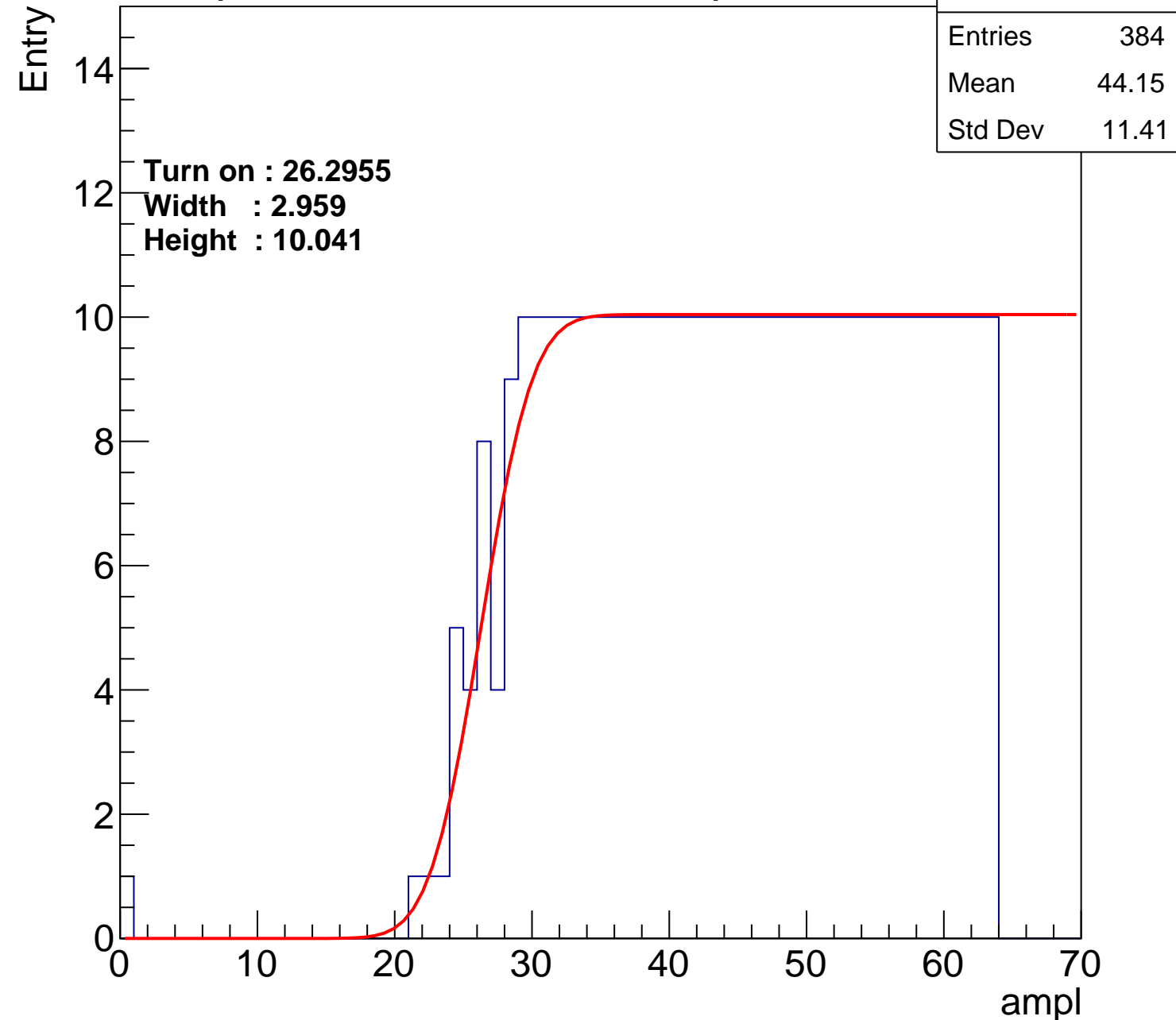
Width : 2.959

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch85

calib_packv5_042523_0143.root, FC#10, port B3

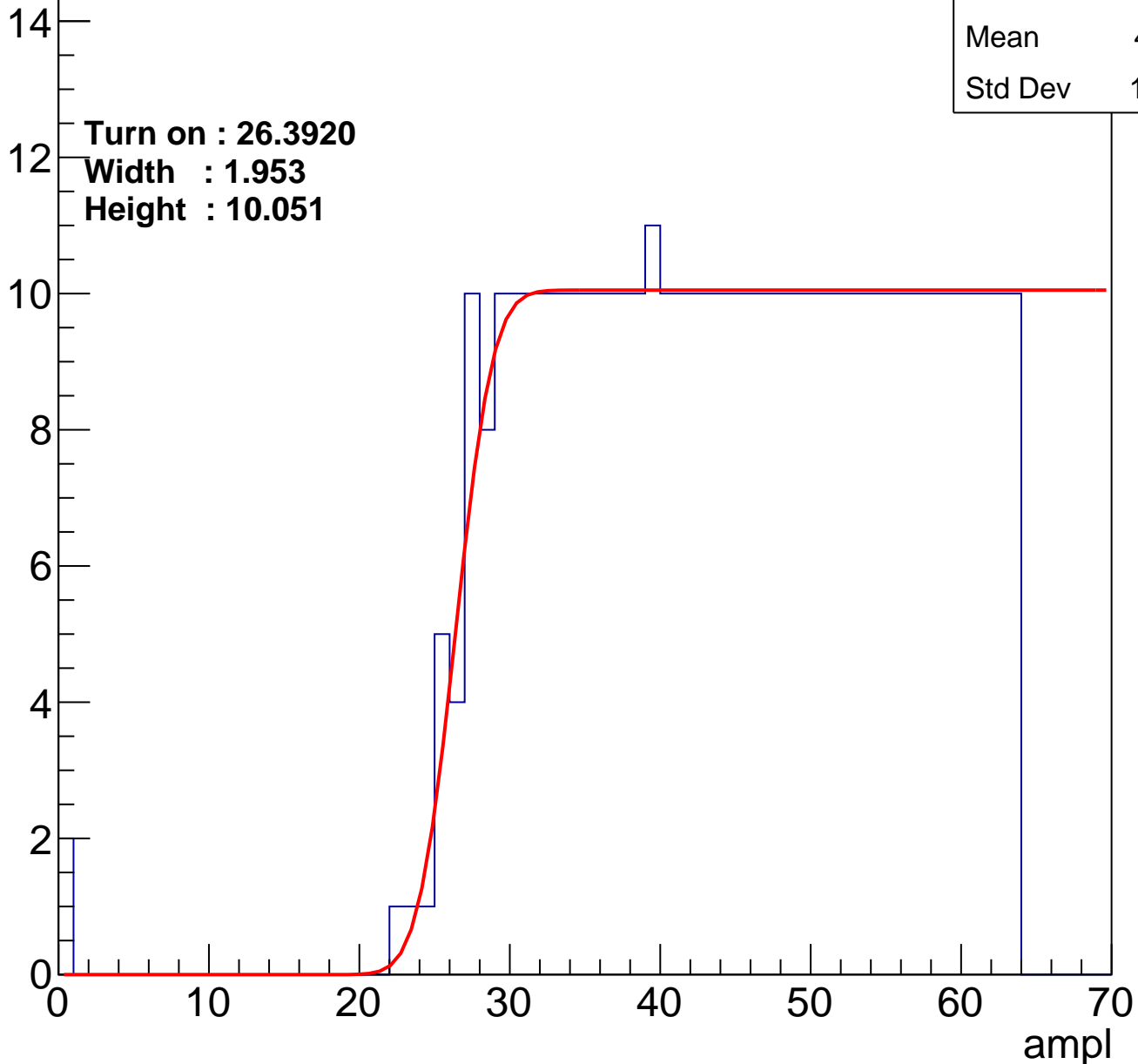
Entries	383
Mean	44.21
Std Dev	11.47

Turn on : 26.3920

Width : 1.953

Height : 10.051

Entry



B1L002S, U19-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.67
Std Dev	12.78

Turn on : 23.9587

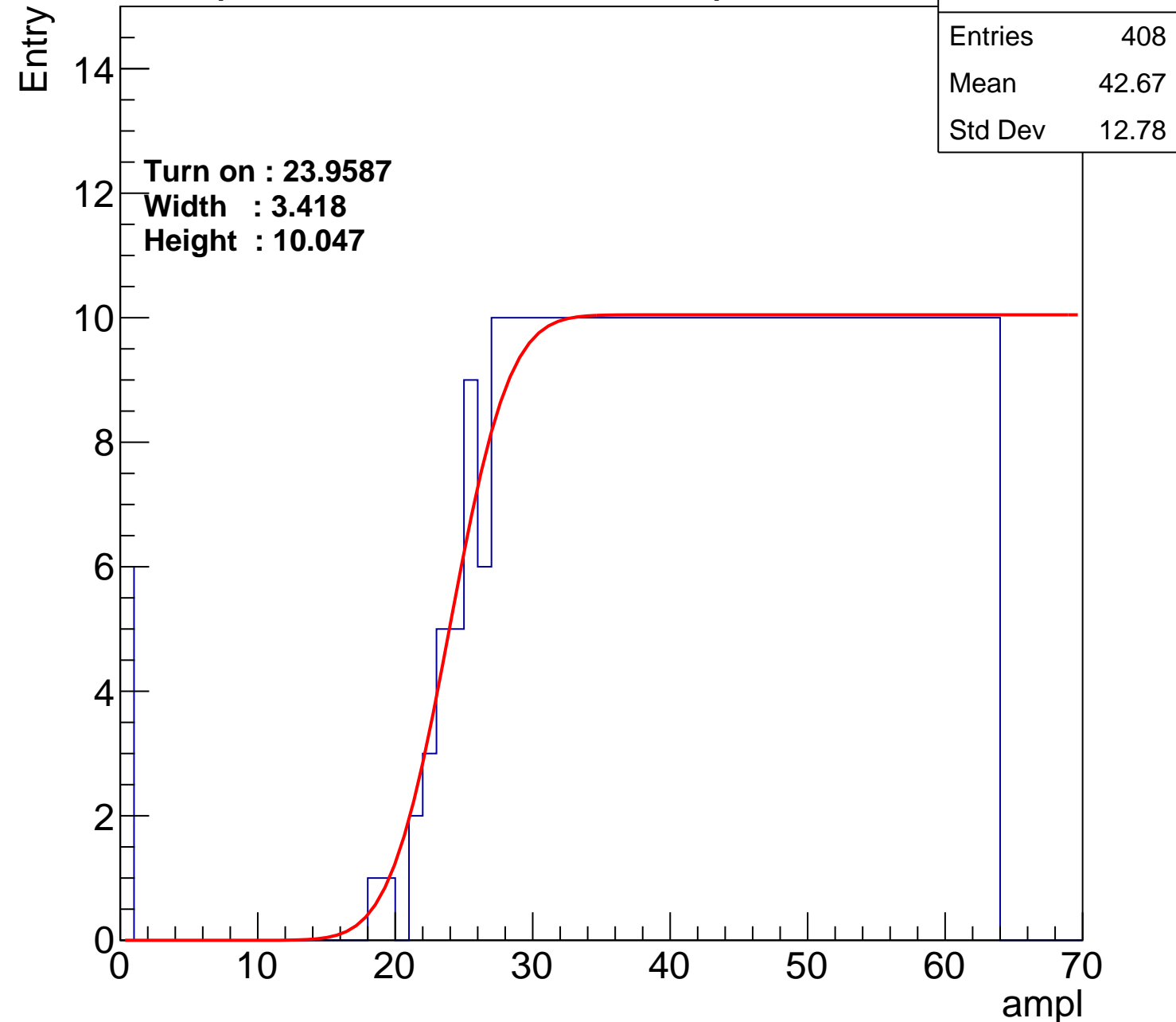
Width : 3.418

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch87

calib_packv5_042523_0143.root, FC#10, port B3

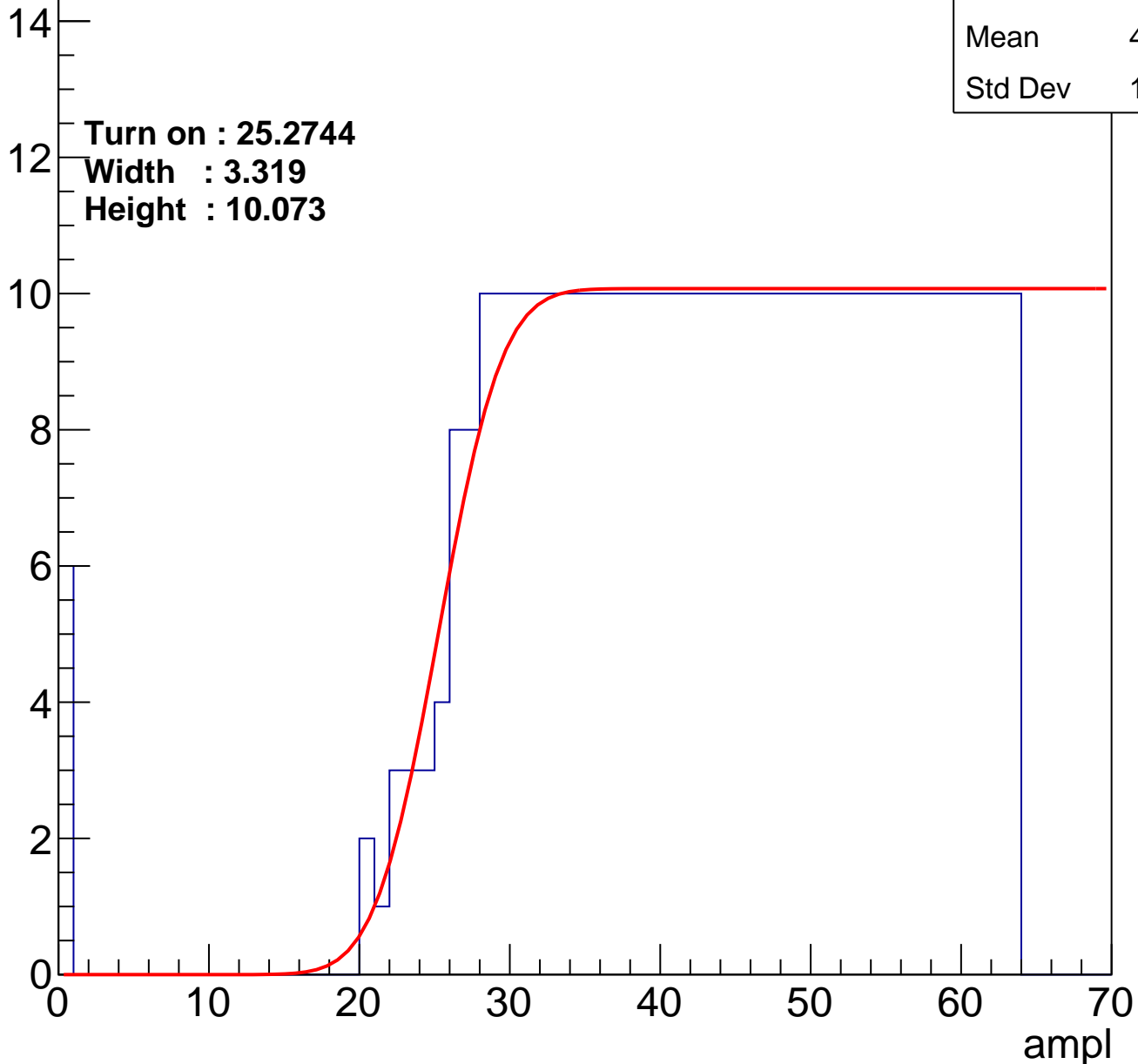
Entries	398
Mean	43.15
Std Dev	12.58

Turn on : 25.2744

Width : 3.319

Height : 10.073

Entry



B1L002S, U19-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.07
Std Dev	11.6

Turn on : 25.5625

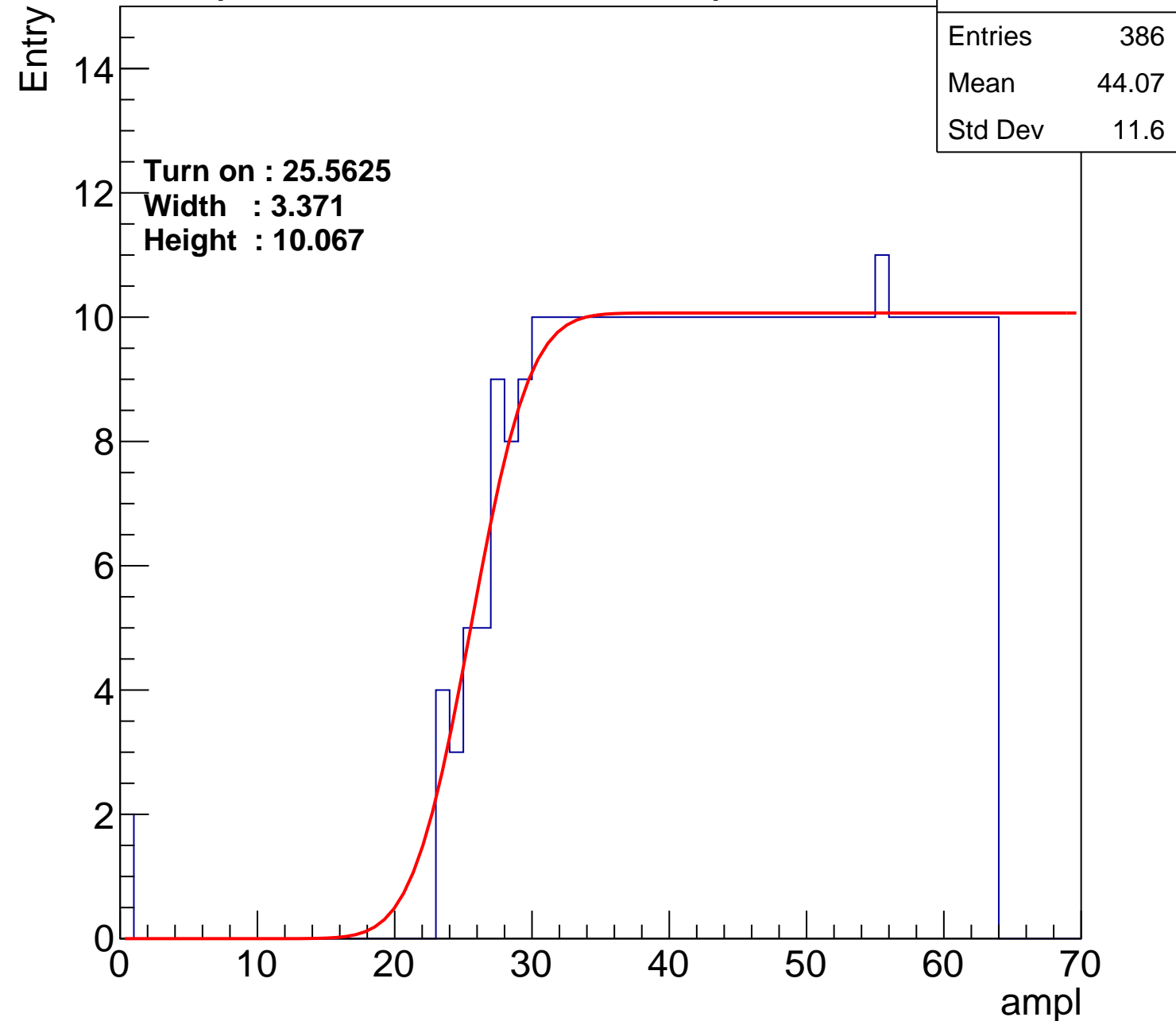
Width : 3.371

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.36
Std Dev	12.12

Turn on : 24.7726

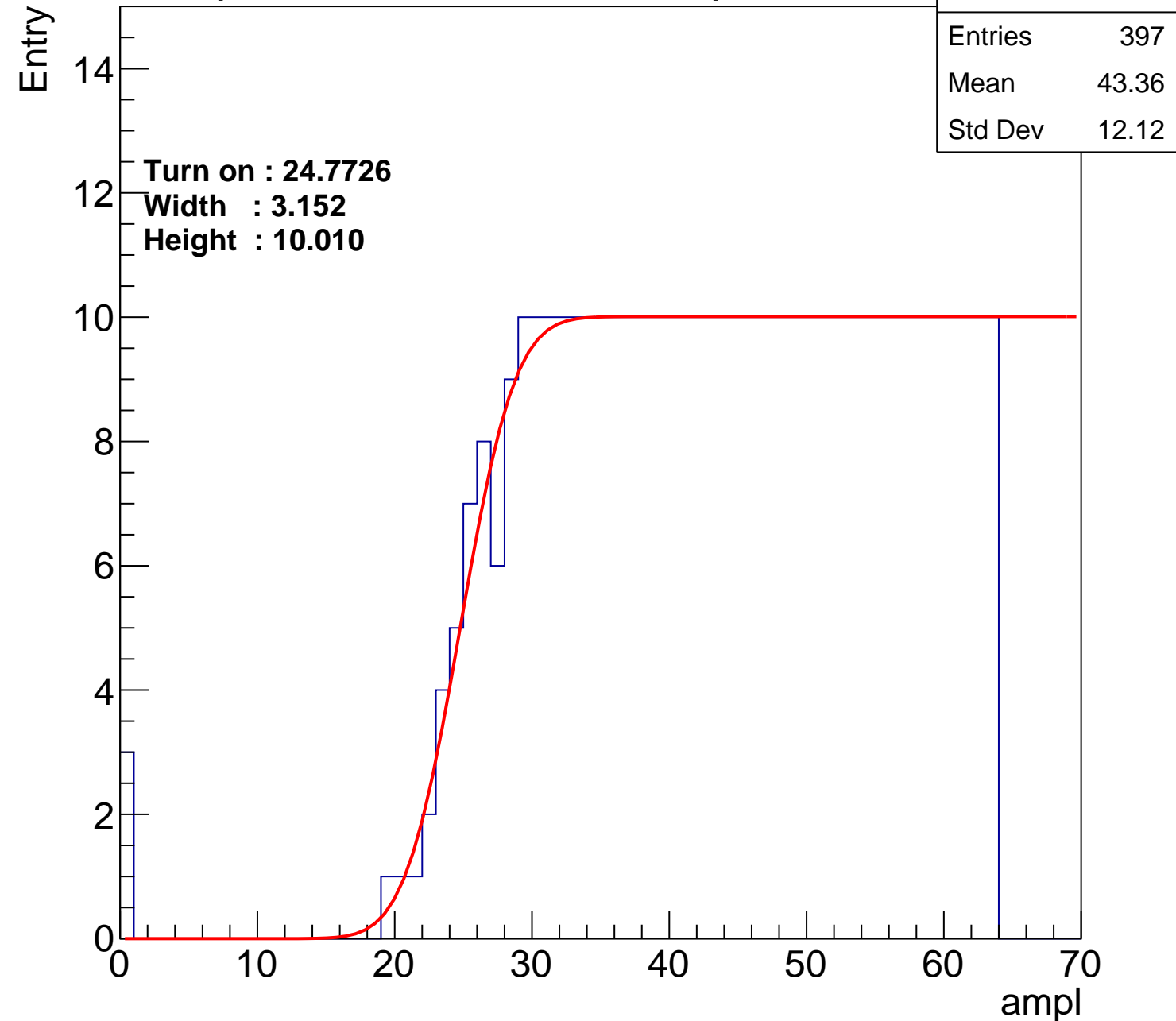
Width : 3.152

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.55
Std Dev	11.74

Turn on : 24.4712

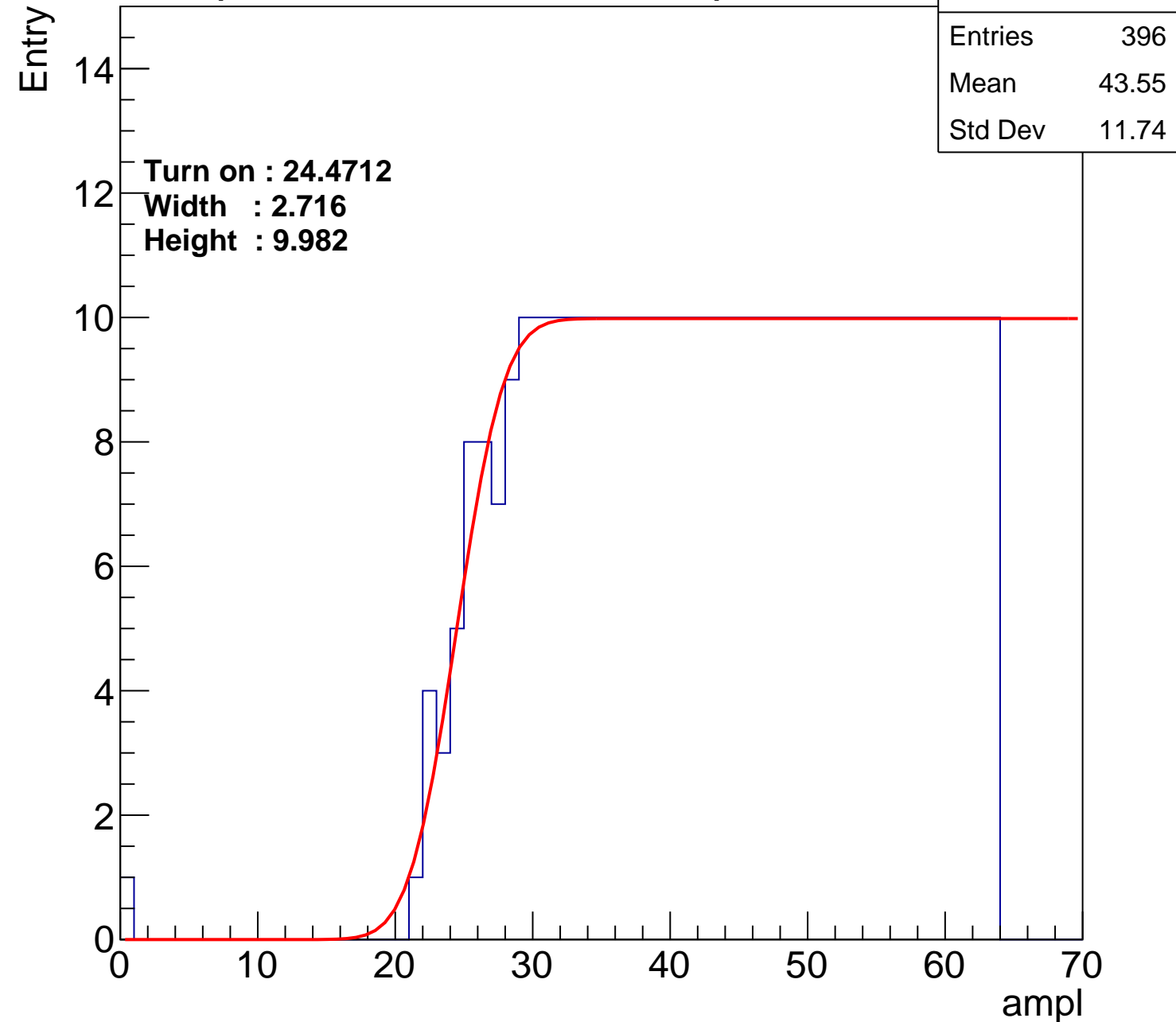
Width : 2.716

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.8627

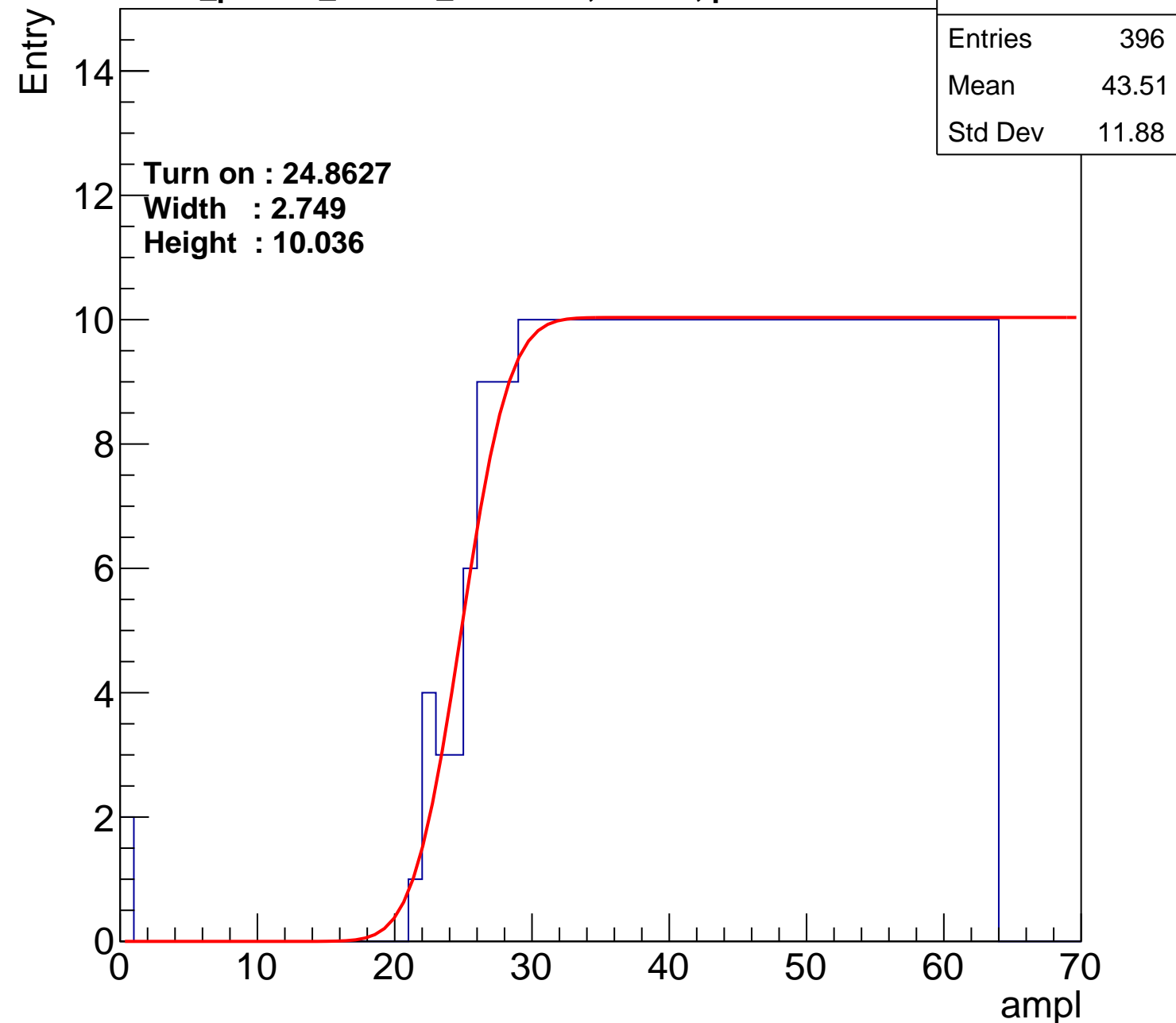
Width : 2.749

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	418
Mean	42.33
Std Dev	12.69

Turn on : 22.6180

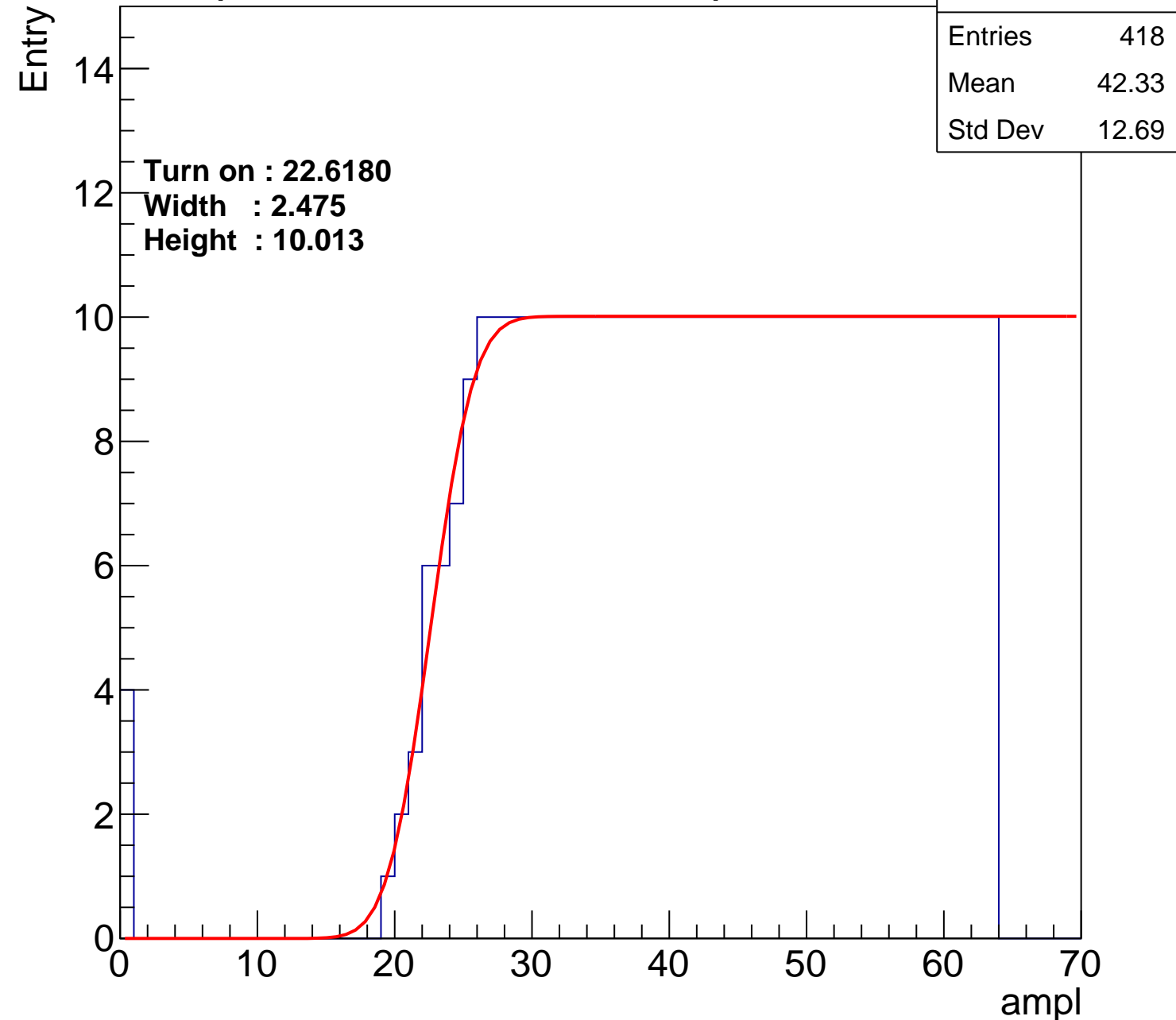
Width : 2.475

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.5
Std Dev	11.77

Turn on : 23.8898

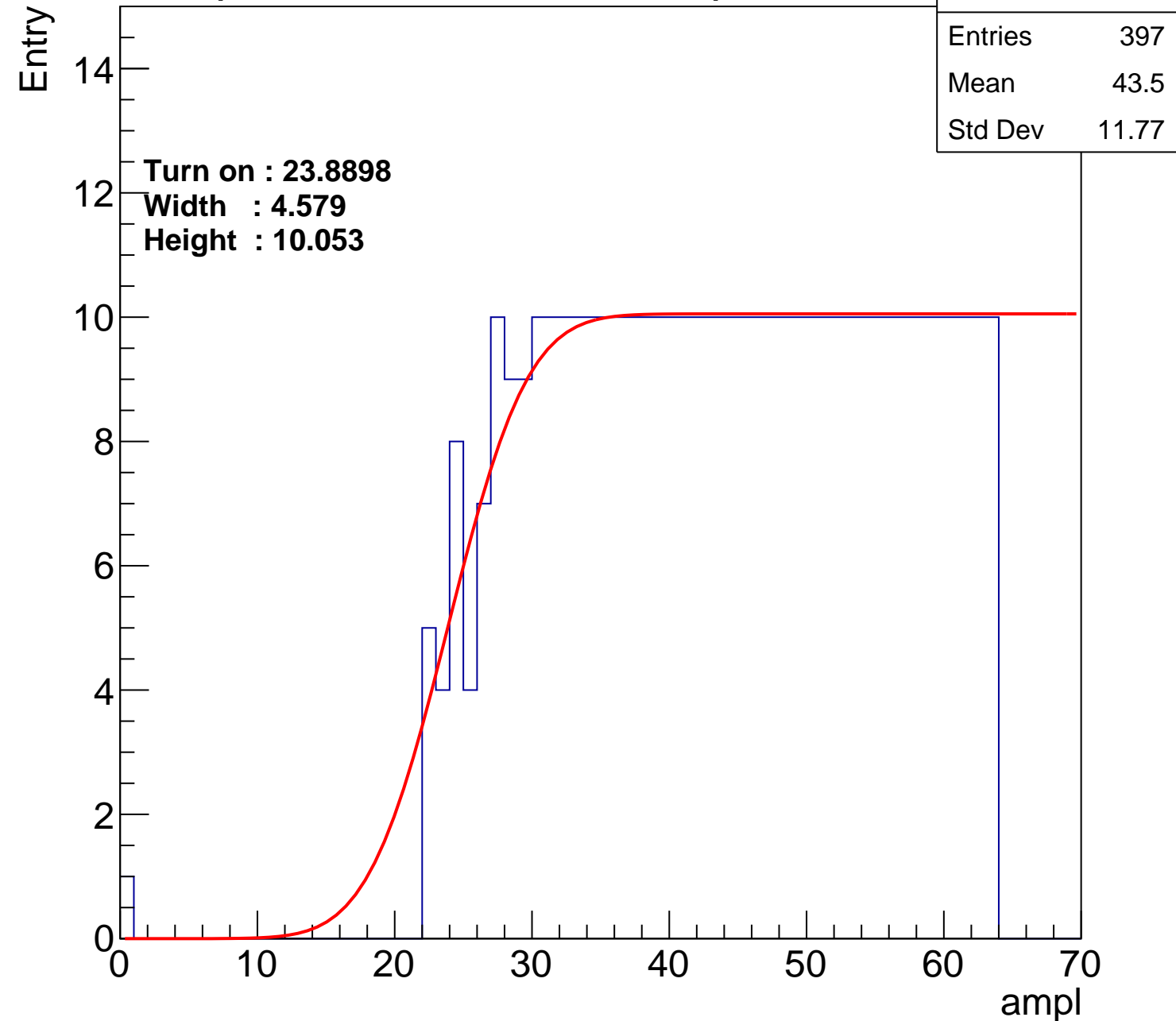
Width : 4.579

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch94

calib_packv5_042523_0143.root, FC#10, port B3

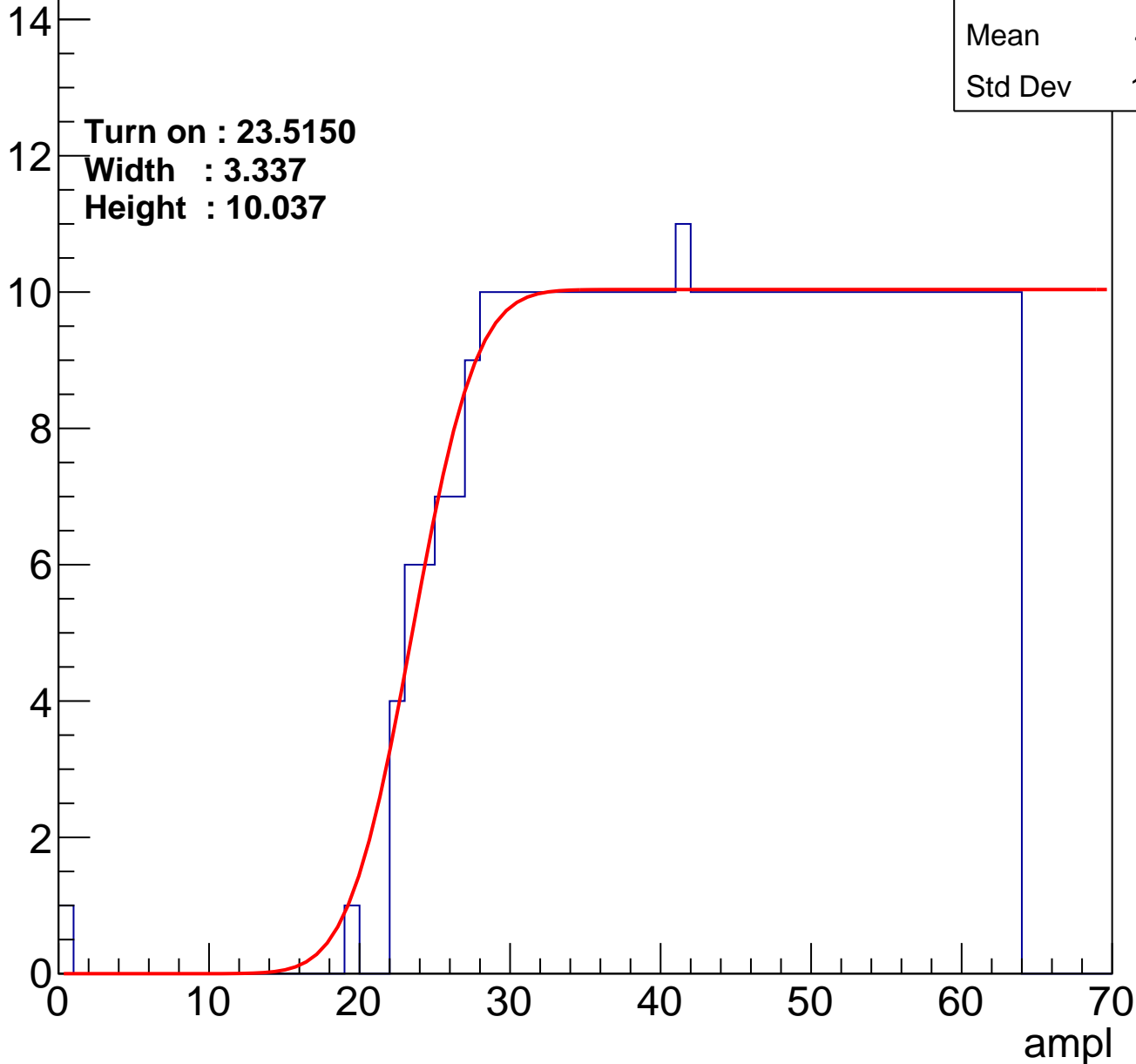
Entries	402
Mean	43.31
Std Dev	11.85

Turn on : 23.5150

Width : 3.337

Height : 10.037

Entry



B1L002S, U19-ch95

calib_packv5_042523_0143.root, FC#10, port B3

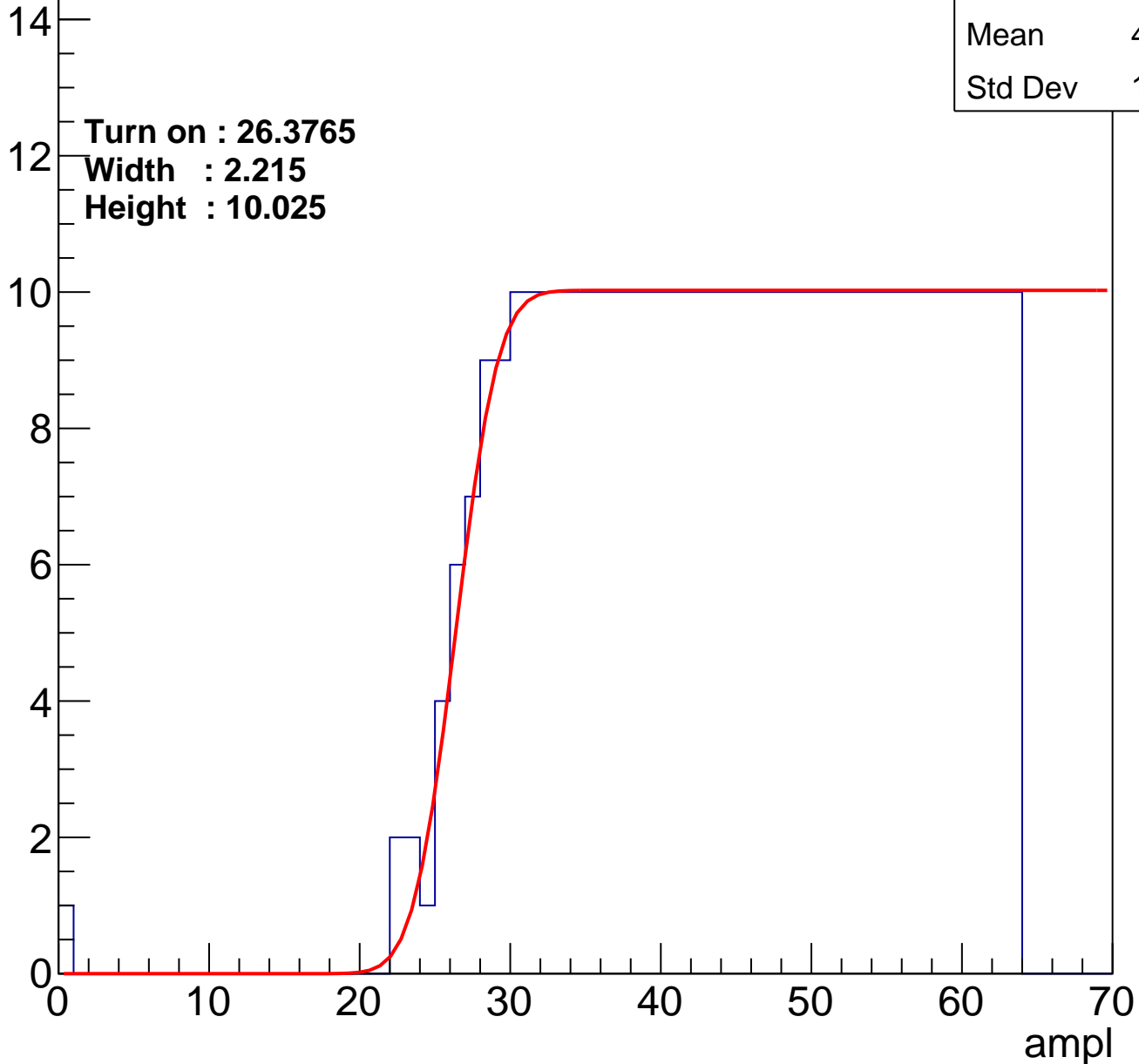
Entries	381
Mean	44.31
Std Dev	11.31

Turn on : 26.3765

Width : 2.215

Height : 10.025

Entry



B1L002S, U19-ch96

calib_packv5_042523_0143.root, FC#10, port B3

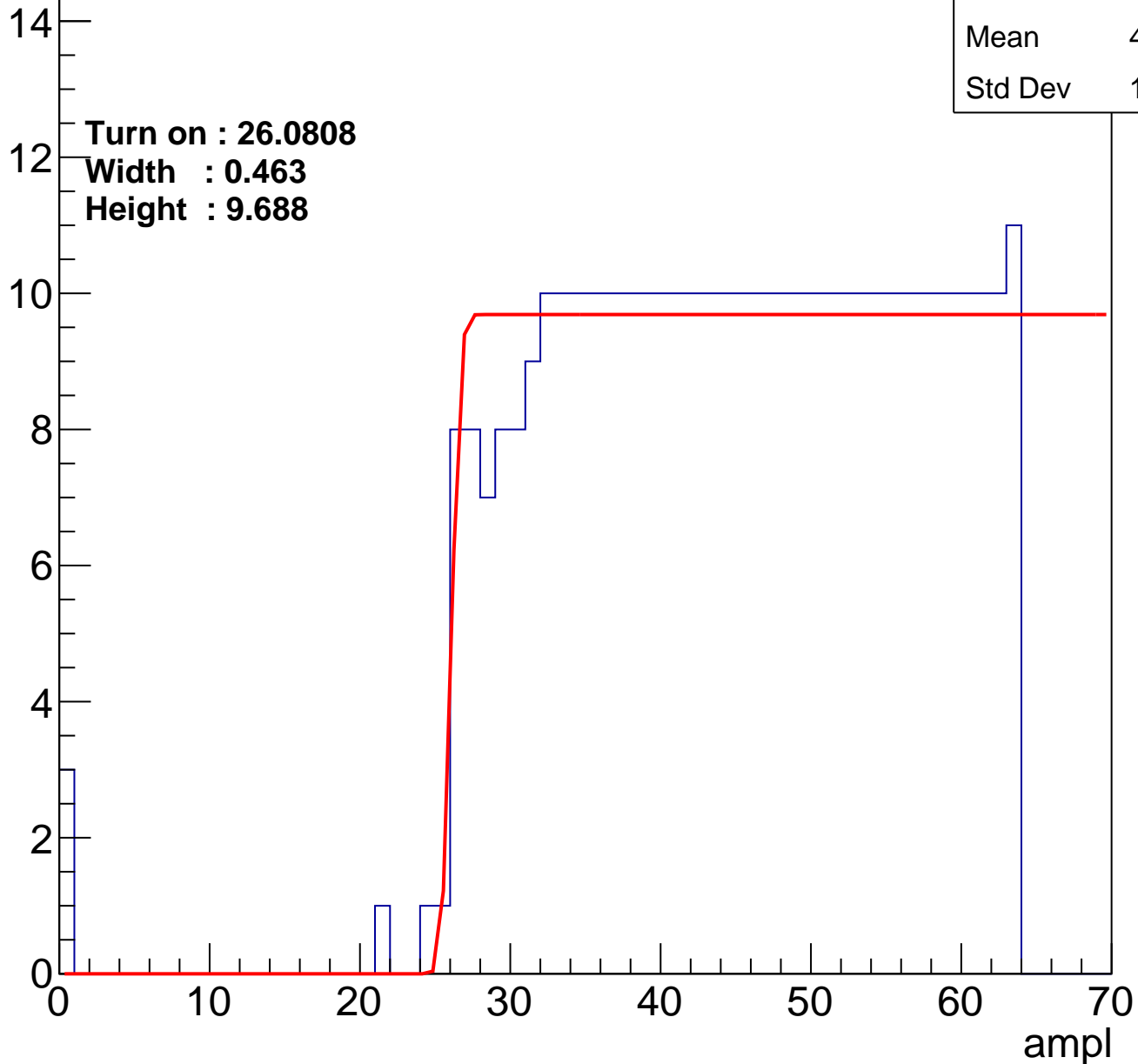
Entries	375
Mean	44.54
Std Dev	11.56

Turn on : 26.0808

Width : 0.463

Height : 9.688

Entry



B1L002S, U19-ch97

calib_packv5_042523_0143.root, FC#10, port B3

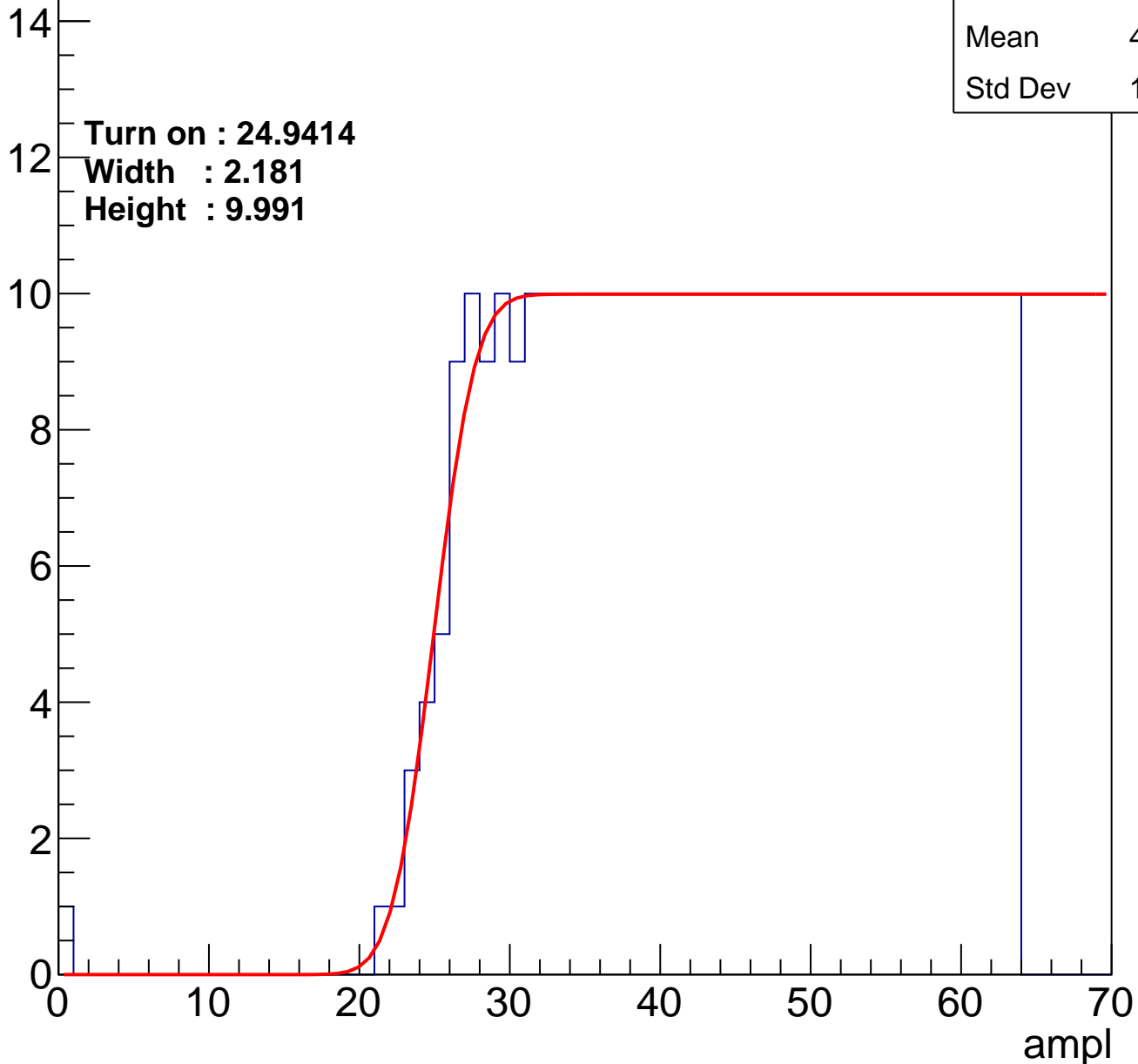
Entries	392
Mean	43.77
Std Dev	11.59

Turn on : 24.9414

Width : 2.181

Height : 9.991

Entry



B1L002S, U19-ch98

calib_packv5_042523_0143.root, FC#10, port B3

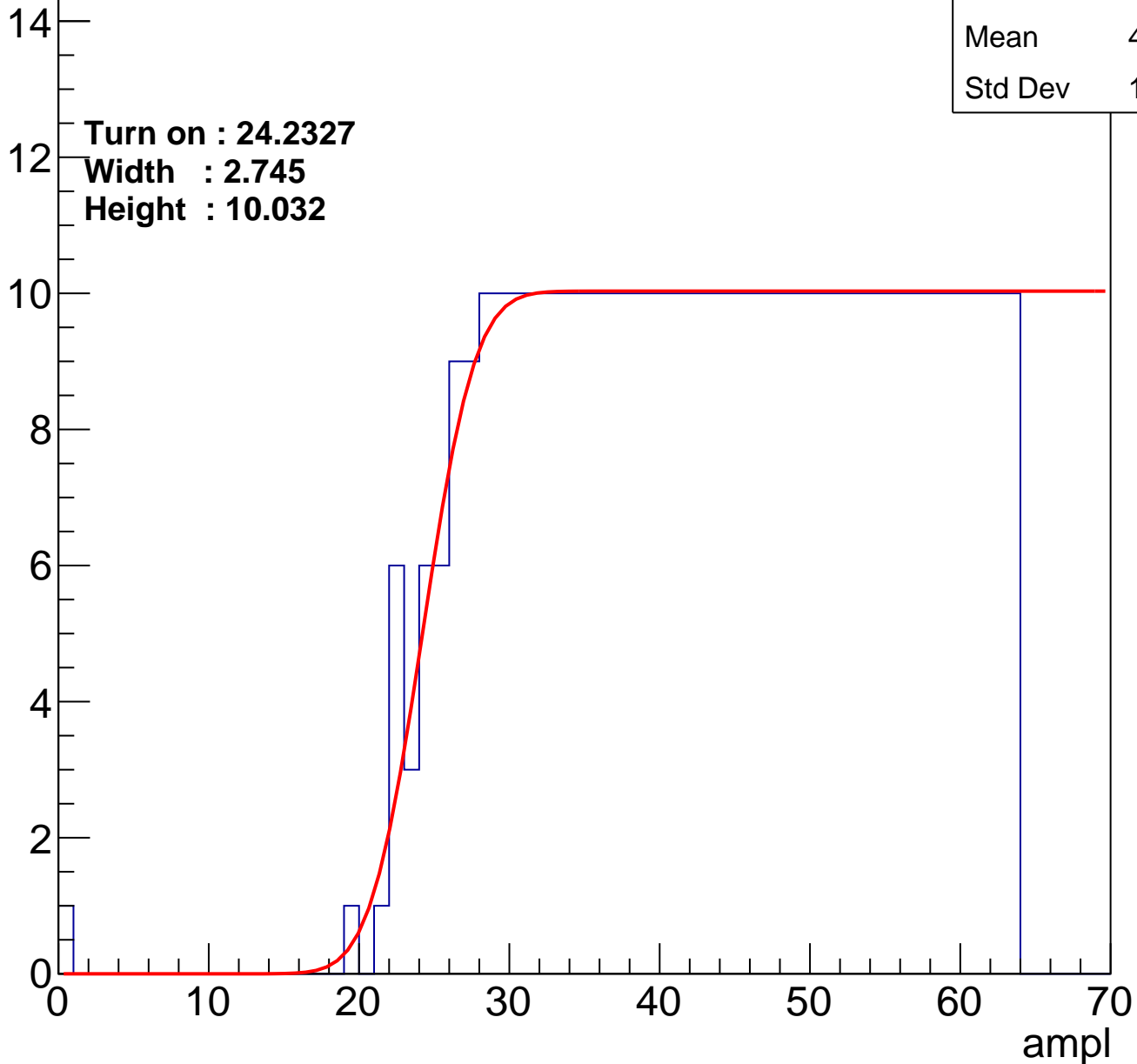
Entries	402
Mean	43.26
Std Dev	11.89

Turn on : 24.2327

Width : 2.745

Height : 10.032

Entry



B1L002S, U19-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.19
Std Dev	11.53

Turn on : 26.3062

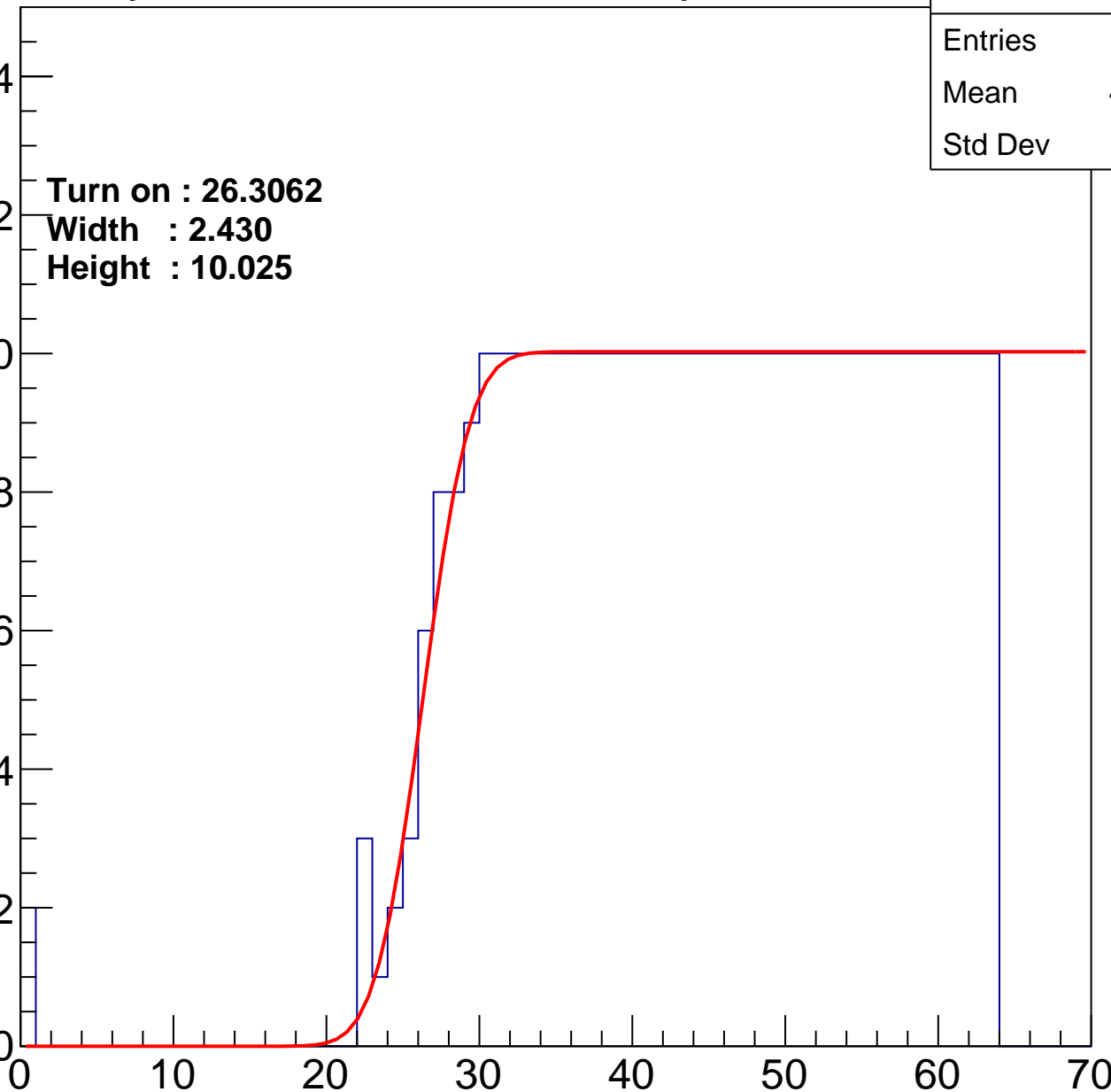
Width : 2.430

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch100

calib_packv5_042523_0143.root, FC#10, port B3

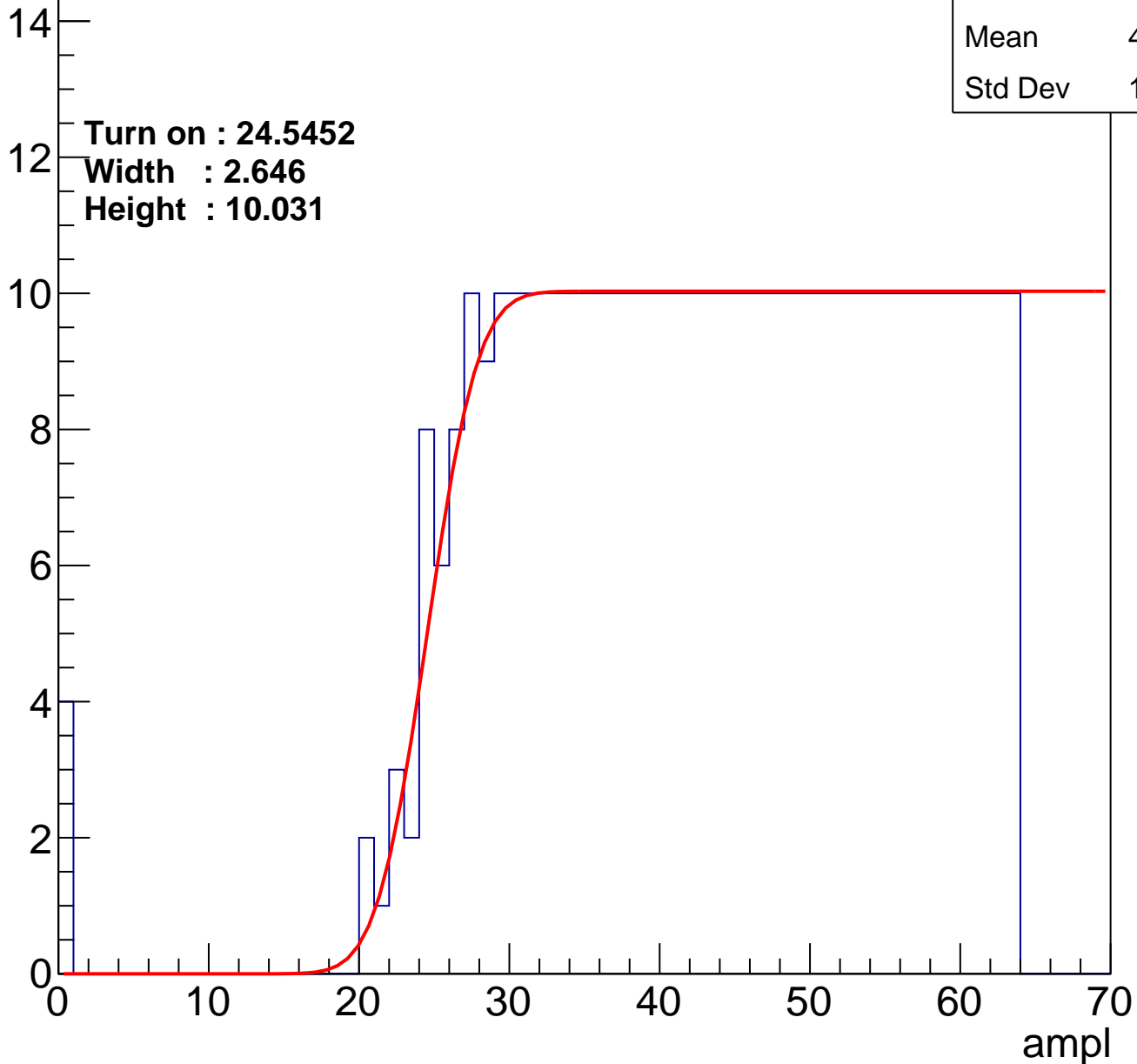
Entries	403
Mean	43.04
Std Dev	12.37

Turn on : 24.5452

Width : 2.646

Height : 10.031

Entry



B1L002S, U19-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.5590

Width : 2.928

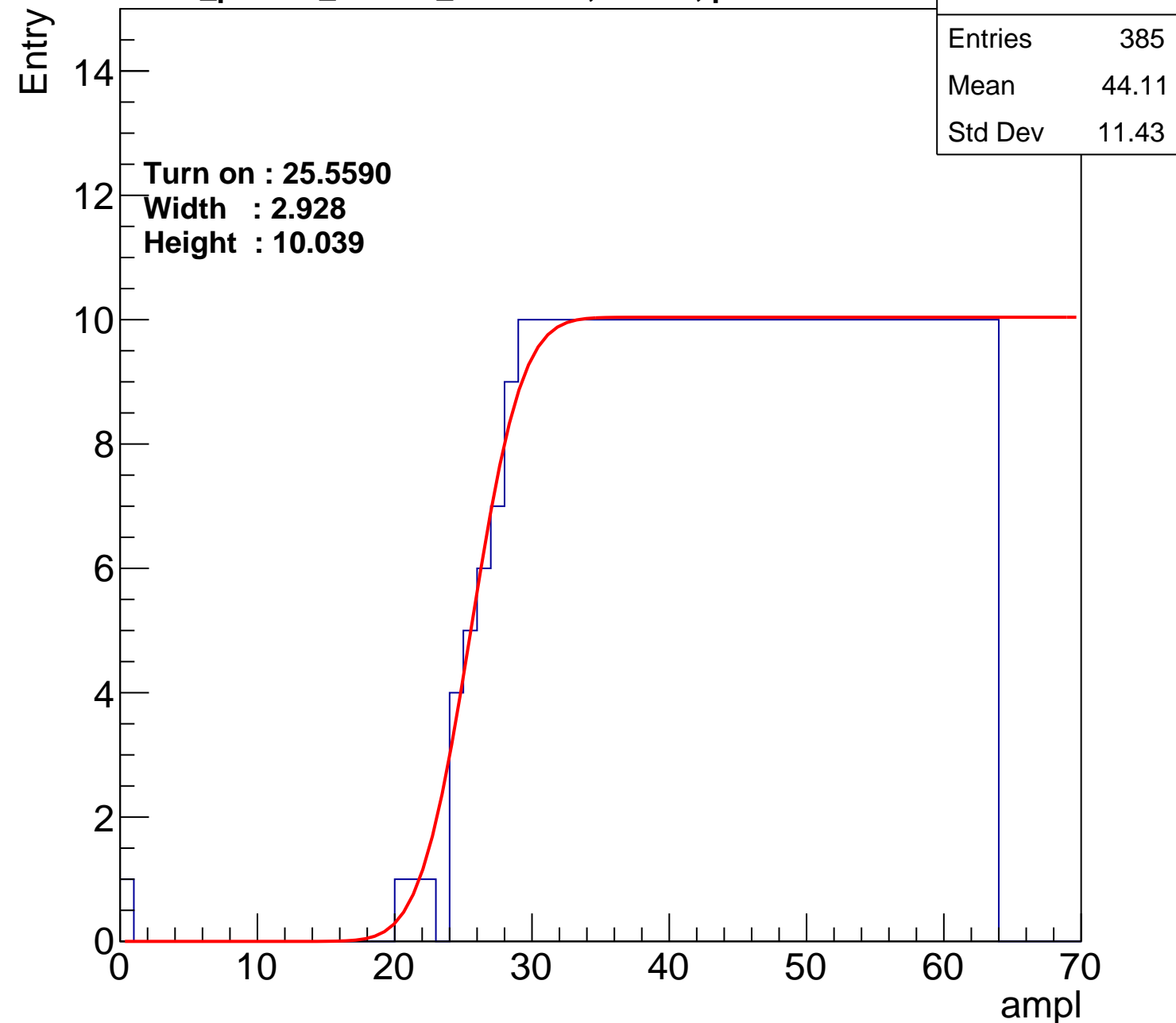
Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U19-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.15
Std Dev	12.29

Turn on : 24.4183

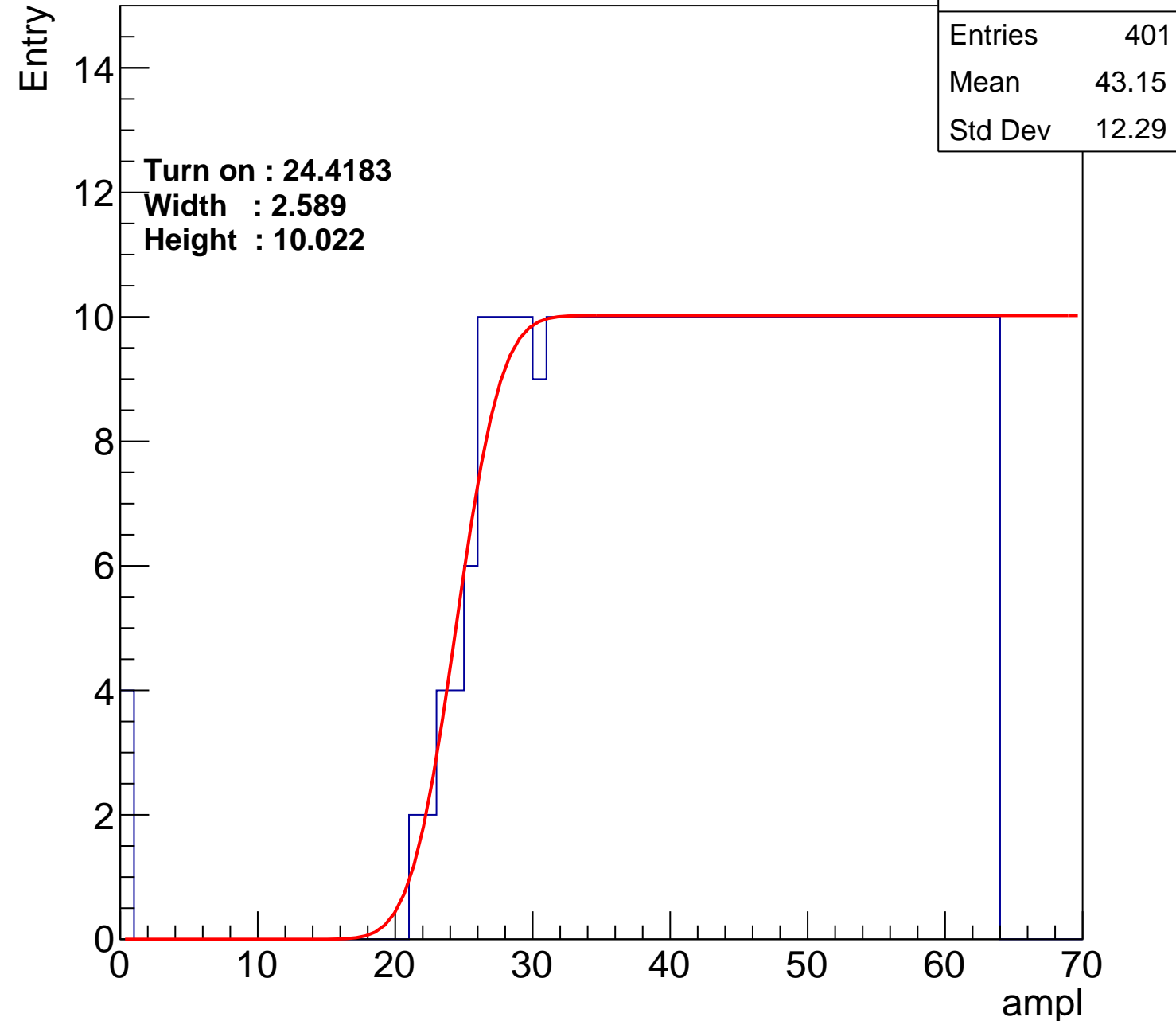
Width : 2.589

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.12
Std Dev	11.4

Turn on : 25.6456

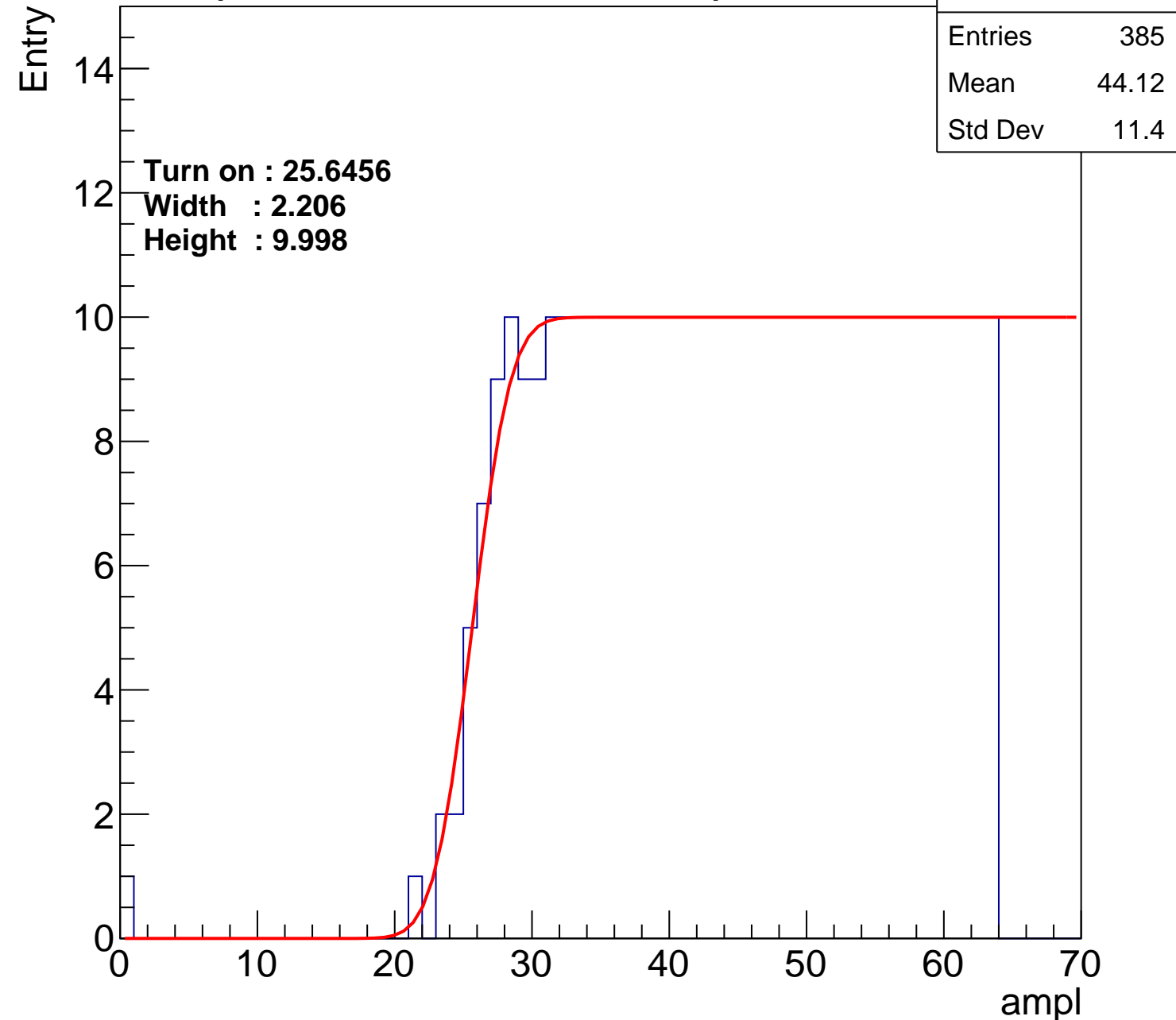
Width : 2.206

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 25.3509

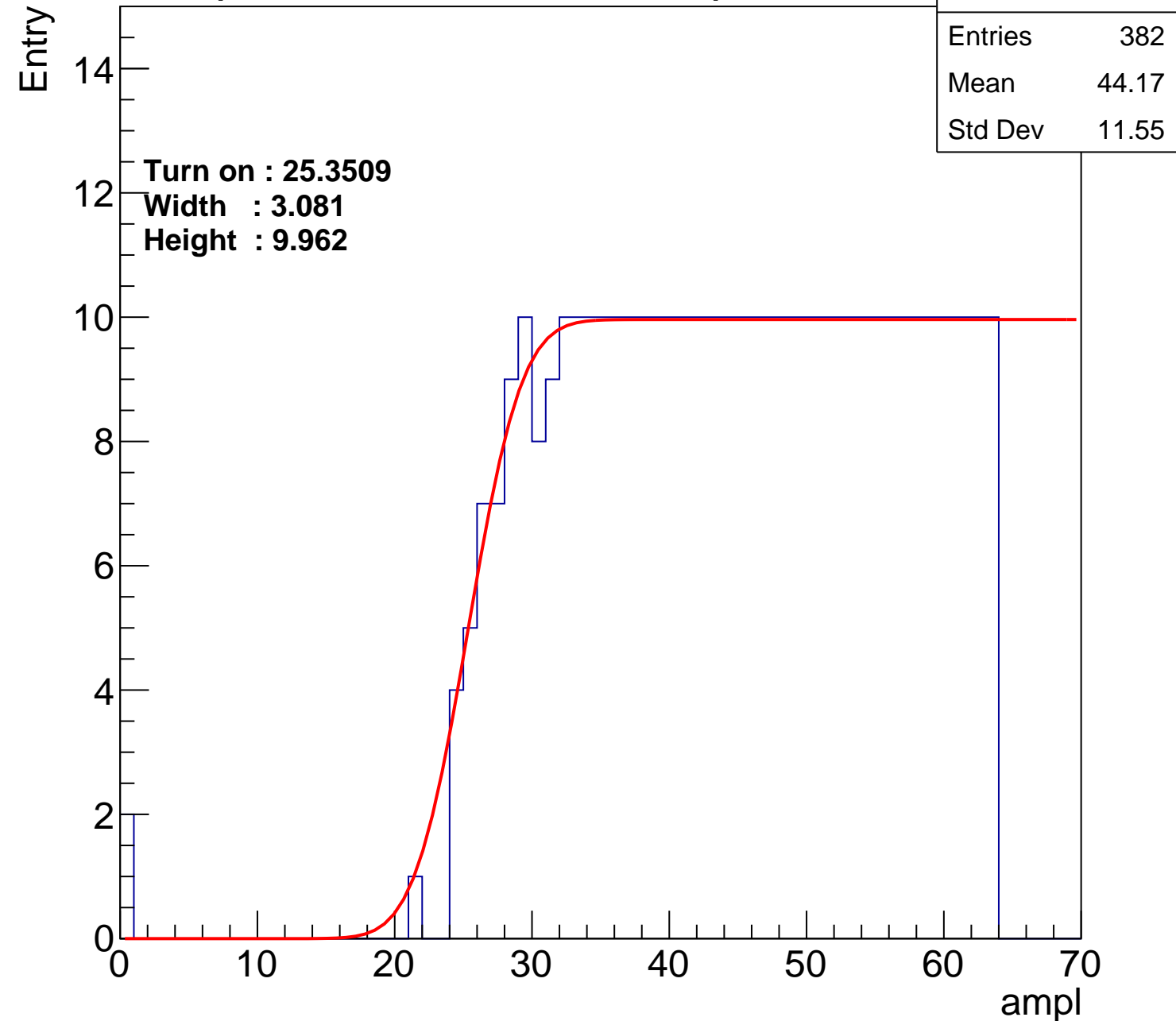
Width : 3.081

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	43.97
Std Dev	12.27

Turn on : 26.8136

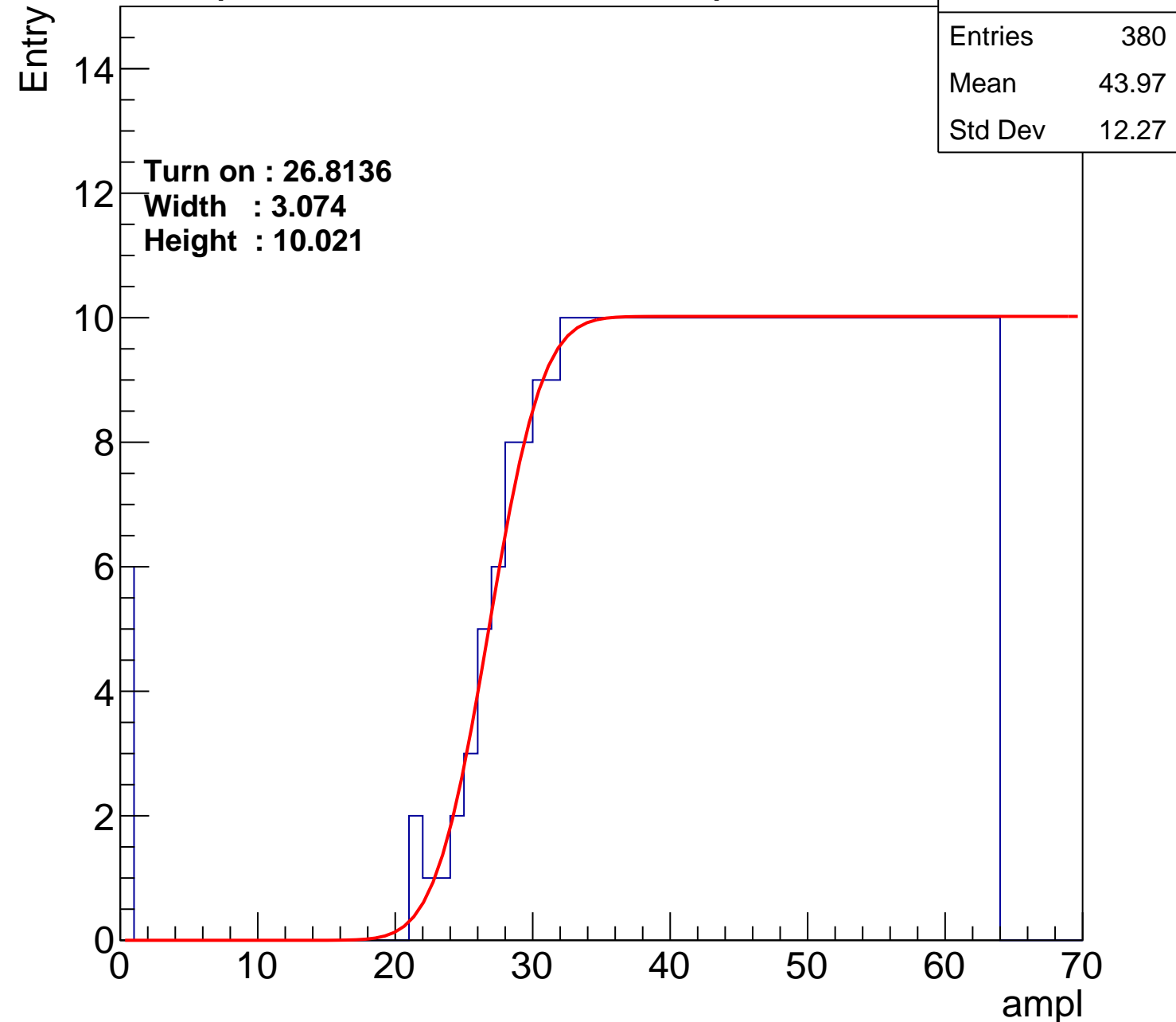
Width : 3.074

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.07
Std Dev	12.32

Turn on : 24.1425

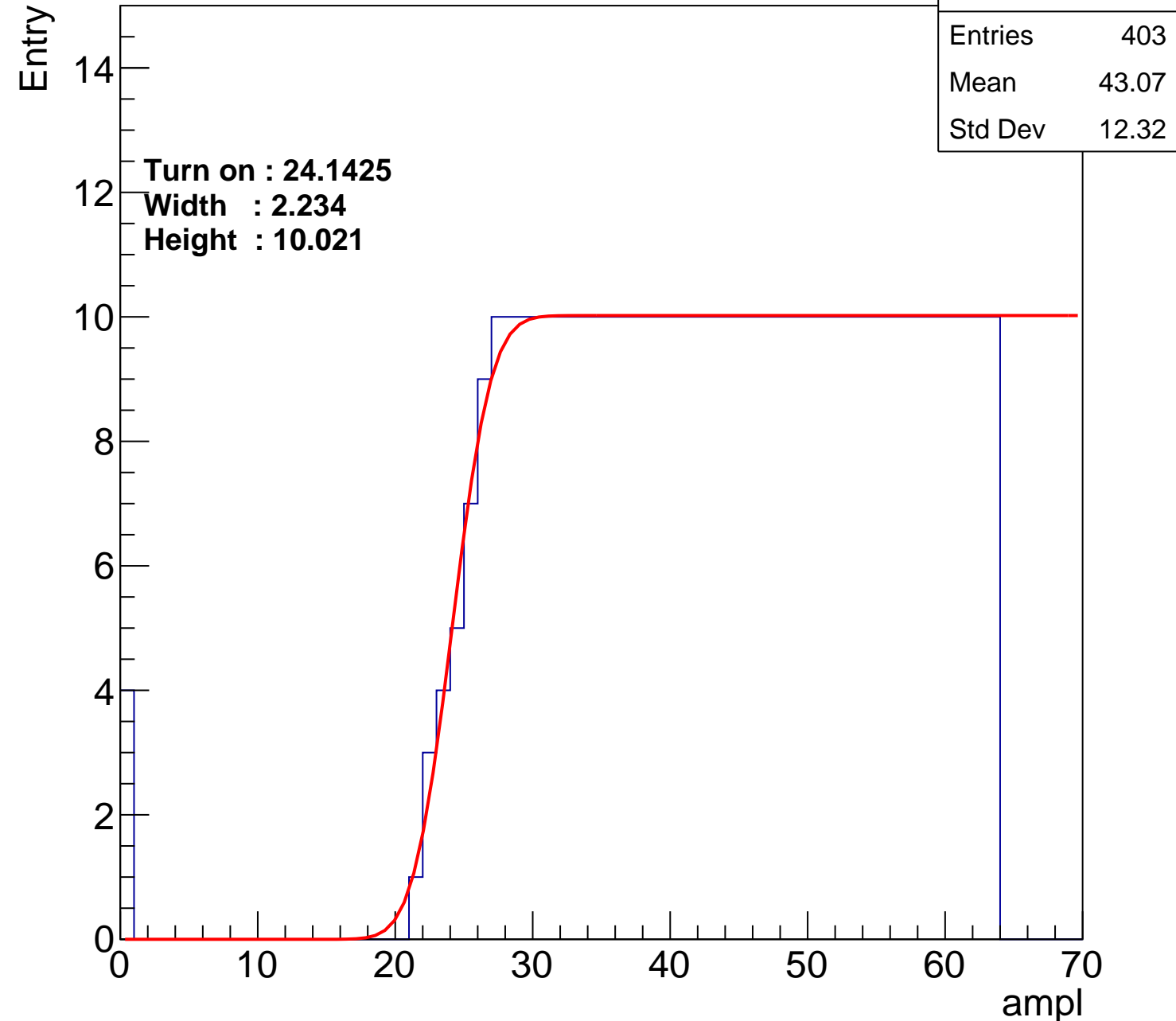
Width : 2.234

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch107

calib_packv5_042523_0143.root, FC#10, port B3

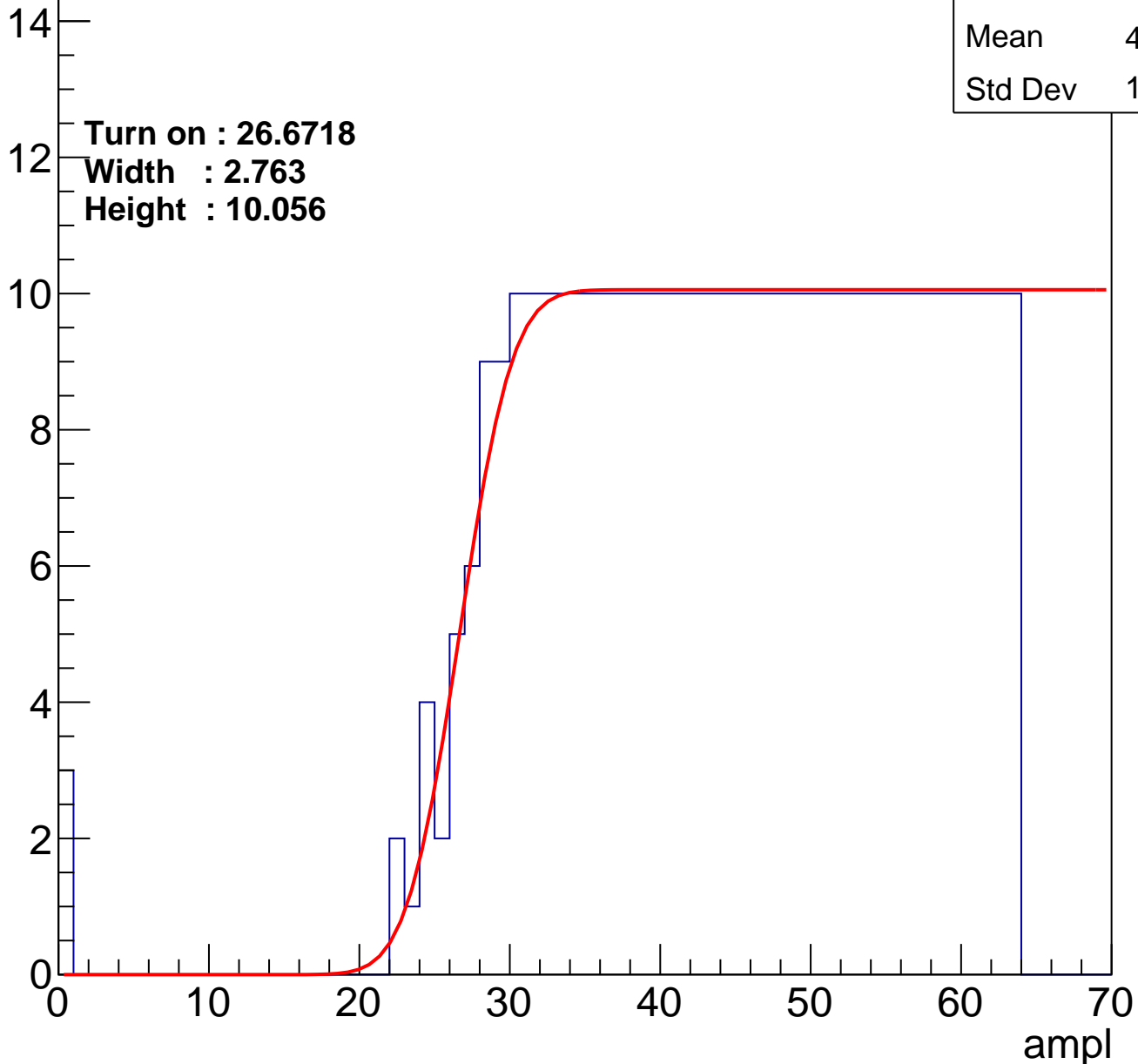
Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.6718

Width : 2.763

Height : 10.056

Entry



B1L002S, U19-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.47
Std Dev	11.94

Turn on : 25.0180

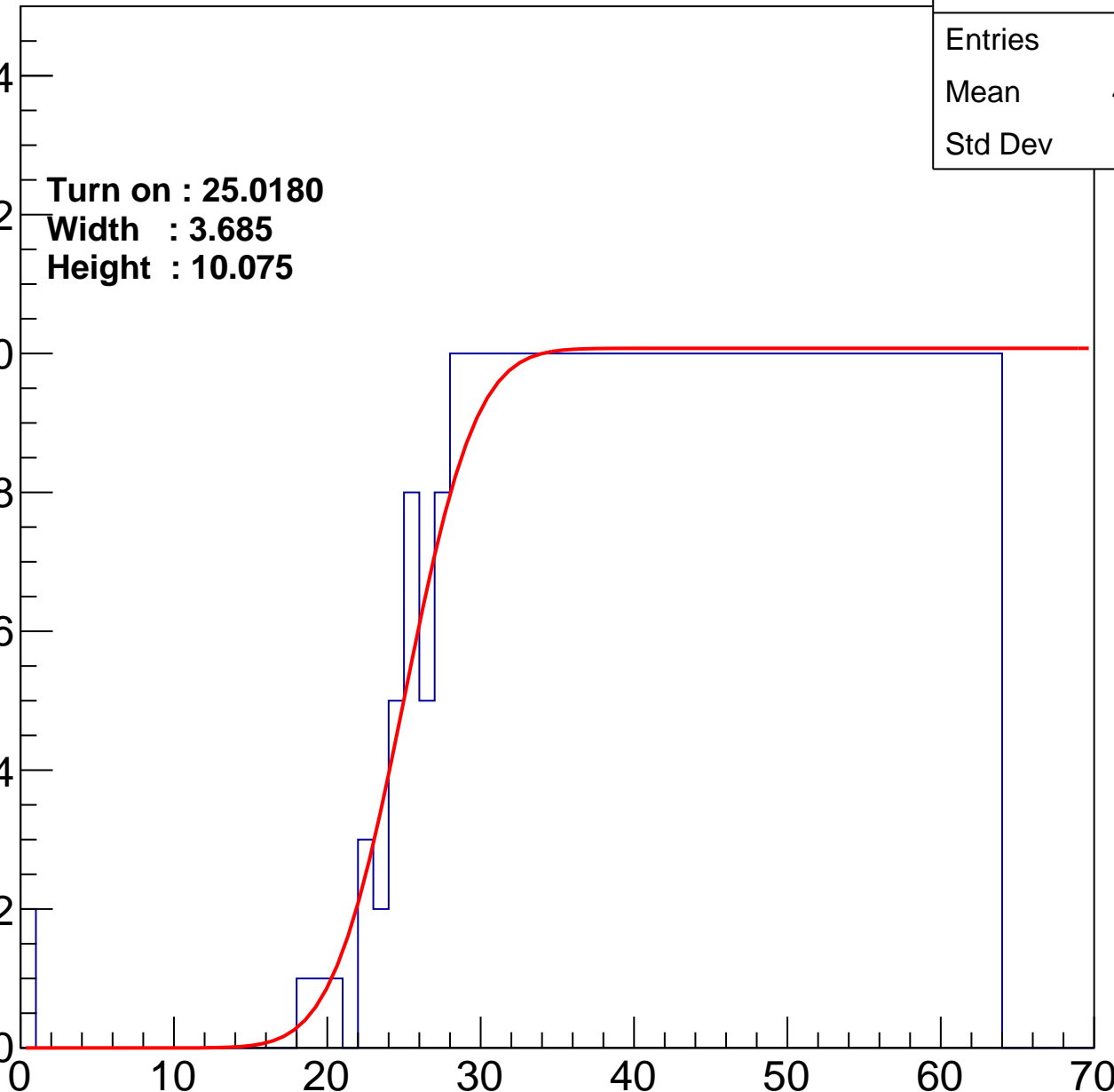
Width : 3.685

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.4648

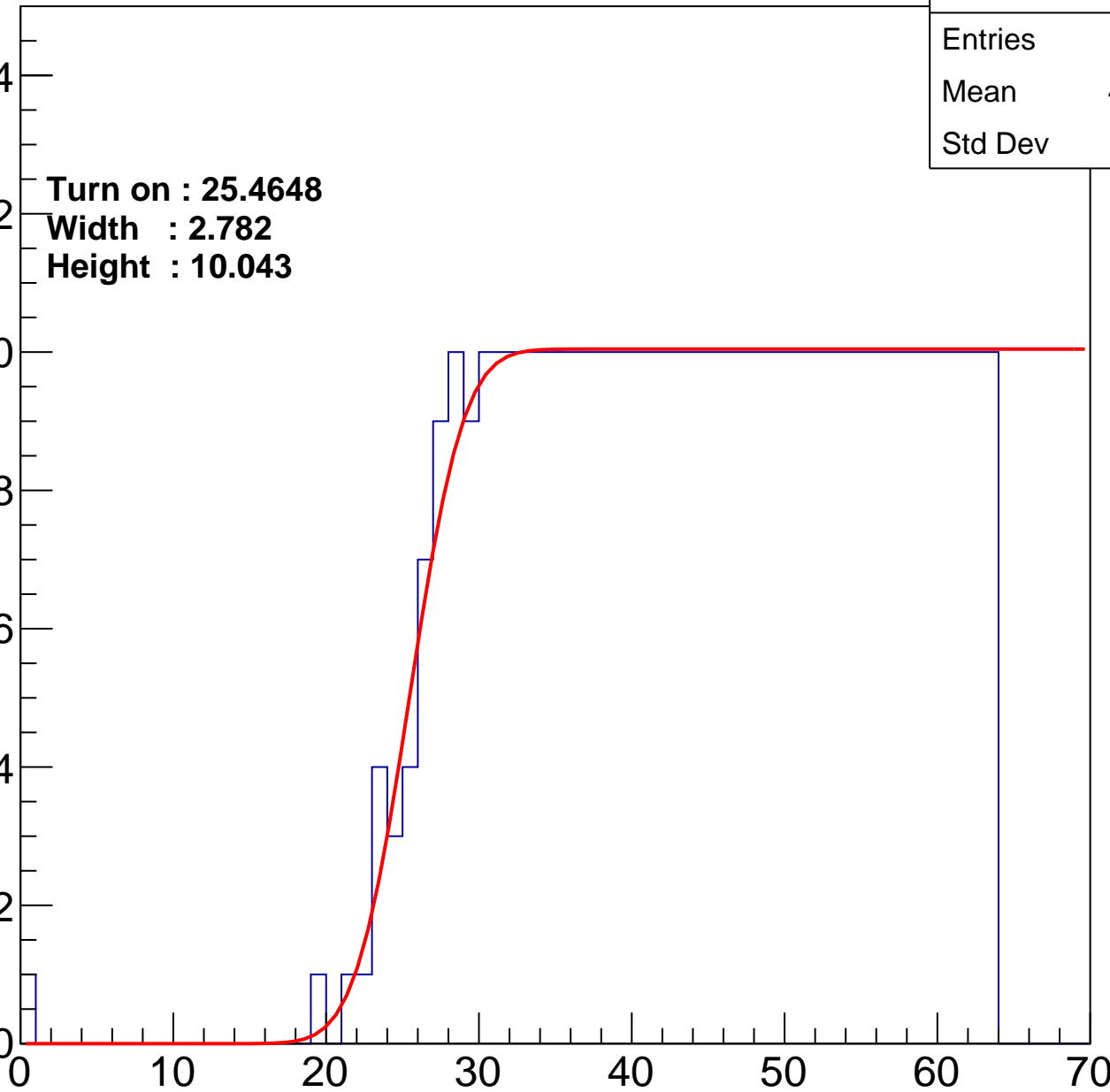
Width : 2.782

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.42
Std Dev	12.19

Turn on : 24.9433

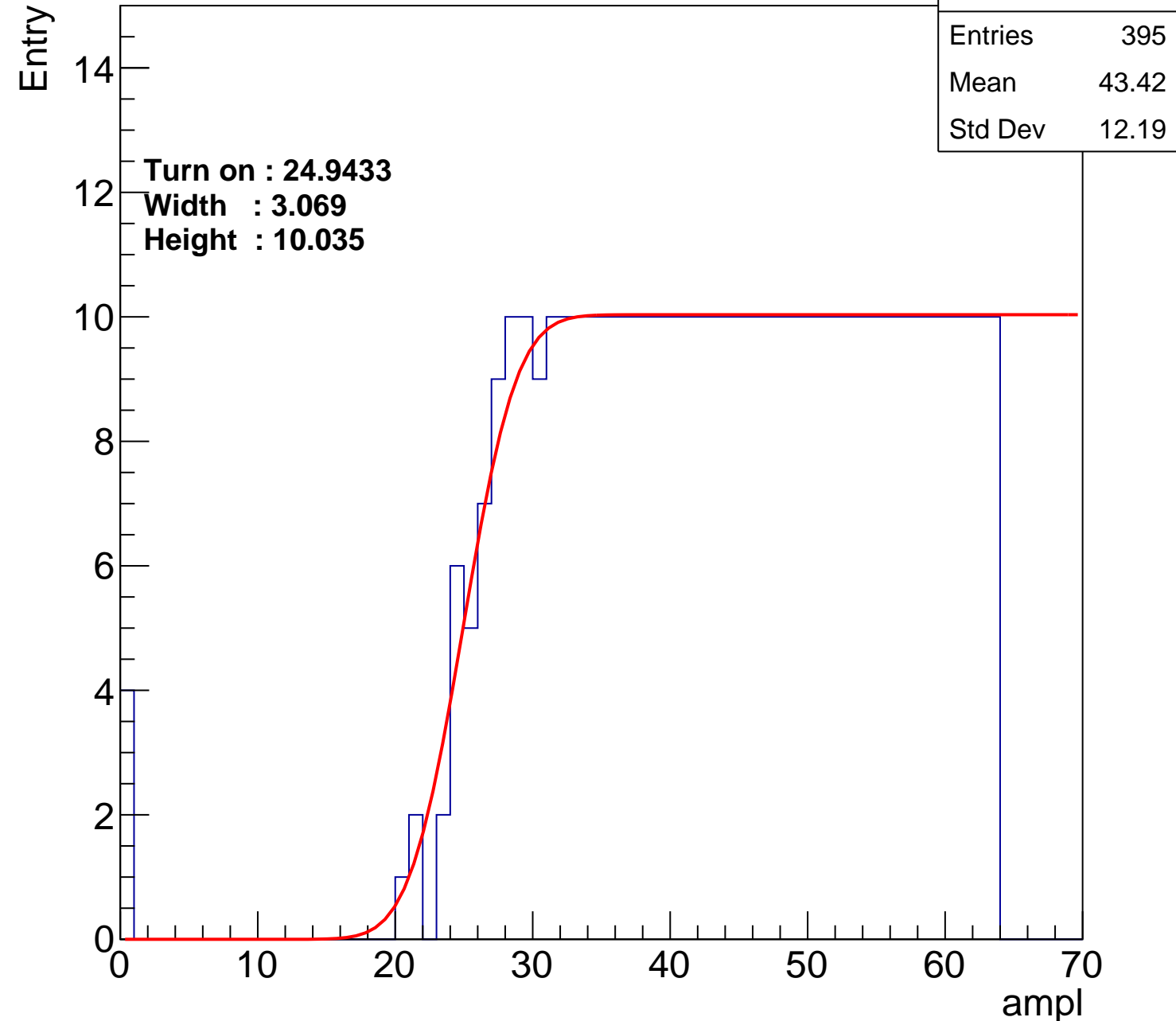
Width : 3.069

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 26.9211

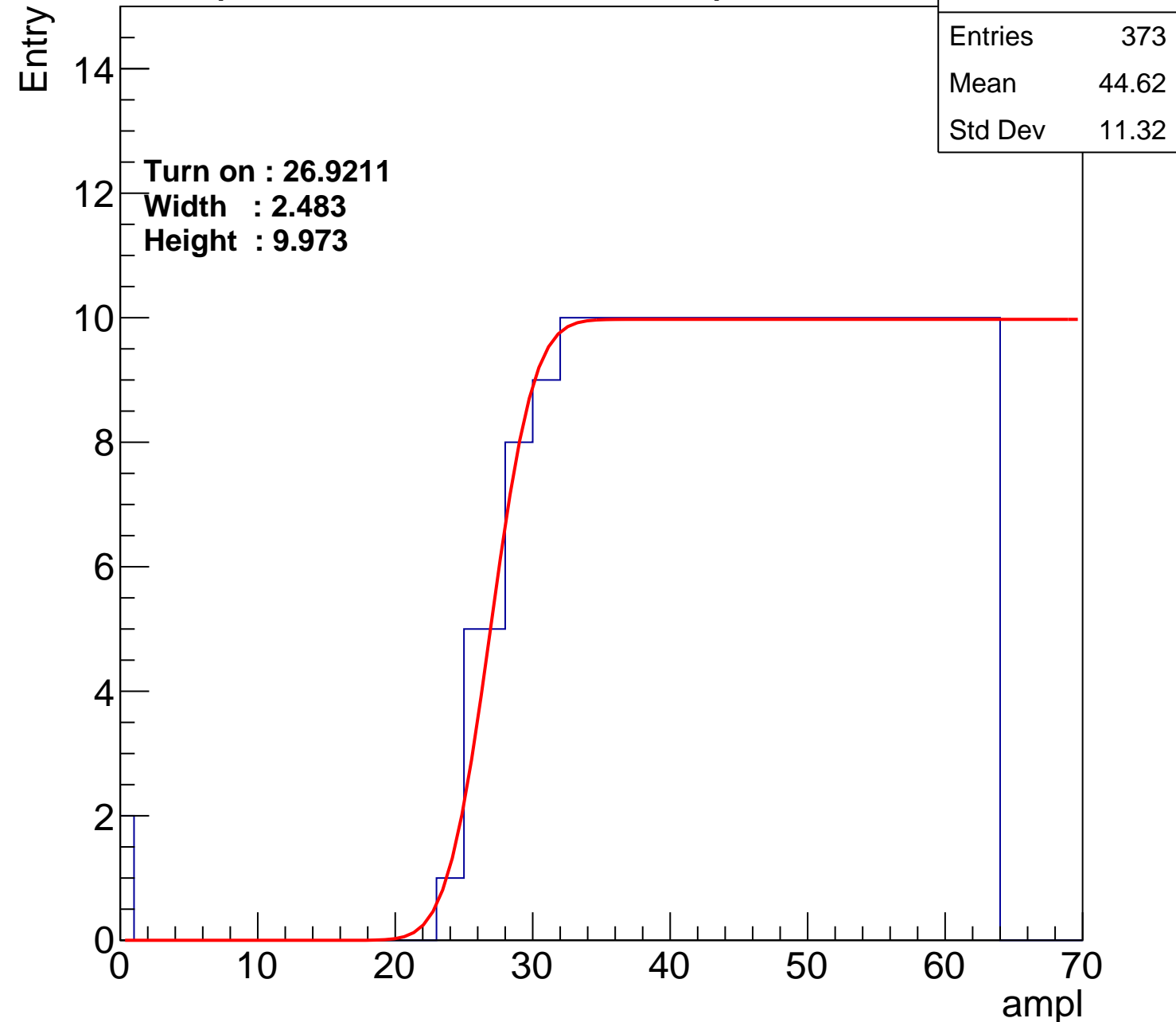
Width : 2.483

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	43
Std Dev	12.06

Turn on : 23.7518

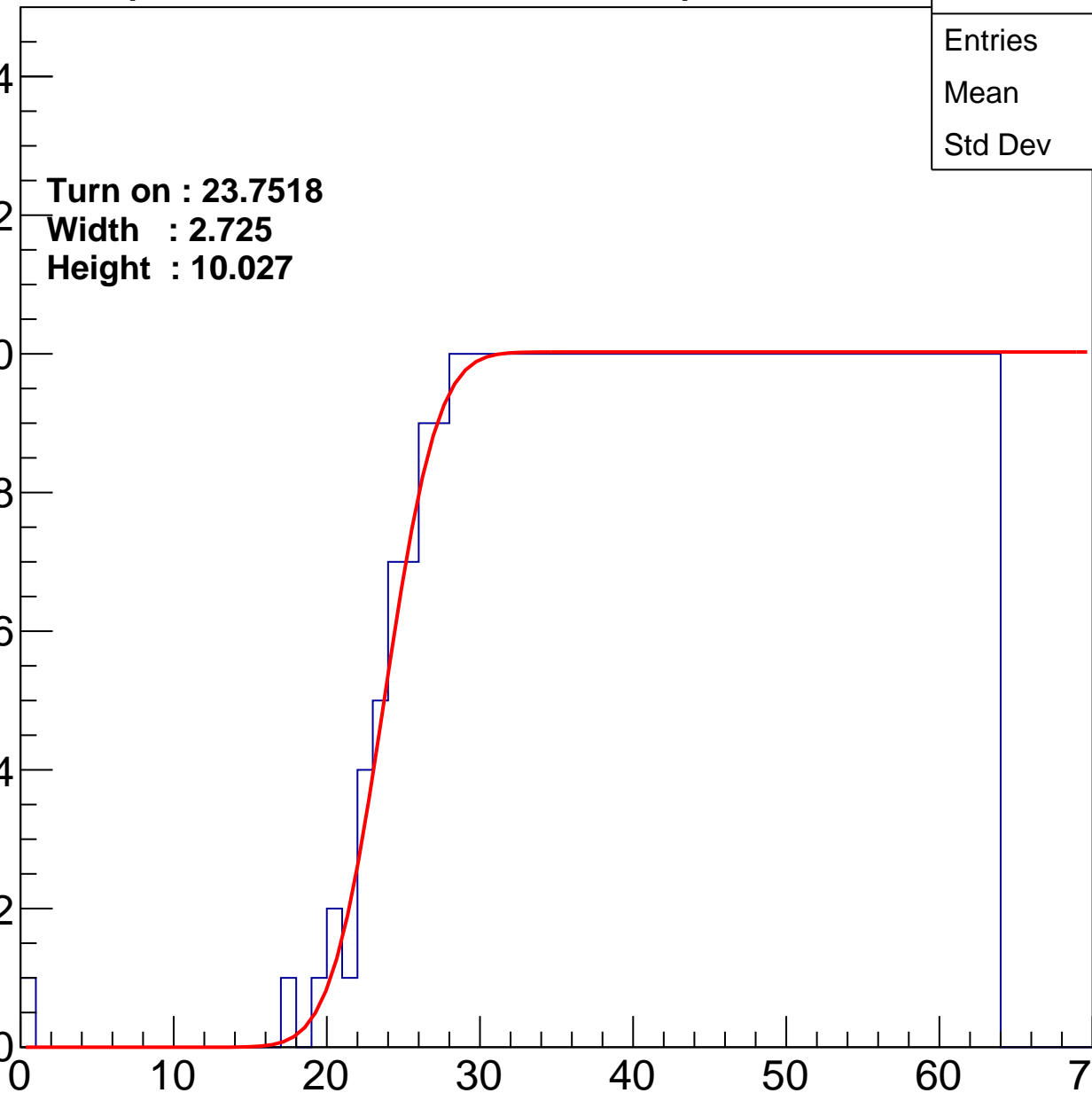
Width : 2.725

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch113

calib_packv5_042523_0143.root, FC#10, port B3

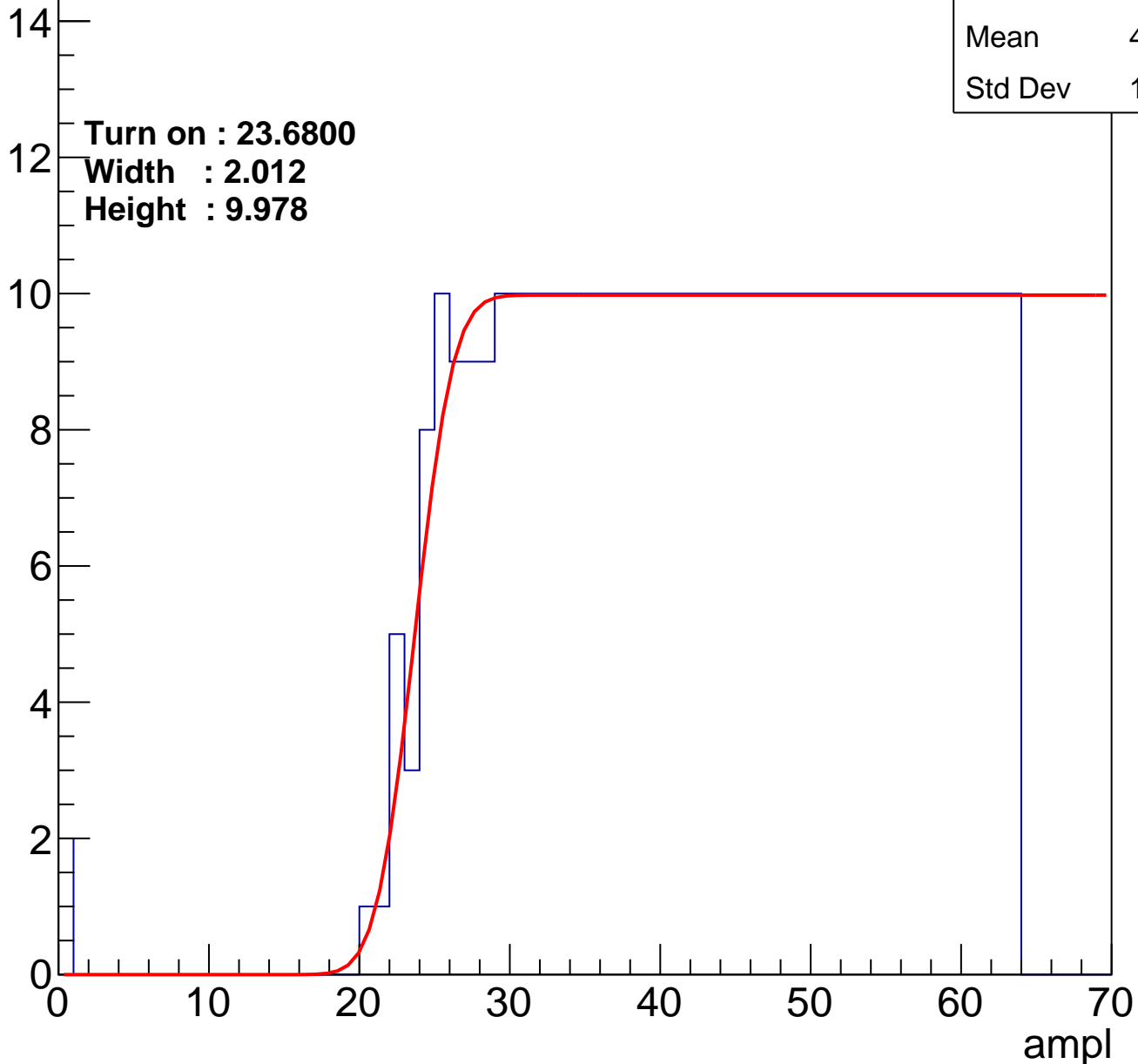
Entries	407
Mean	42.98
Std Dev	12.14

Turn on : 23.6800

Width : 2.012

Height : 9.978

Entry



B1L002S, U19-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.44
Std Dev	11.94

Turn on : 24.9771

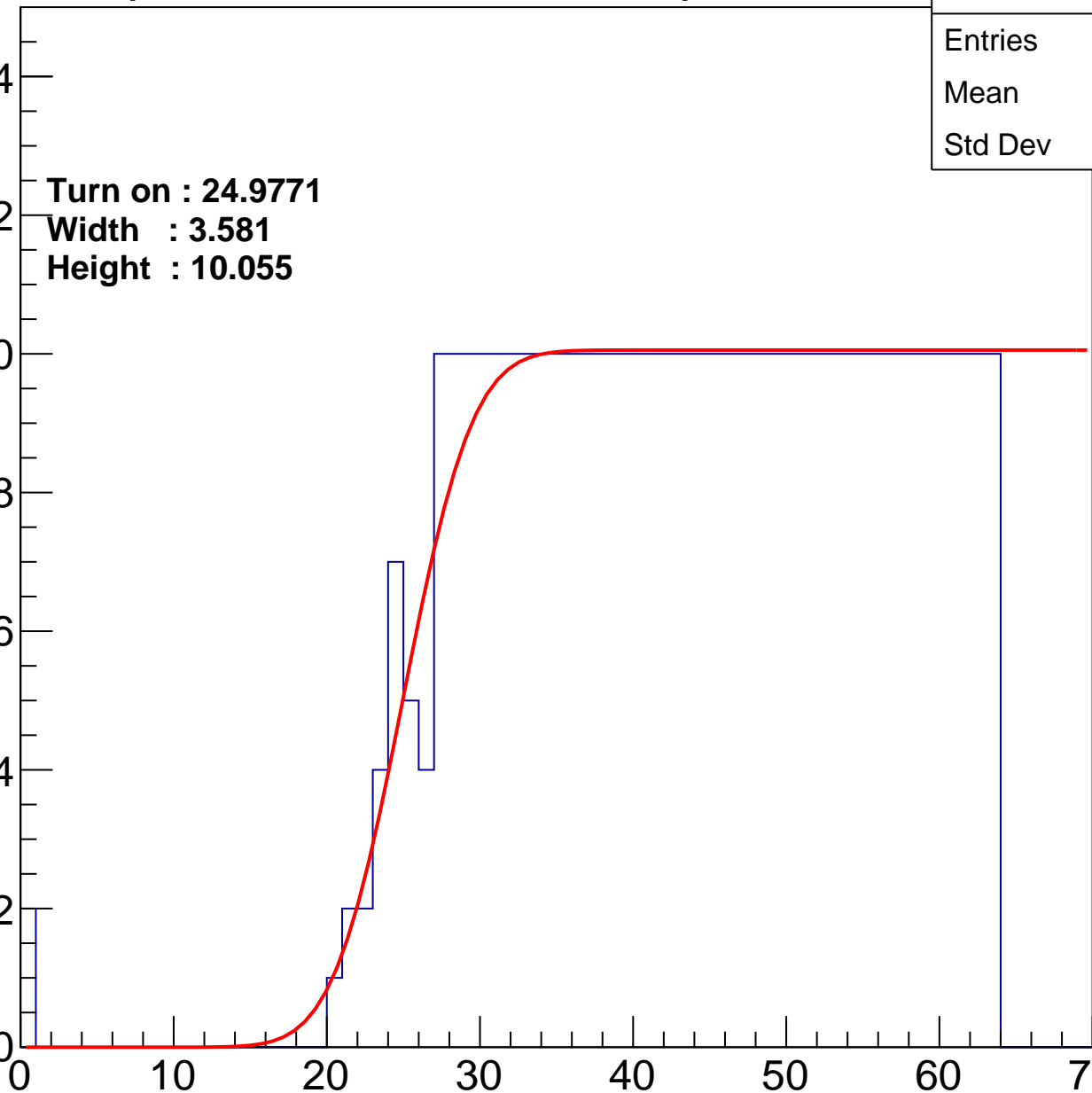
Width : 3.581

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.34
Std Dev	11.99

Turn on : 24.5093

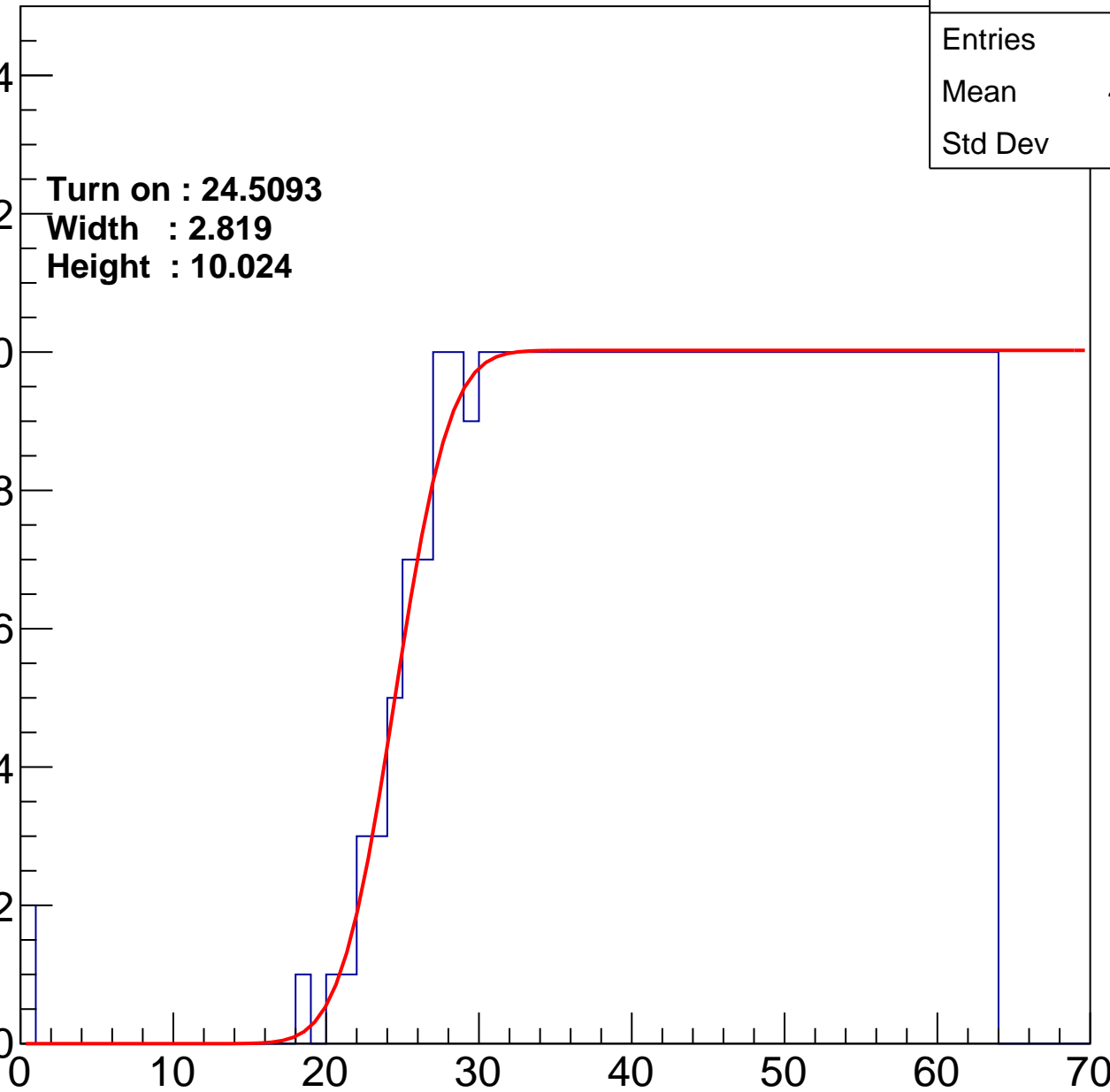
Width : 2.819

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.8
Std Dev	11.68

Turn on : 25.3508

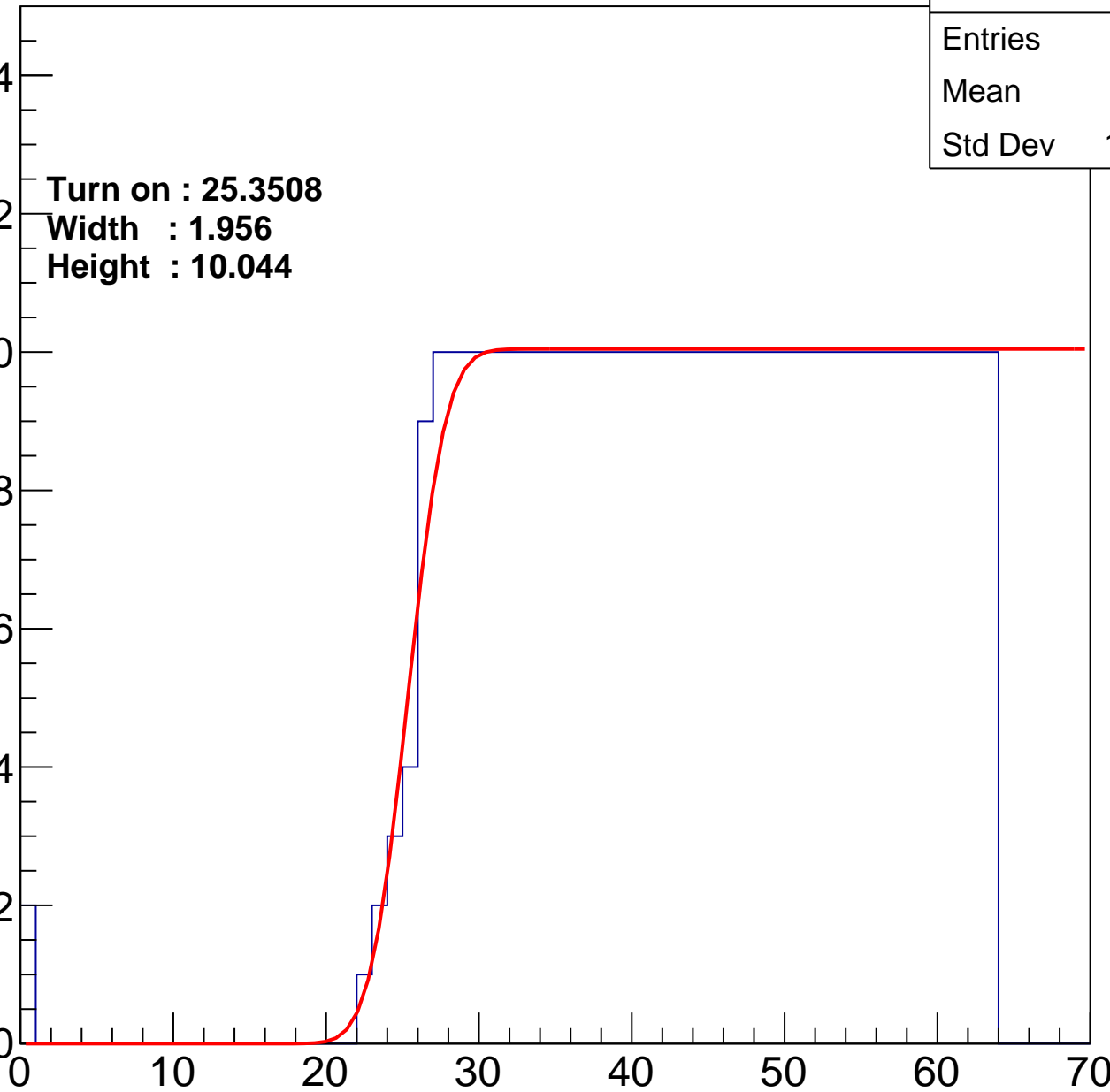
Width : 1.956

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.2682

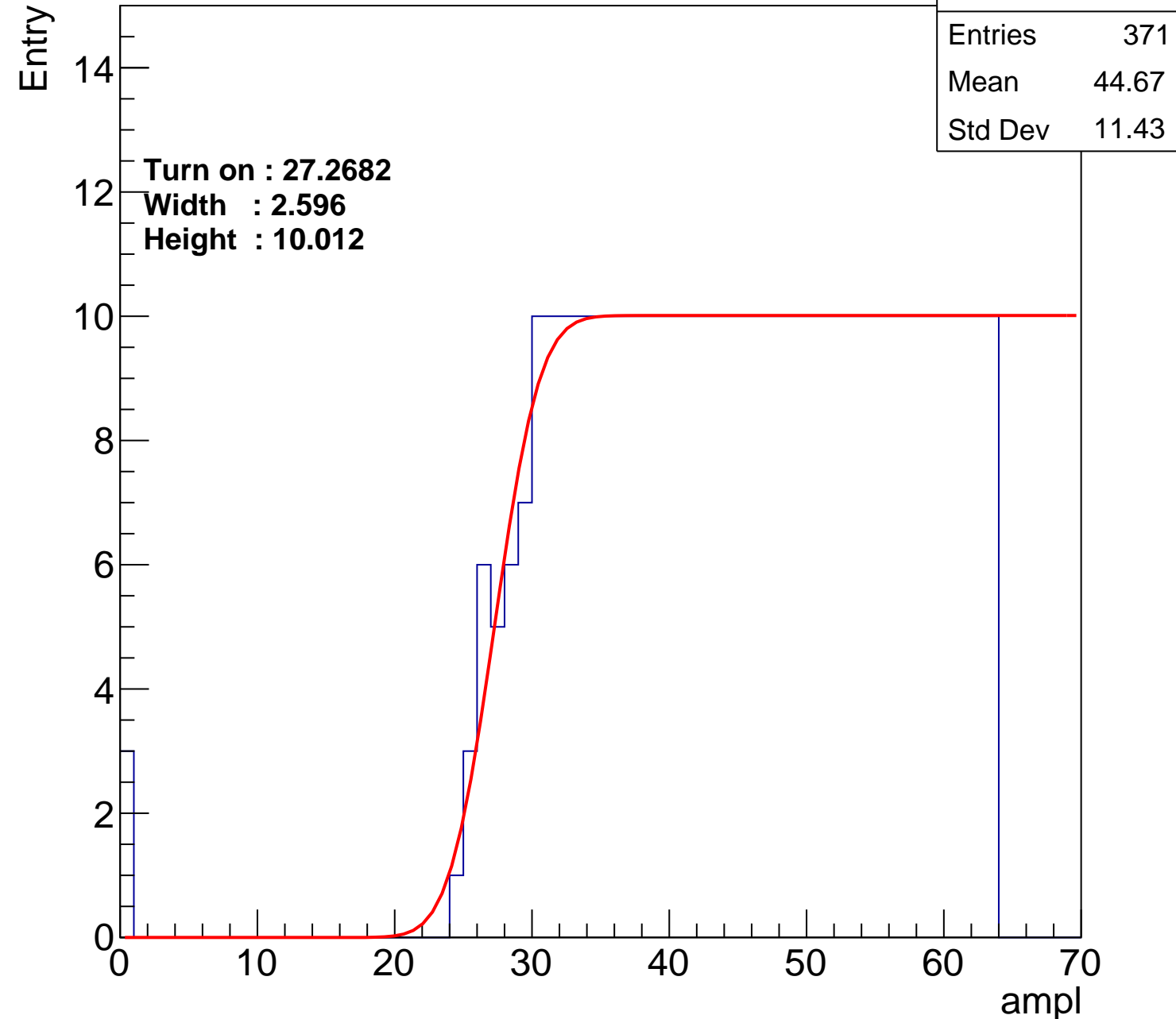
Width : 2.596

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch118

calib_packv5_042523_0143.root, FC#10, port B3

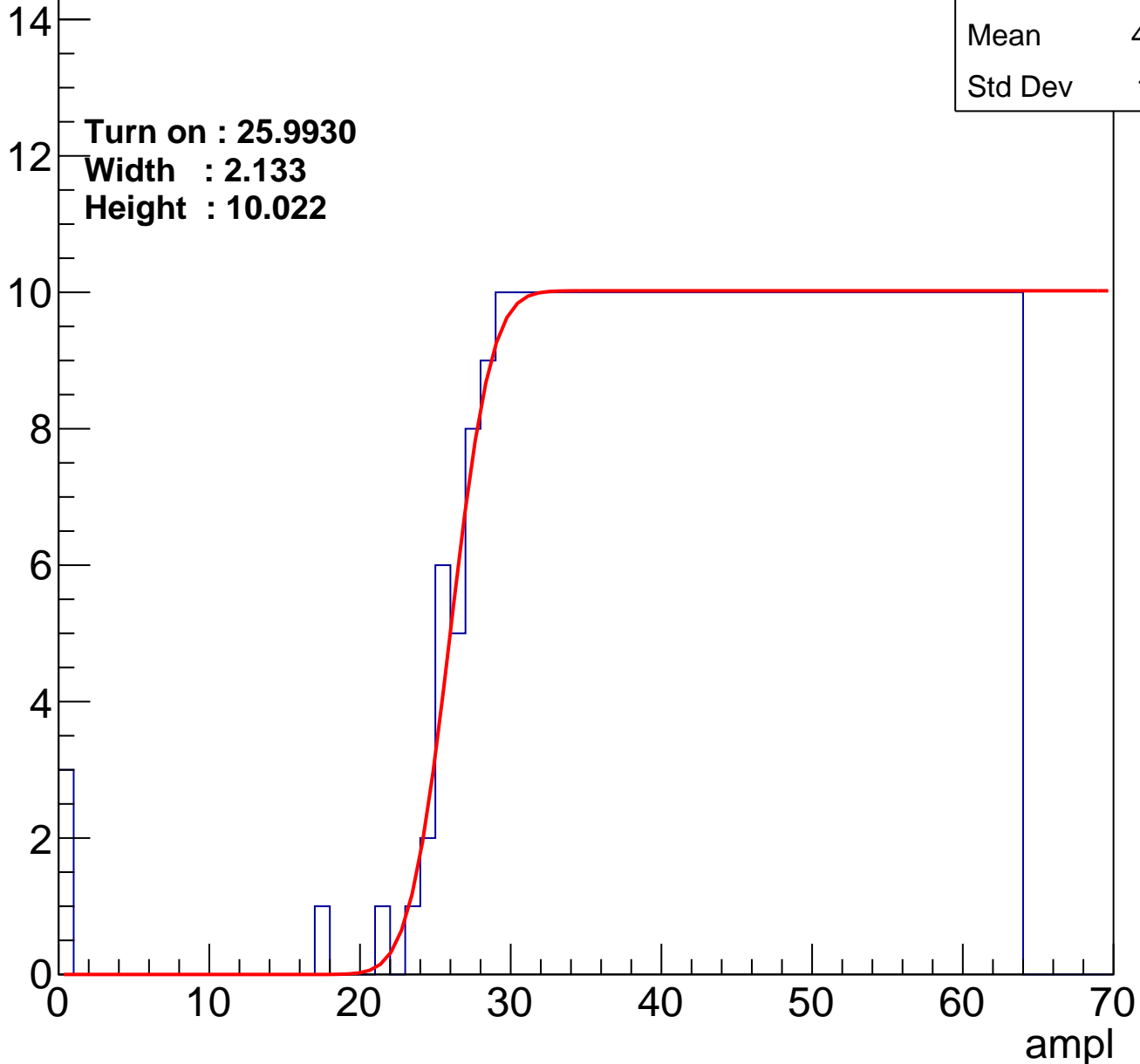
Entries	386
Mean	43.93
Std Dev	11.81

Turn on : 25.9930

Width : 2.133

Height : 10.022

Entry



B1L002S, U19-ch119

calib_packv5_042523_0143.root, FC#10, port B3

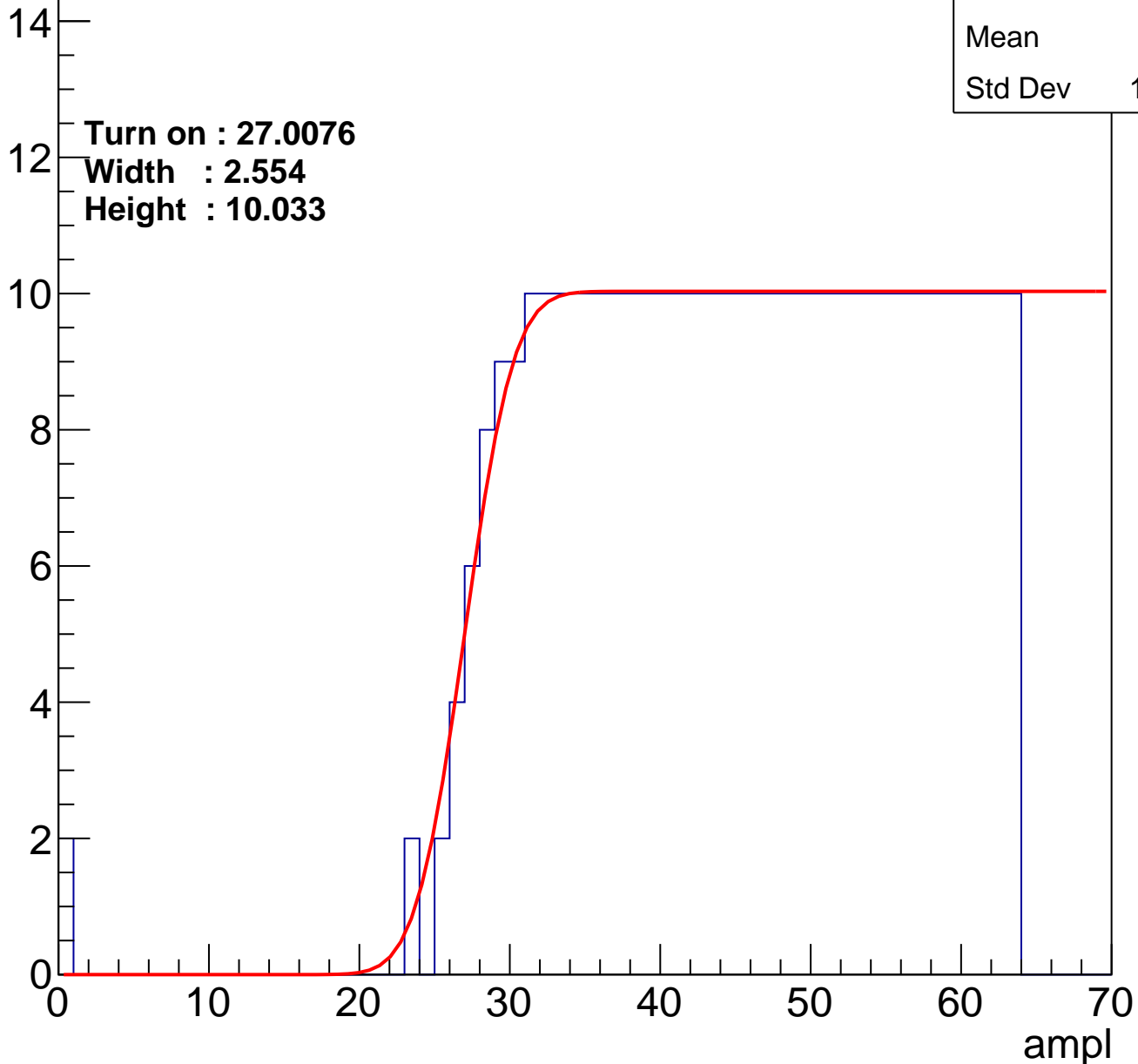
Entries	372
Mean	44.7
Std Dev	11.25

Turn on : 27.0076

Width : 2.554

Height : 10.033

Entry



B1L002S, U19-ch120

calib_packv5_042523_0143.root, FC#10, port B3

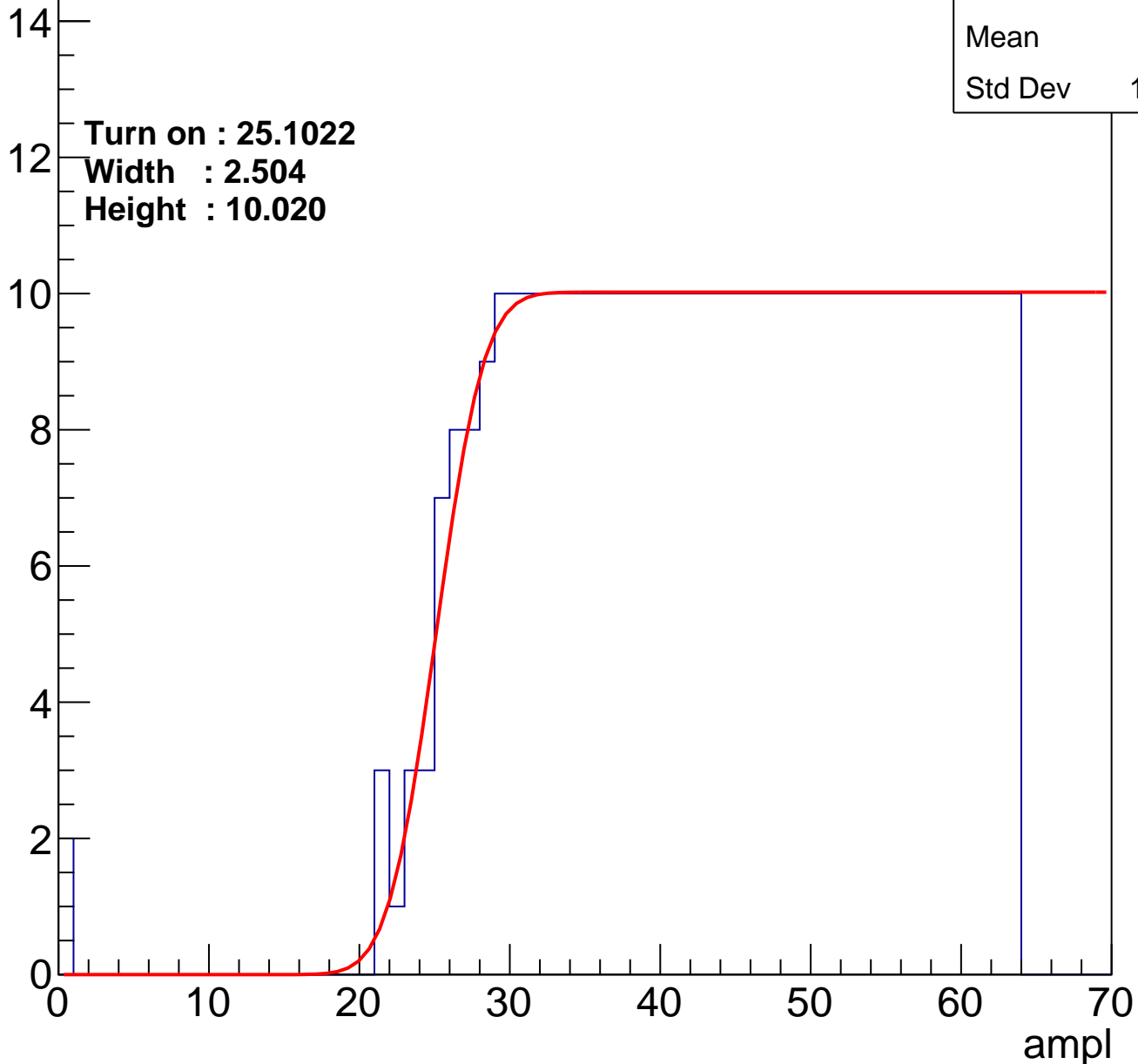
Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.1022

Width : 2.504

Height : 10.020

Entry



B1L002S, U19-ch121

calib_packv5_042523_0143.root, FC#10, port B3

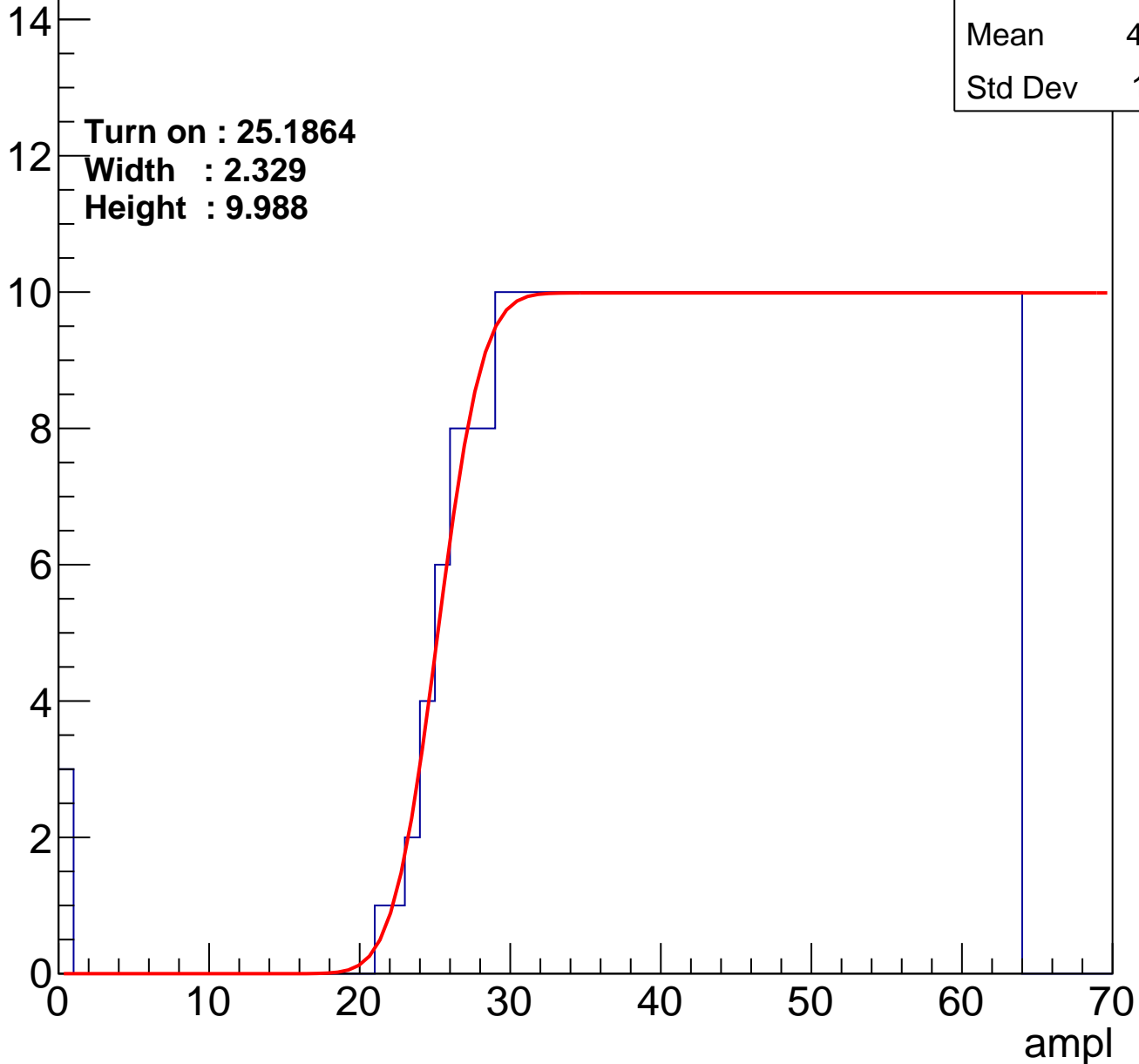
Entries	391
Mean	43.69
Std Dev	11.91

Turn on : 25.1864

Width : 2.329

Height : 9.988

Entry



B1L002S, U19-ch122

calib_packv5_042523_0143.root, FC#10, port B3

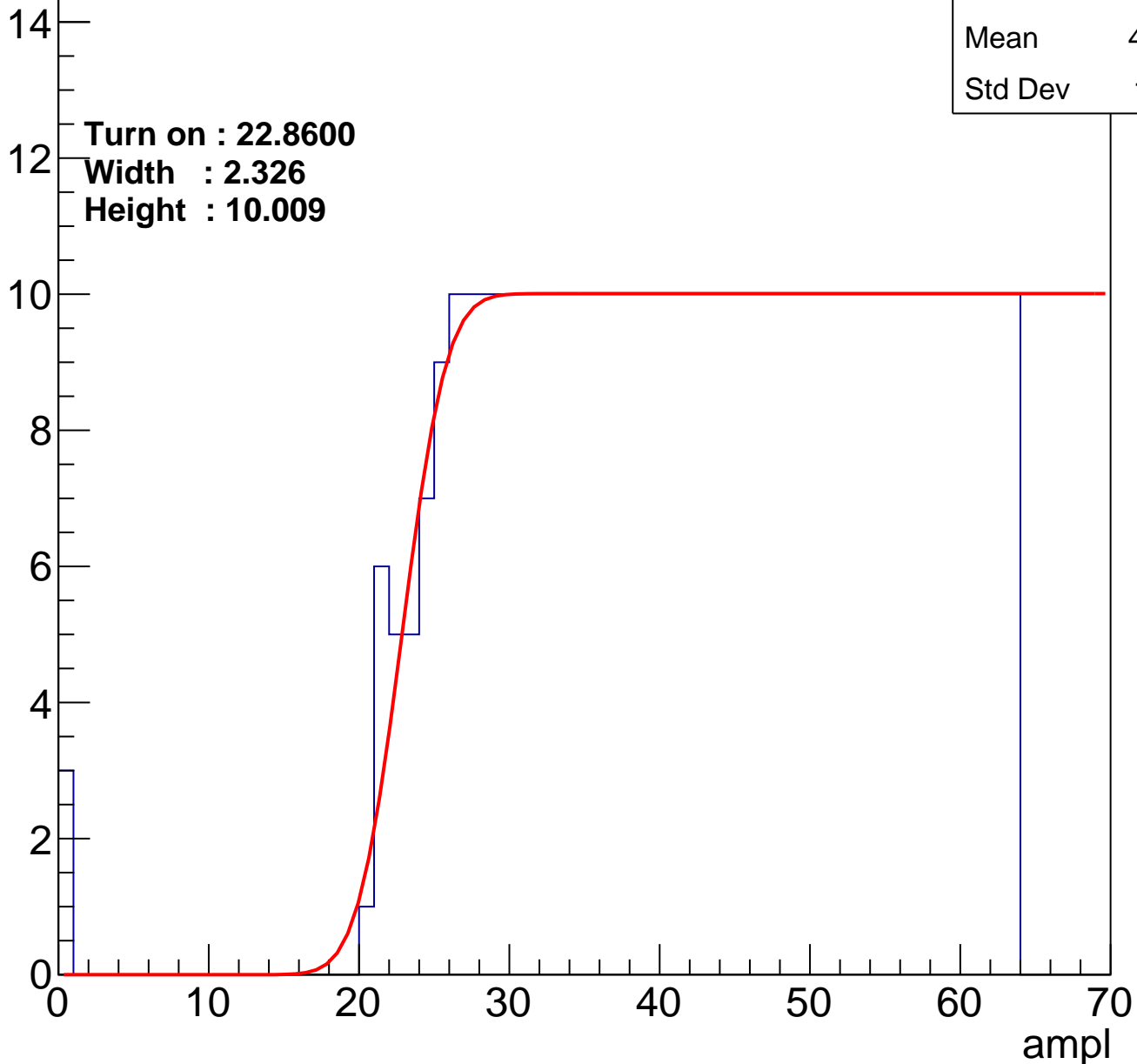
Entries	416
Mean	42.49
Std Dev	12.51

Turn on : 22.8600

Width : 2.326

Height : 10.009

Entry



B1L002S, U19-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.84
Std Dev	11.83

Turn on : 25.7114

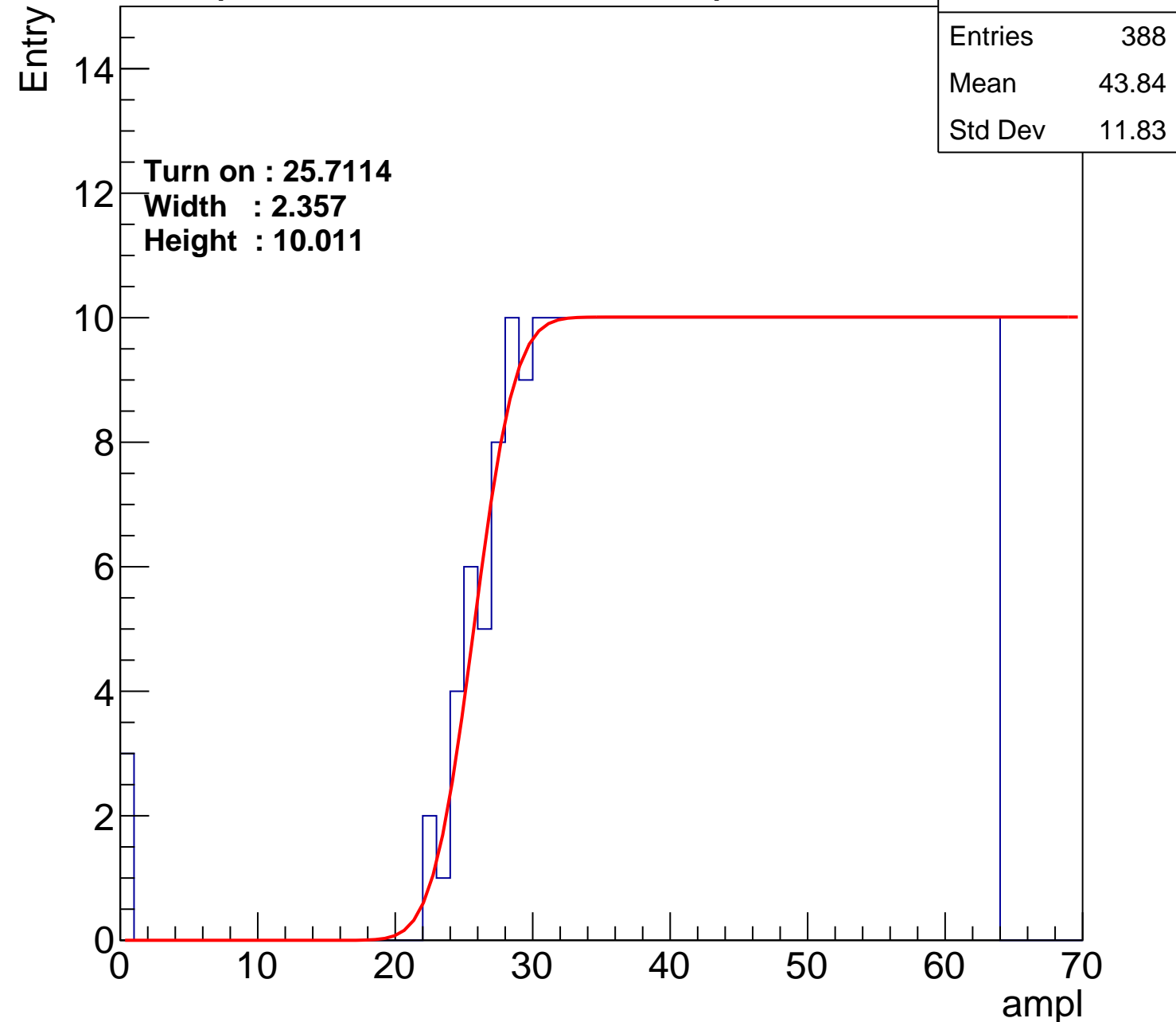
Width : 2.357

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.08
Std Dev	12.36

Turn on : 24.6626

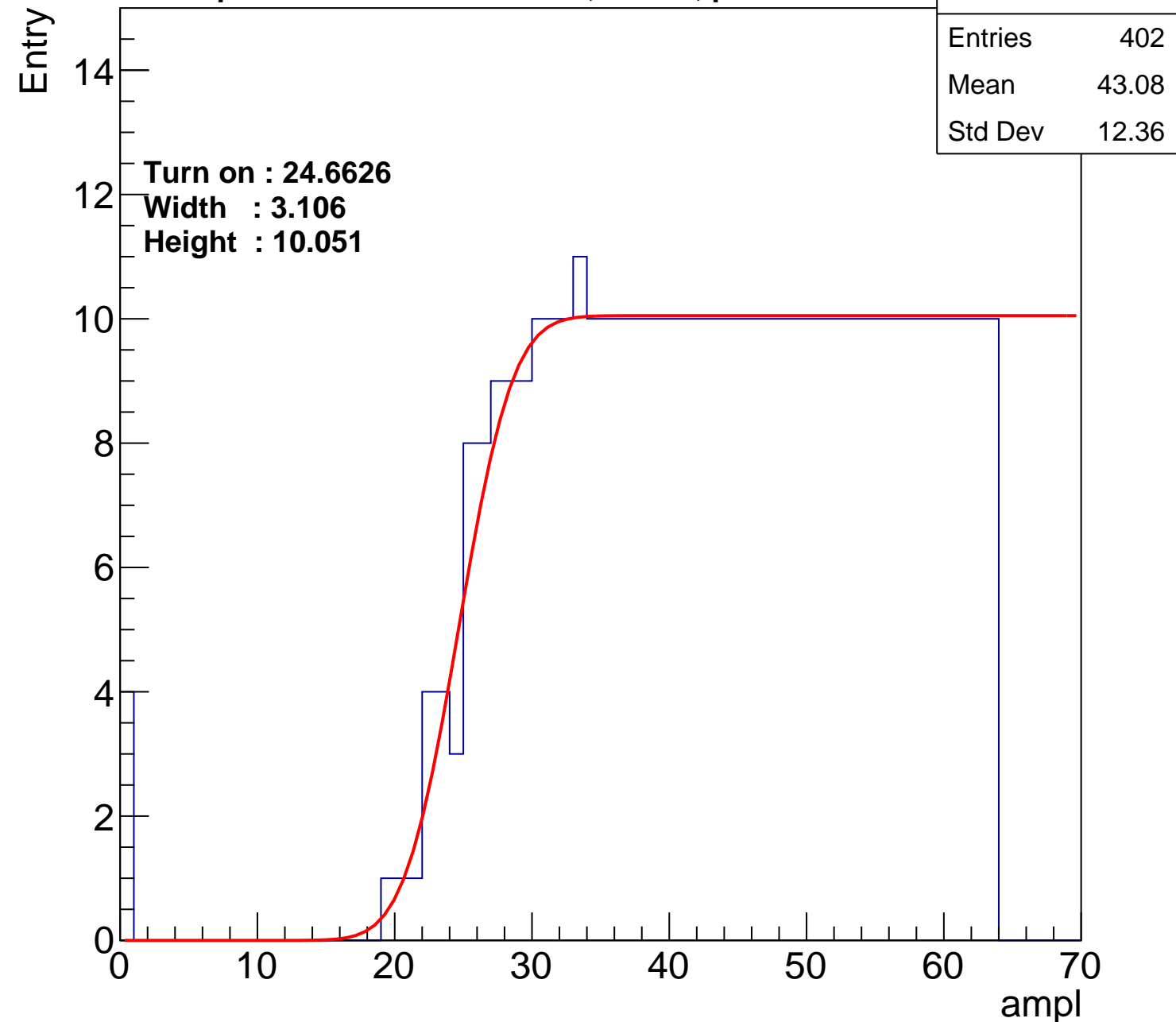
Width : 3.106

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.53
Std Dev	11.9

Turn on : 25.4295

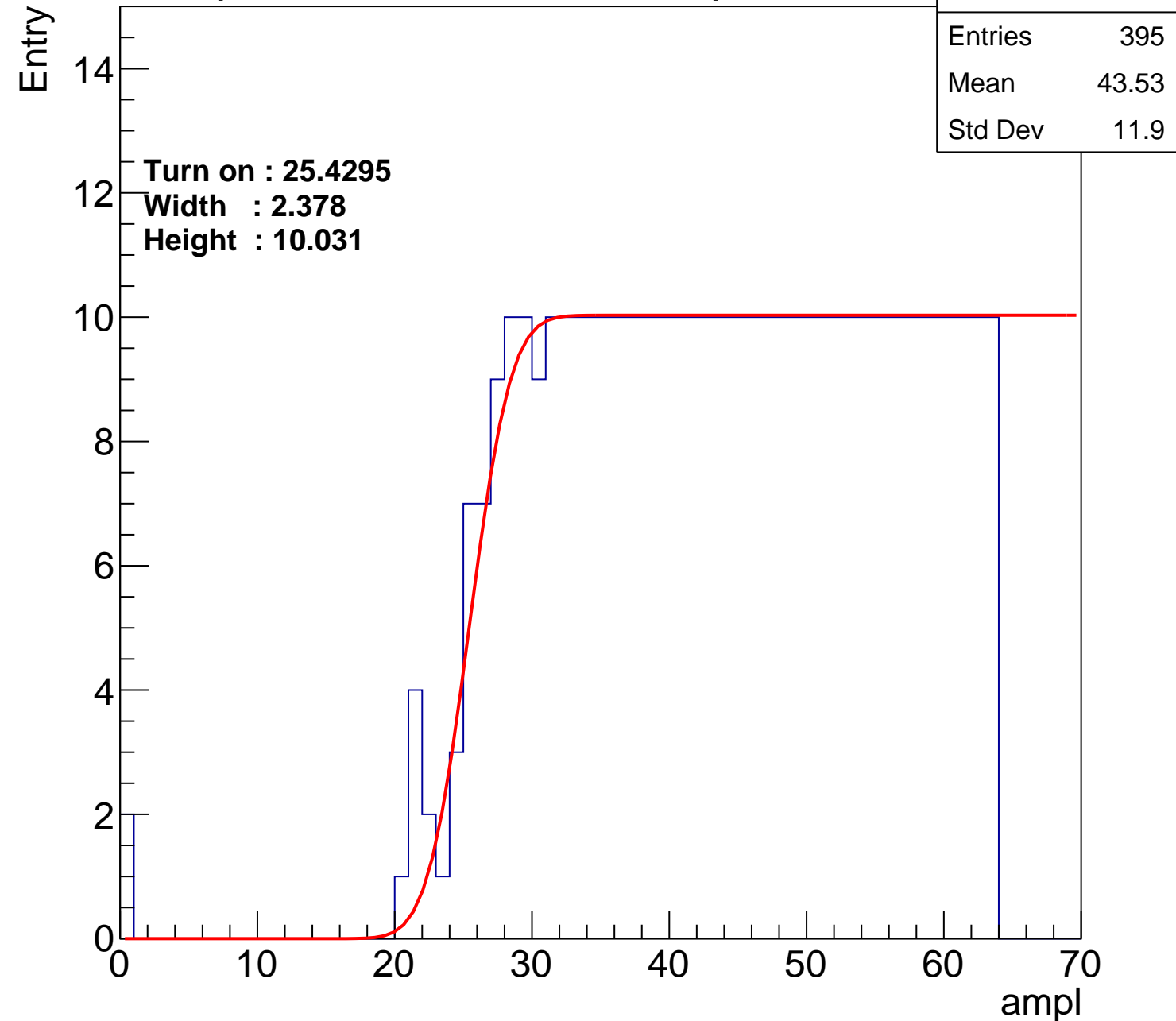
Width : 2.378

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U19-ch126

calib_packv5_042523_0143.root, FC#10, port B3

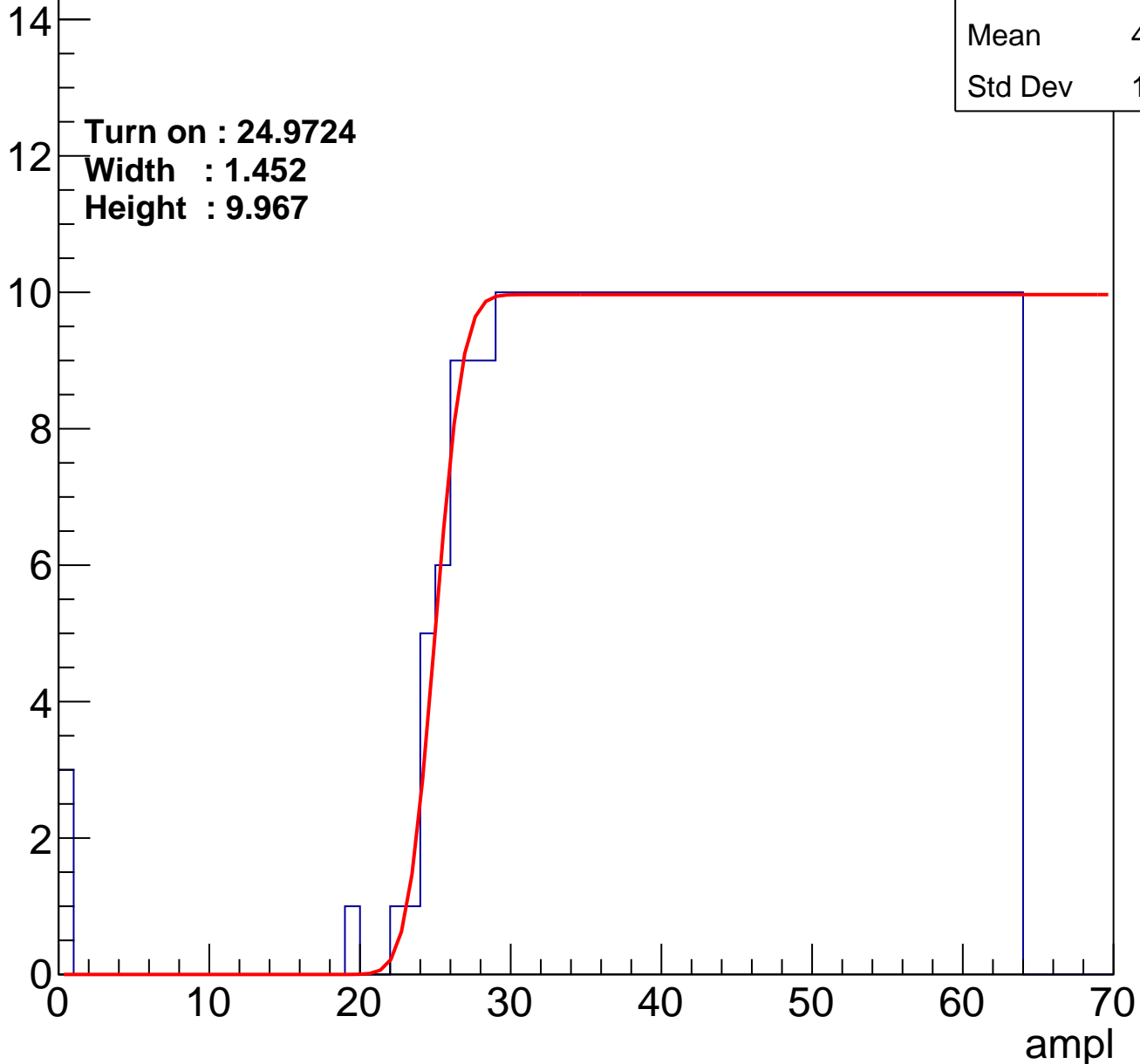
Entries	394
Mean	43.56
Std Dev	11.96

Turn on : 24.9724

Width : 1.452

Height : 9.967

Entry



B1L002S, U19-ch127

calib_packv5_042523_0143.root, FC#10, port B3

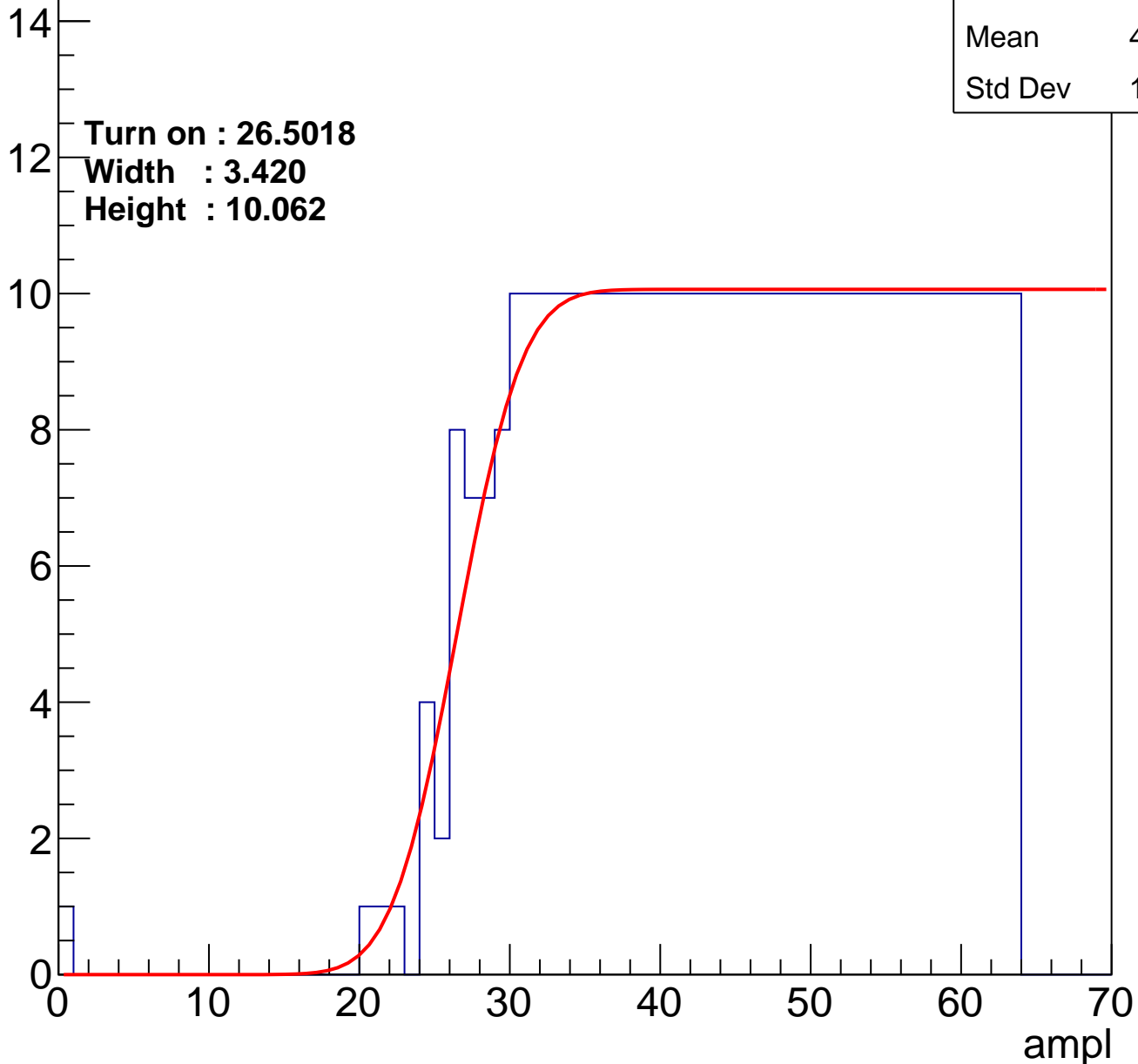
Entries	380
Mean	44.33
Std Dev	11.34

Turn on : 26.5018

Width : 3.420

Height : 10.062

Entry



B1L002S, U19-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.33
Std Dev	11.34

Turn on : 26.5018

Width : 3.420

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl

