

B1L103S, U22-ch0

calib_packv5_042523_0143.root, FC#7, port C2

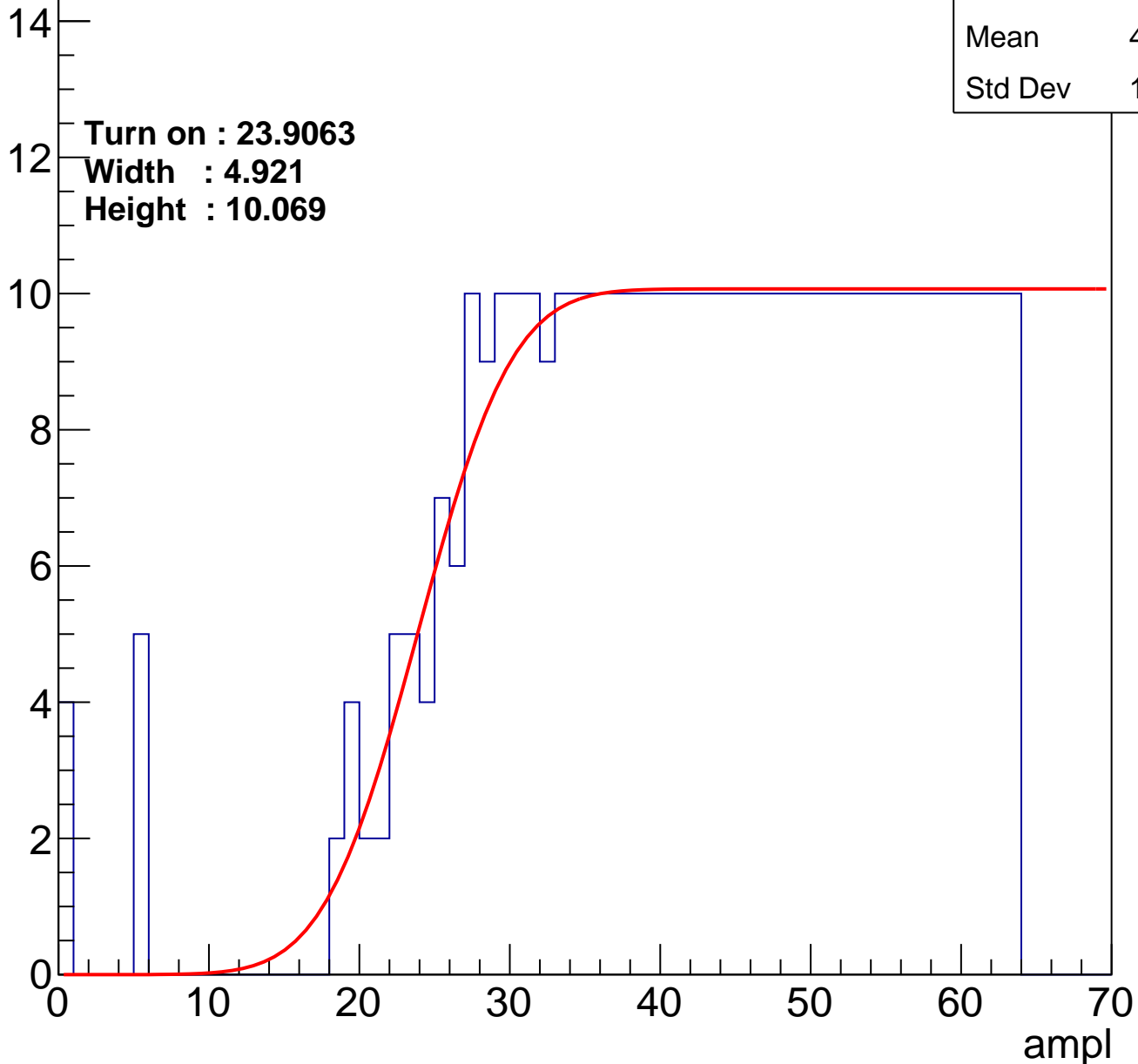
Entries	414
Mean	42.18
Std Dev	13.27

Turn on : 23.9063

Width : 4.921

Height : 10.069

Entry



B1L103S, U22-ch1

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	43.81
Std Dev	12.33

Turn on : 26.6275

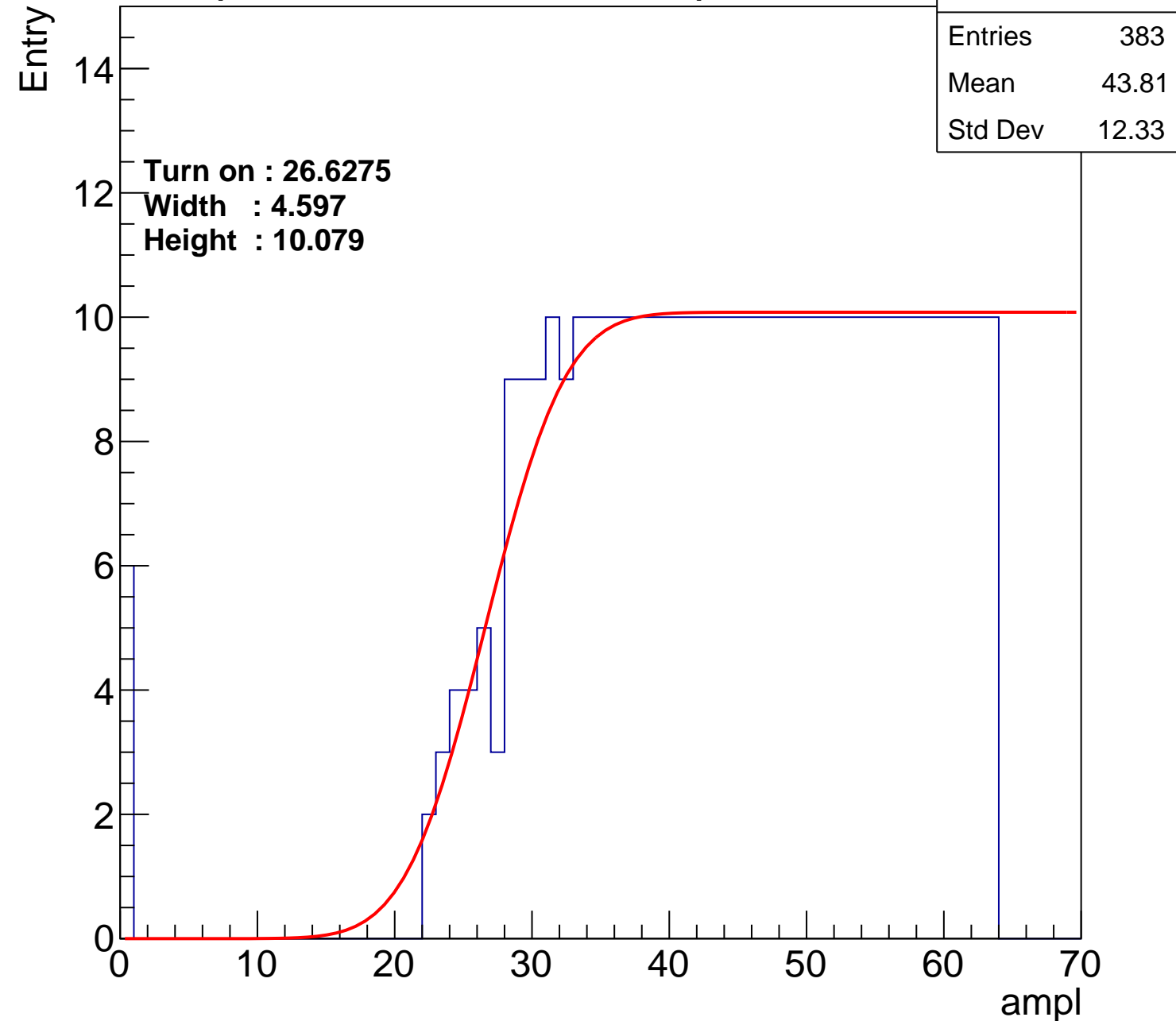
Width : 4.597

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch2

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.38
Std Dev	11.67

Turn on : 27.0818

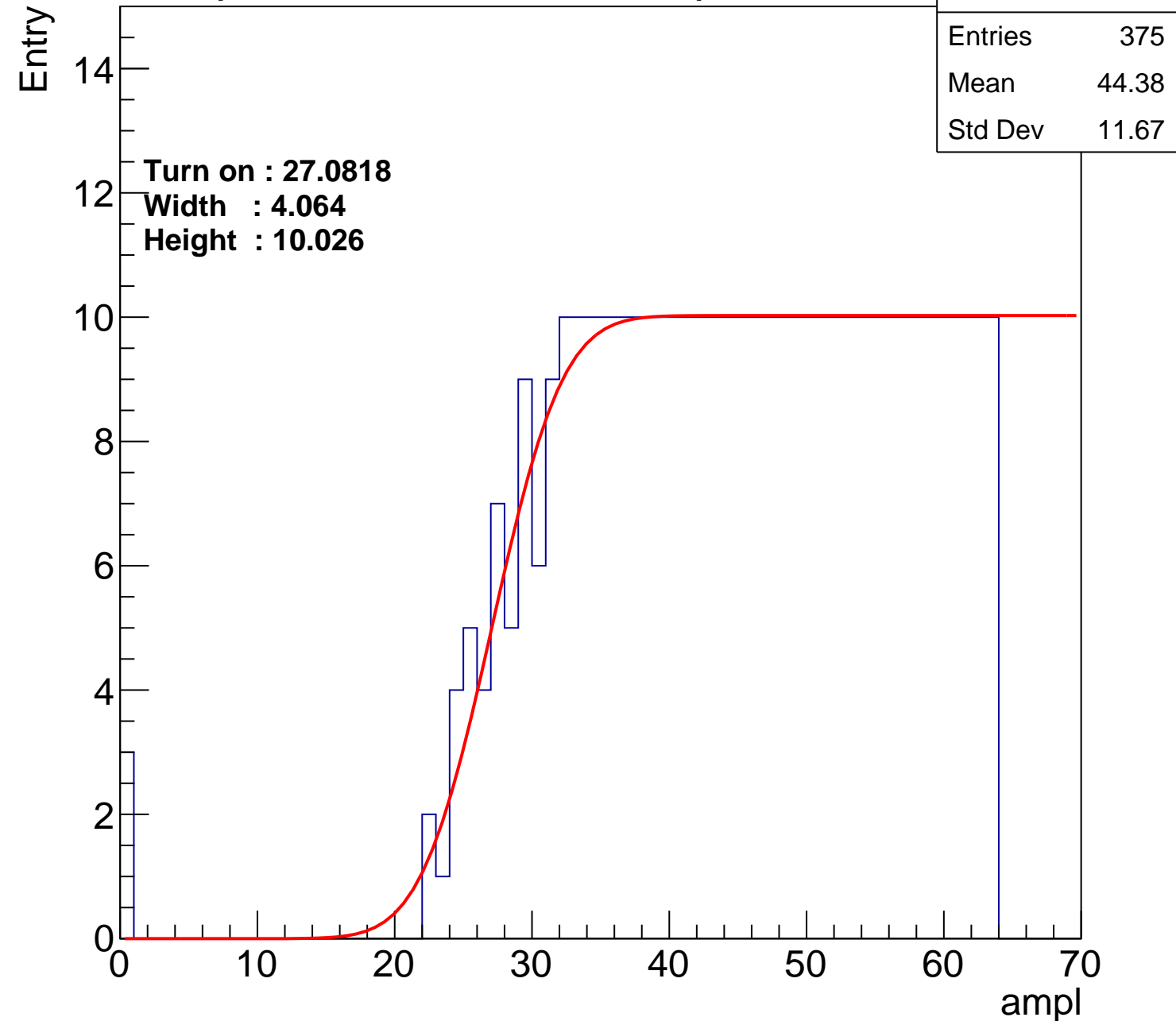
Width : 4.064

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch3

calib_packv5_042523_0143.root, FC#7, port C2

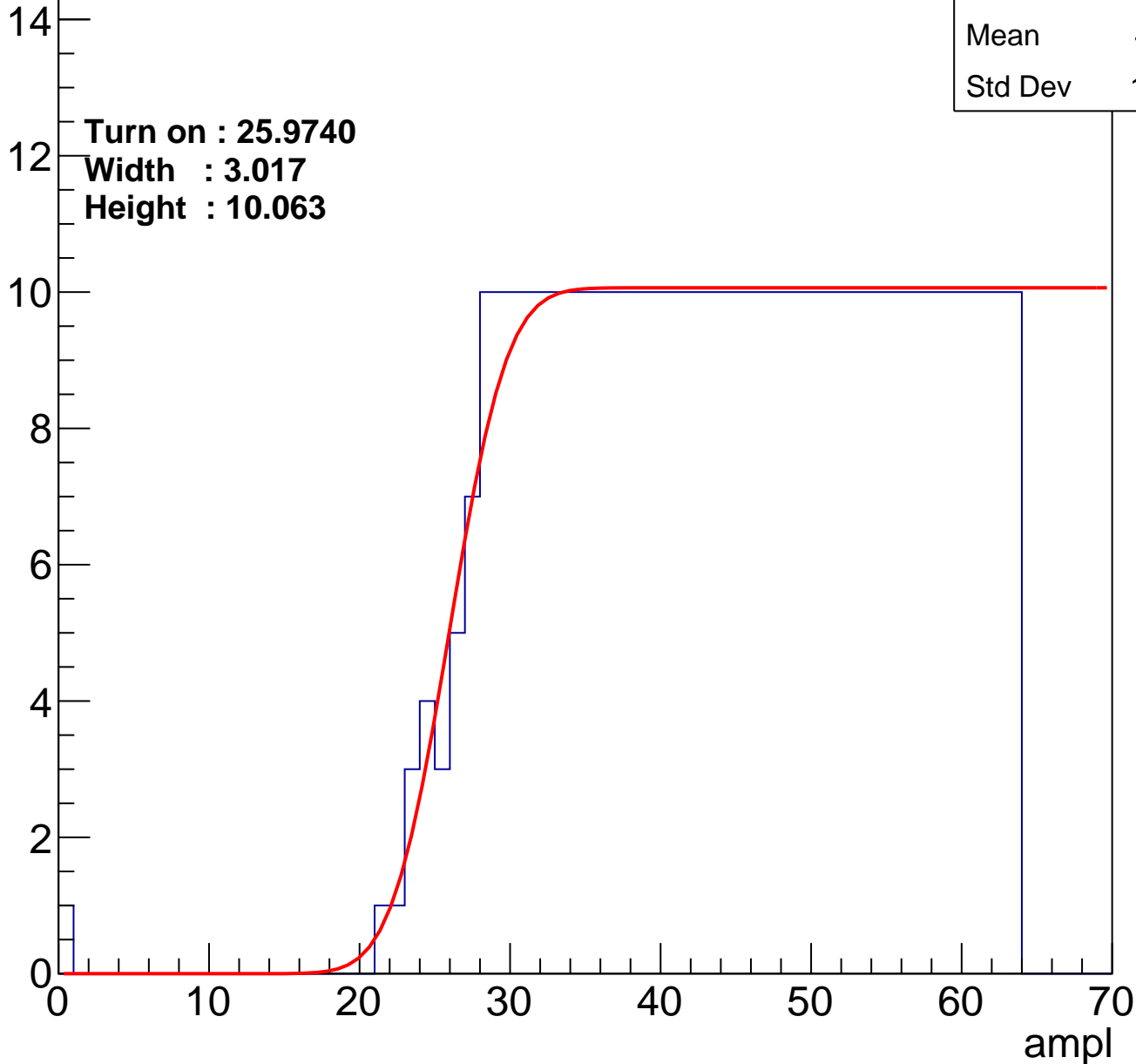
Entries	385
Mean	44.11
Std Dev	11.42

Turn on : 25.9740

Width : 3.017

Height : 10.063

Entry



B1L103S, U22-ch4

calib_packv5_042523_0143.root, FC#7, port C2

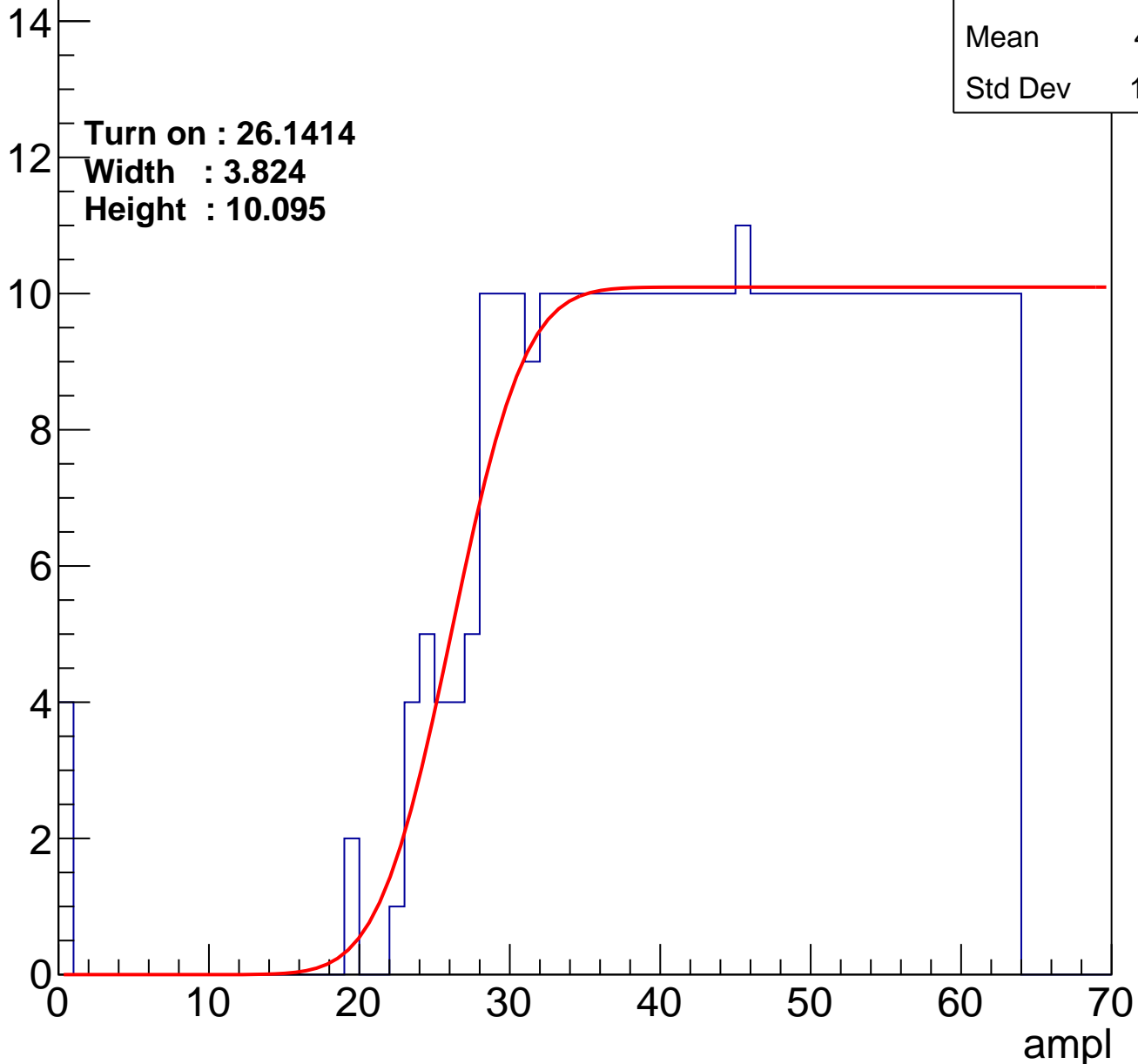
Entries	389
Mean	43.71
Std Dev	12.09

Turn on : 26.1414

Width : 3.824

Height : 10.095

Entry



B1L103S, U22-ch5

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.95
Std Dev	11.17

Turn on : 27.6592

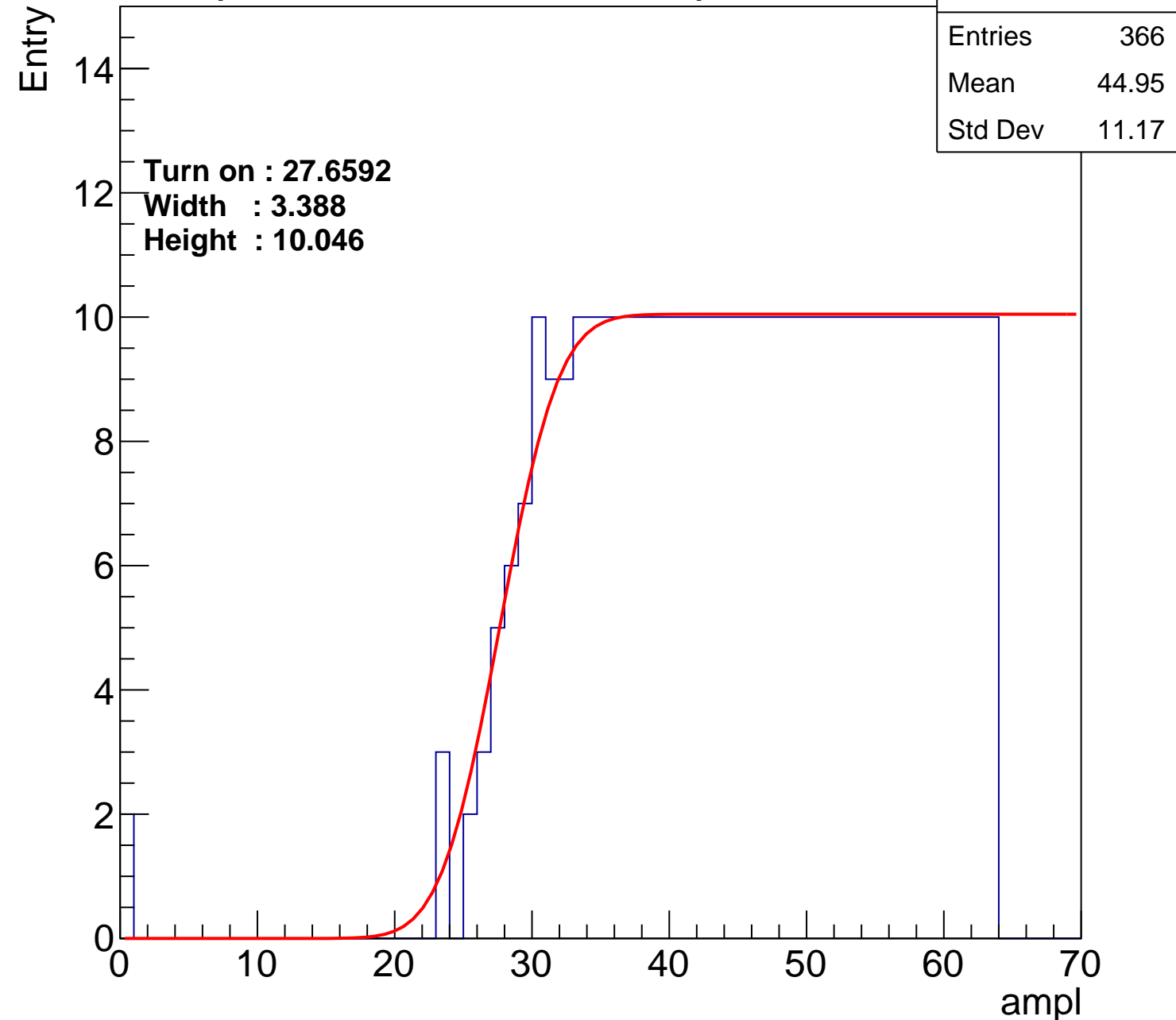
Width : 3.388

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch6

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.51
Std Dev	12.47

Turn on : 26.4268

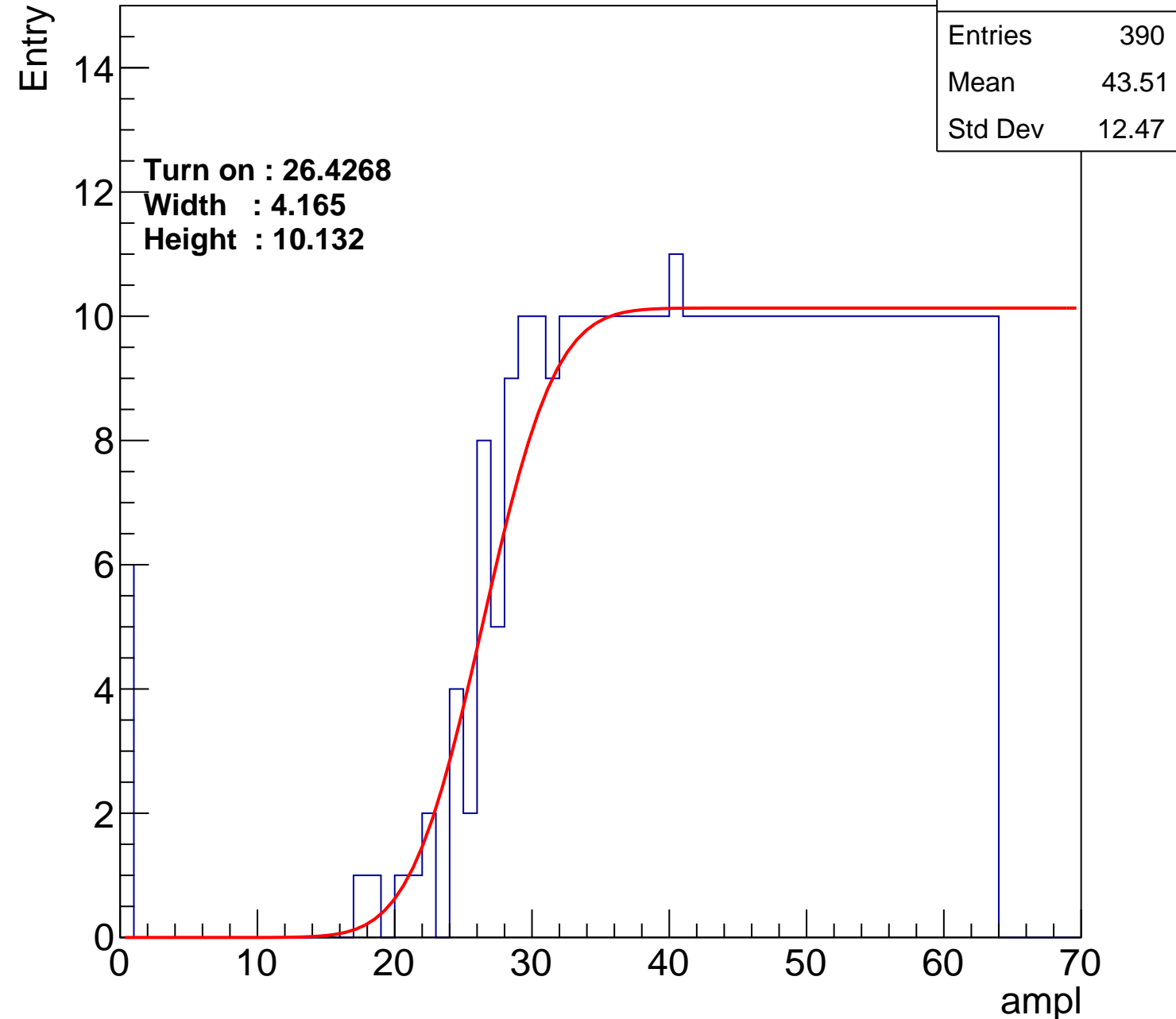
Width : 4.165

Height : 10.132

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch7

calib_packv5_042523_0143.root, FC#7, port C2

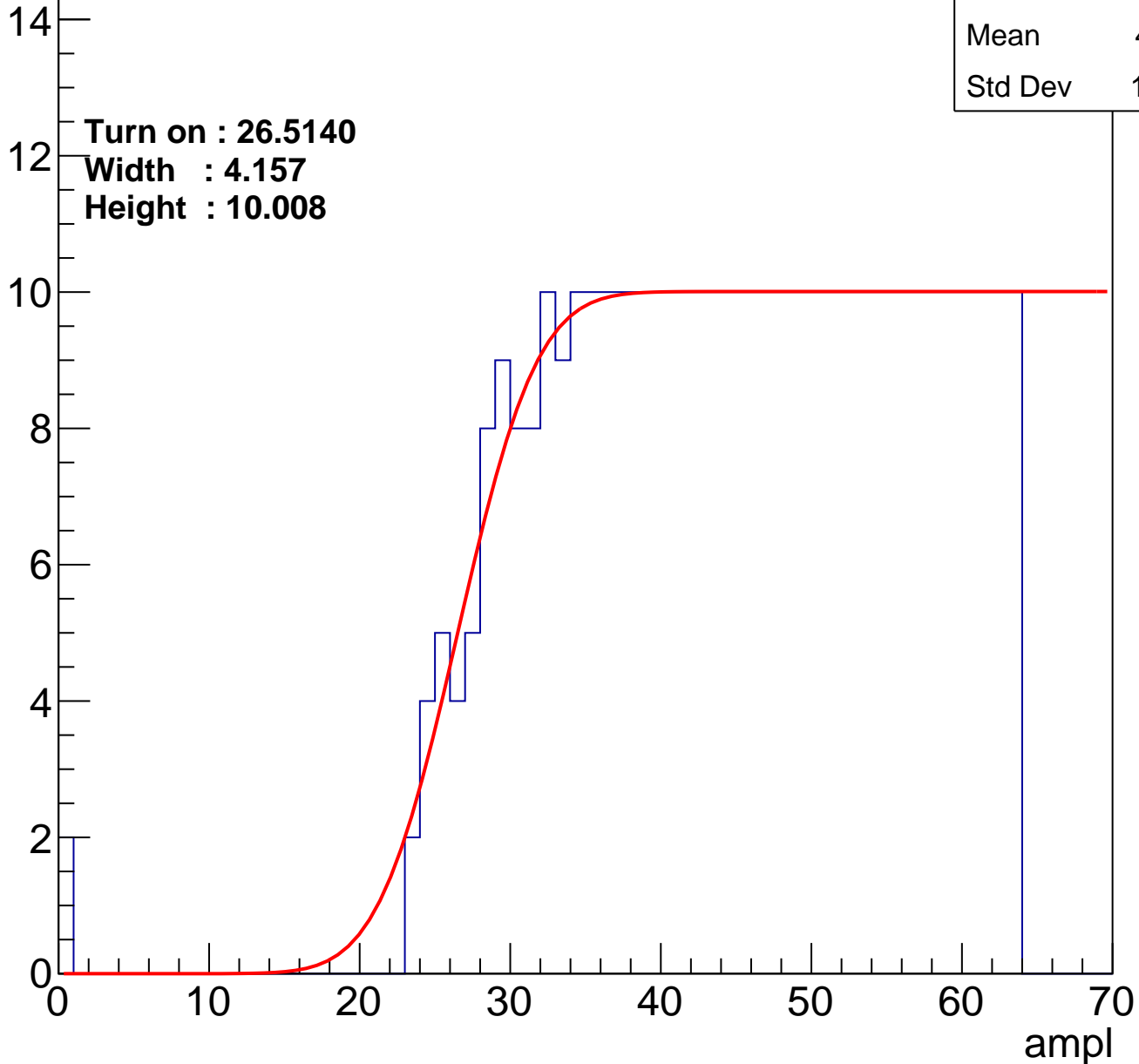
Entries	374
Mean	44.51
Std Dev	11.43

Turn on : 26.5140

Width : 4.157

Height : 10.008

Entry



B1L103S, U22-ch8

calib_packv5_042523_0143.root, FC#7, port C2

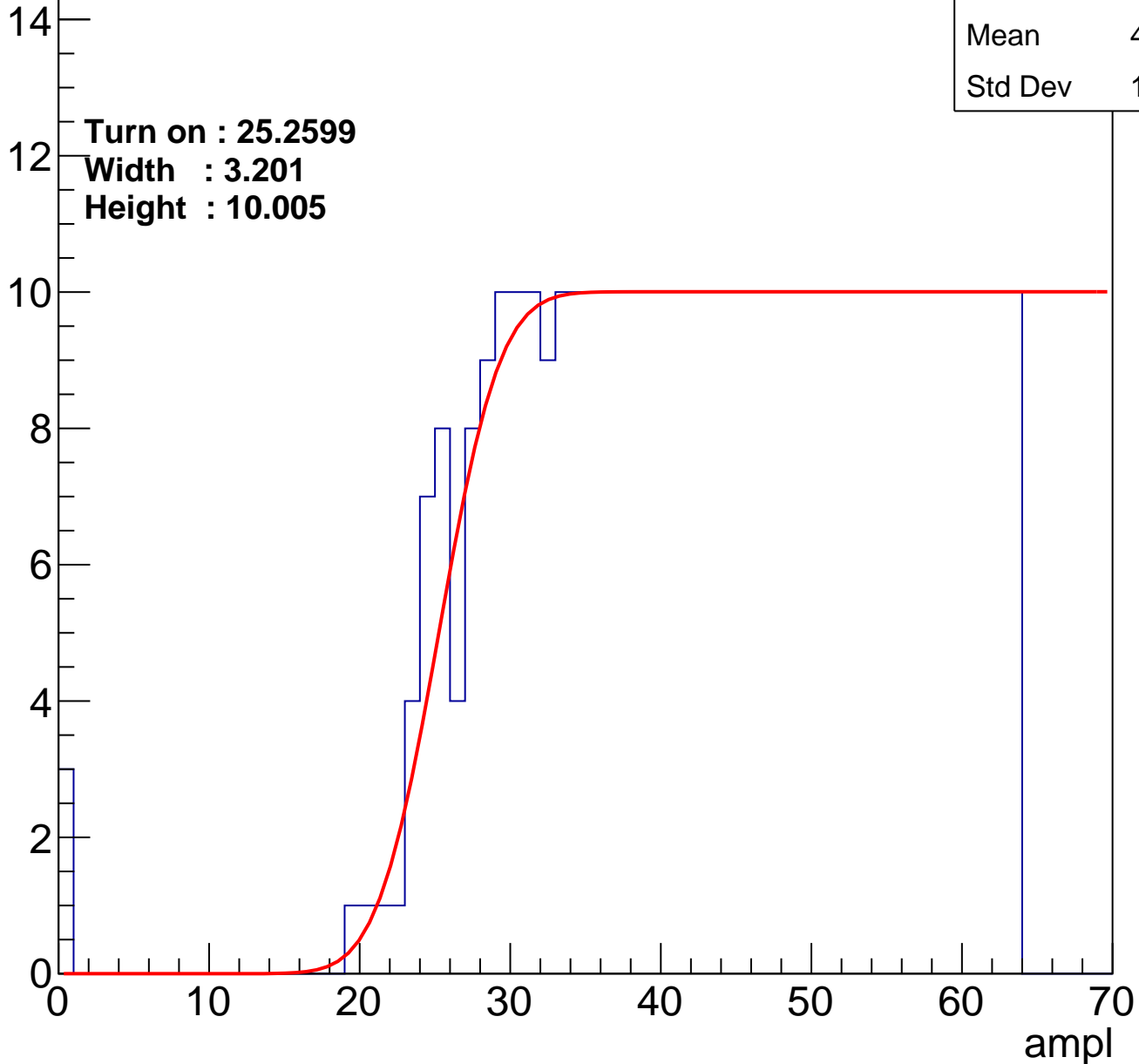
Entries	396
Mean	43.39
Std Dev	12.12

Turn on : 25.2599

Width : 3.201

Height : 10.005

Entry



B1L103S, U22-ch9

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.0749

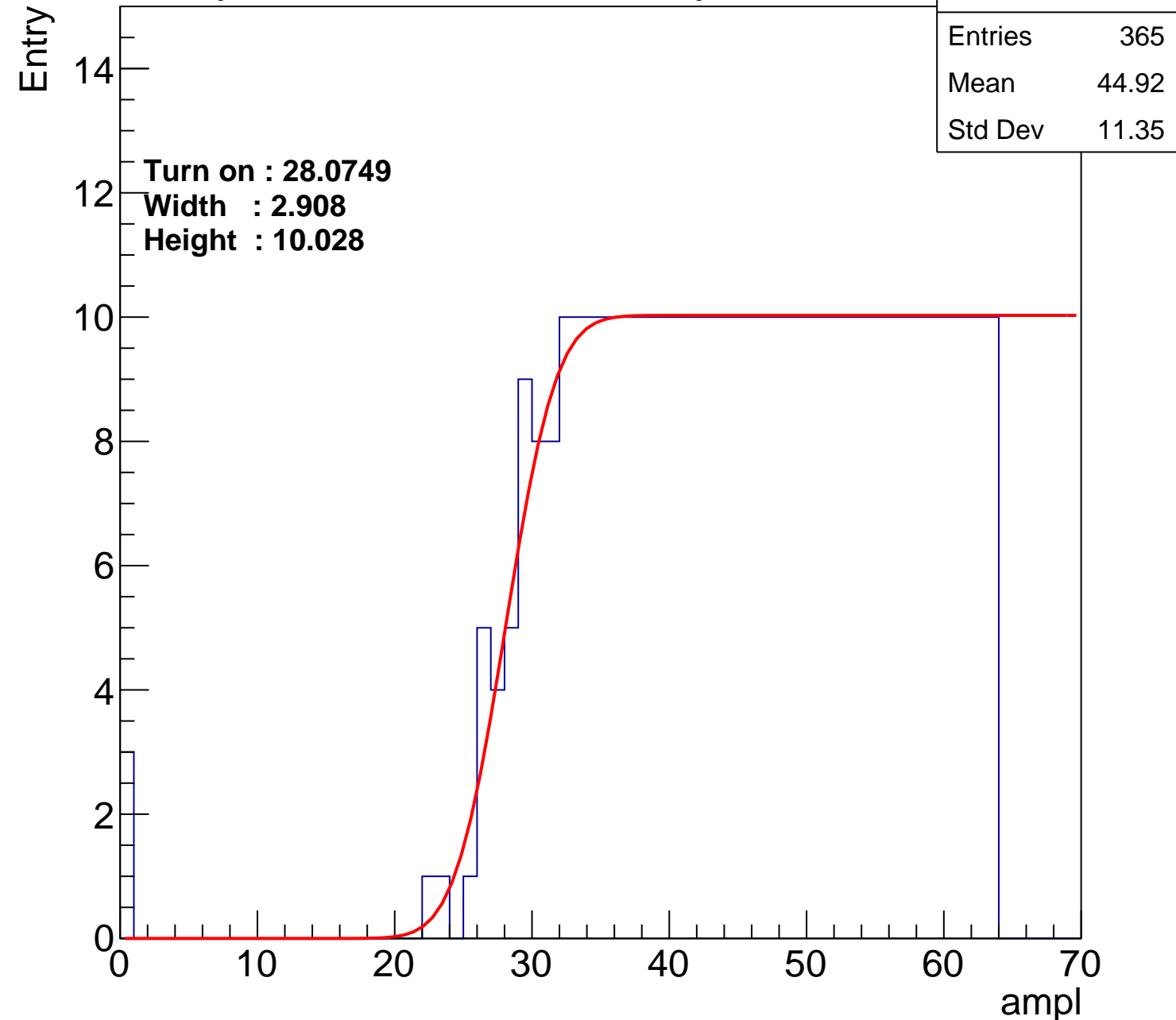
Width : 2.908

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch10

calib_packv5_042523_0143.root, FC#7, port C2

Entries	361
Mean	45.23
Std Dev	10.89

Turn on : 28.7731

Width : 3.748

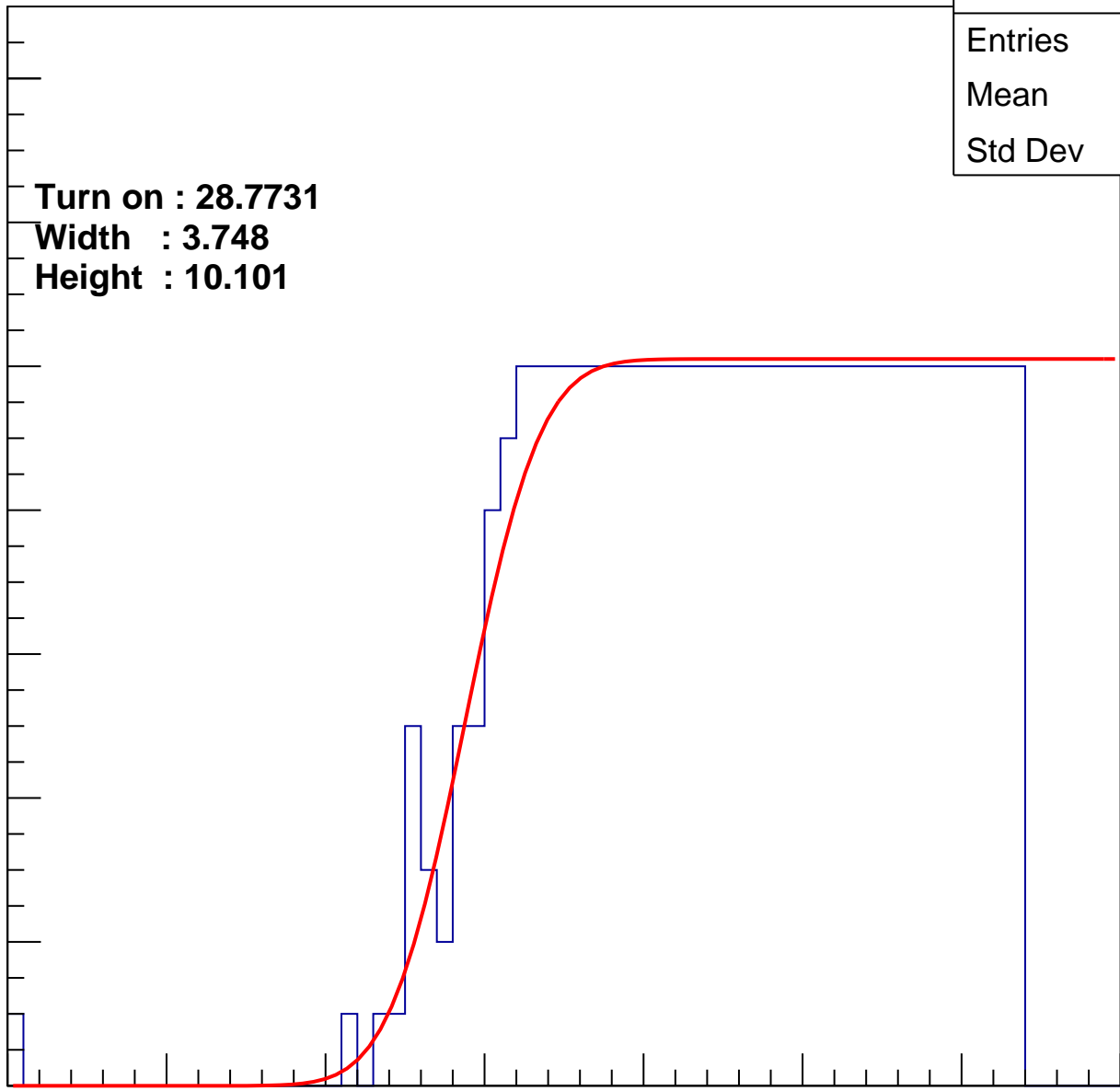
Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U22-ch11

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
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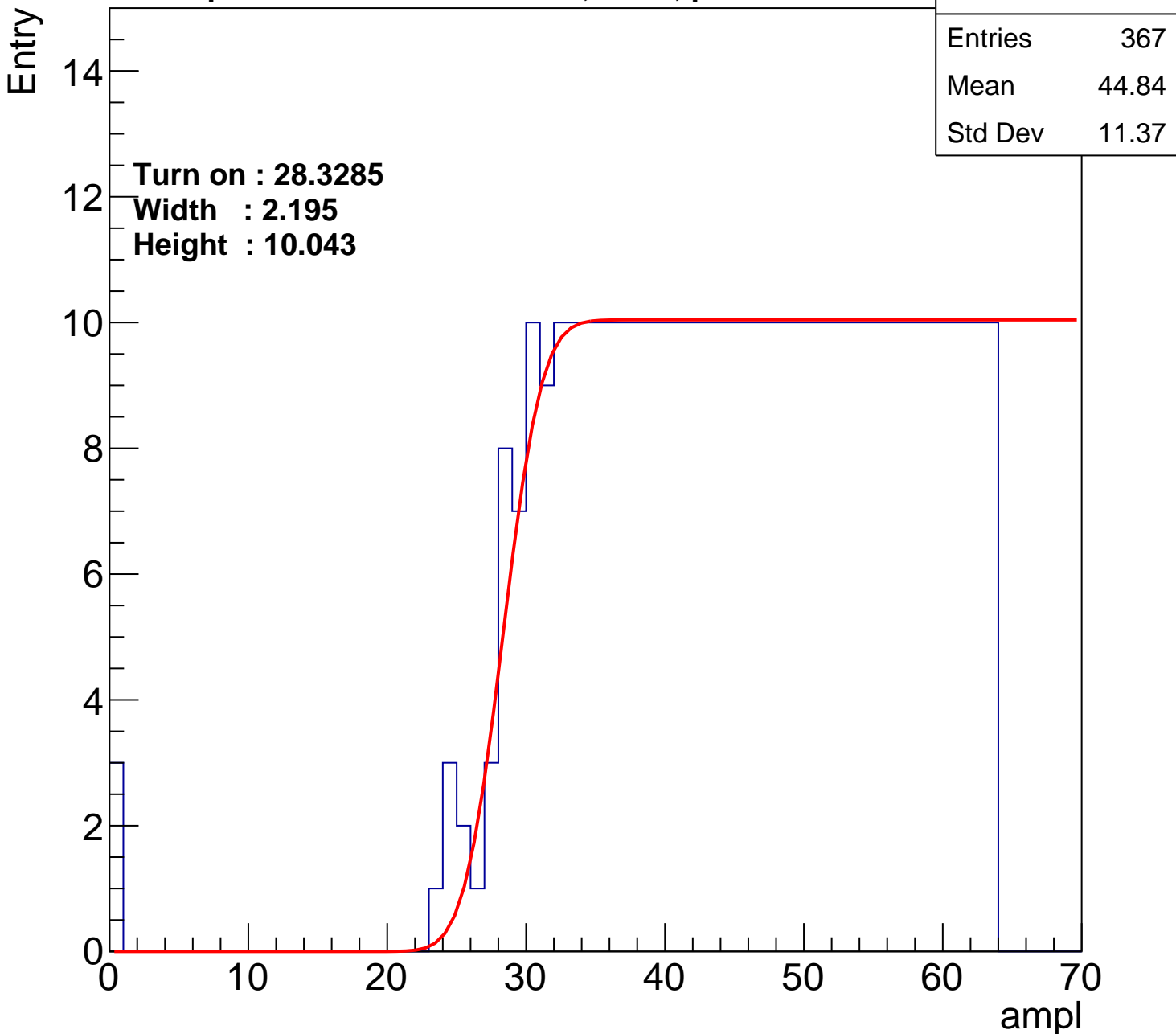
Mean	44.84
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Std Dev	11.37
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Turn on : 28.3285

Width : 2.195

Height : 10.043



B1L103S, U22-ch12

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.58
Std Dev	12.11

Turn on : 25.2904

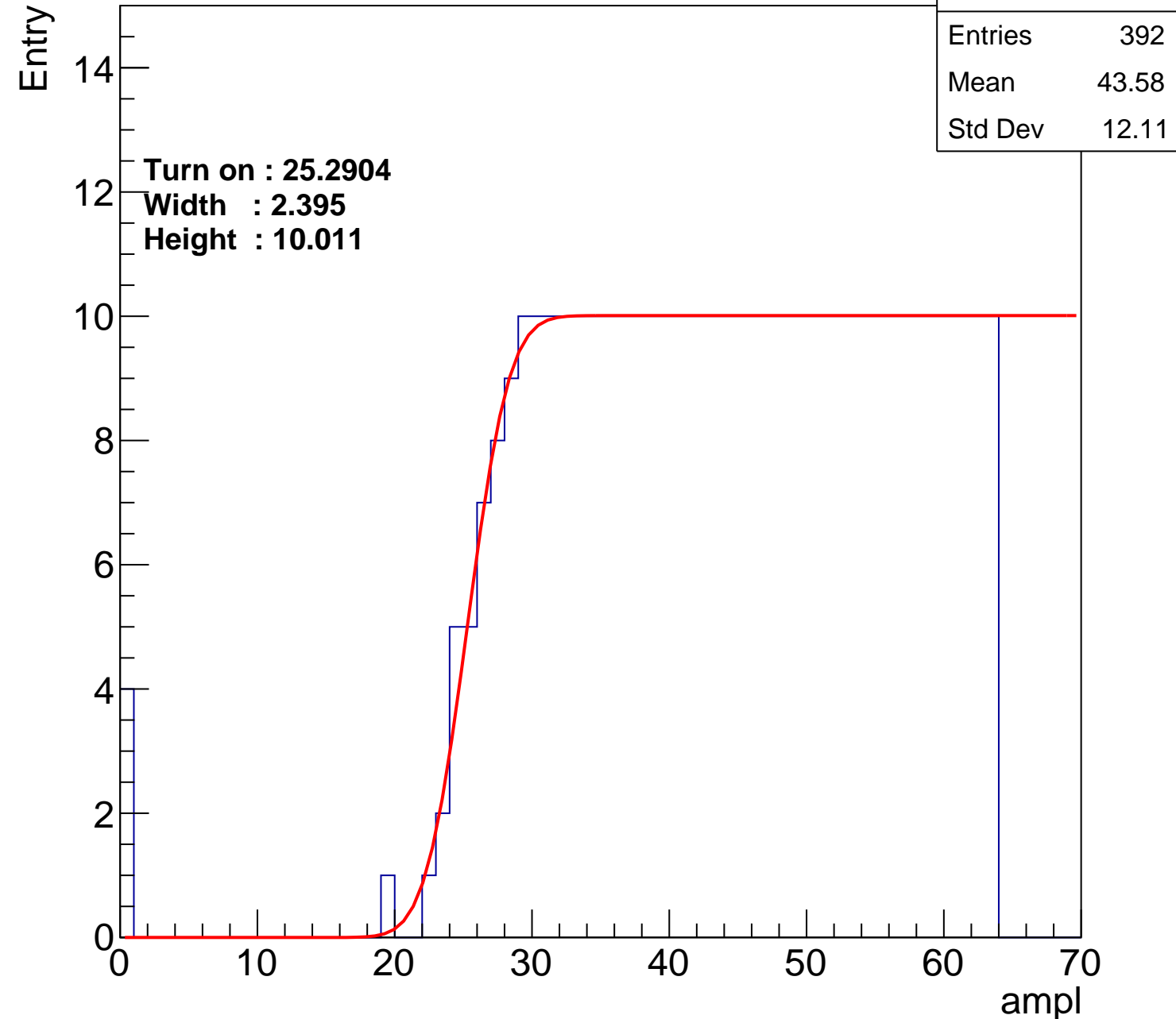
Width : 2.395

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch13

calib_packv5_042523_0143.root, FC#7, port C2

Entries	368
Mean	44.72
Std Dev	11.61

Turn on : 28.4171

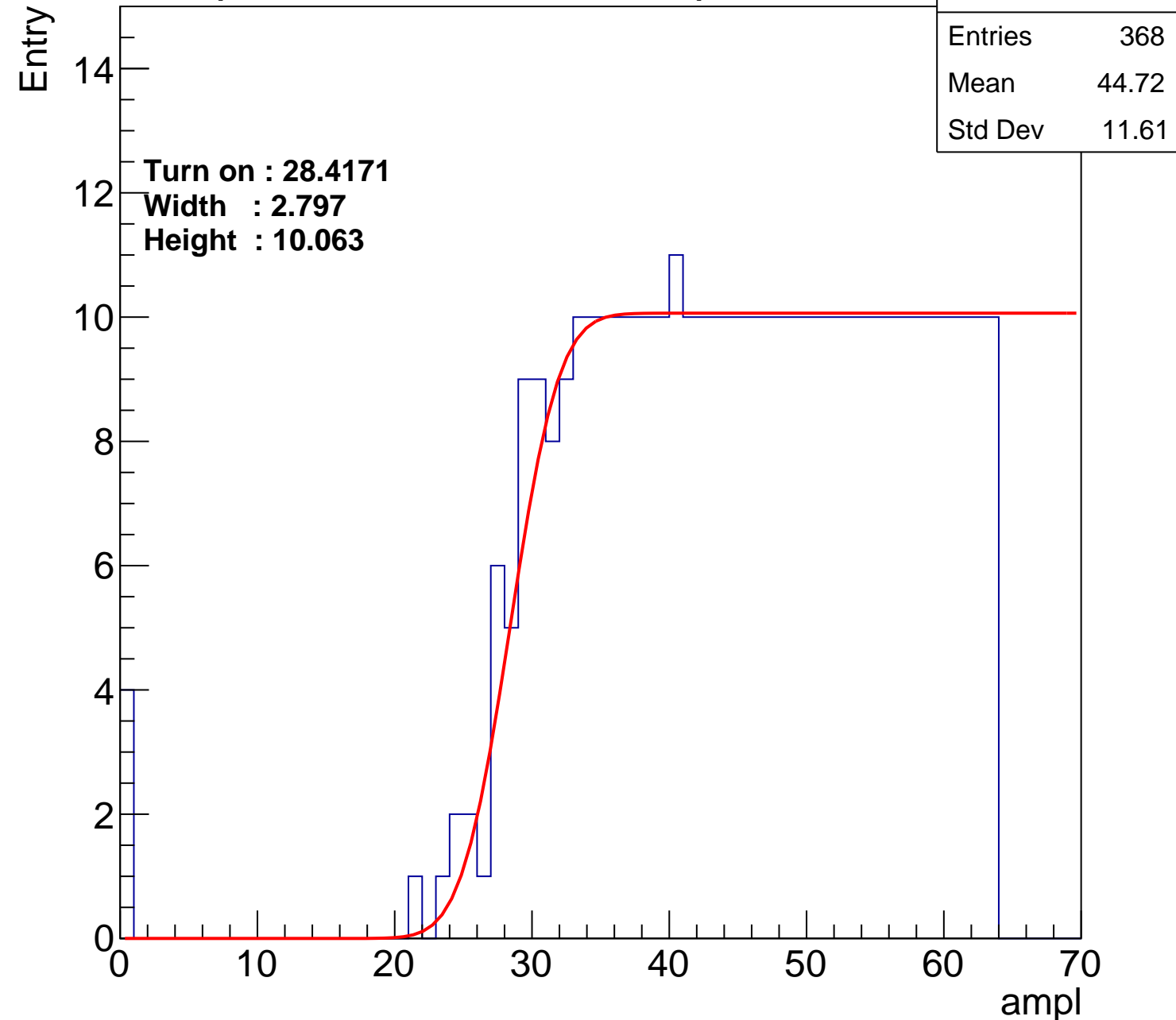
Width : 2.797

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch14

calib_packv5_042523_0143.root, FC#7, port C2

Entries	364
Mean	45.01
Std Dev	11.16

Turn on : 28.2271

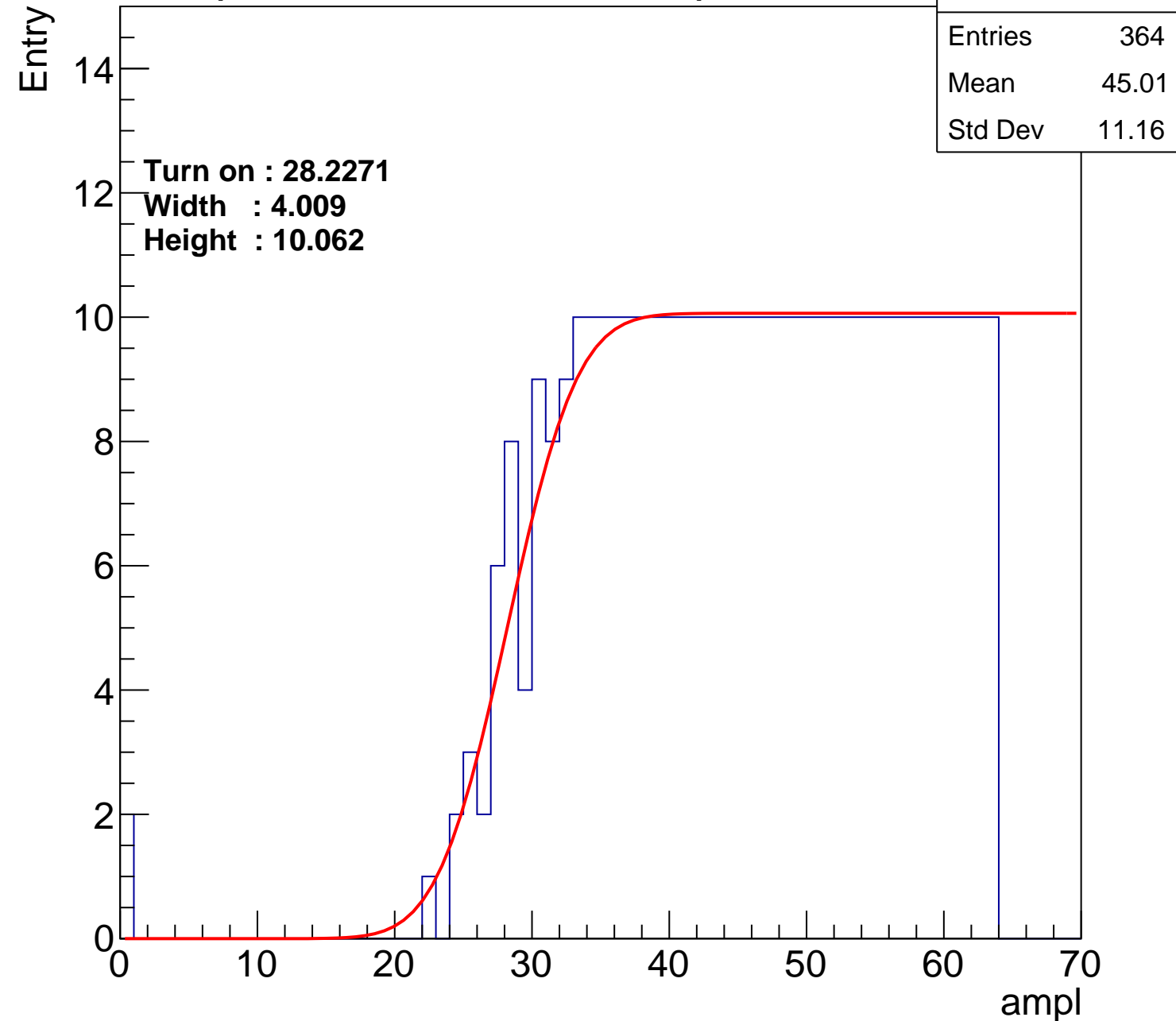
Width : 4.009

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch15

calib_packv5_042523_0143.root, FC#7, port C2

Entries	359
Mean	45.22
Std Dev	11.2

Turn on : 28.5914

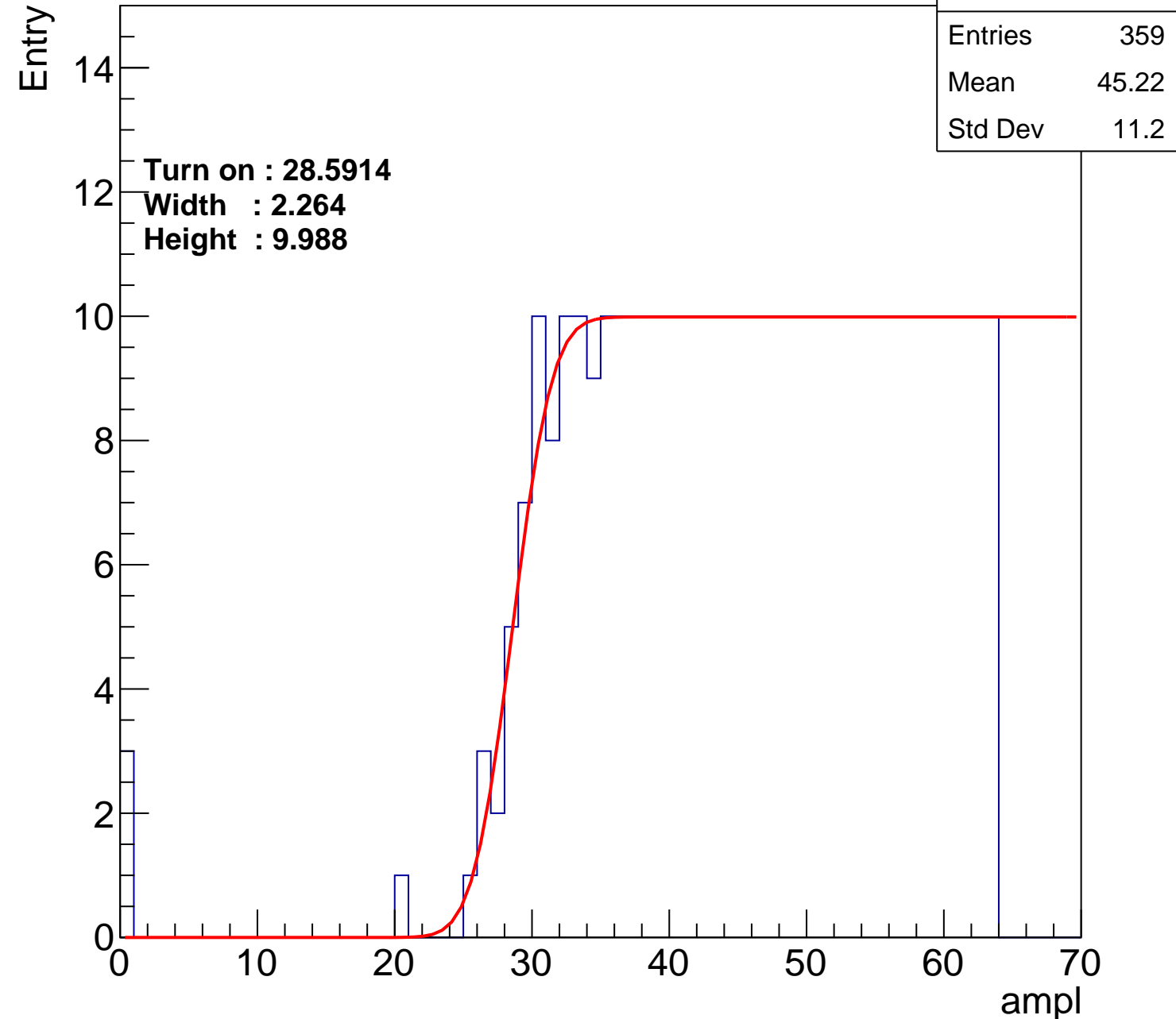
Width : 2.264

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch16

calib_packv5_042523_0143.root, FC#7, port C2

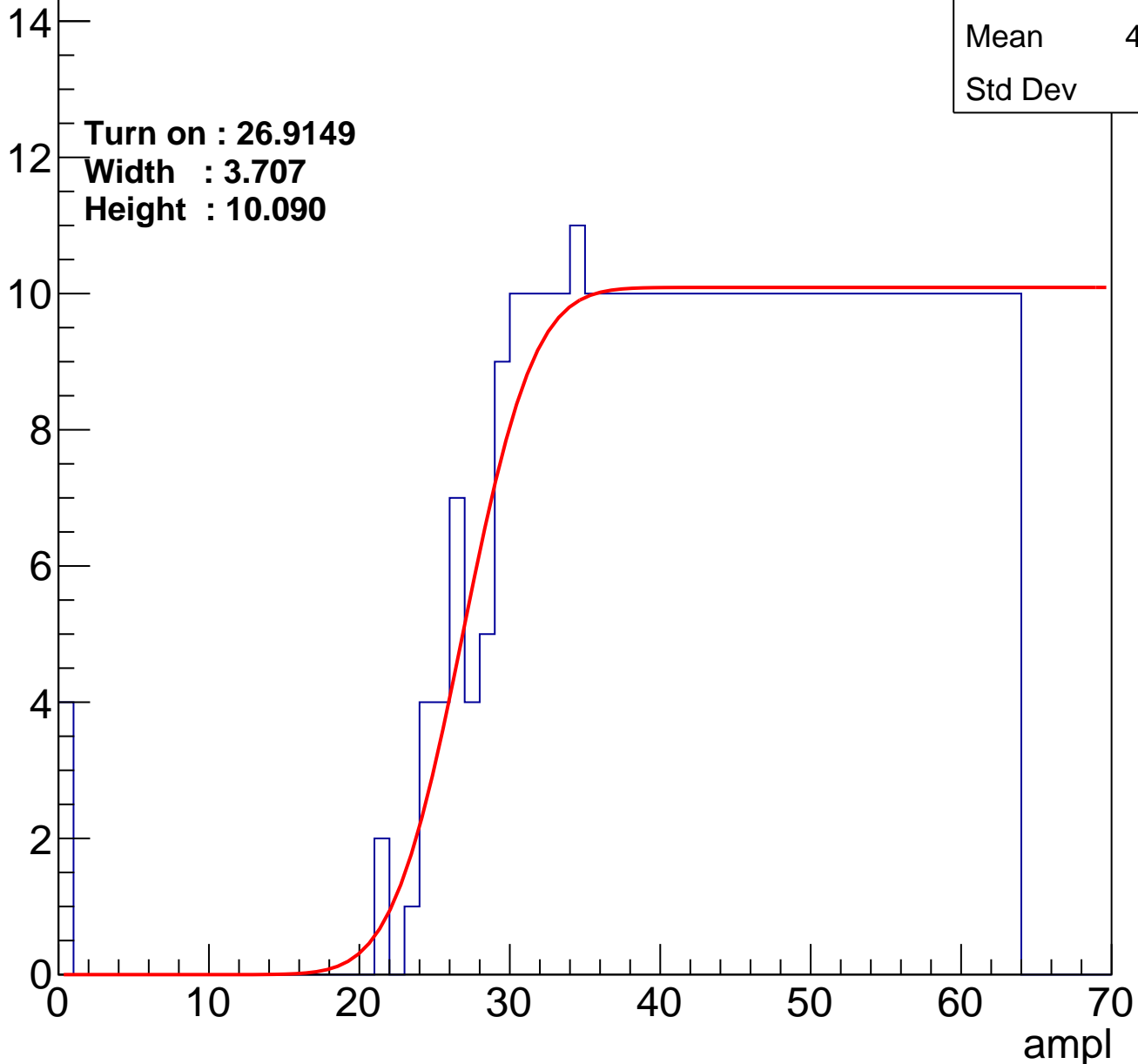
Entries	381
Mean	44.08
Std Dev	11.9

Turn on : 26.9149

Width : 3.707

Height : 10.090

Entry



B1L103S, U22-ch17

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.72
Std Dev	11.13

Turn on : 27.3048

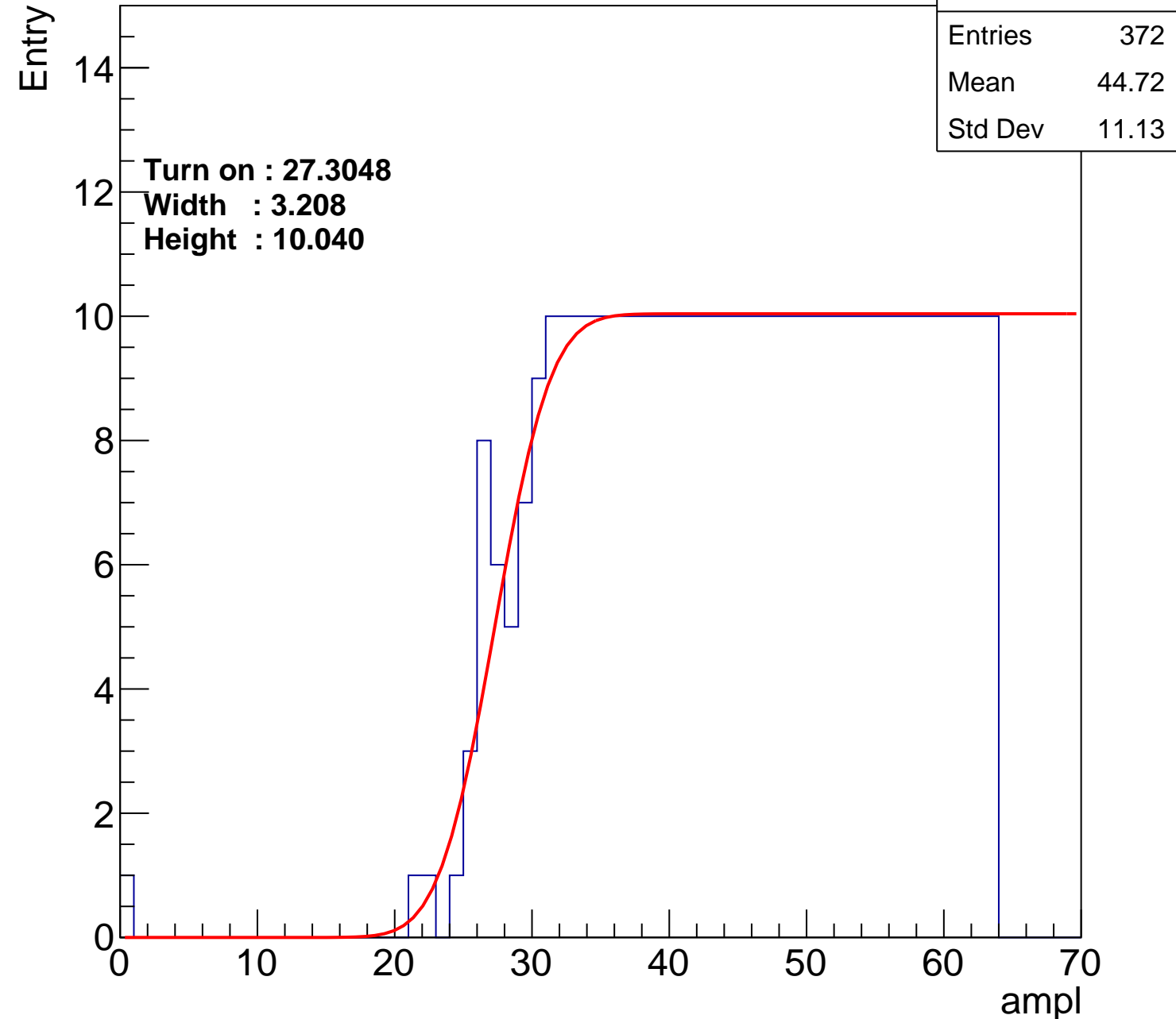
Width : 3.208

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch18

calib_packv5_042523_0143.root, FC#7, port C2

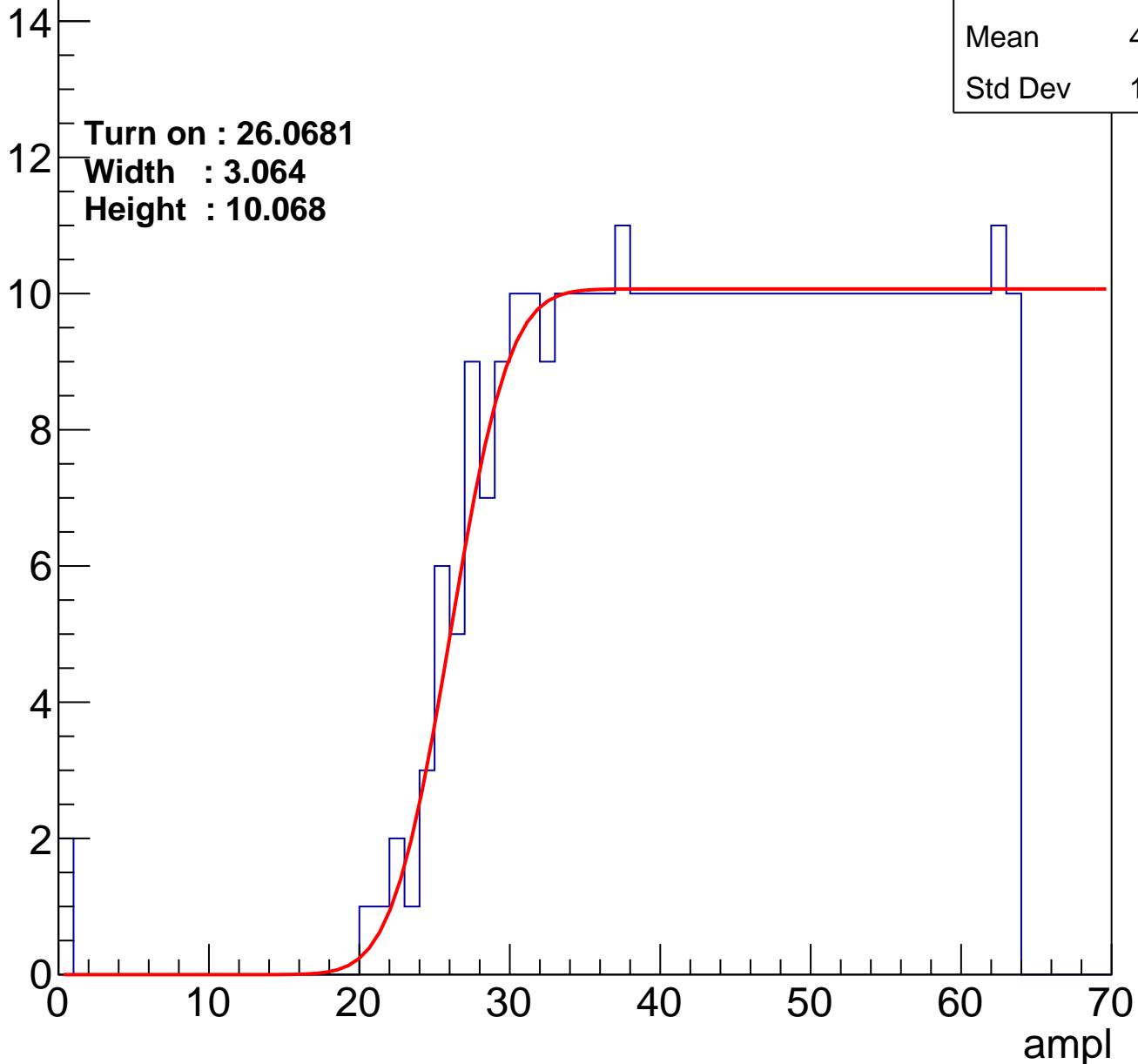
Entries	387
Mean	44.02
Std Dev	11.69

Turn on : 26.0681

Width : 3.064

Height : 10.068

Entry



B1L103S, U22-ch19

calib_packv5_042523_0143.root, FC#7, port C2

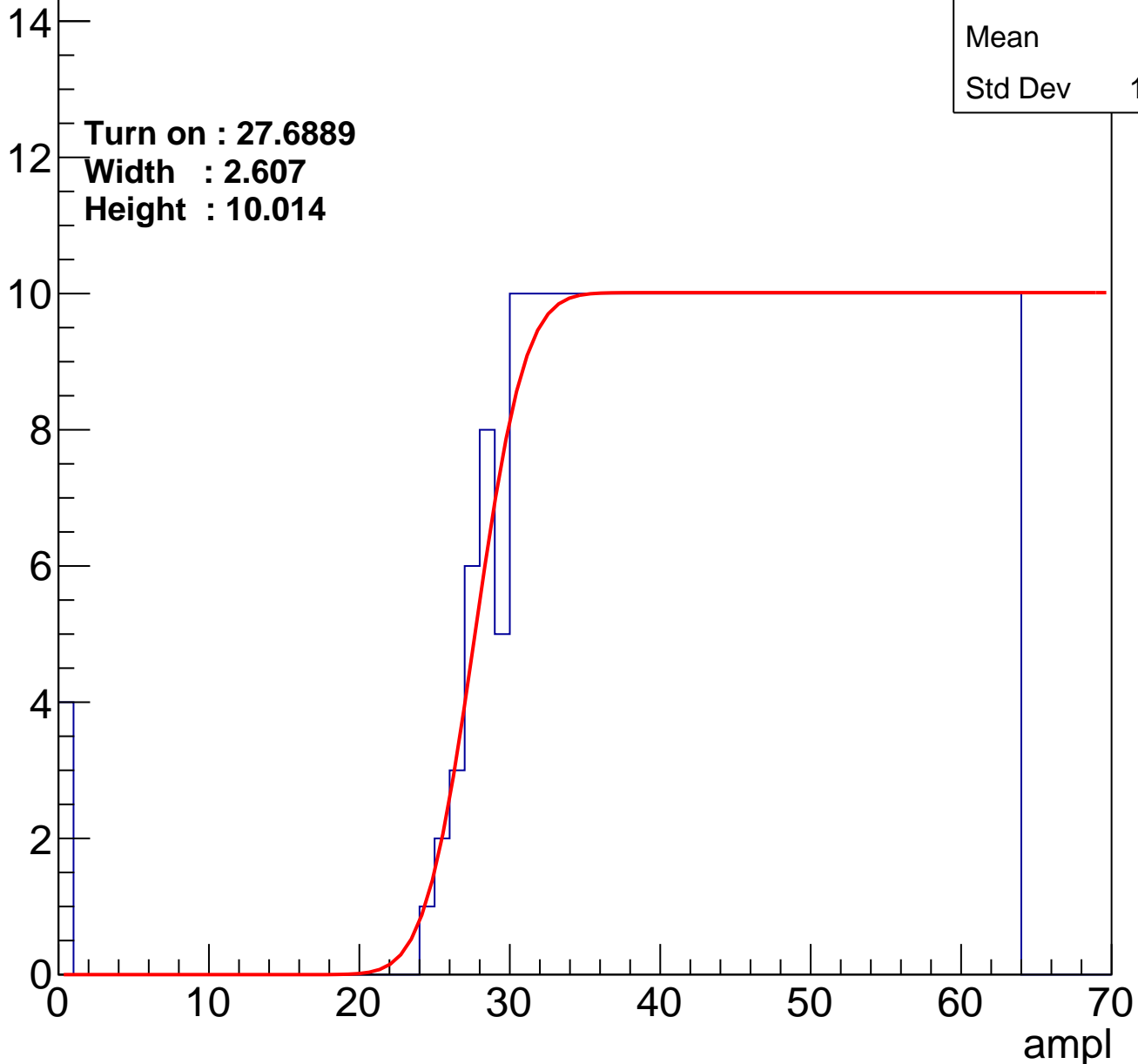
Entries	369
Mean	44.7
Std Dev	11.57

Turn on : 27.6889

Width : 2.607

Height : 10.014

Entry



B1L103S, U22-ch20

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.73
Std Dev	11.65

Turn on : 24.7337

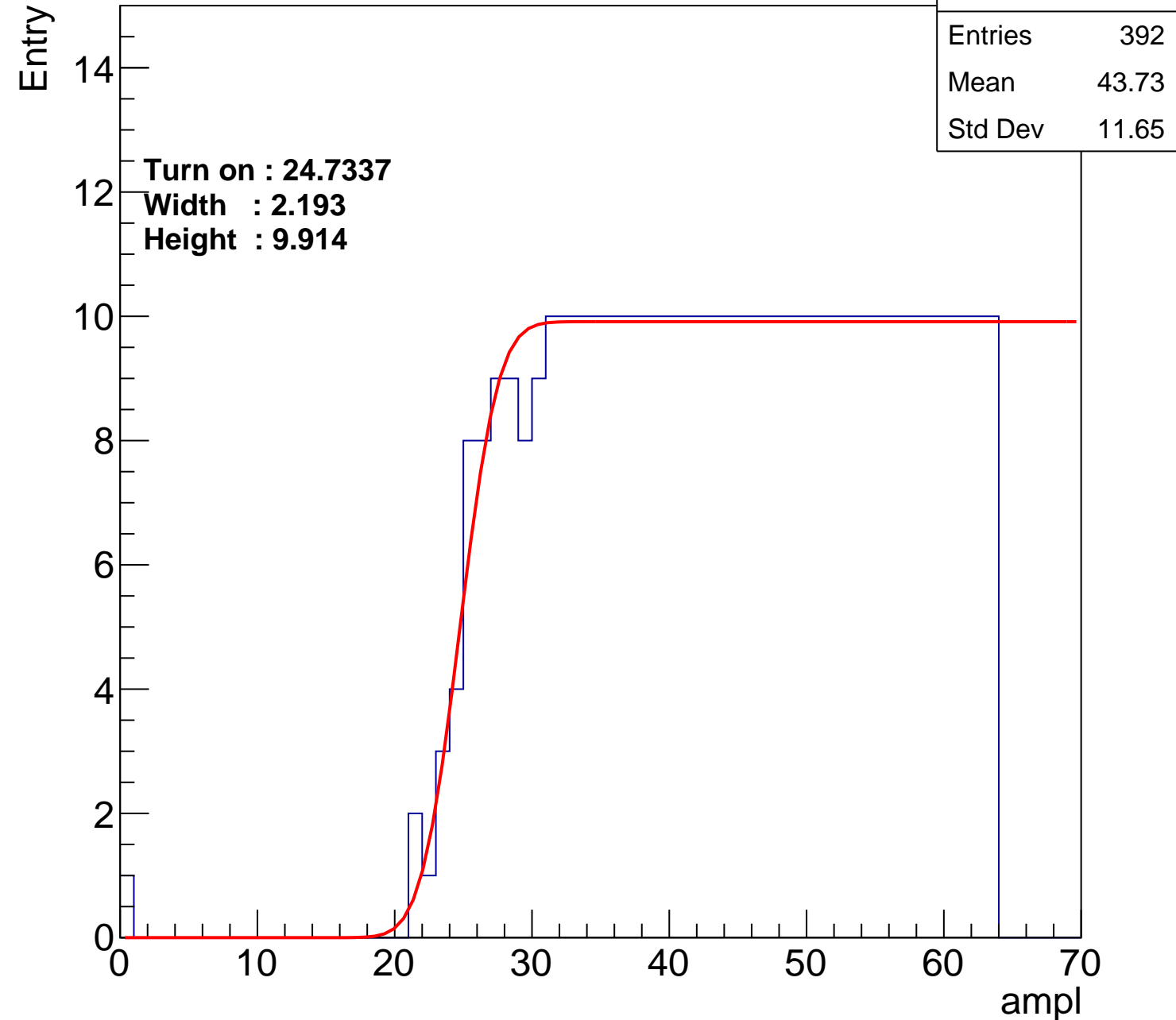
Width : 2.193

Height : 9.914

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch21

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.0545

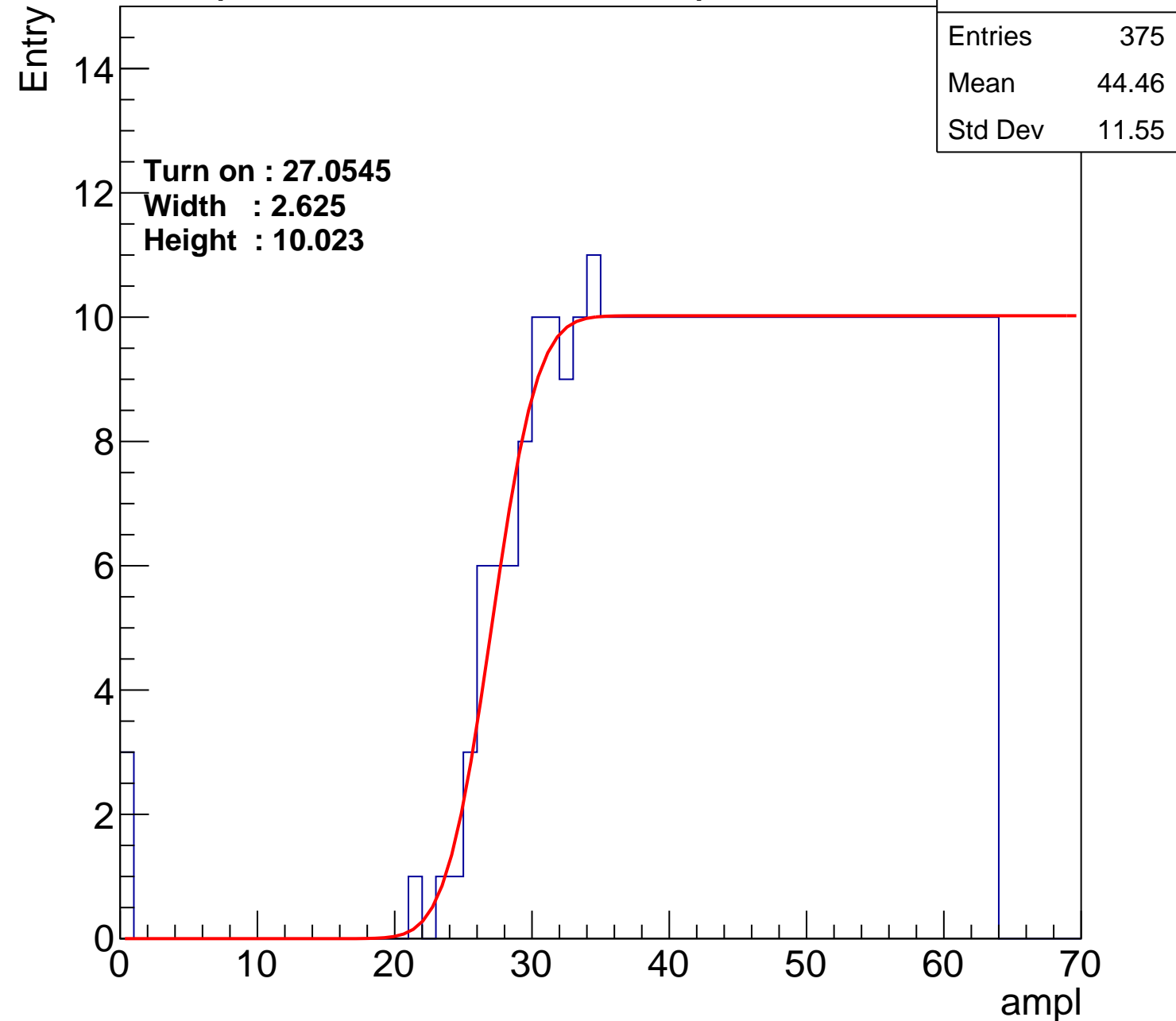
Width : 2.625

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch22

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.2
Std Dev	11.5

Turn on : 26.8552

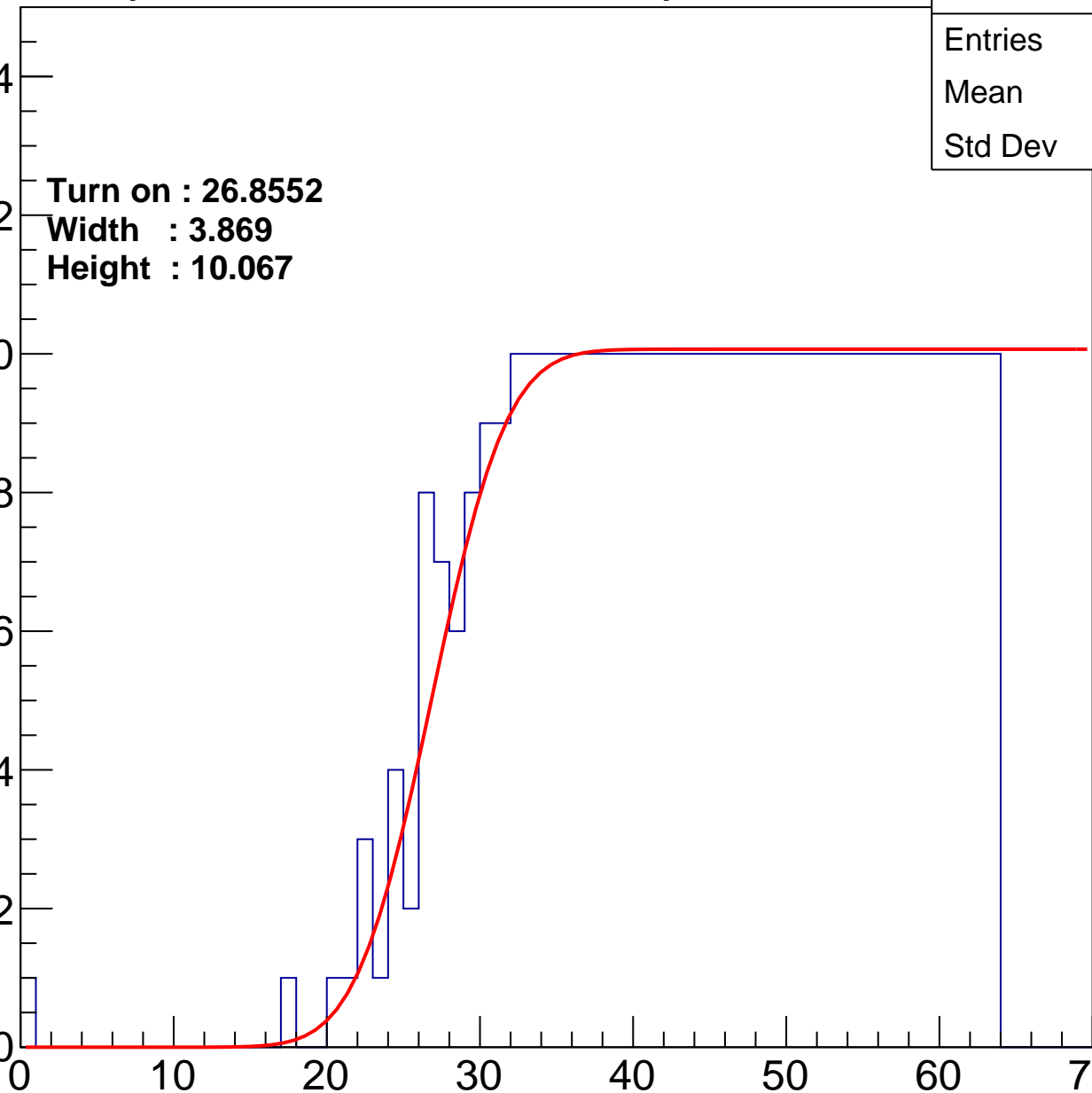
Width : 3.869

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch23

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.88
Std Dev	11.79

Turn on : 25.2687

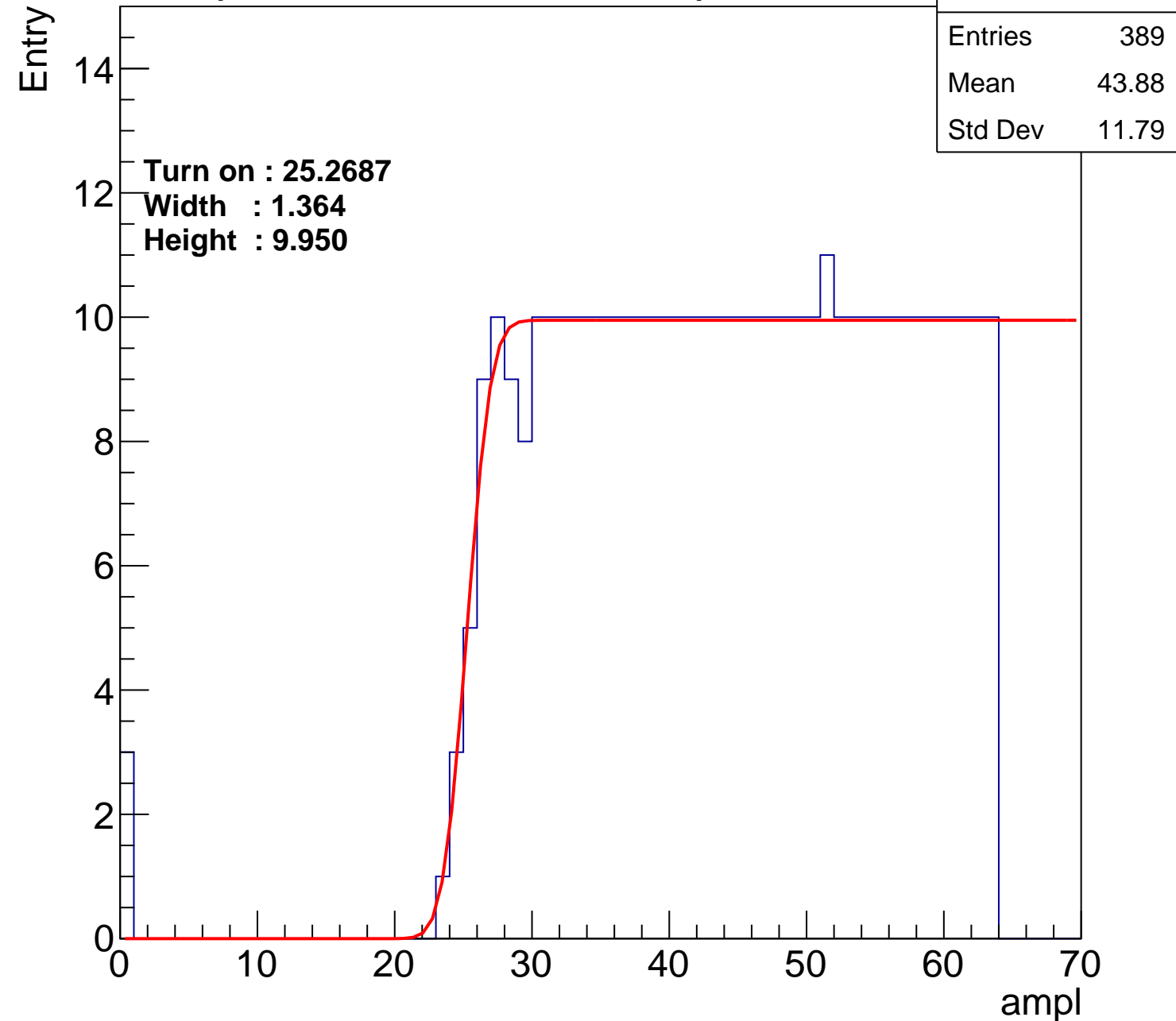
Width : 1.364

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch24

calib_packv5_042523_0143.root, FC#7, port C2

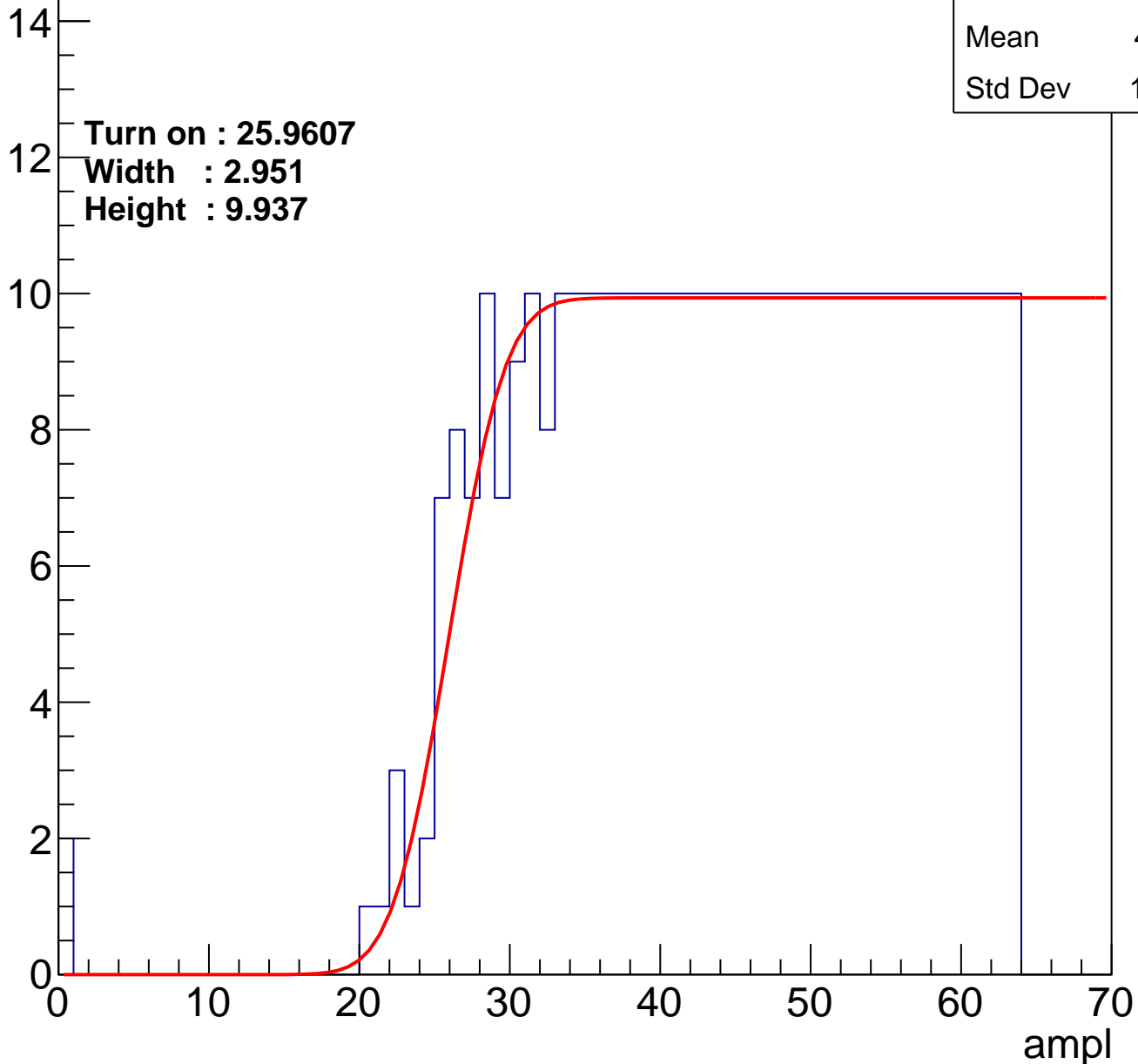
Entries	386
Mean	43.91
Std Dev	11.75

Turn on : 25.9607

Width : 2.951

Height : 9.937

Entry



B1L103S, U22-ch25

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.52
Std Dev	12.22

Turn on : 26.0121

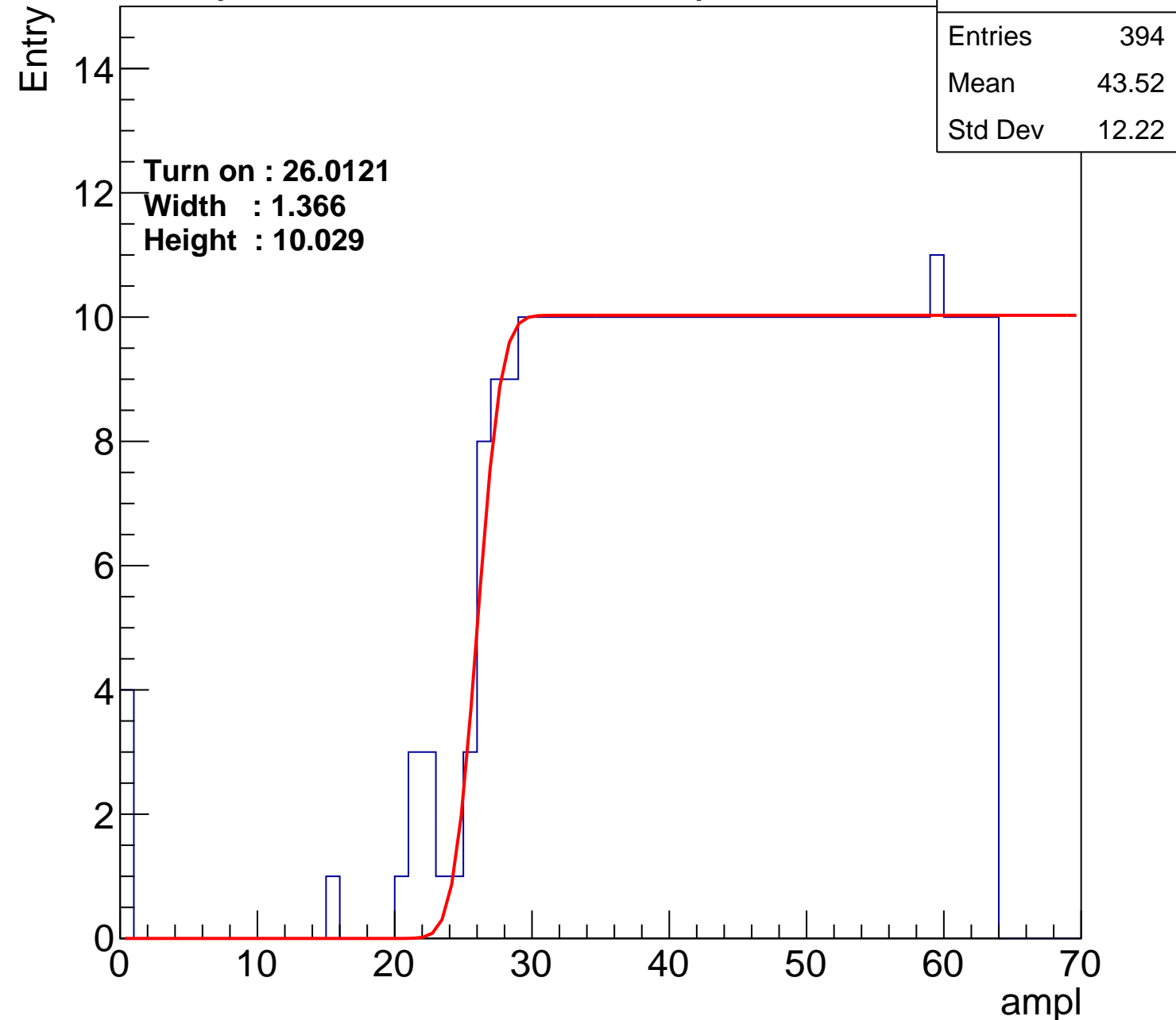
Width : 1.366

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch26

calib_packv5_042523_0143.root, FC#7, port C2

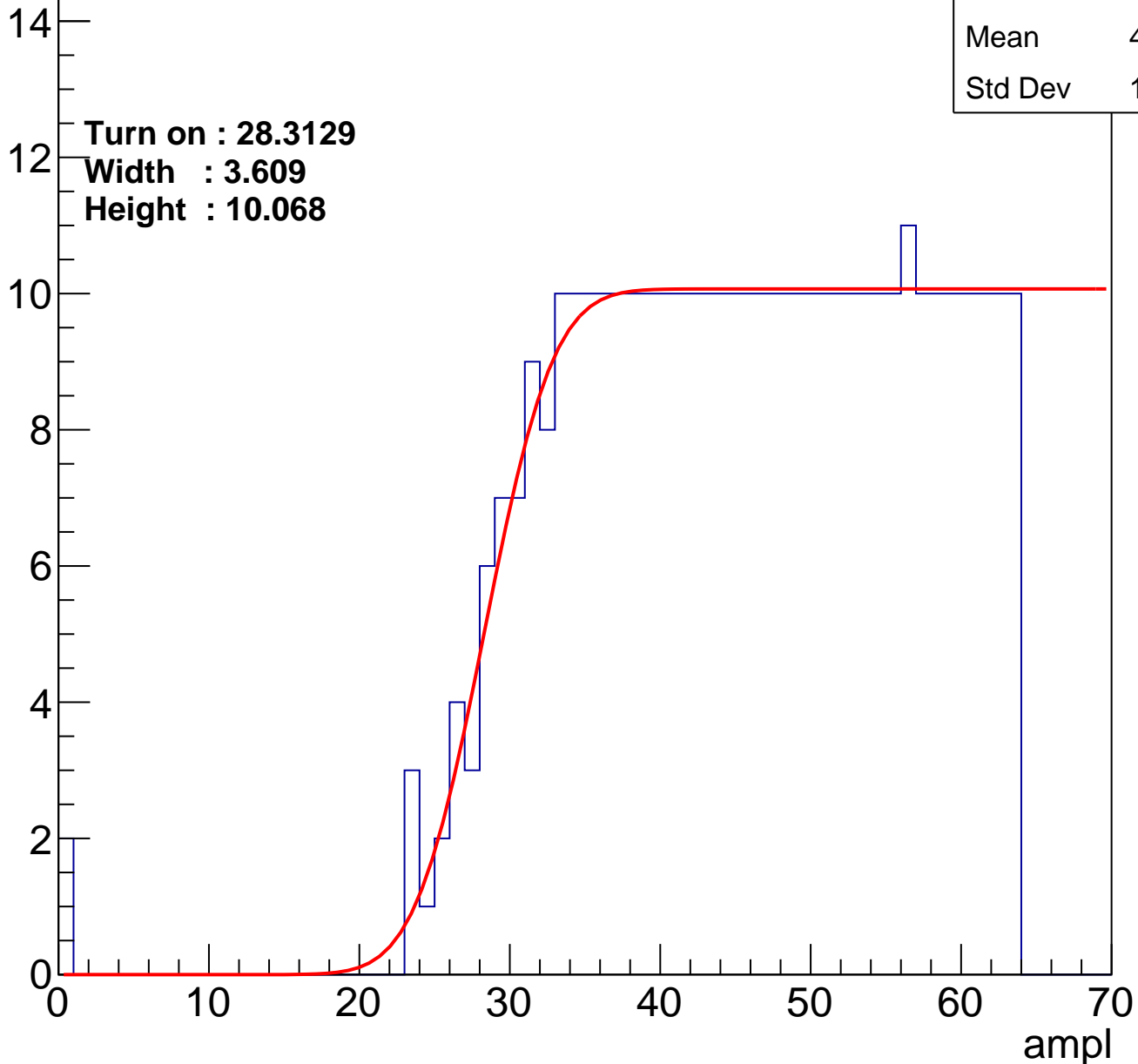
Entries	363
Mean	45.12
Std Dev	11.14

Turn on : 28.3129

Width : 3.609

Height : 10.068

Entry



B1L103S, U22-ch27

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.51
Std Dev	11.92

Turn on : 26.9980

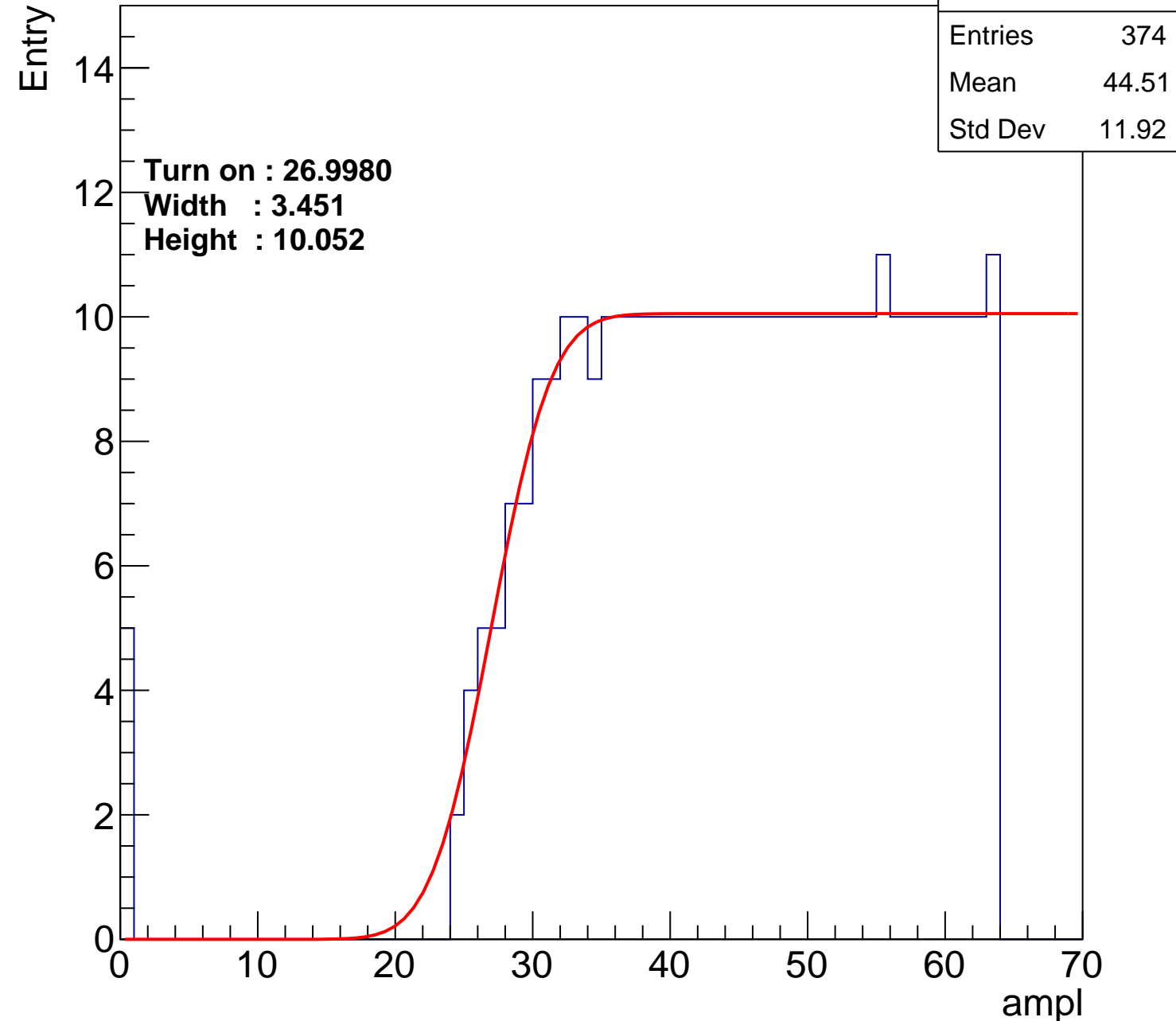
Width : 3.451

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch28

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.97
Std Dev	11.18

Turn on : 28.4220

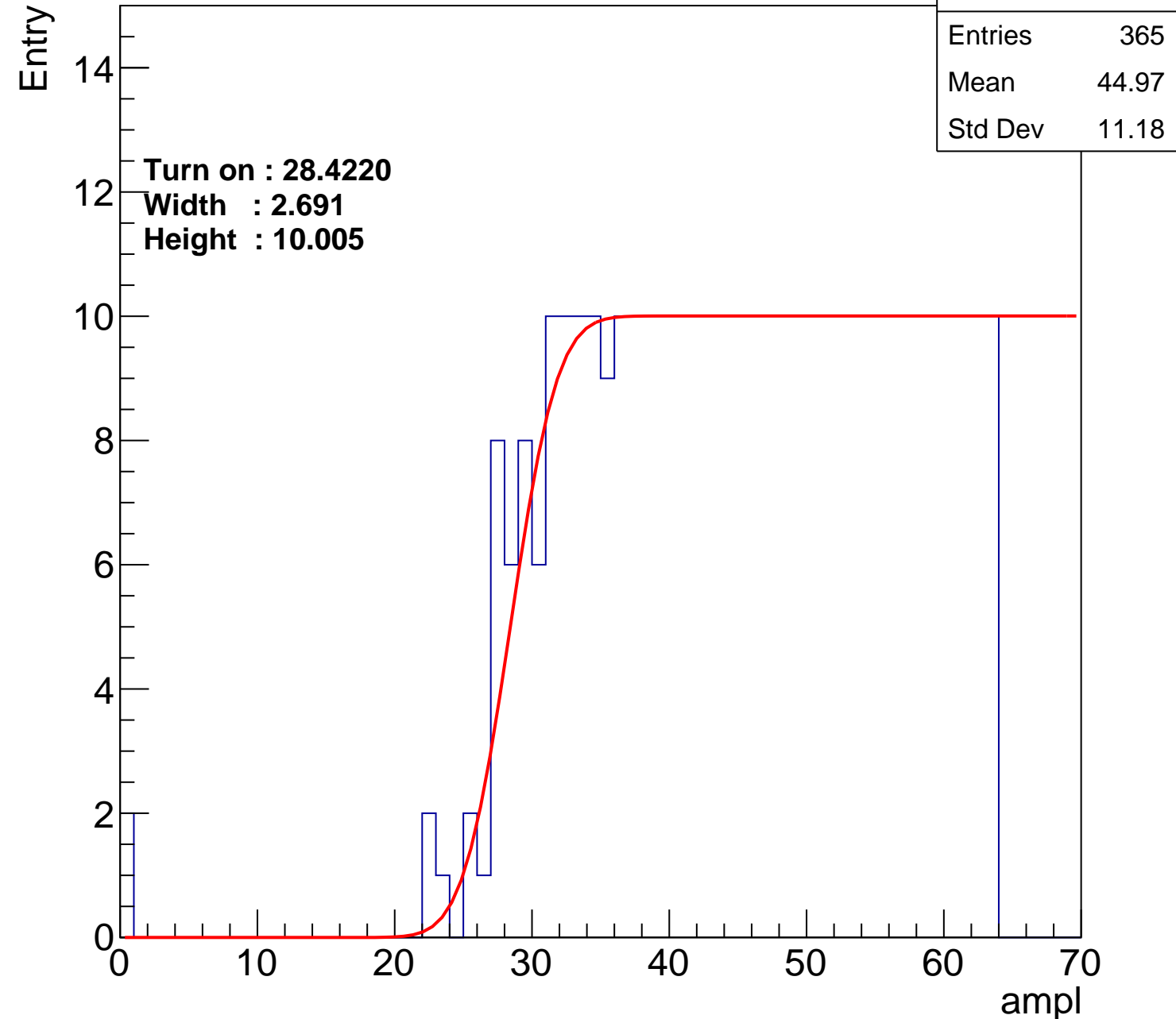
Width : 2.691

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch29

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.83
Std Dev	11.22

Turn on : 27.6986

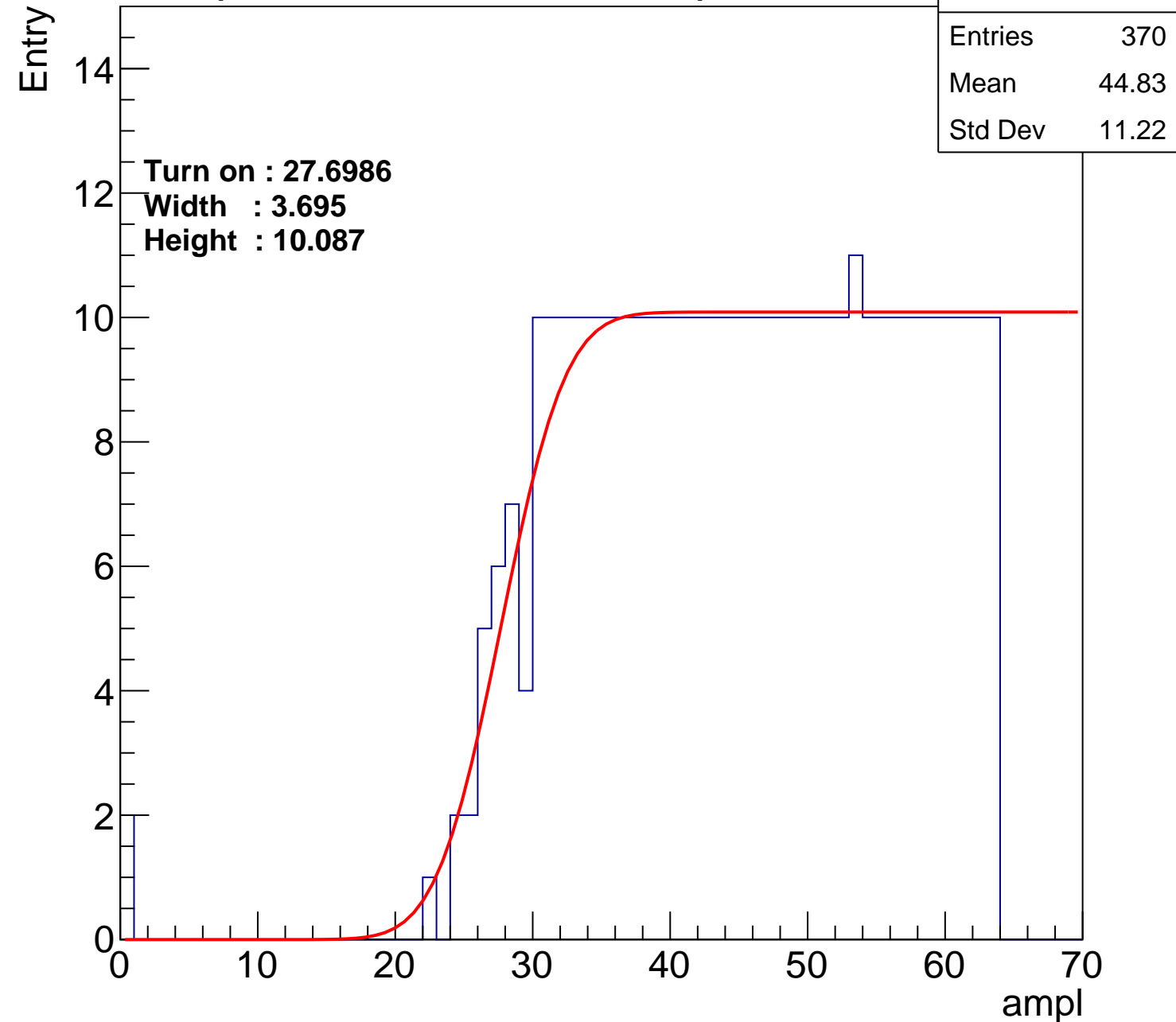
Width : 3.695

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch30

calib_packv5_042523_0143.root, FC#7, port C2

Entries	378
Mean	44.27
Std Dev	11.6

Turn on : 26.6422

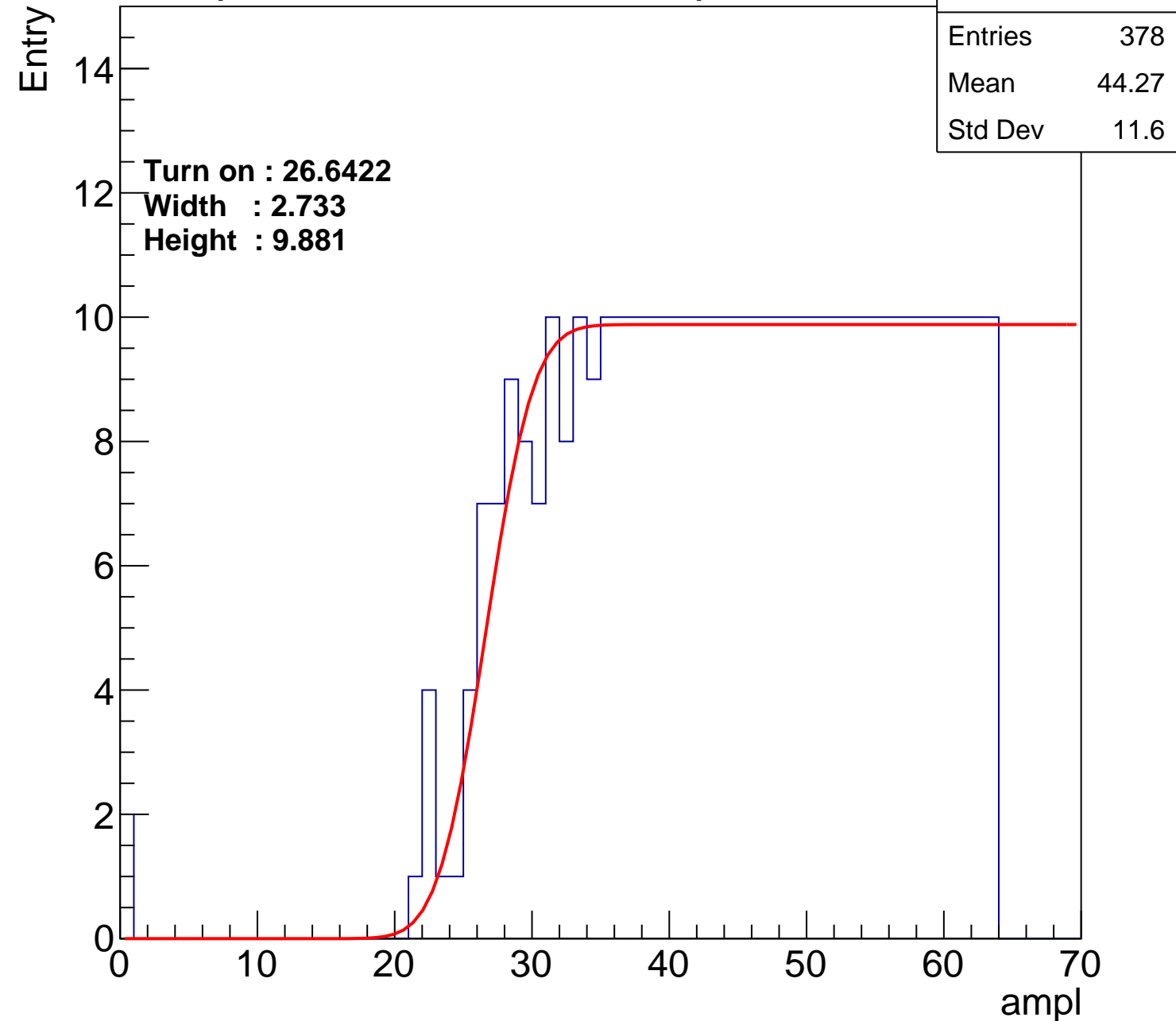
Width : 2.733

Height : 9.881

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch31

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.59
Std Dev	11.49

Turn on : 27.5970

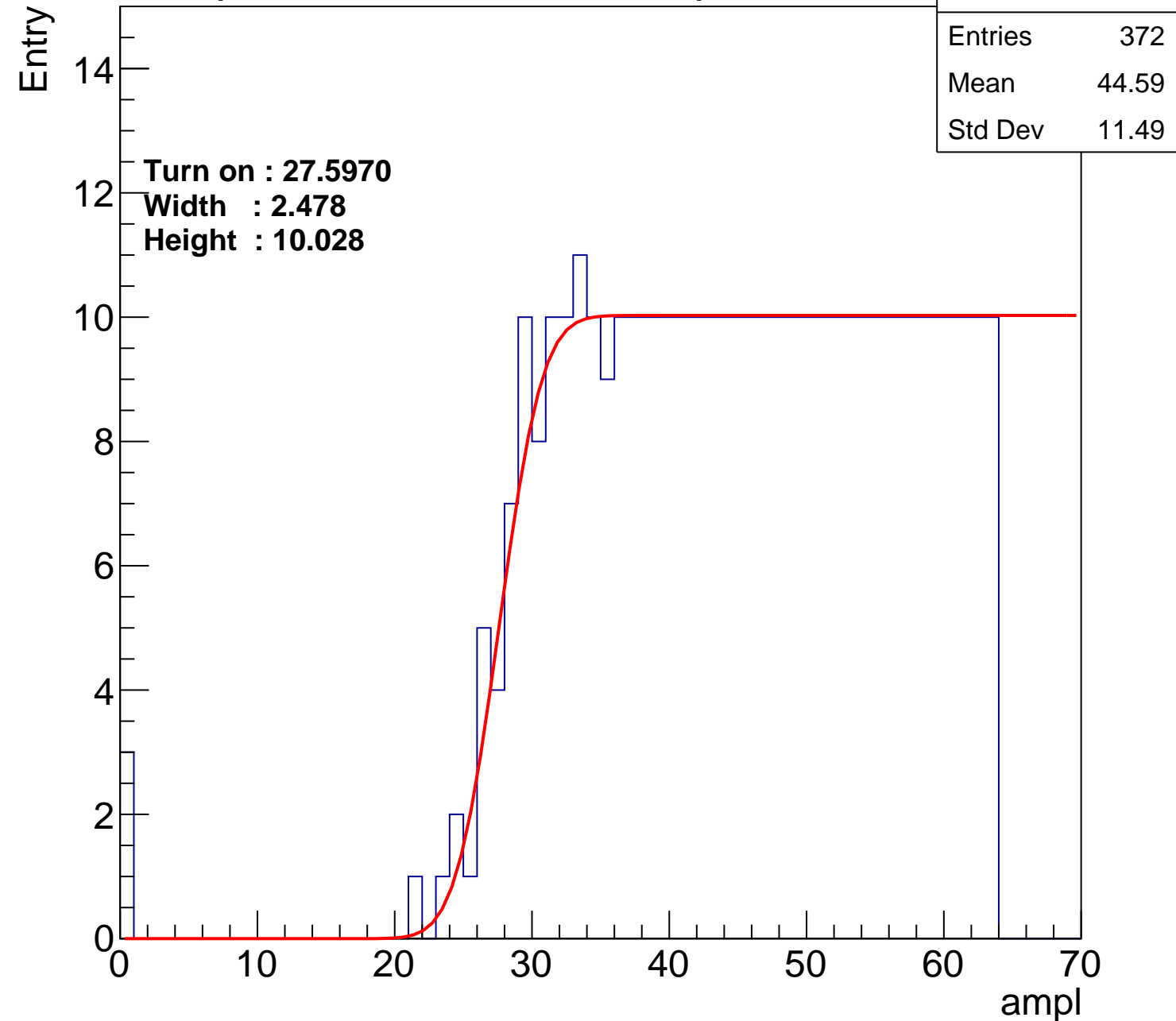
Width : 2.478

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch32

calib_packv5_042523_0143.root, FC#7, port C2

Entries	374
Mean	44.51
Std Dev	11.59

Turn on : 26.8935

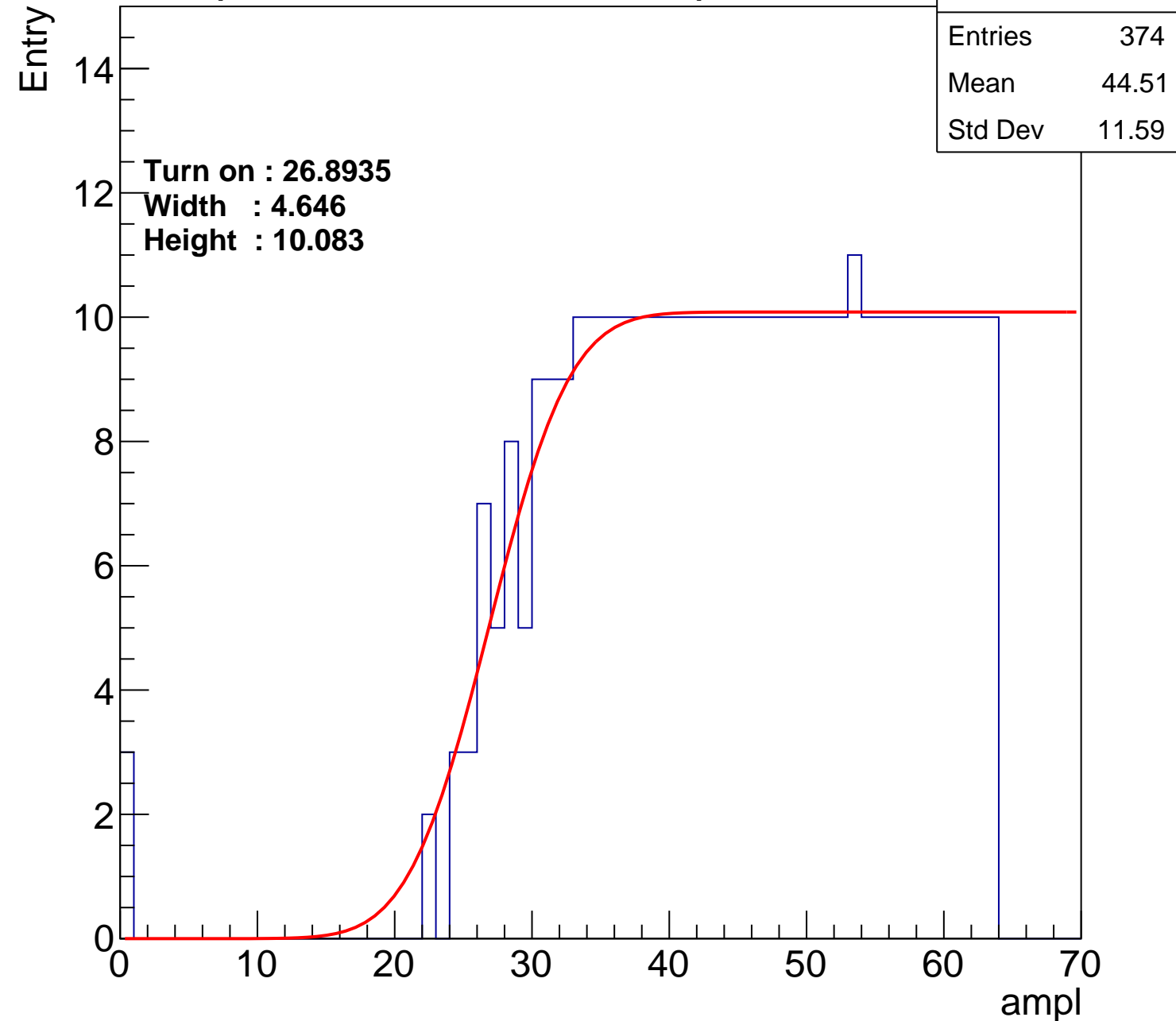
Width : 4.646

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch33

calib_packv5_042523_0143.root, FC#7, port C2

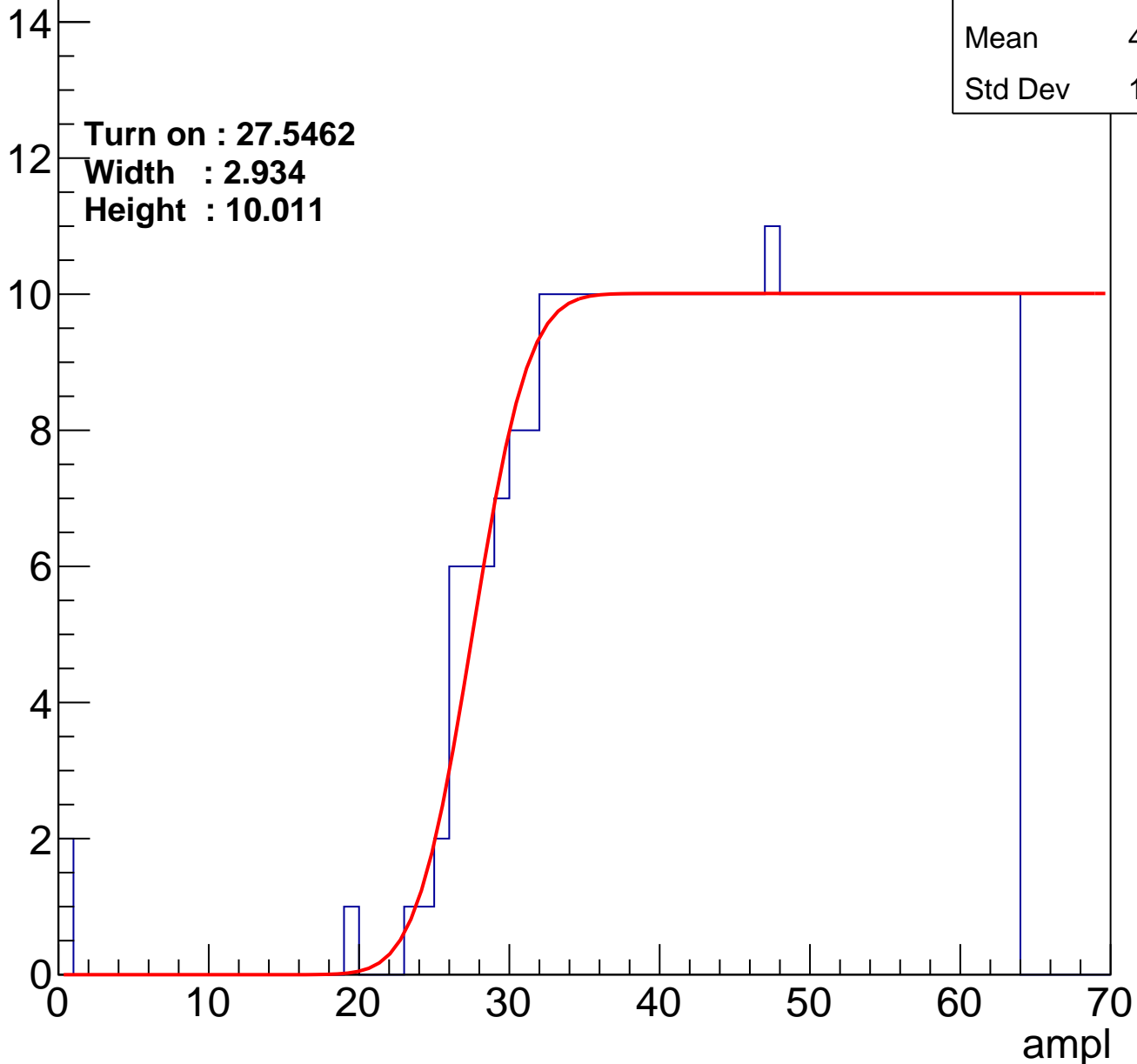
Entries	369
Mean	44.82
Std Dev	11.25

Turn on : 27.5462

Width : 2.934

Height : 10.011

Entry



B1L103S, U22-ch34

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.52
Std Dev	11.61

Turn on : 27.8144

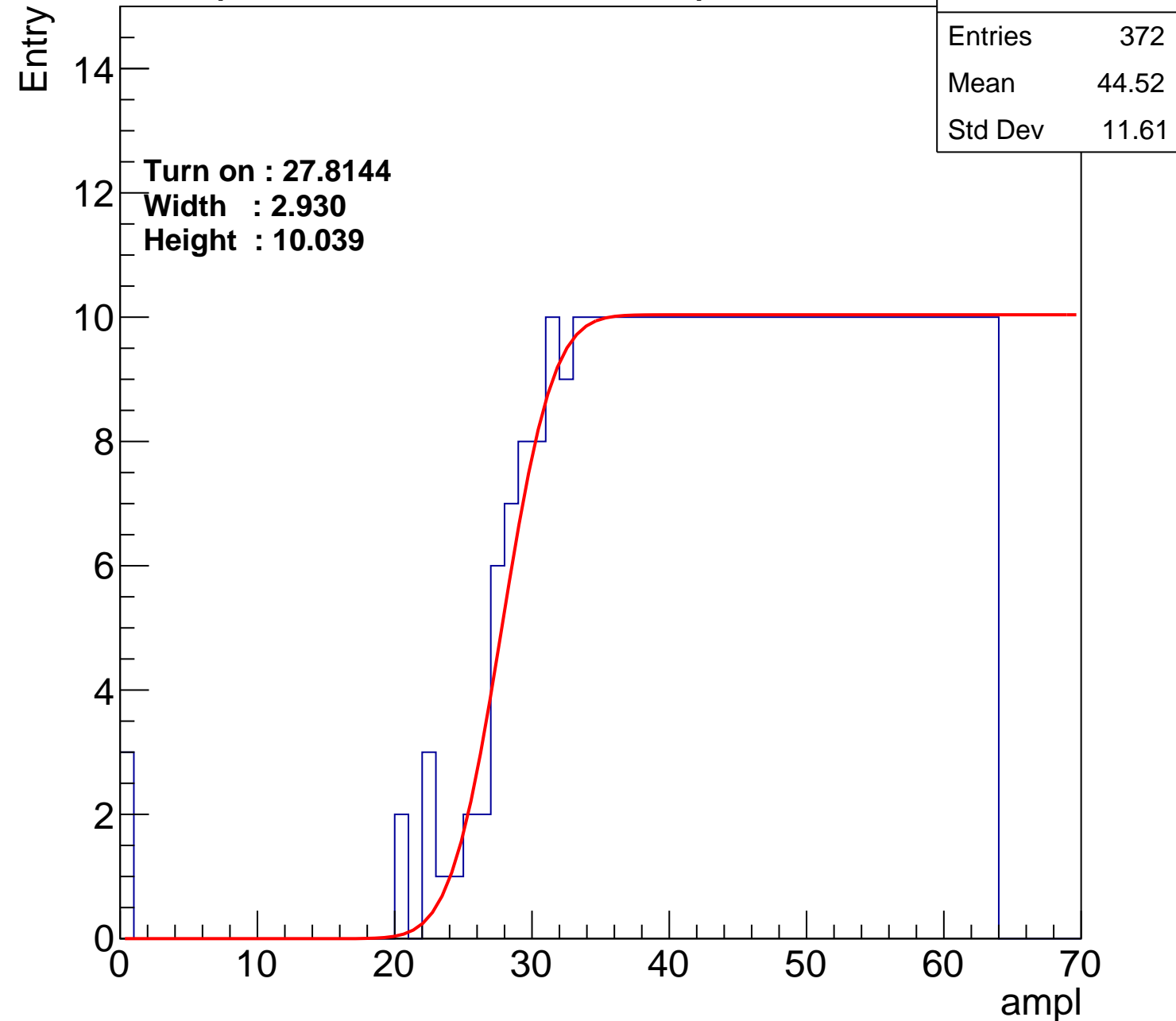
Width : 2.930

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch35

calib_packv5_042523_0143.root, FC#7, port C2

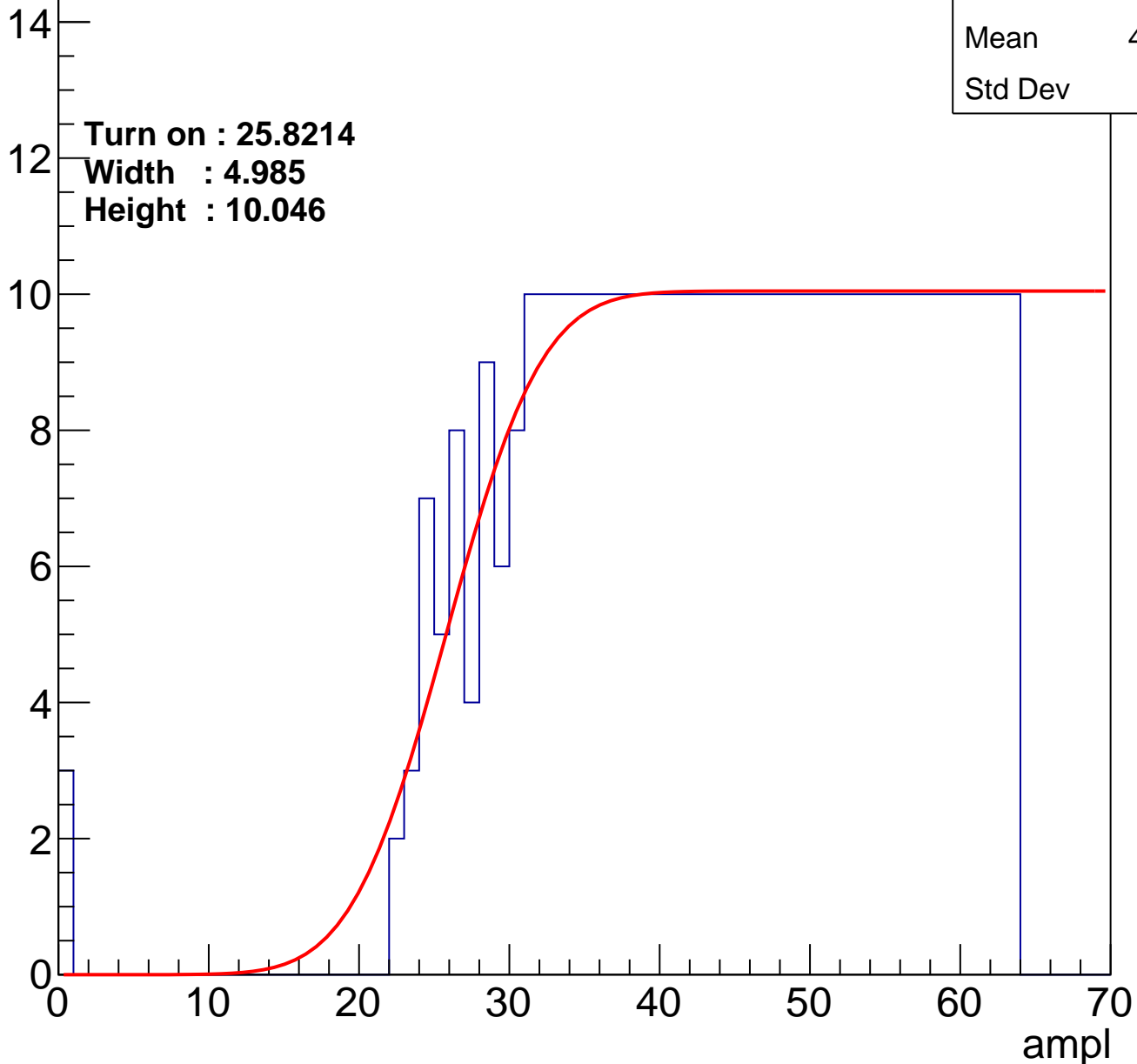
Entries	385
Mean	43.89
Std Dev	11.9

Turn on : 25.8214

Width : 4.985

Height : 10.046

Entry



B1L103S, U22-ch36

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.72
Std Dev	12.09

Turn on : 25.9237

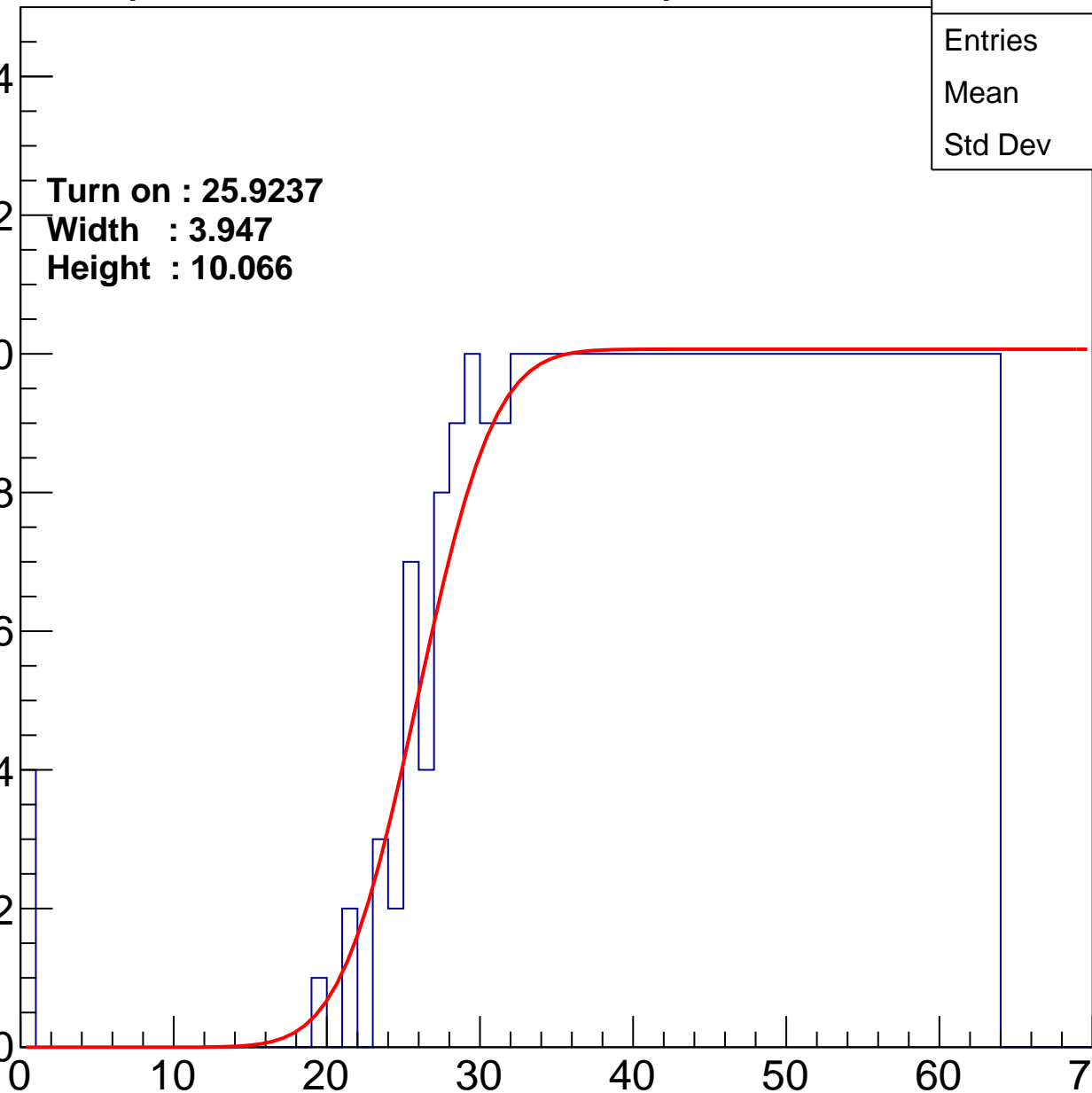
Width : 3.947

Height : 10.066

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch37

calib_packv5_042523_0143.root, FC#7, port C2

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.4081

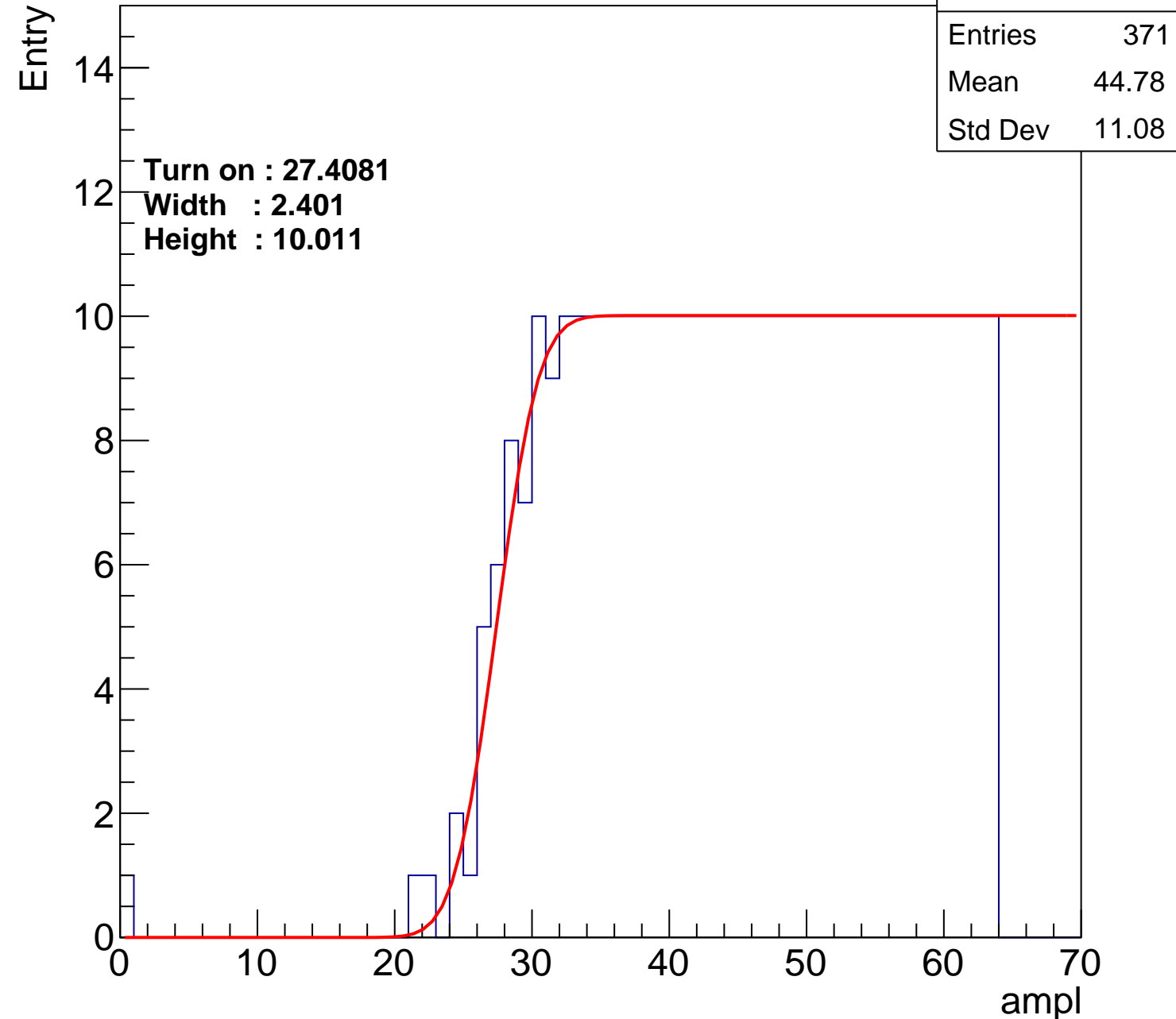
Width : 2.401

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch38

calib_packv5_042523_0143.root, FC#7, port C2

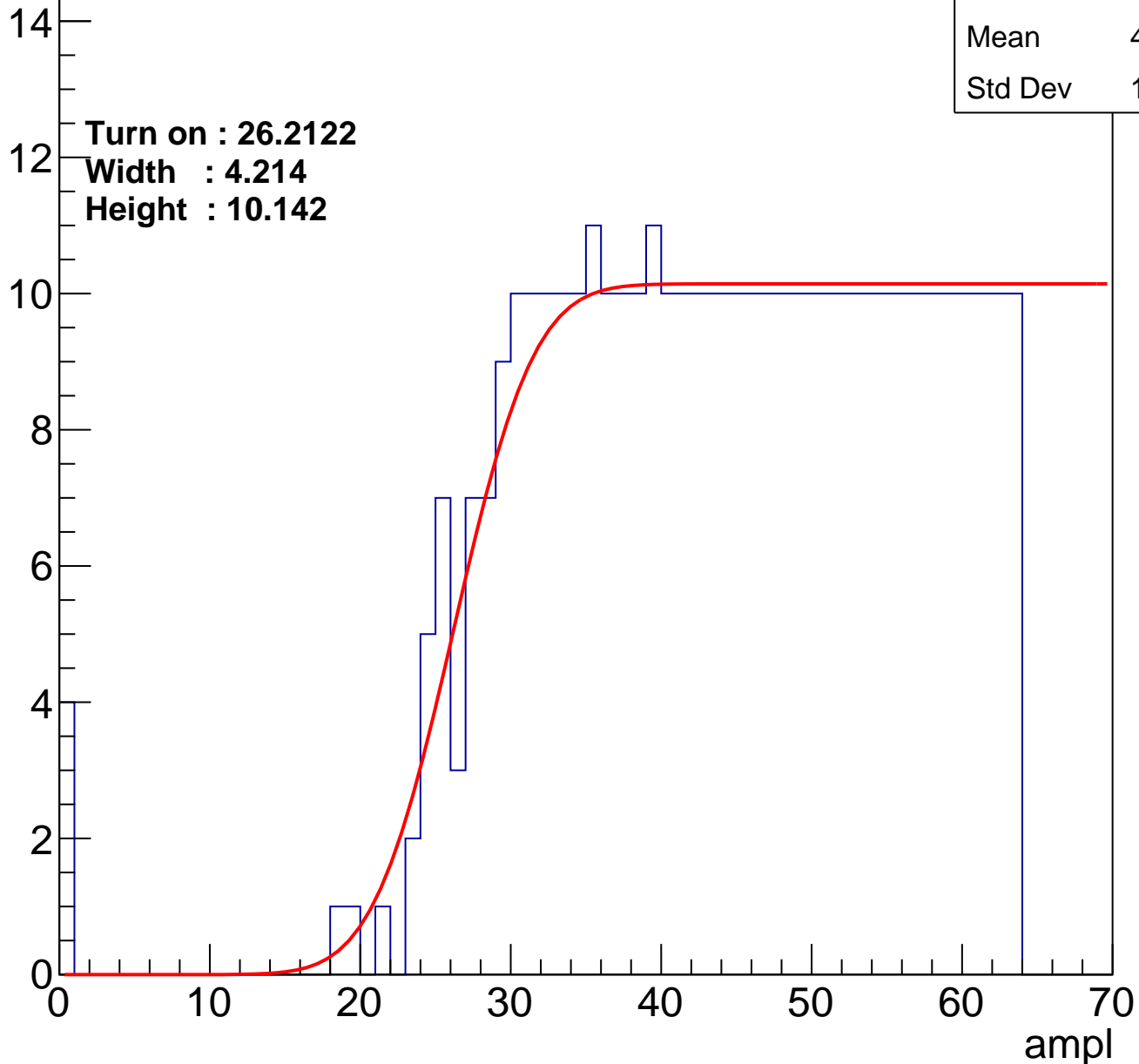
Entries	389
Mean	43.72
Std Dev	12.08

Turn on : 26.2122

Width : 4.214

Height : 10.142

Entry



B1L103S, U22-ch39

calib_packv5_042523_0143.root, FC#7, port C2

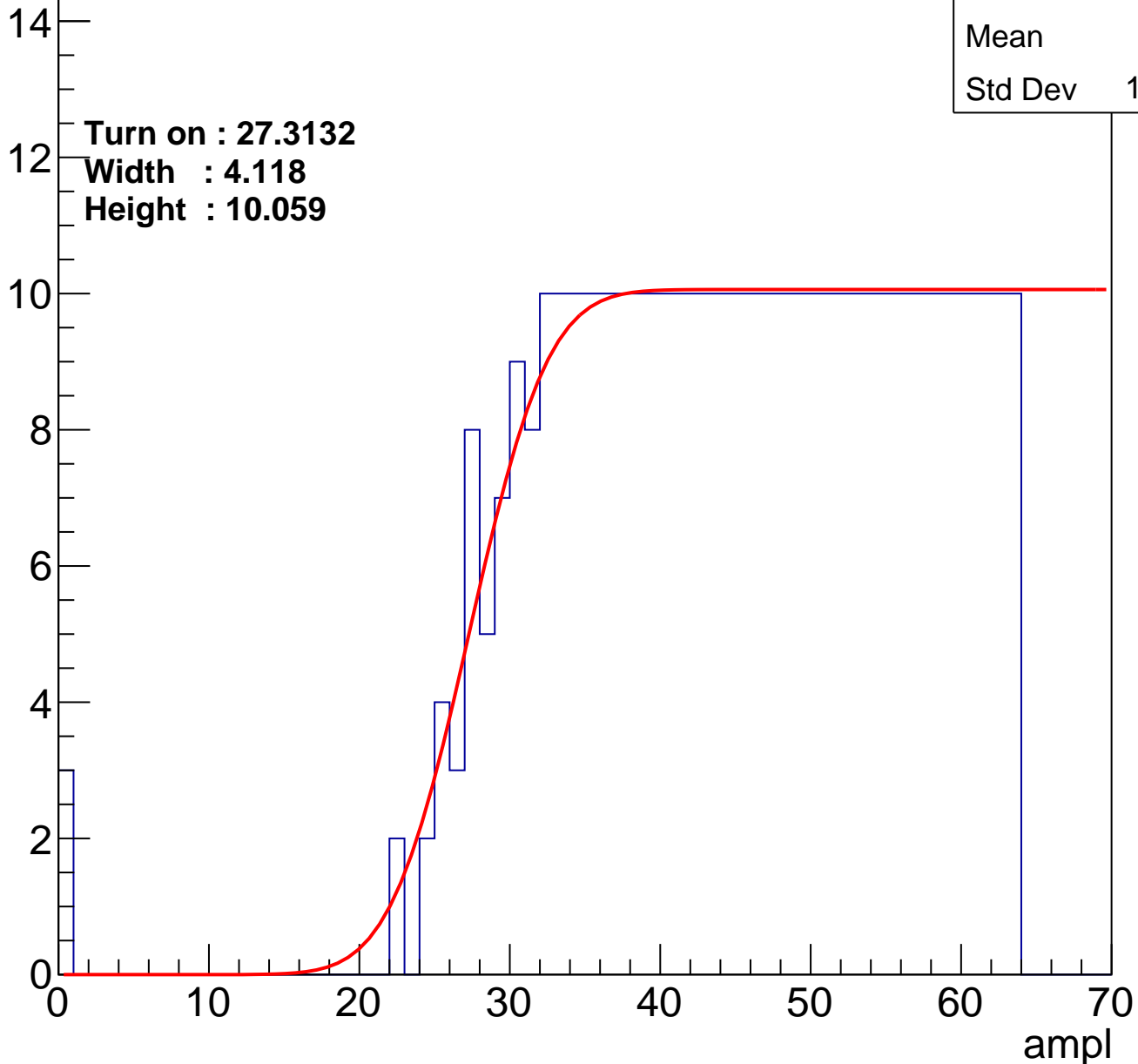
Entries	371
Mean	44.6
Std Dev	11.53

Turn on : 27.3132

Width : 4.118

Height : 10.059

Entry



B1L103S, U22-ch40

calib_packv5_042523_0143.root, FC#7, port C2

Entries	411
Mean	42.87
Std Dev	12.34

Turn on : 23.0810

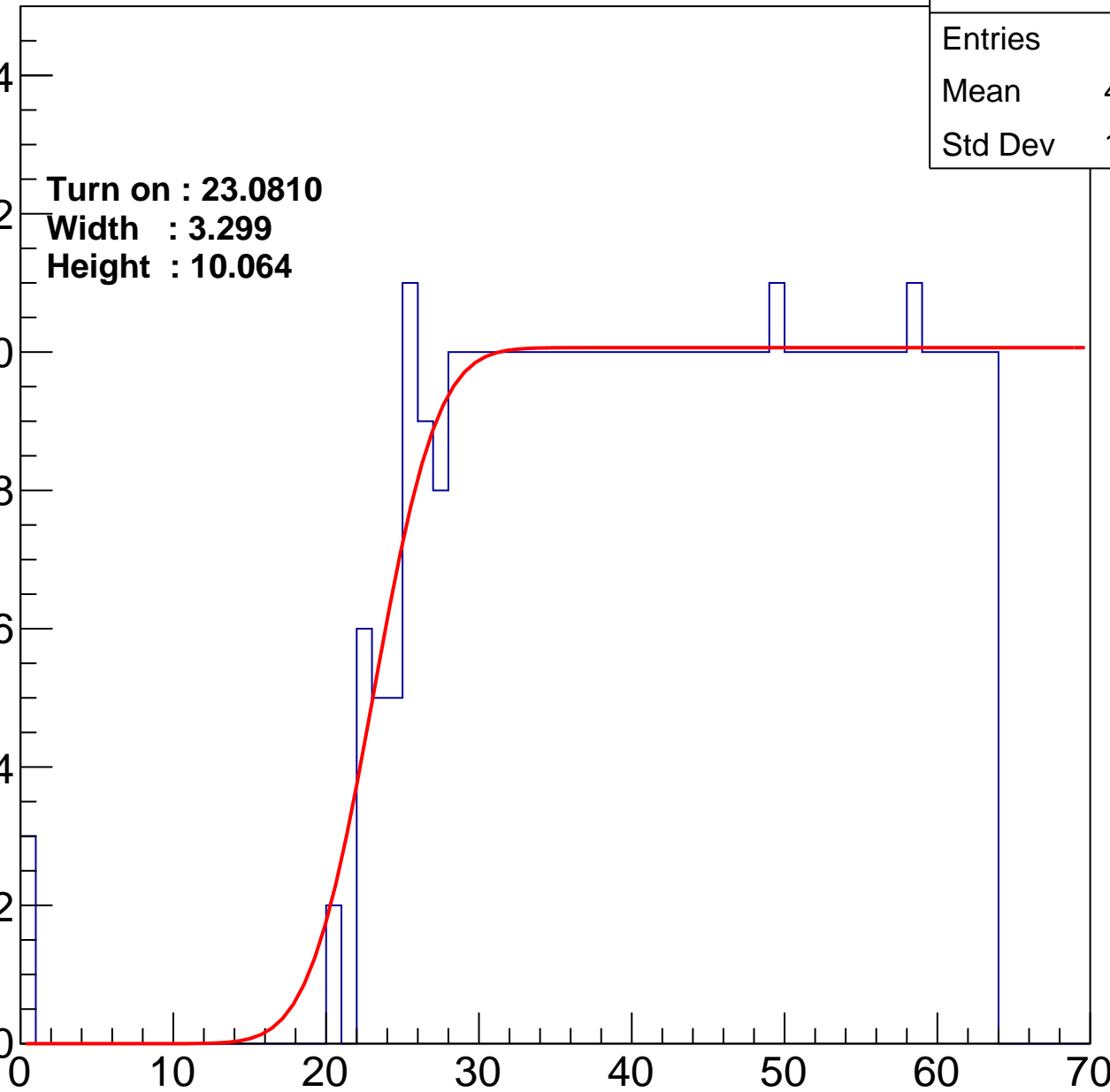
Width : 3.299

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch41

calib_packv5_042523_0143.root, FC#7, port C2

Entries	365
Mean	44.9
Std Dev	11.4

Turn on : 28.5757

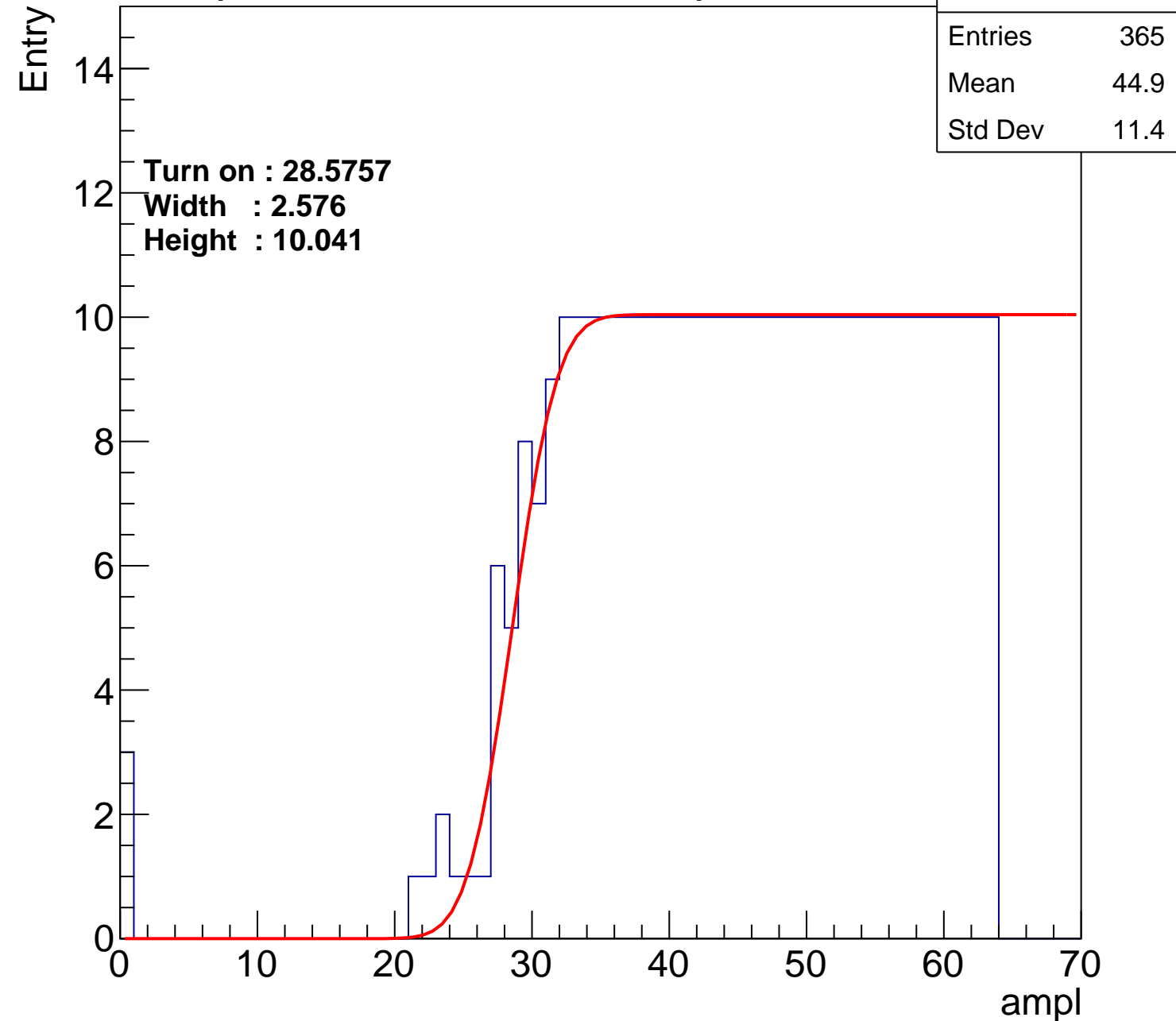
Width : 2.576

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch42

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.31
Std Dev	11.71

Turn on : 26.6220

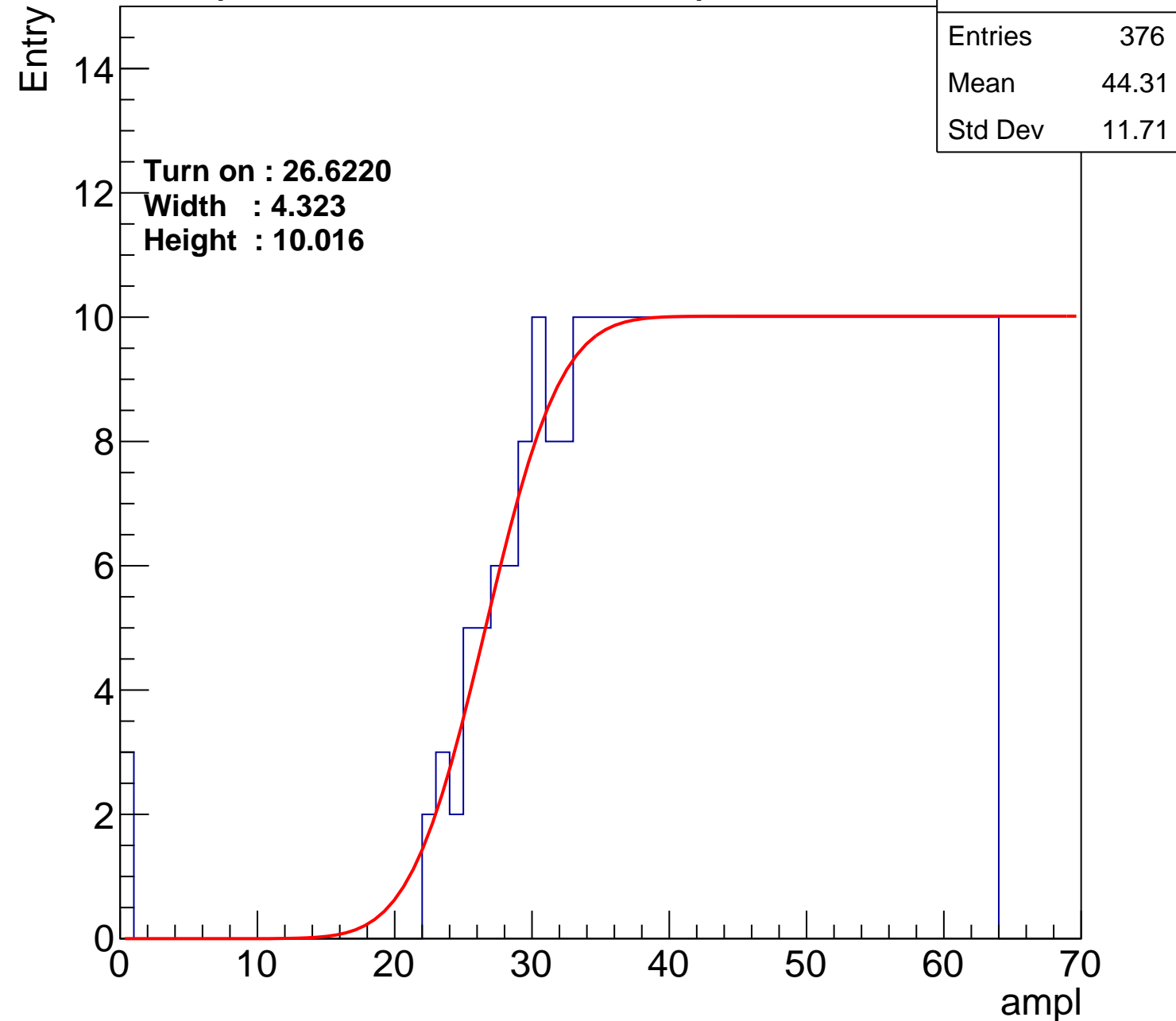
Width : 4.323

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch43

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.78
Std Dev	12.05

Turn on : 26.4917

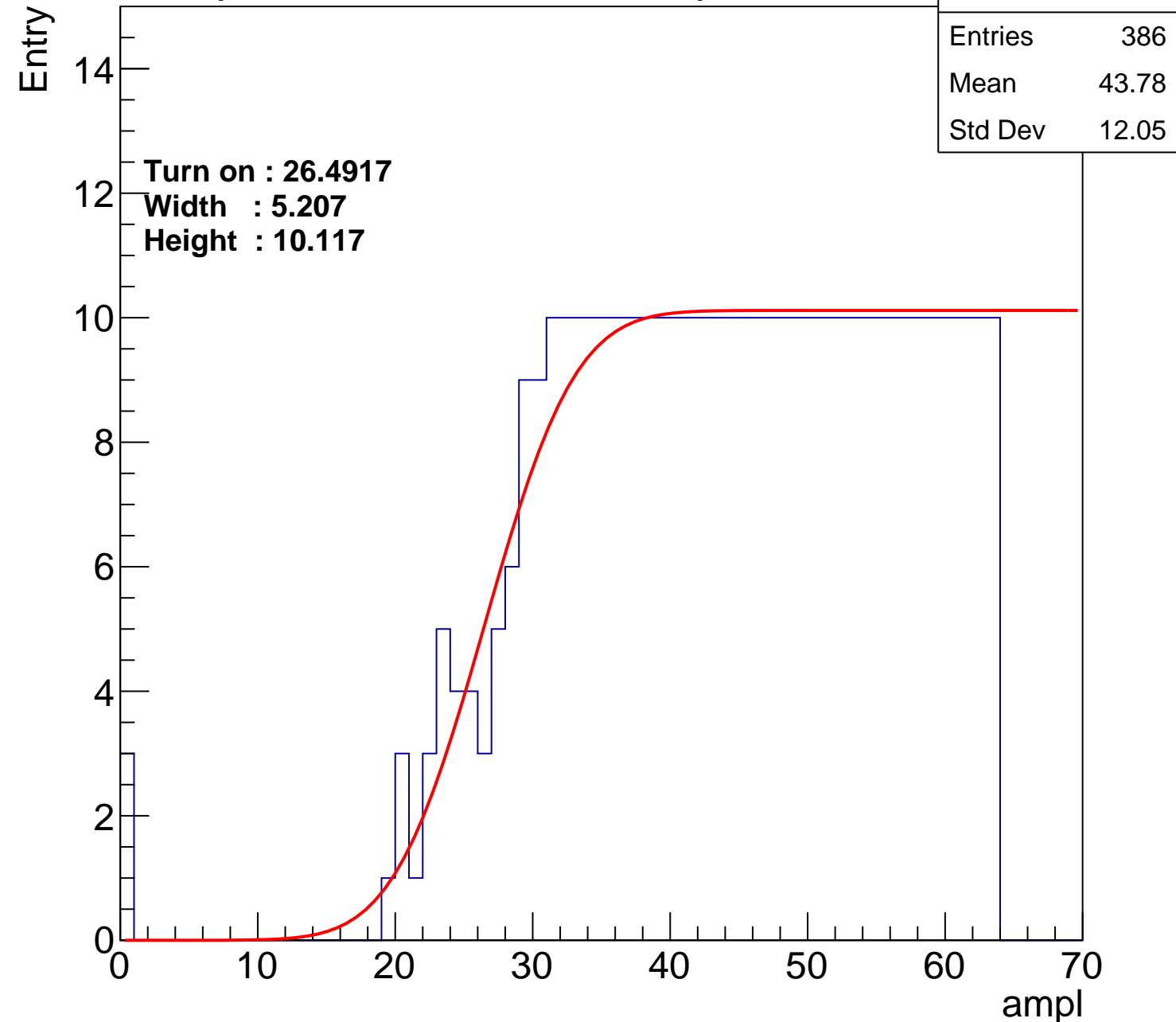
Width : 5.207

Height : 10.117

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch44

calib_packv5_042523_0143.root, FC#7, port C2

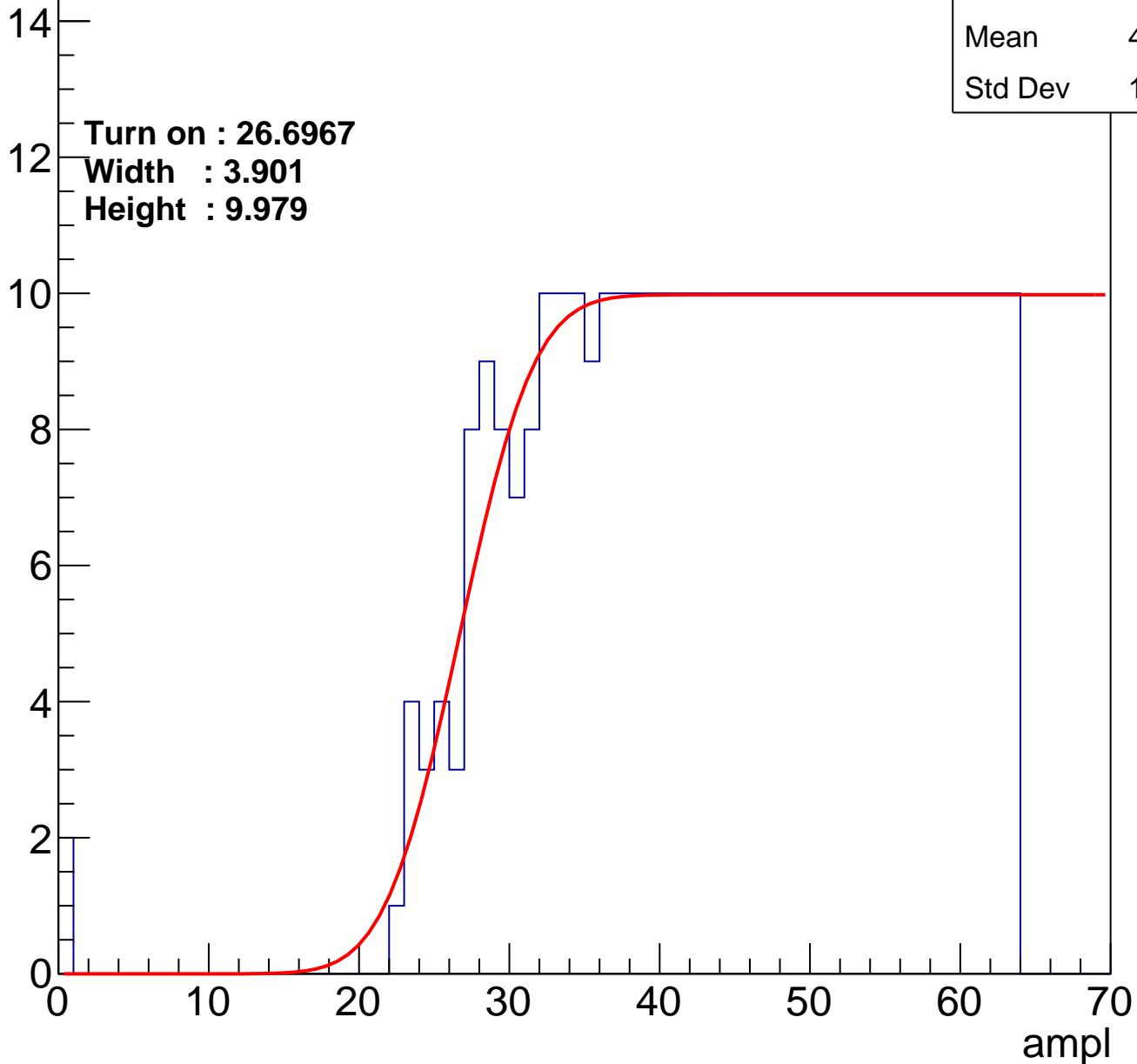
Entries	376
Mean	44.38
Std Dev	11.52

Turn on : 26.6967

Width : 3.901

Height : 9.979

Entry



B1L103S, U22-ch45

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.71
Std Dev	11.22

Turn on : 27.0871

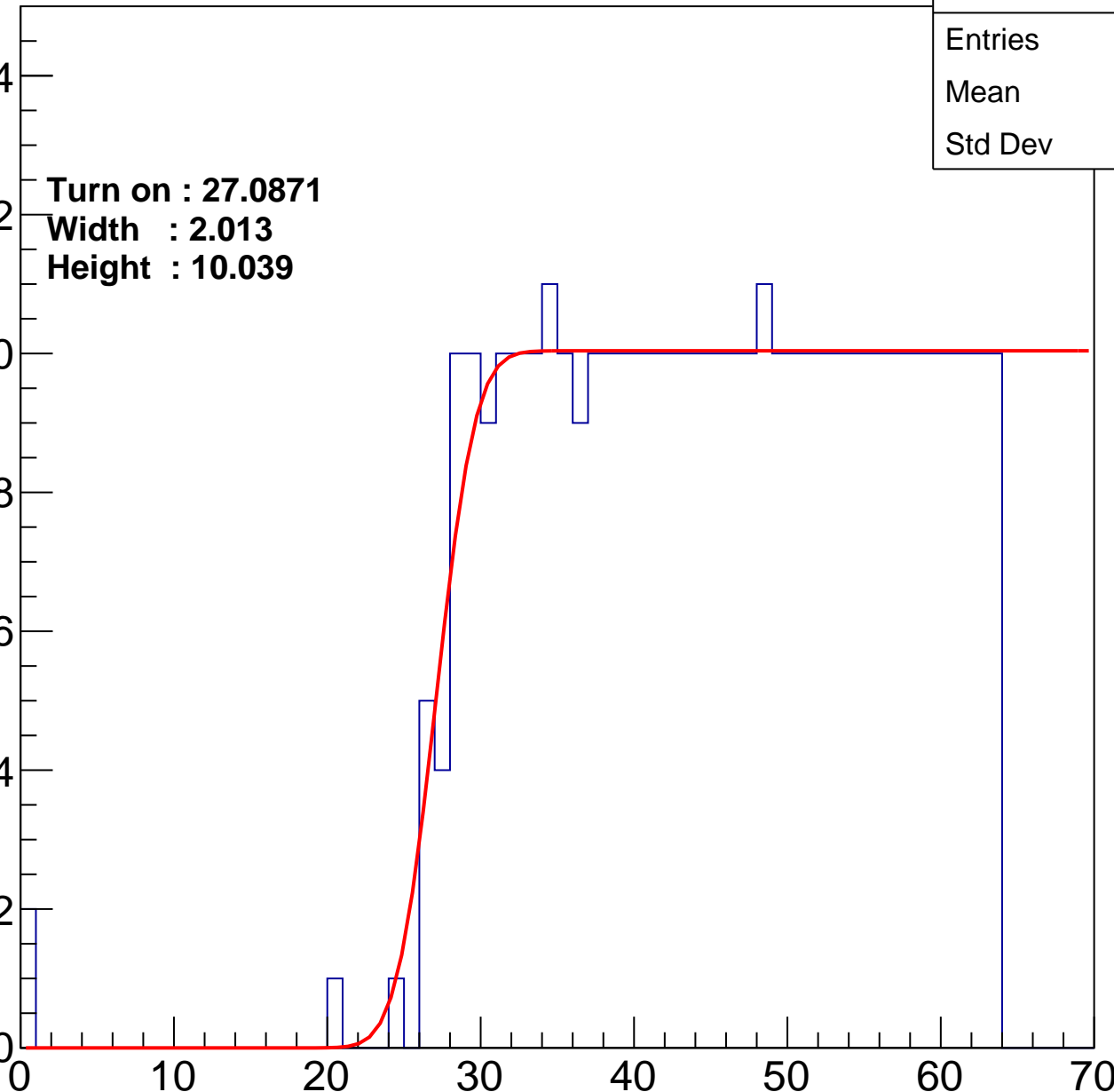
Width : 2.013

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch46

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	44.24
Std Dev	11.58

Turn on : 26.6357

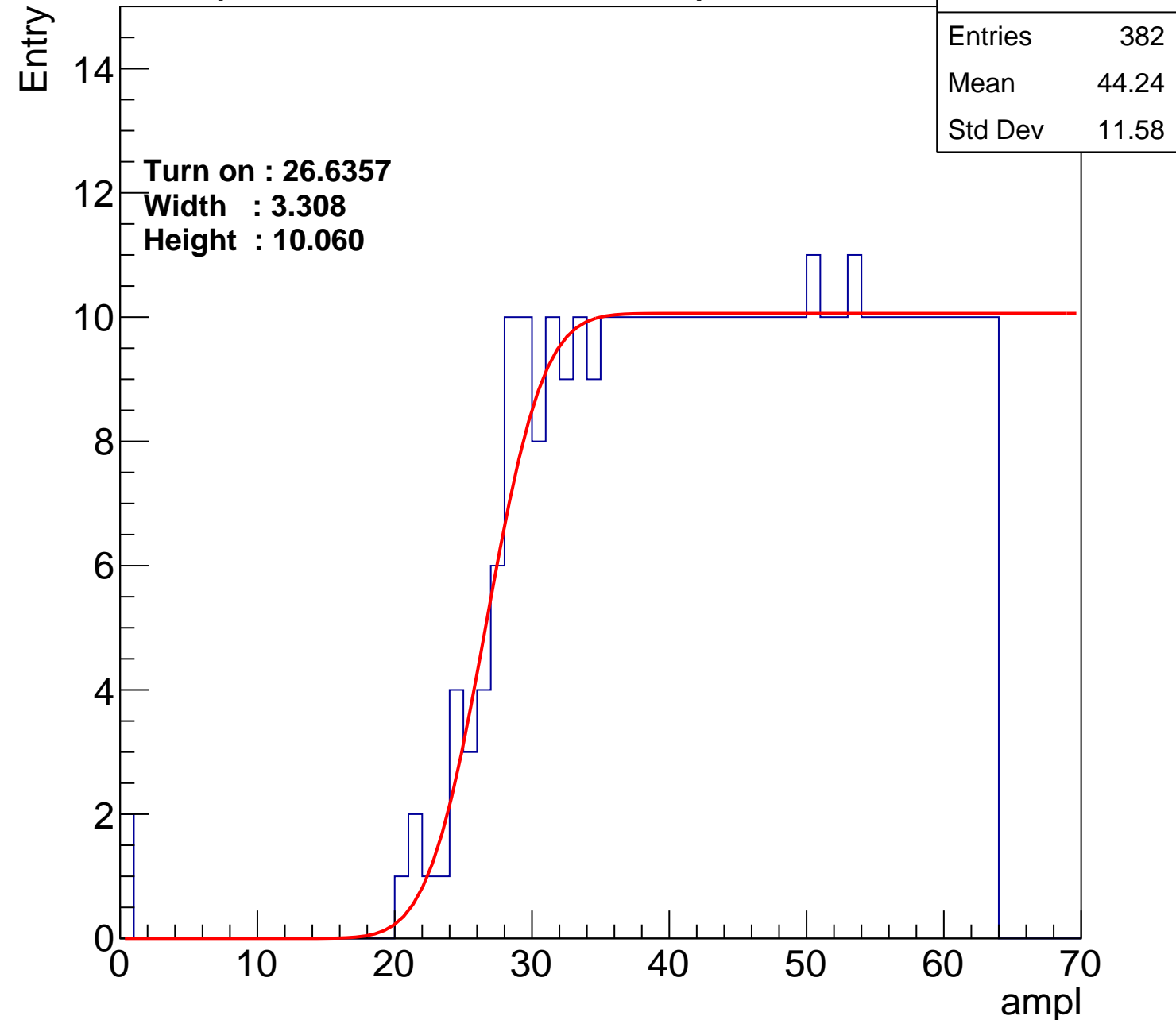
Width : 3.308

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch47

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.1
Std Dev	11.79

Turn on : 26.7814

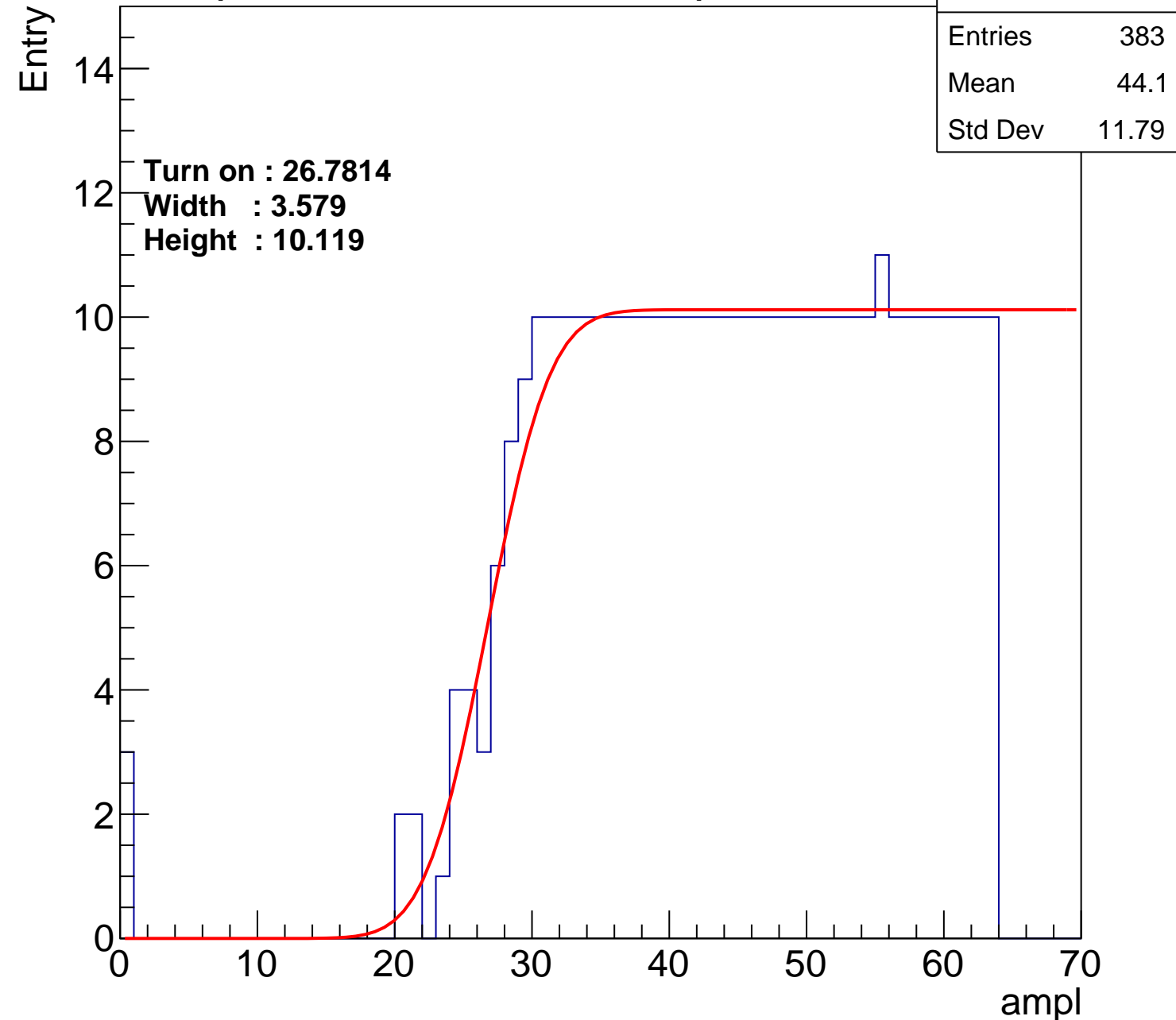
Width : 3.579

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch48

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.92
Std Dev	11.57

Turn on : 25.8970

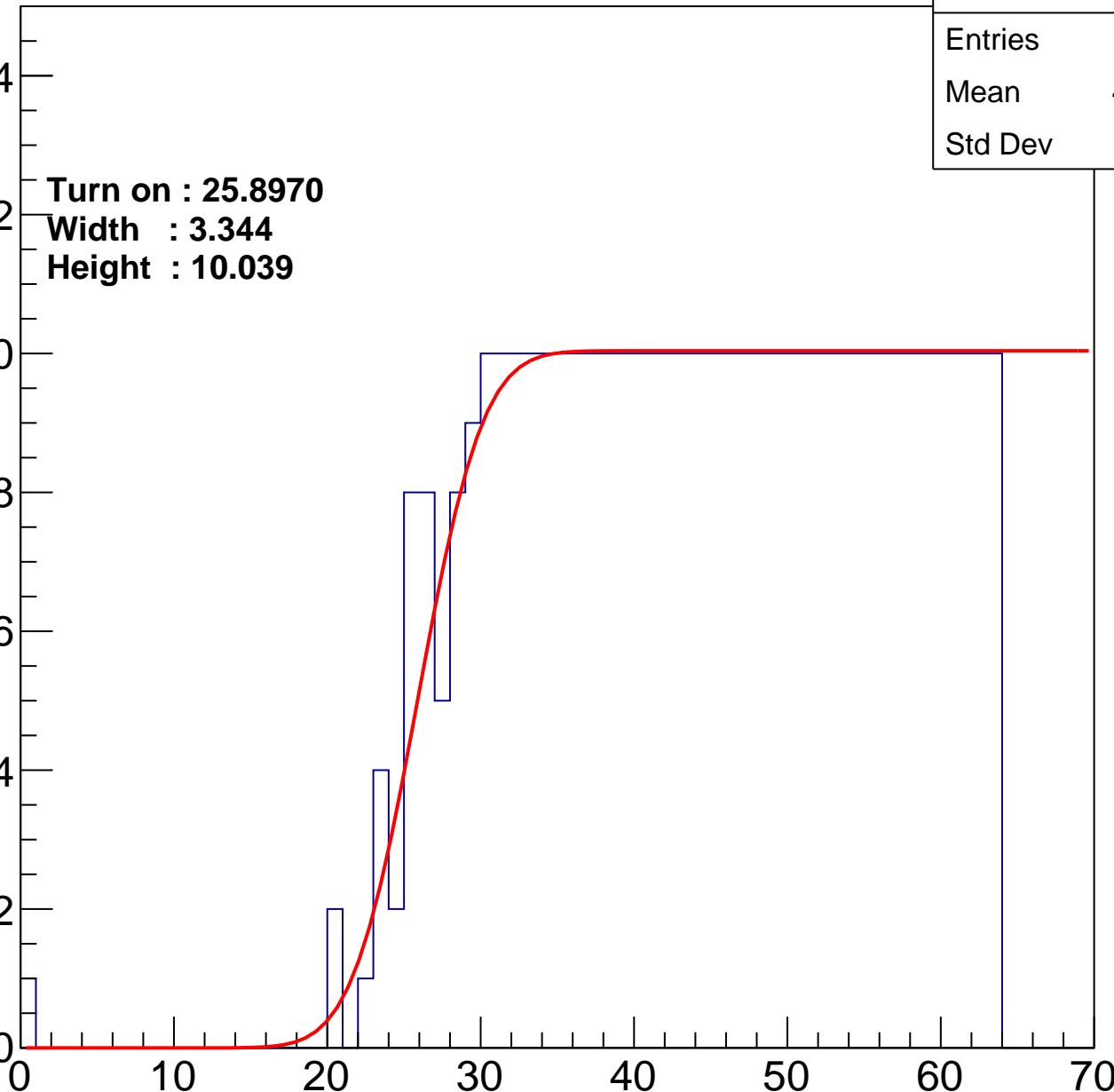
Width : 3.344

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch49

calib_packv5_042523_0143.root, FC#7, port C2

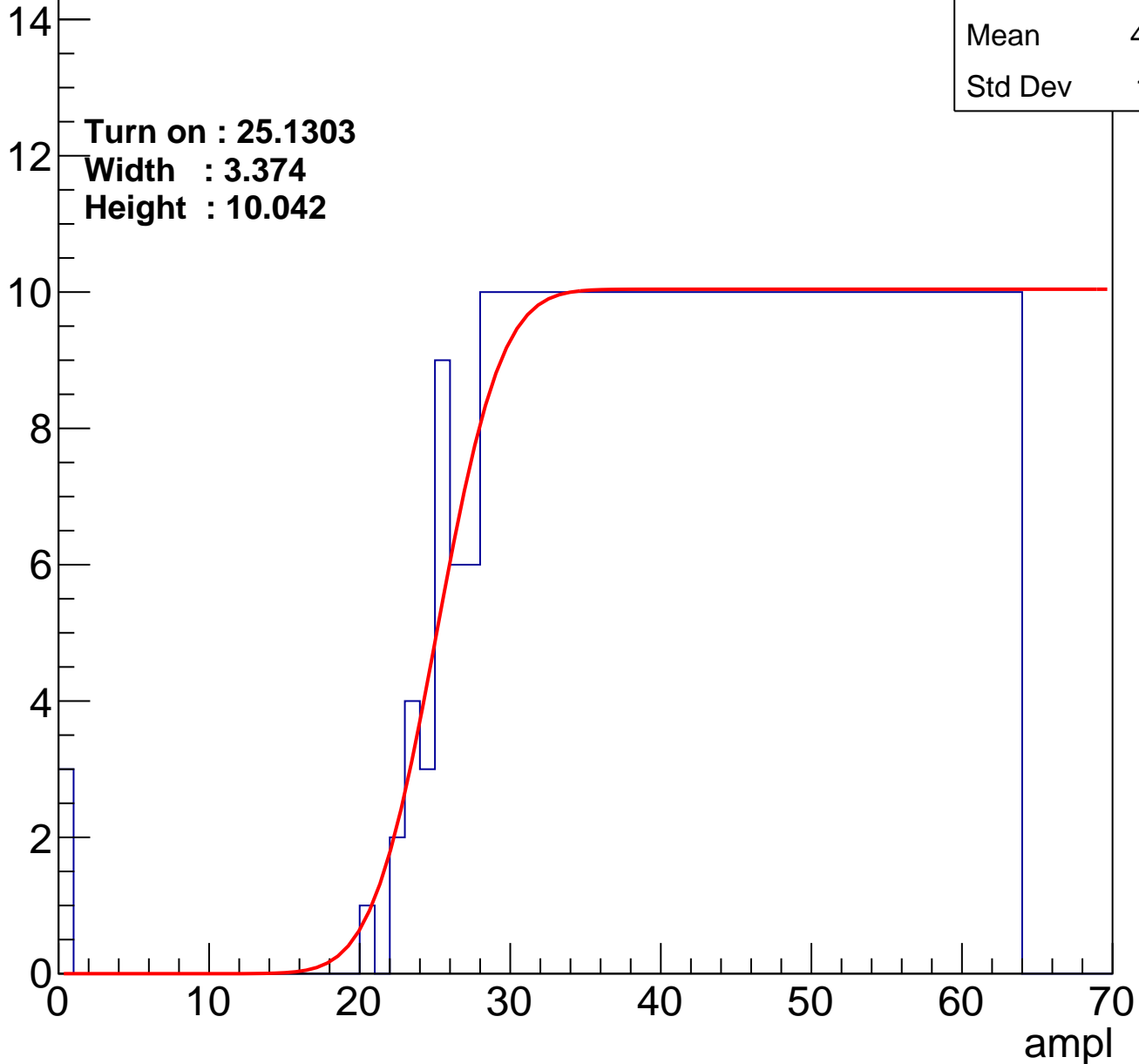
Entries	394
Mean	43.53
Std Dev	12.01

Turn on : 25.1303

Width : 3.374

Height : 10.042

Entry



B1L103S, U22-ch50

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.03
Std Dev	11.67

Turn on : 25.8864

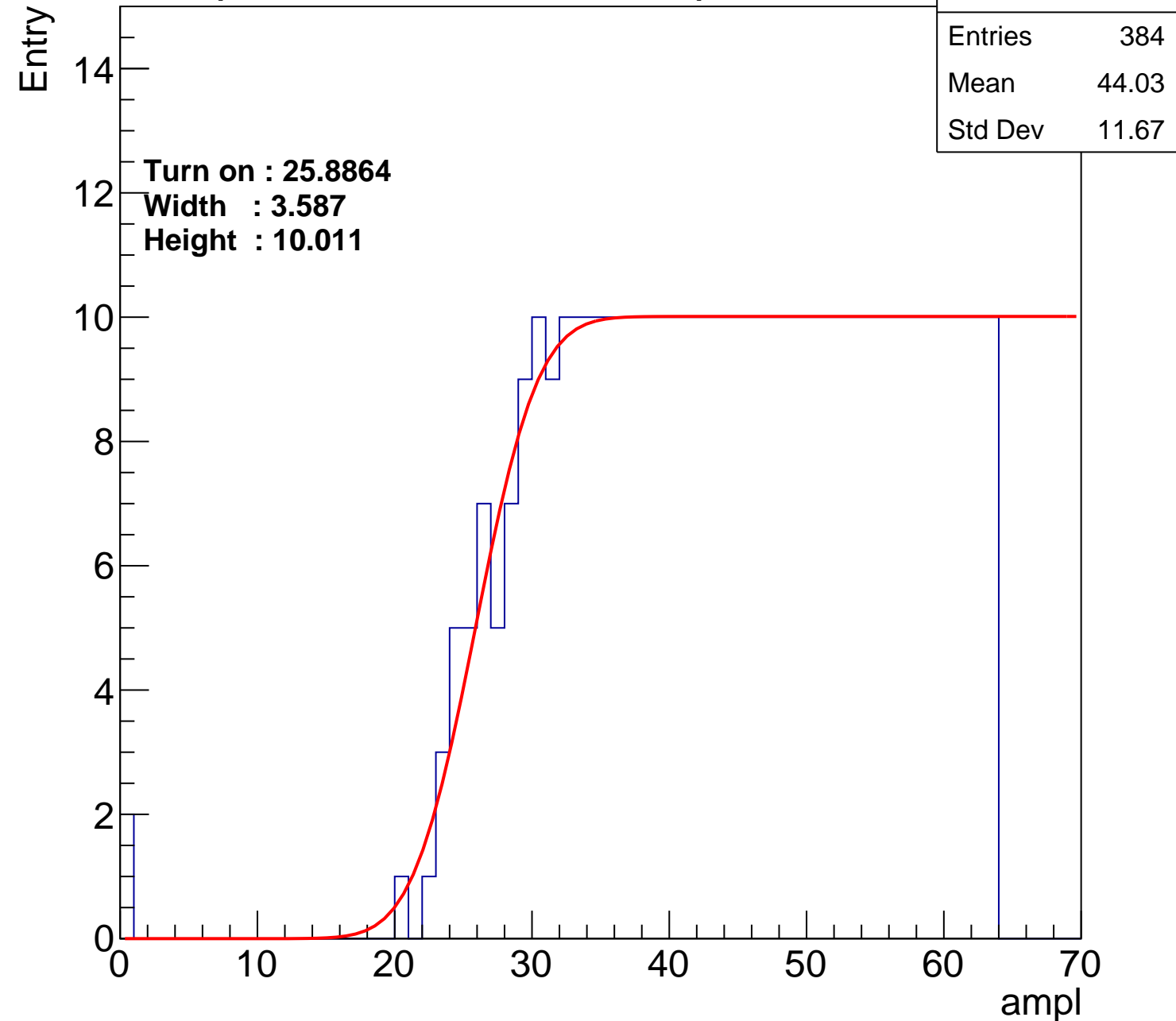
Width : 3.587

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch51

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.99
Std Dev	11.34

Turn on : 28.3072

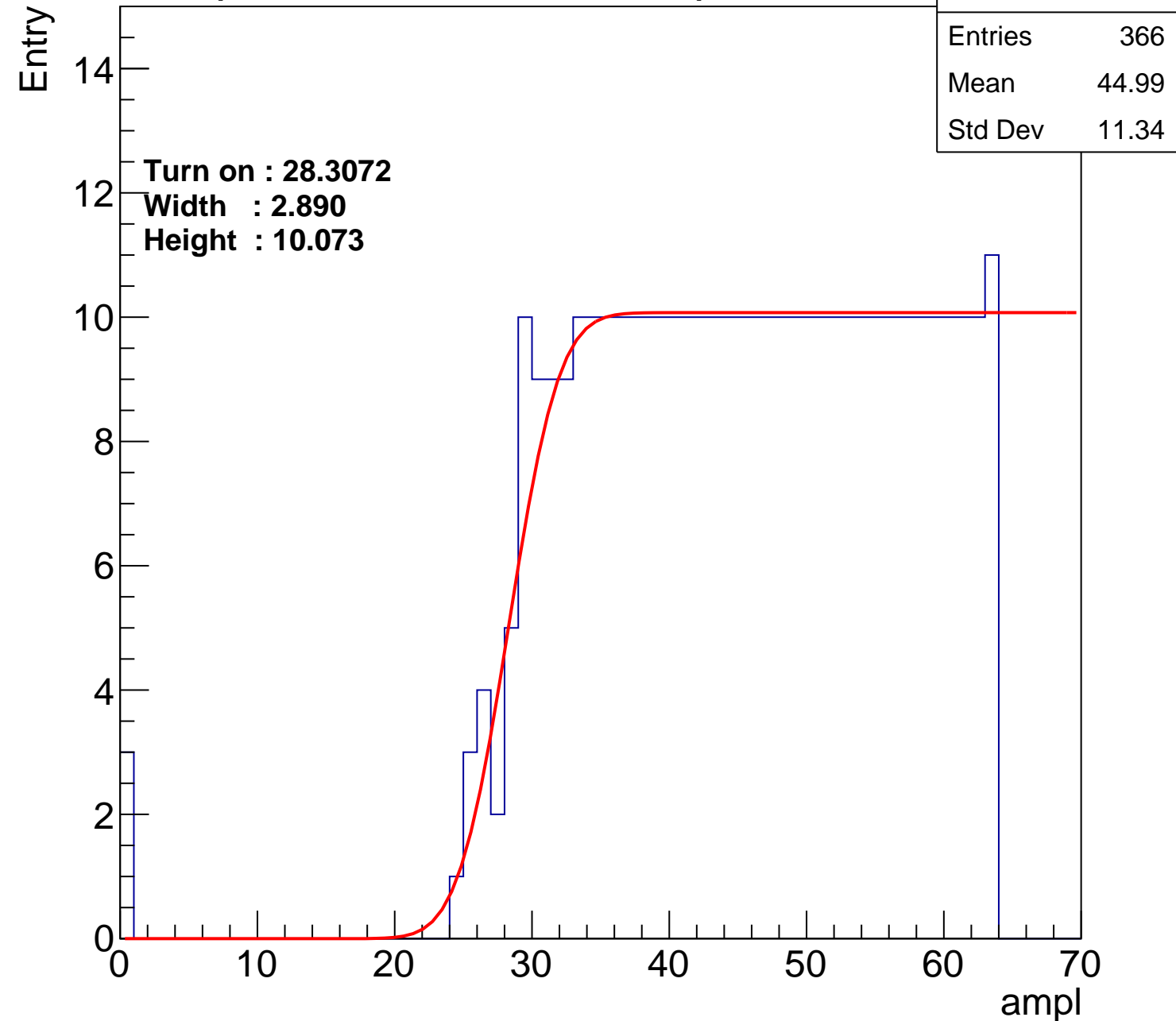
Width : 2.890

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch52

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.98
Std Dev	11.57

Turn on : 26.0361

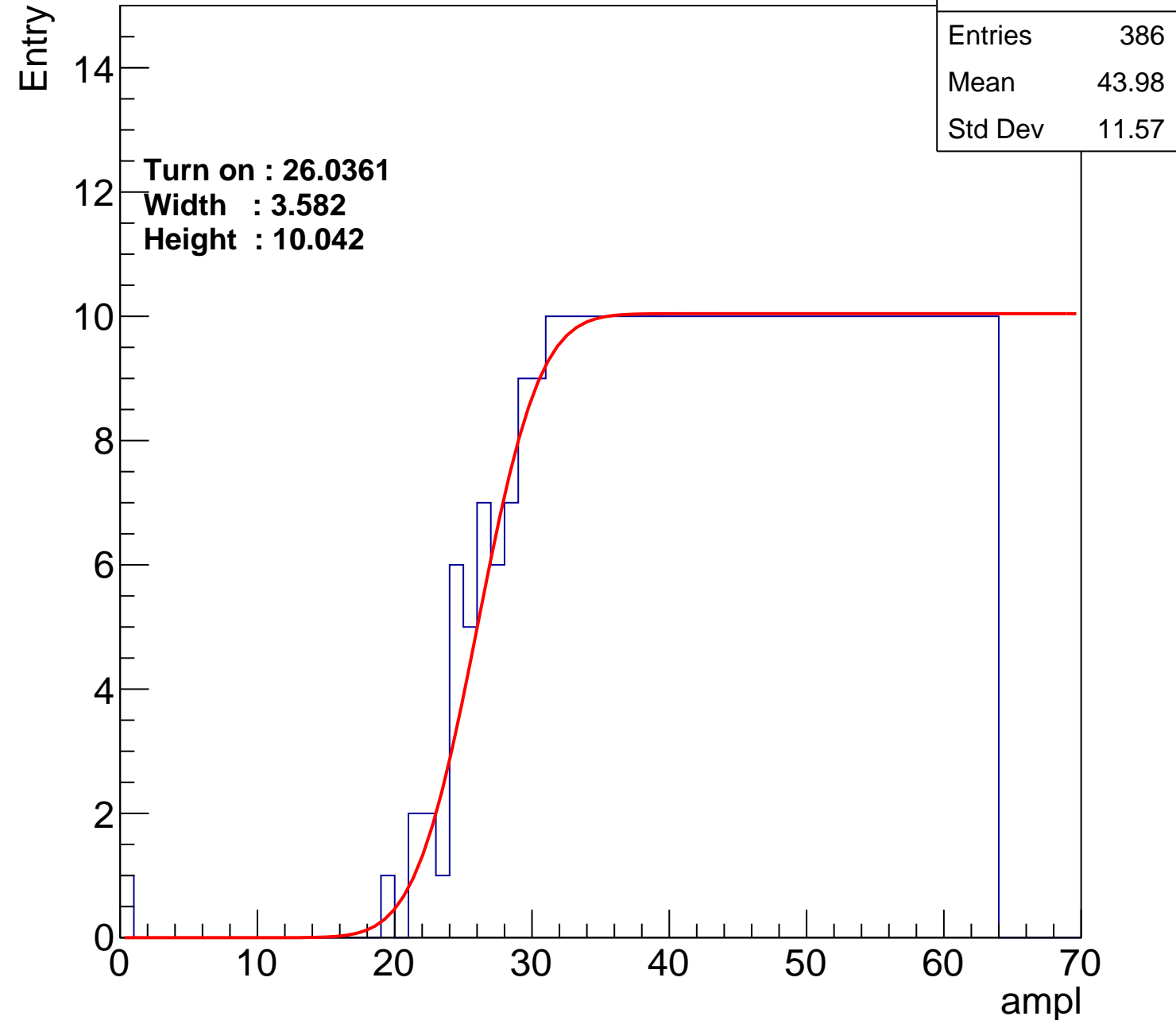
Width : 3.582

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch53

calib_packv5_042523_0143.root, FC#7, port C2

Entries	360
Mean	45.19
Std Dev	11.19

Turn on : 28.4728

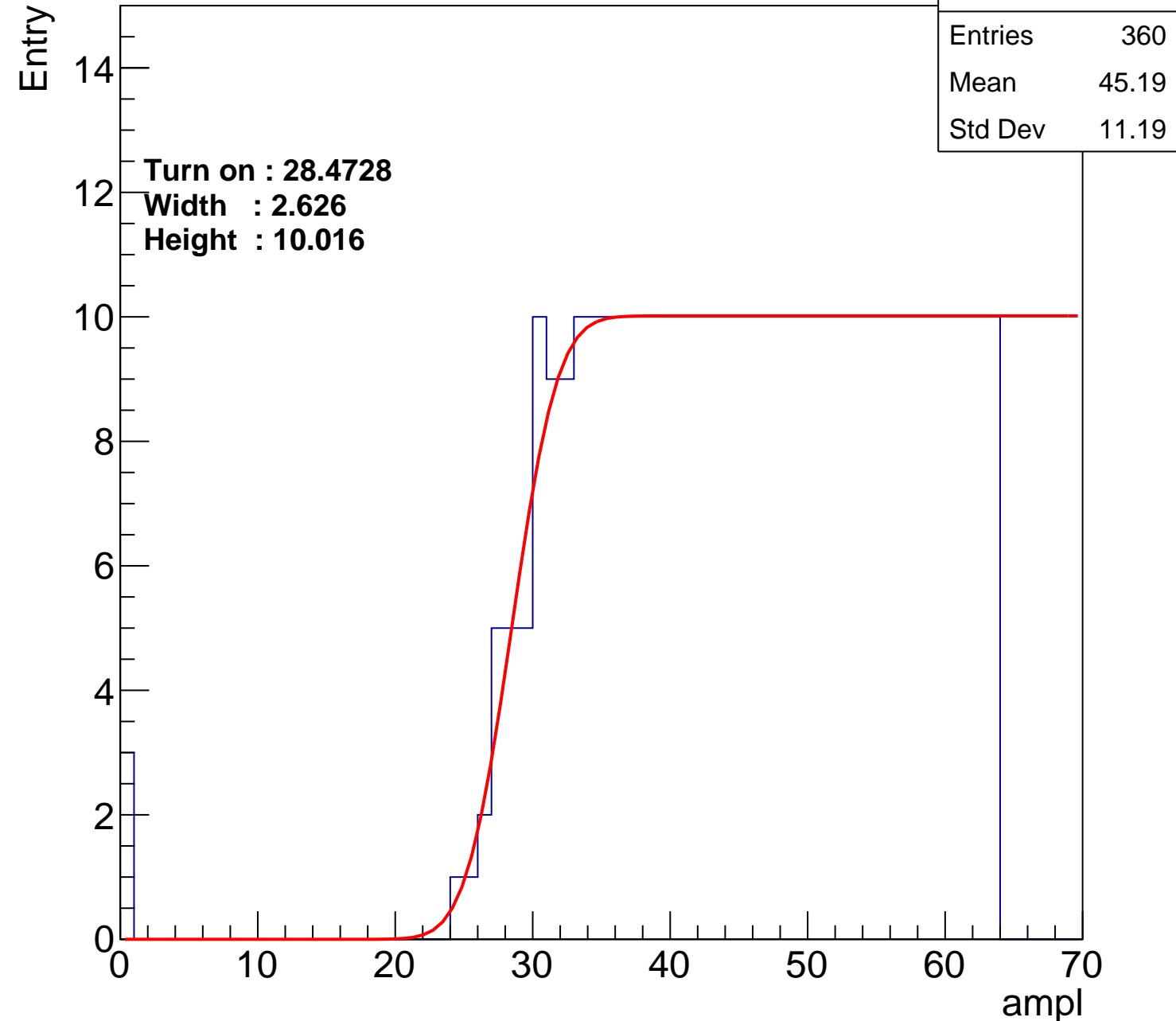
Width : 2.626

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch54

calib_packv5_042523_0143.root, FC#7, port C2

Entries	387
Mean	43.66
Std Dev	12.31

Turn on : 26.5651

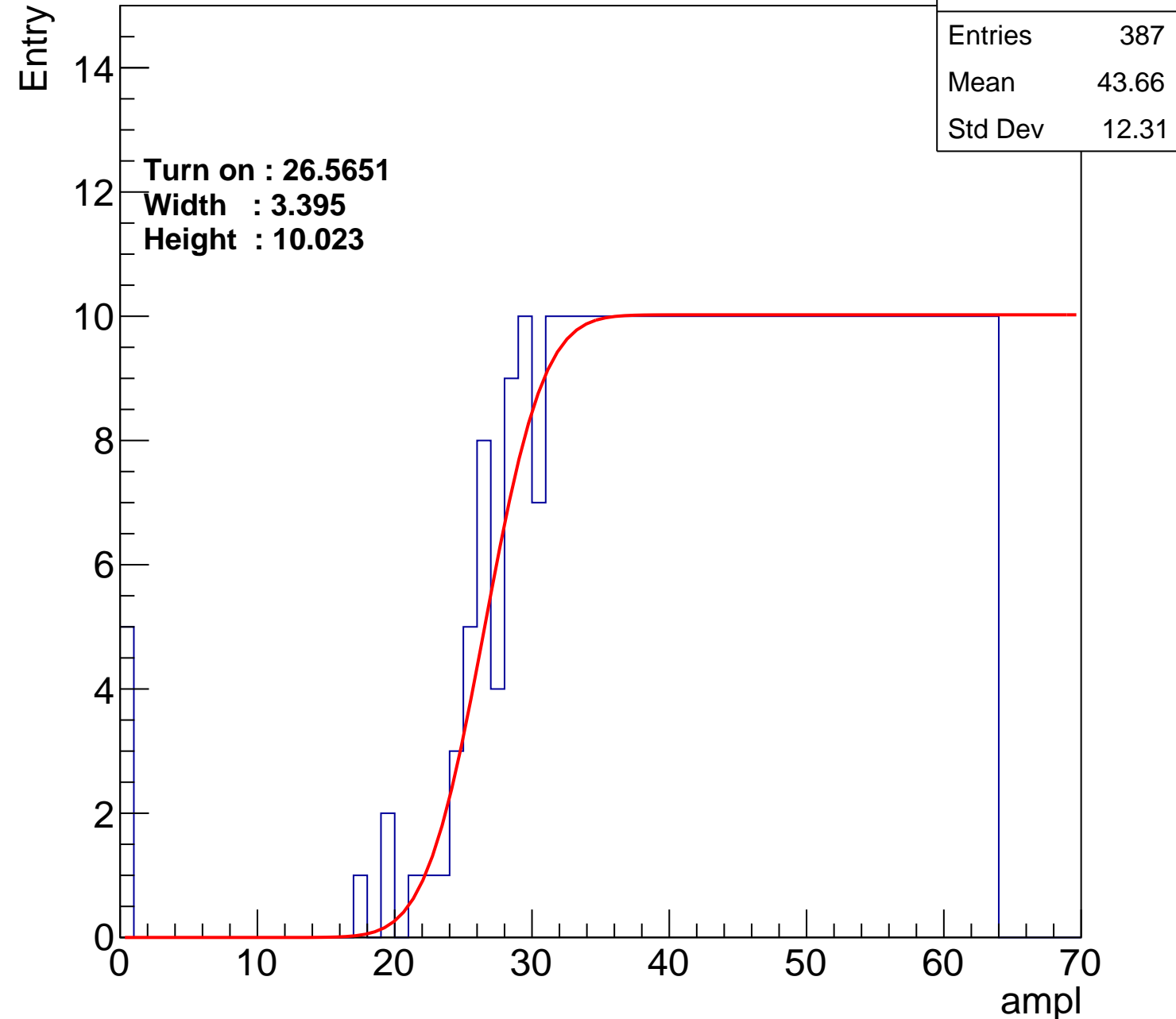
Width : 3.395

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch55

calib_packv5_042523_0143.root, FC#7, port C2

Entries	381
Mean	44.25
Std Dev	11.41

Turn on : 26.3152

Width : 2.979

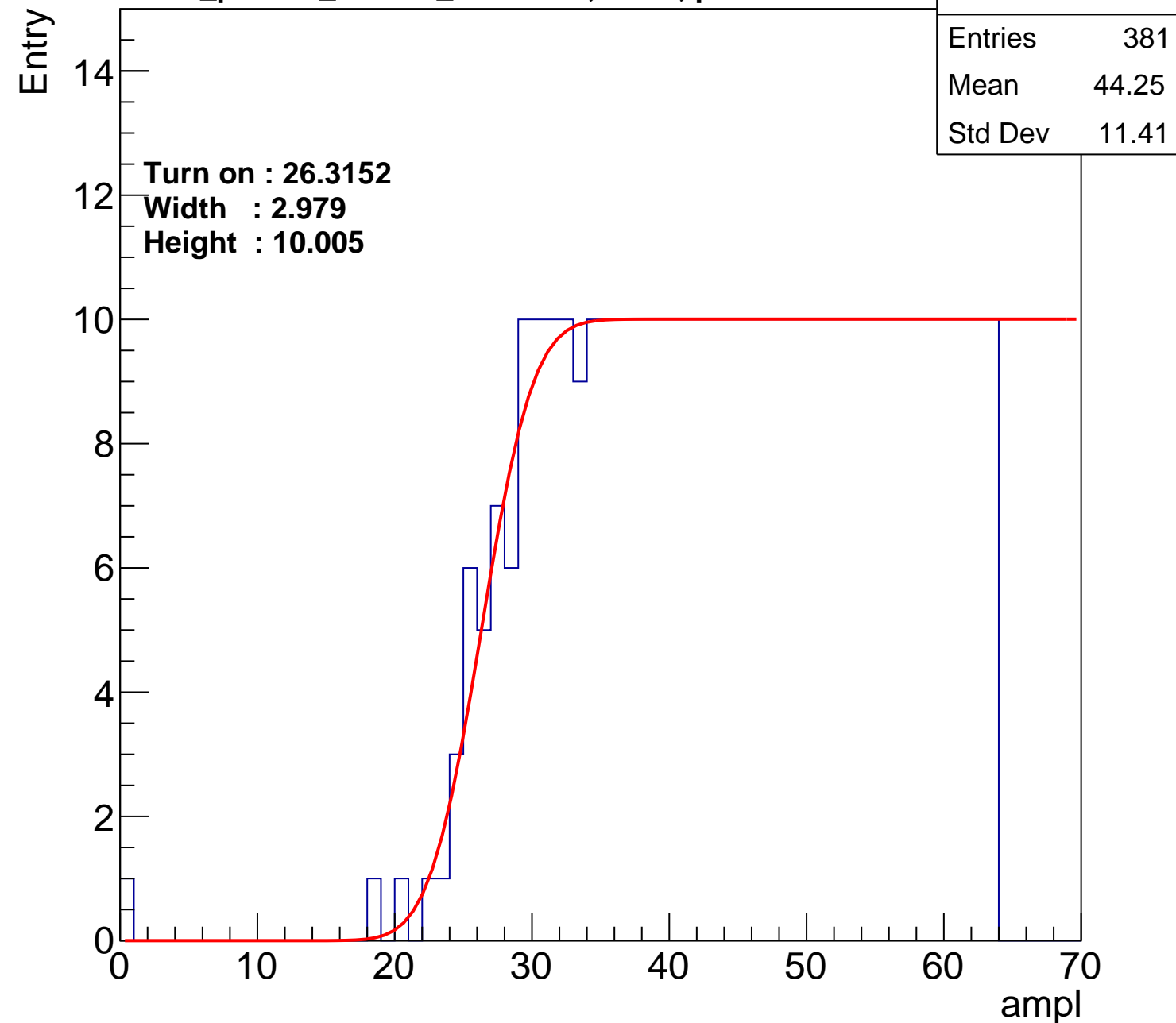
Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U22-ch56

calib_packv5_042523_0143.root, FC#7, port C2

Entries	379
Mean	44.31
Std Dev	11.58

Turn on : 26.9822

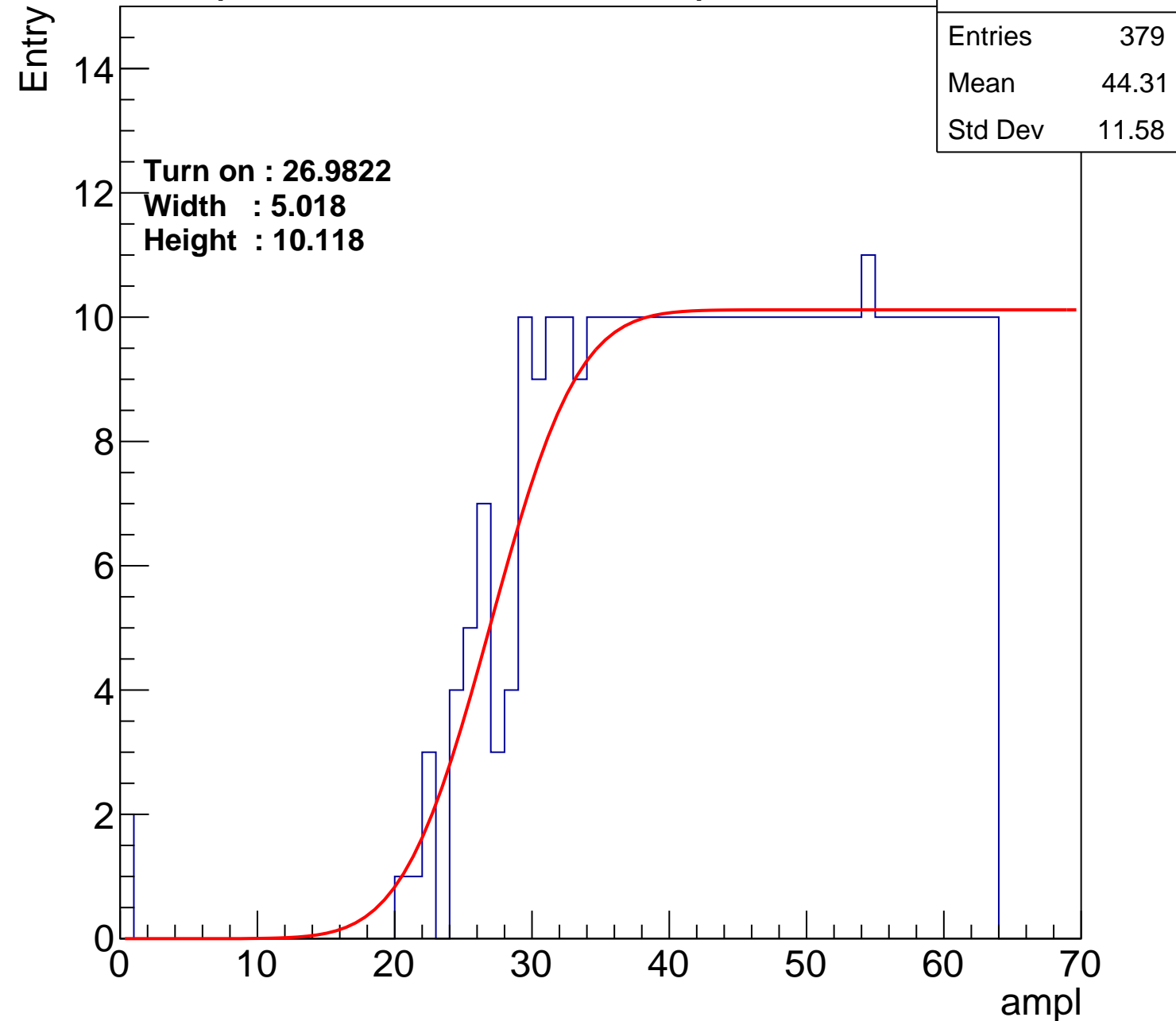
Width : 5.018

Height : 10.118

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch57

calib_packv5_042523_0143.root, FC#7, port C2

Entries	351
Mean	45.65
Std Dev	10.85

Turn on : 29.4790

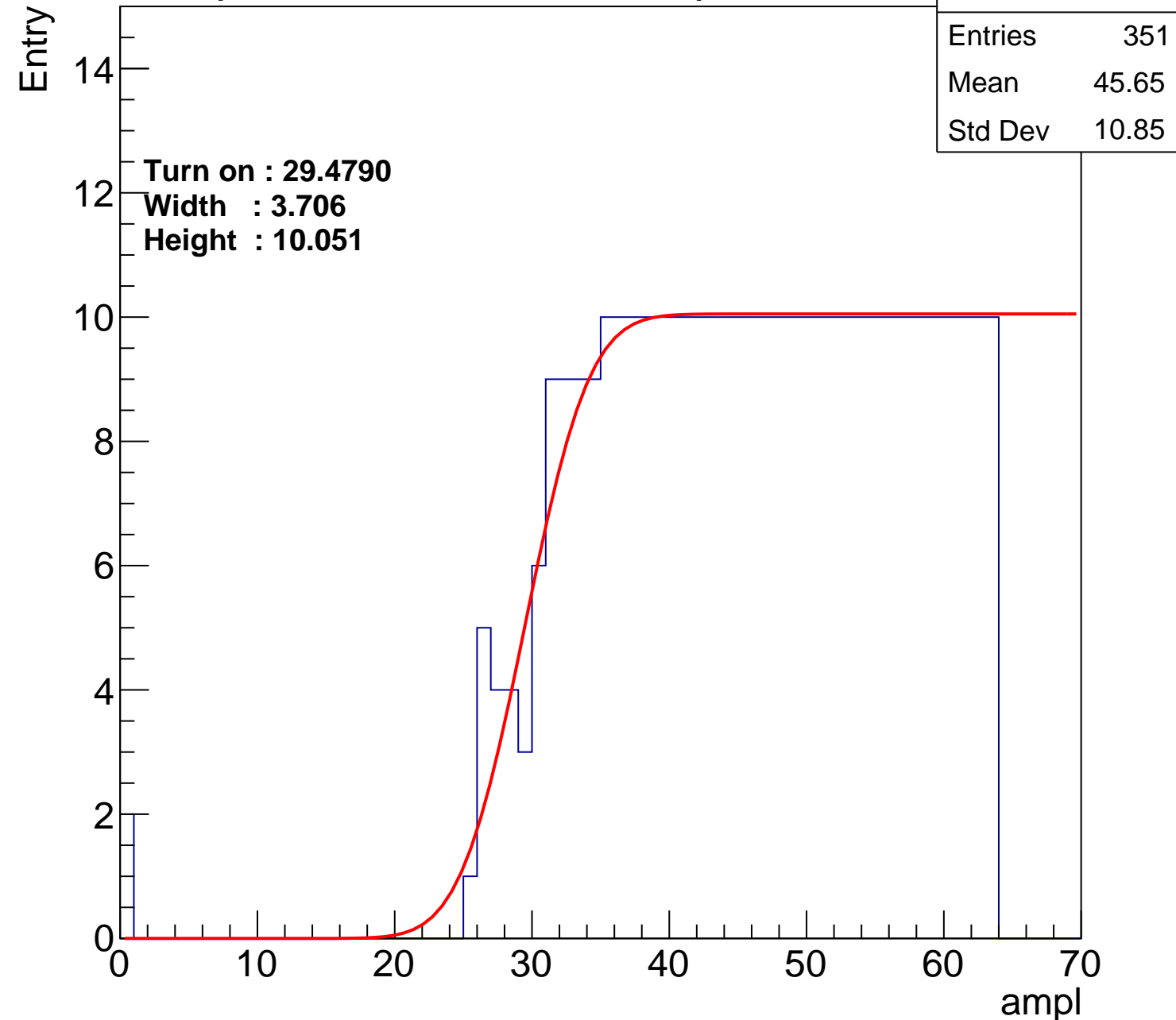
Width : 3.706

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch58

calib_packv5_042523_0143.root, FC#7, port C2

Entries	413
Mean	42.62
Std Dev	12.45

Turn on : 23.2011

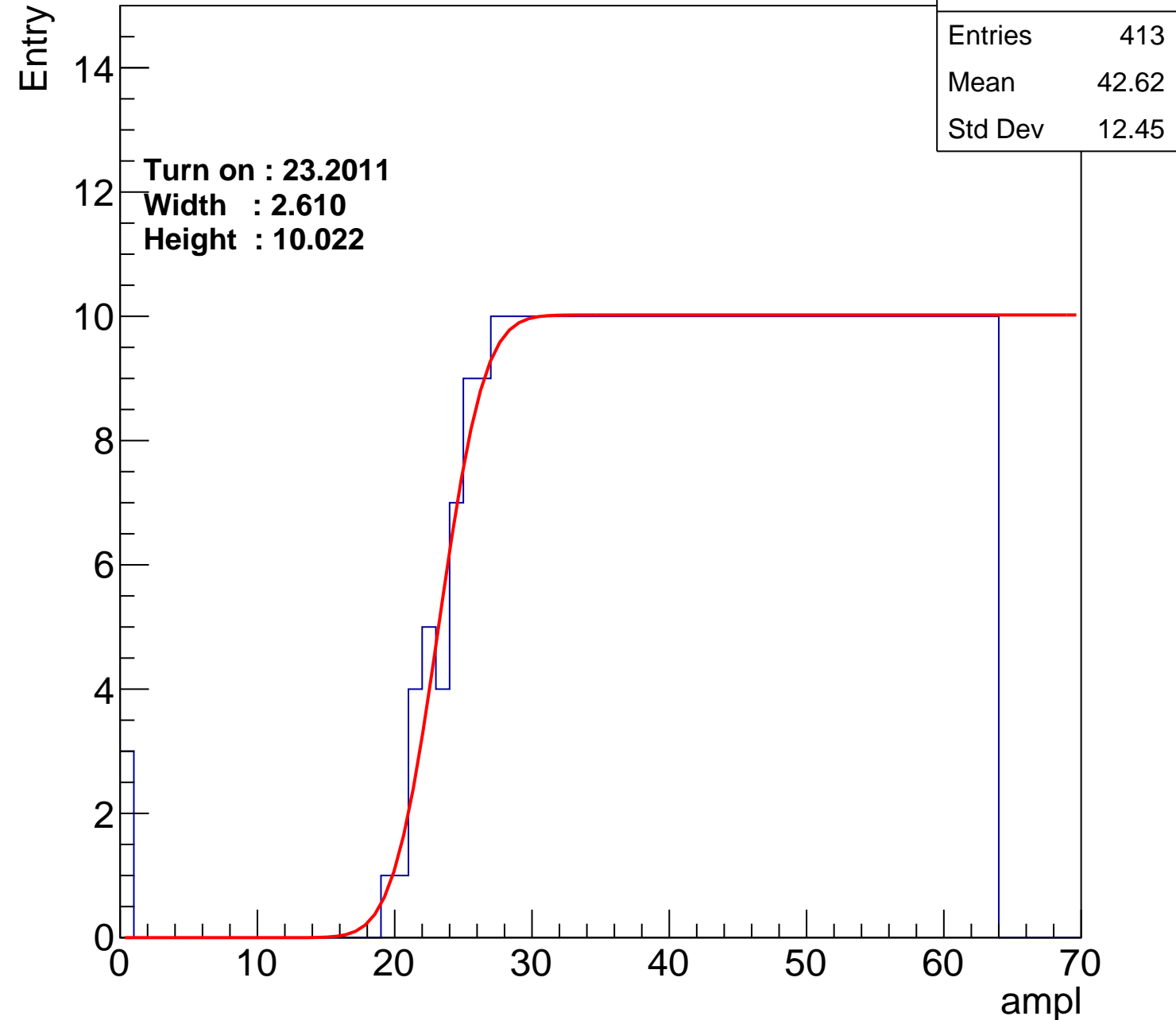
Width : 2.610

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch59

calib_packv5_042523_0143.root, FC#7, port C2

Entries	366
Mean	44.83
Std Dev	11.43

Turn on : 27.4386

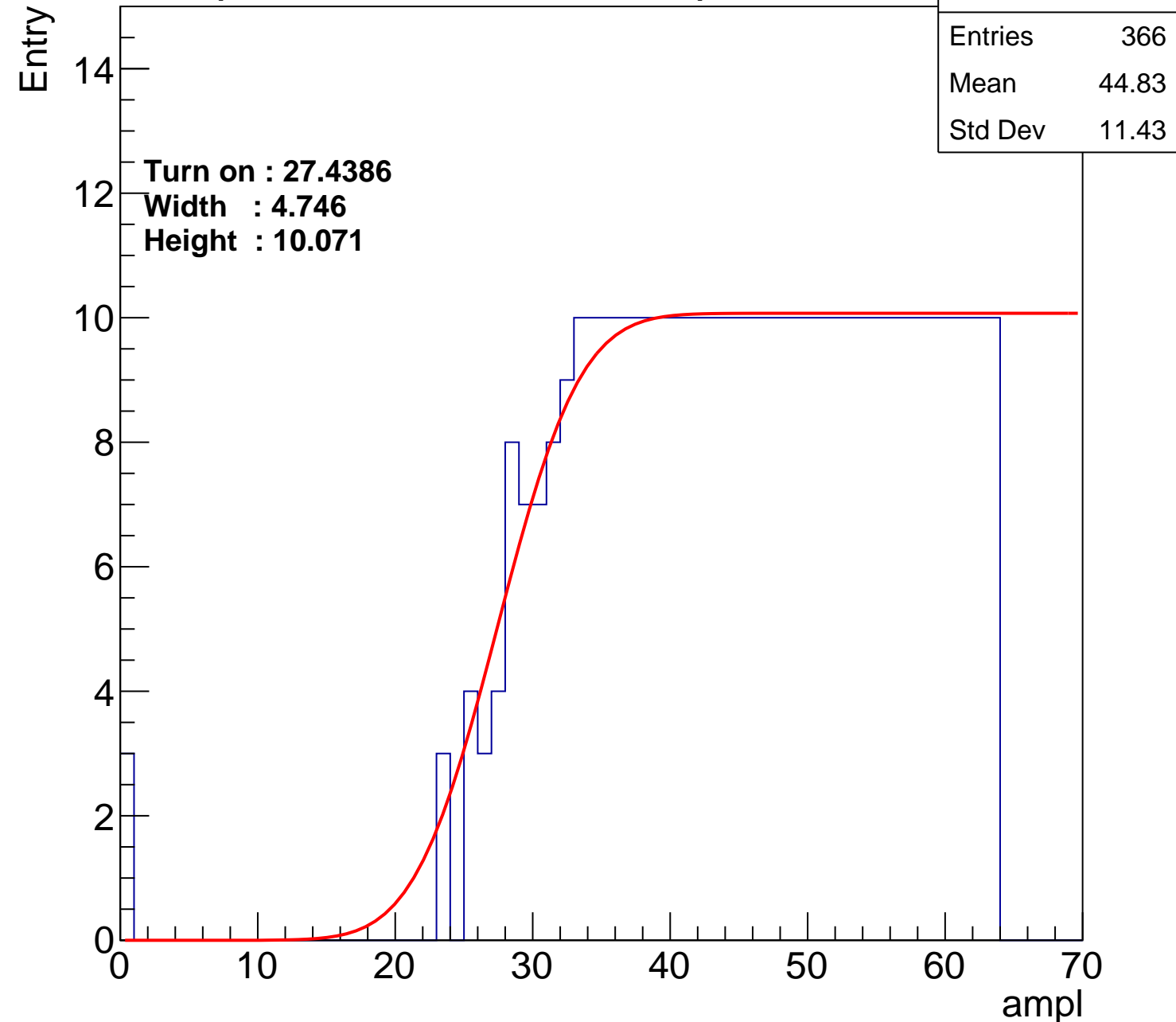
Width : 4.746

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch60

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.48
Std Dev	12.4

Turn on : 25.6636

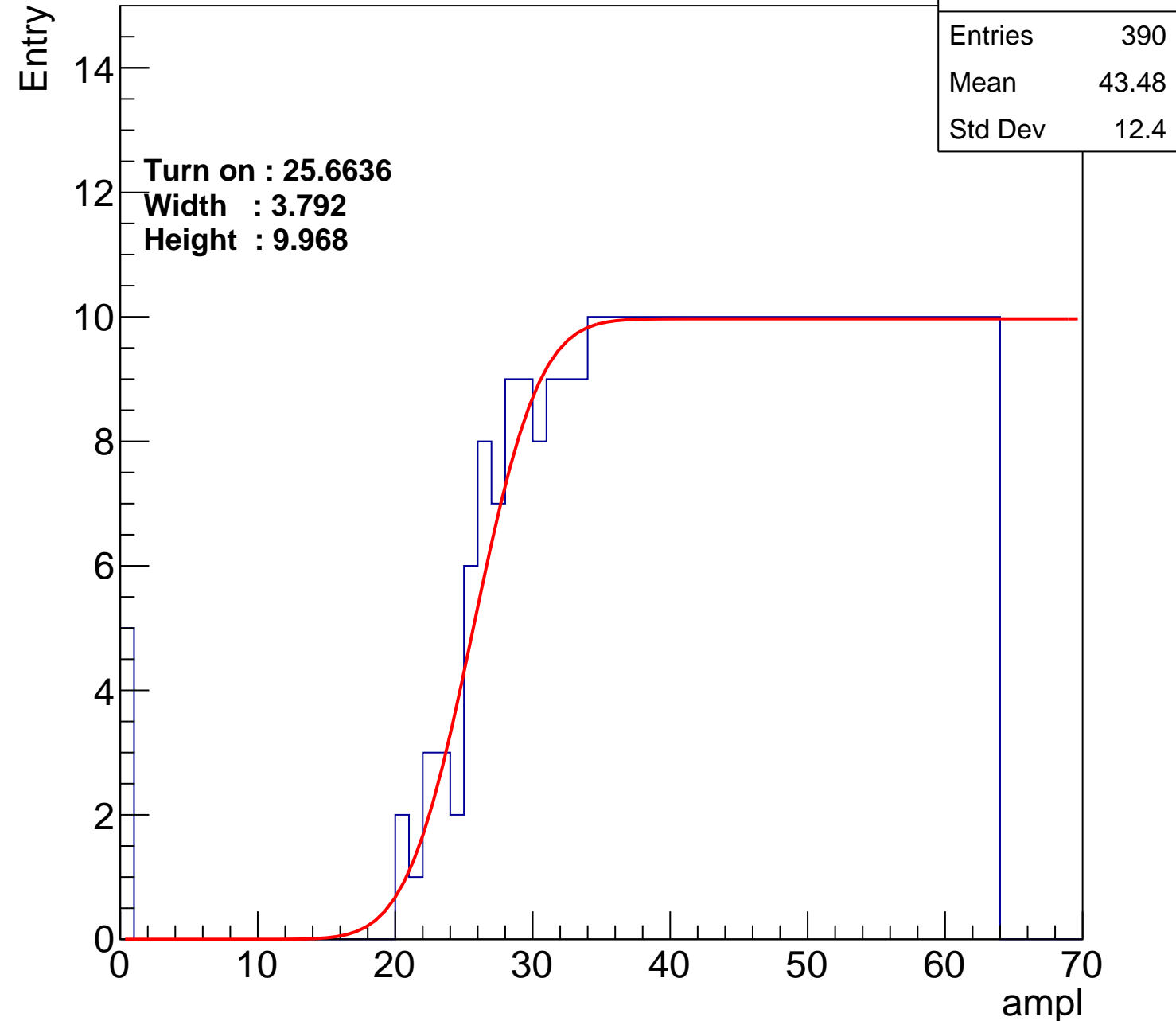
Width : 3.792

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch61

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.41
Std Dev	11.63

Turn on : 27.2241

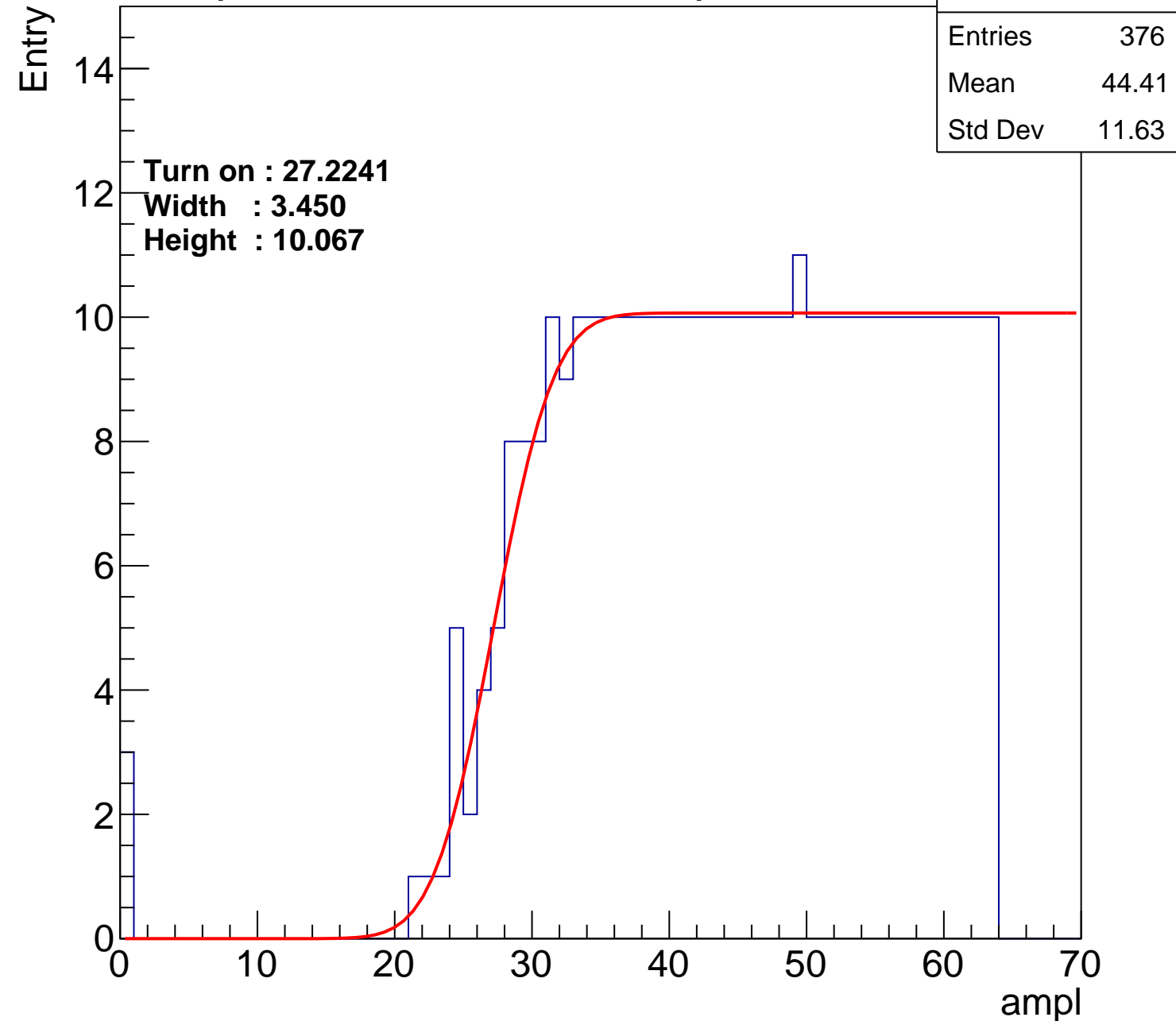
Width : 3.450

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch62

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.37
Std Dev	12.06

Turn on : 24.9974

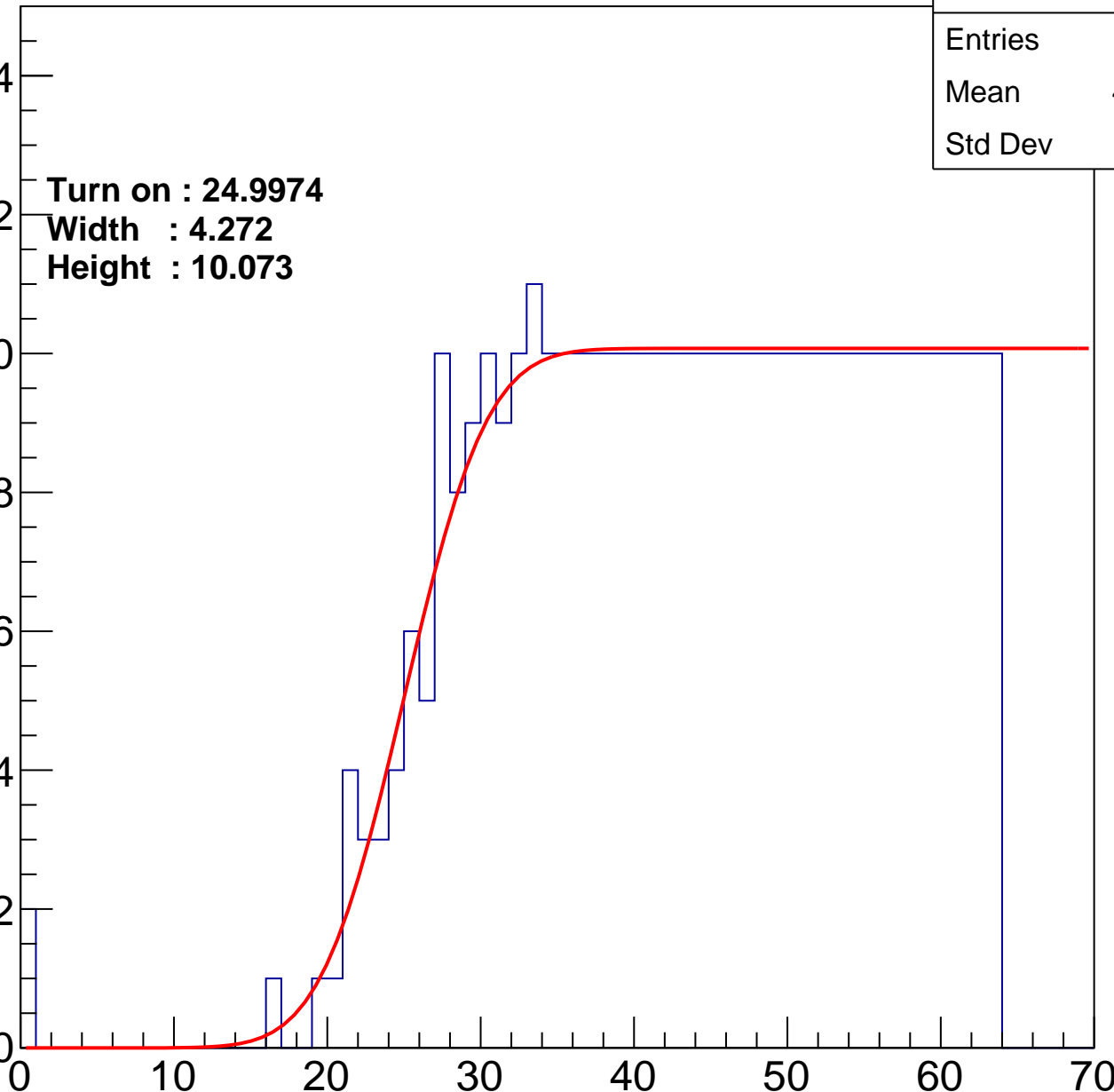
Width : 4.272

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch63

calib_packv5_042523_0143.root, FC#7, port C2

Entries	375
Mean	44.43
Std Dev	11.59

Turn on : 26.8525

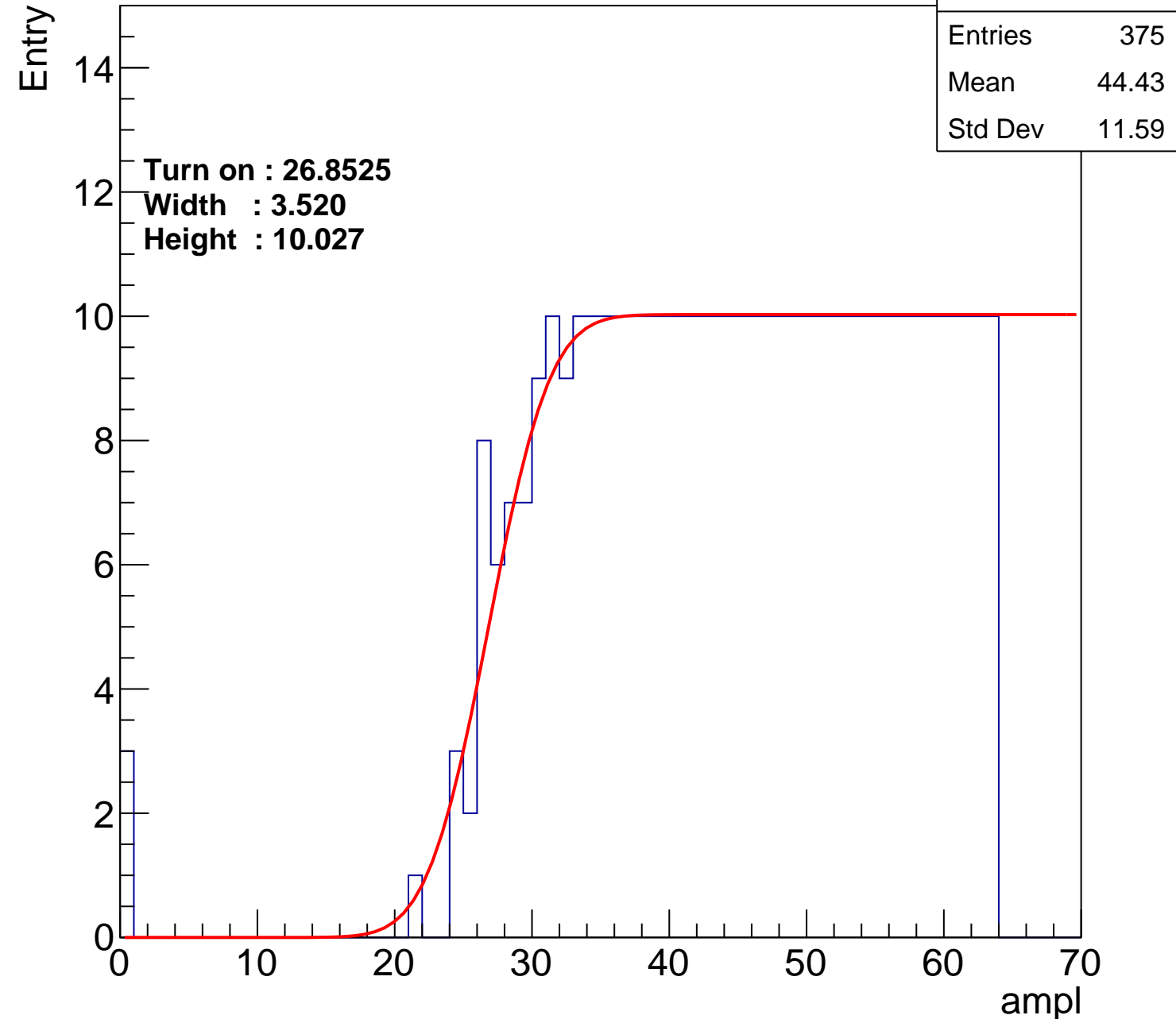
Width : 3.520

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch64

calib_packv5_042523_0143.root, FC#7, port C2

Entries	376
Mean	44.2
Std Dev	11.91

Turn on : 27.7456

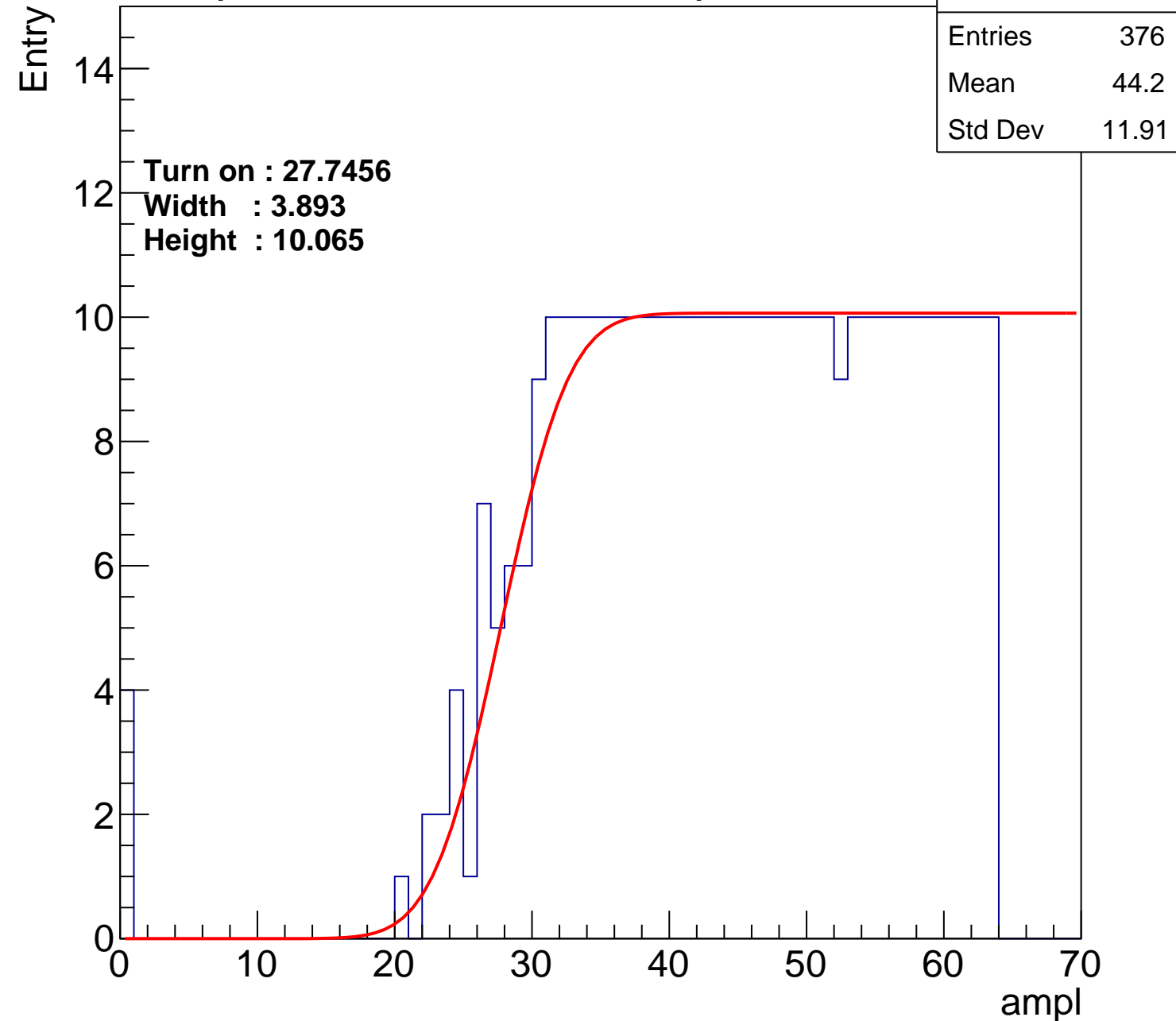
Width : 3.893

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch65

calib_packv5_042523_0143.root, FC#7, port C2

Entries	392
Mean	43.44
Std Dev	12.38

Turn on : 25.7687

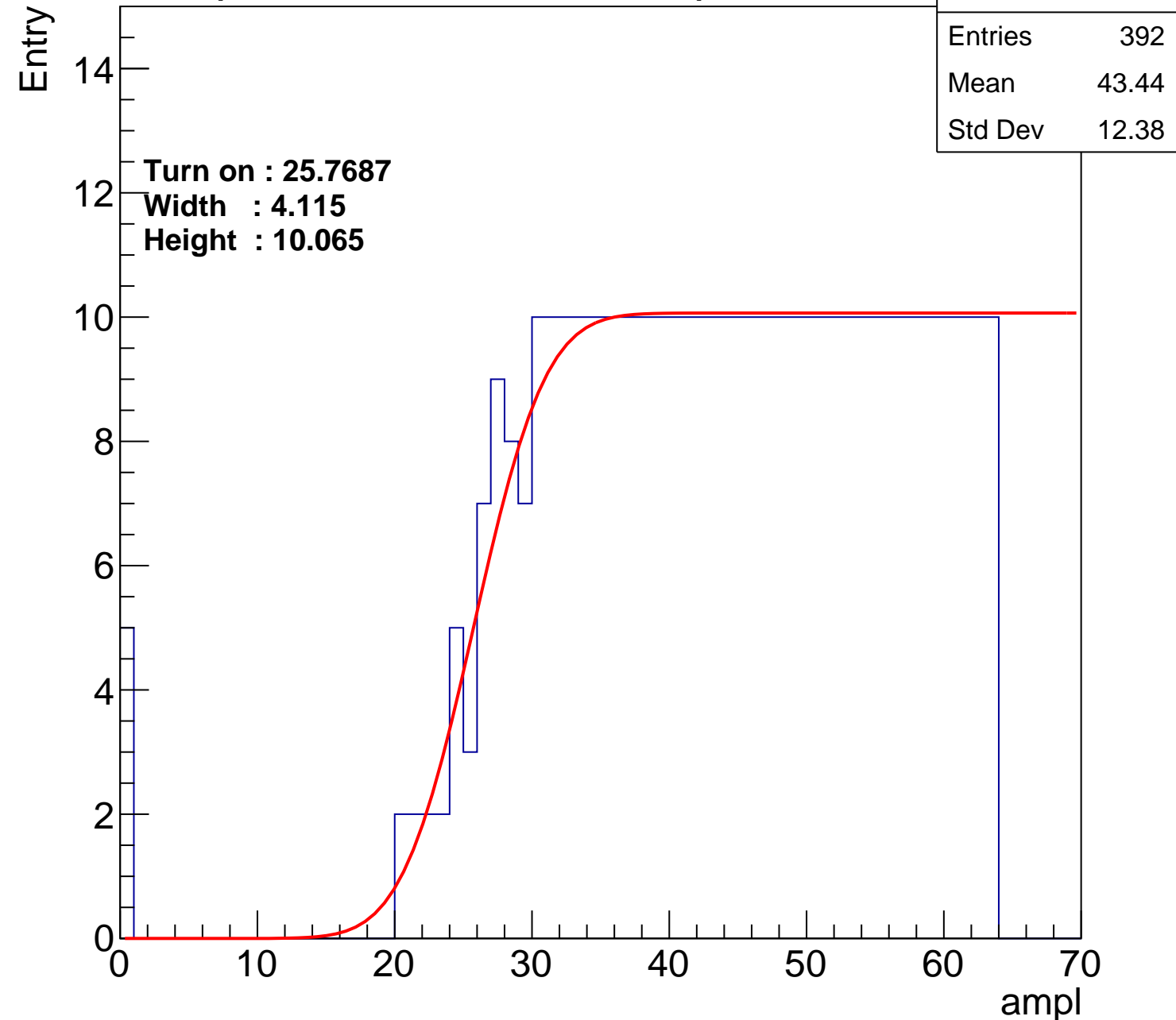
Width : 4.115

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch66

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.19
Std Dev	11.42

Turn on : 26.1909

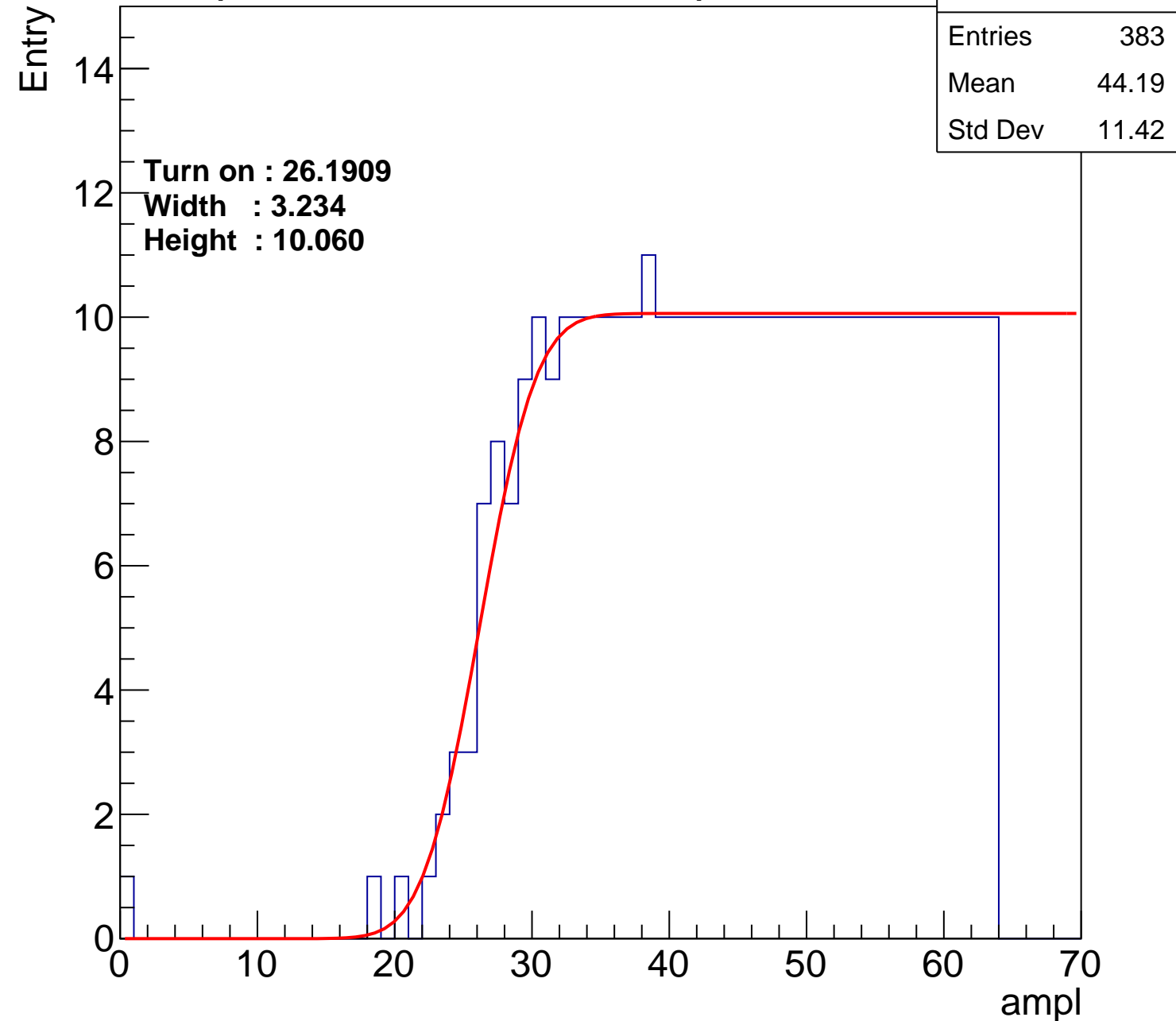
Width : 3.234

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch67

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.11
Std Dev	11.43

Turn on : 25.9345

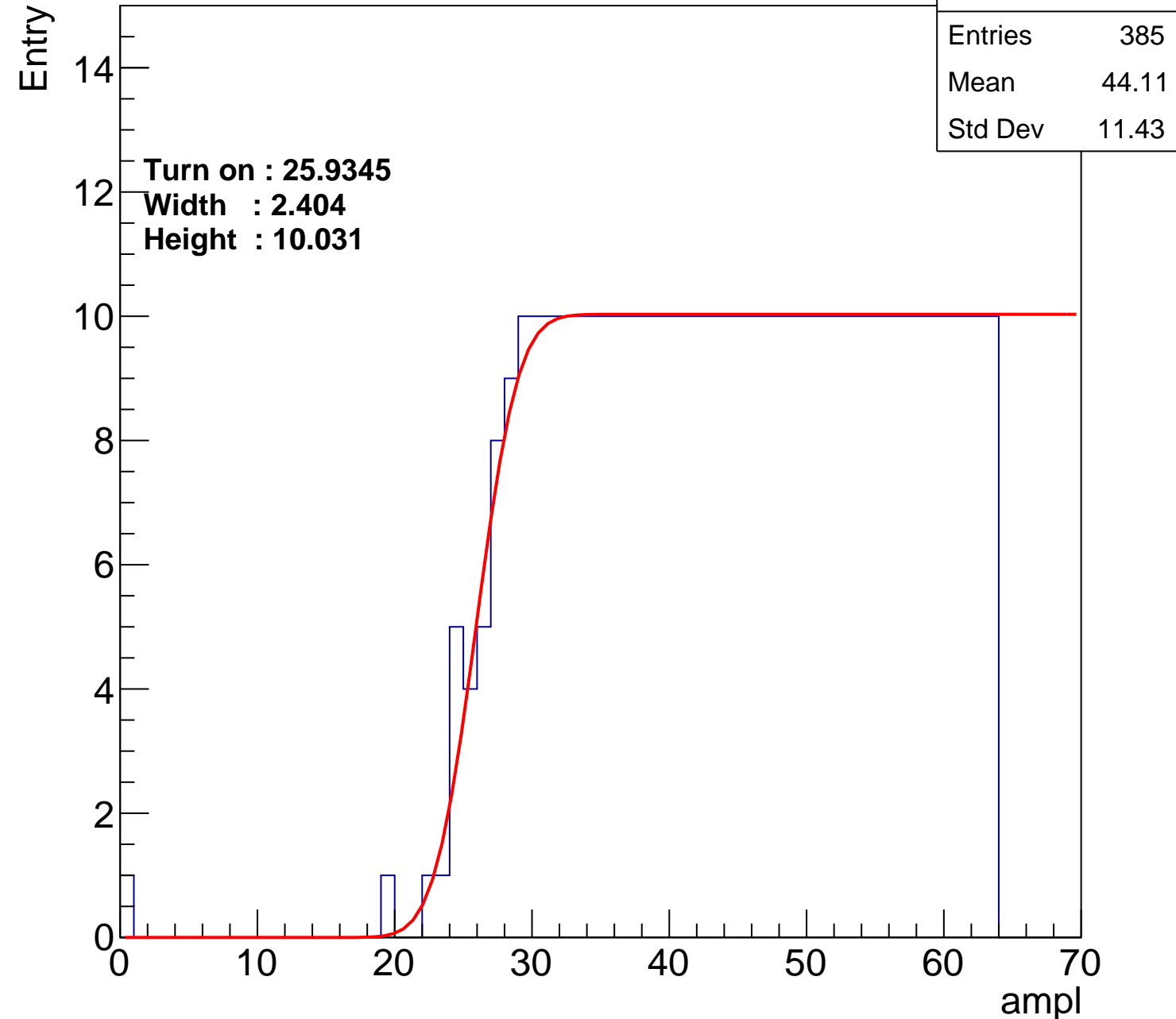
Width : 2.404

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch68

calib_packv5_042523_0143.root, FC#7, port C2

Entries	389
Mean	43.86
Std Dev	11.62

Turn on : 25.2817

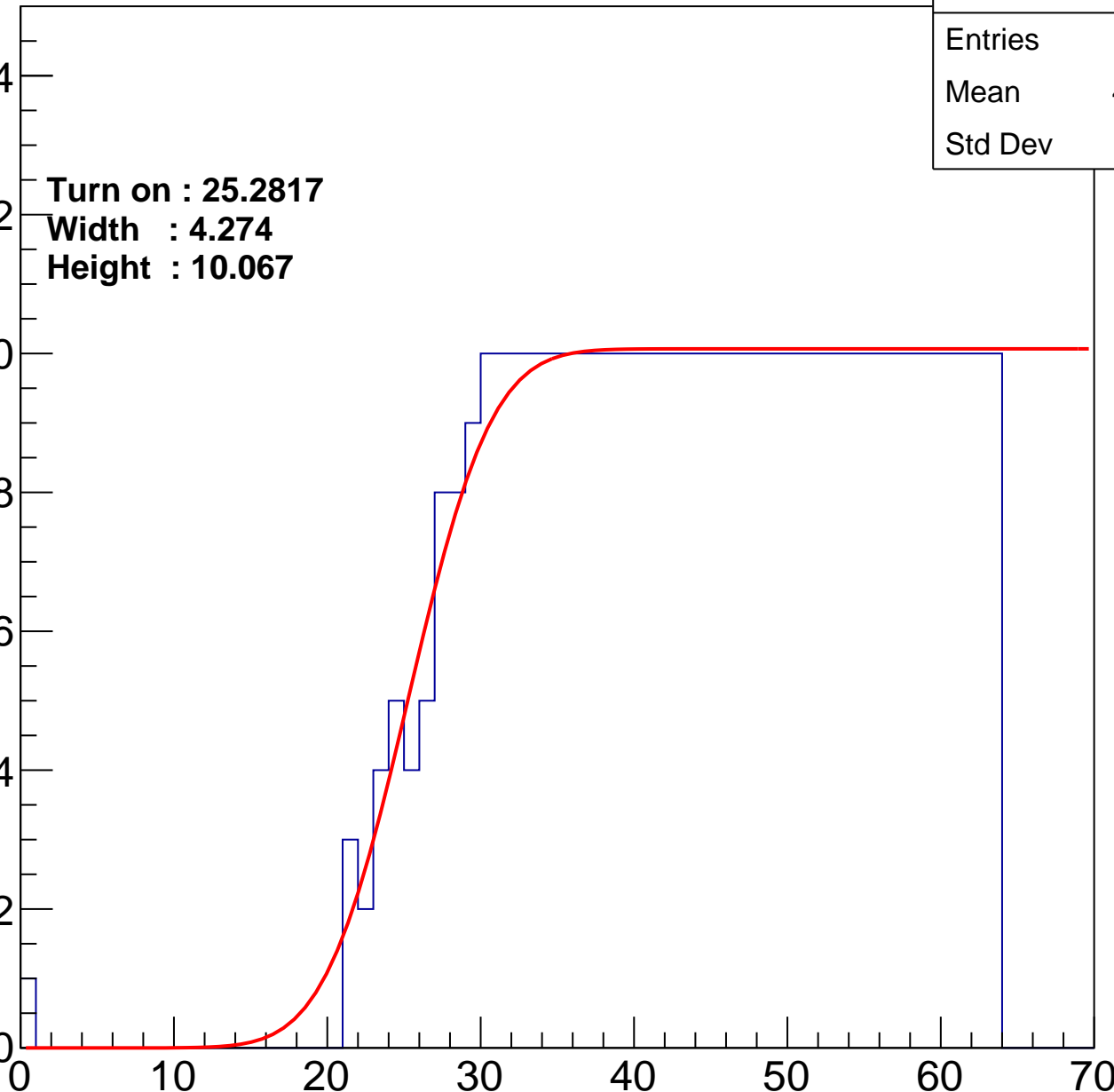
Width : 4.274

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch69

calib_packv5_042523_0143.root, FC#7, port C2

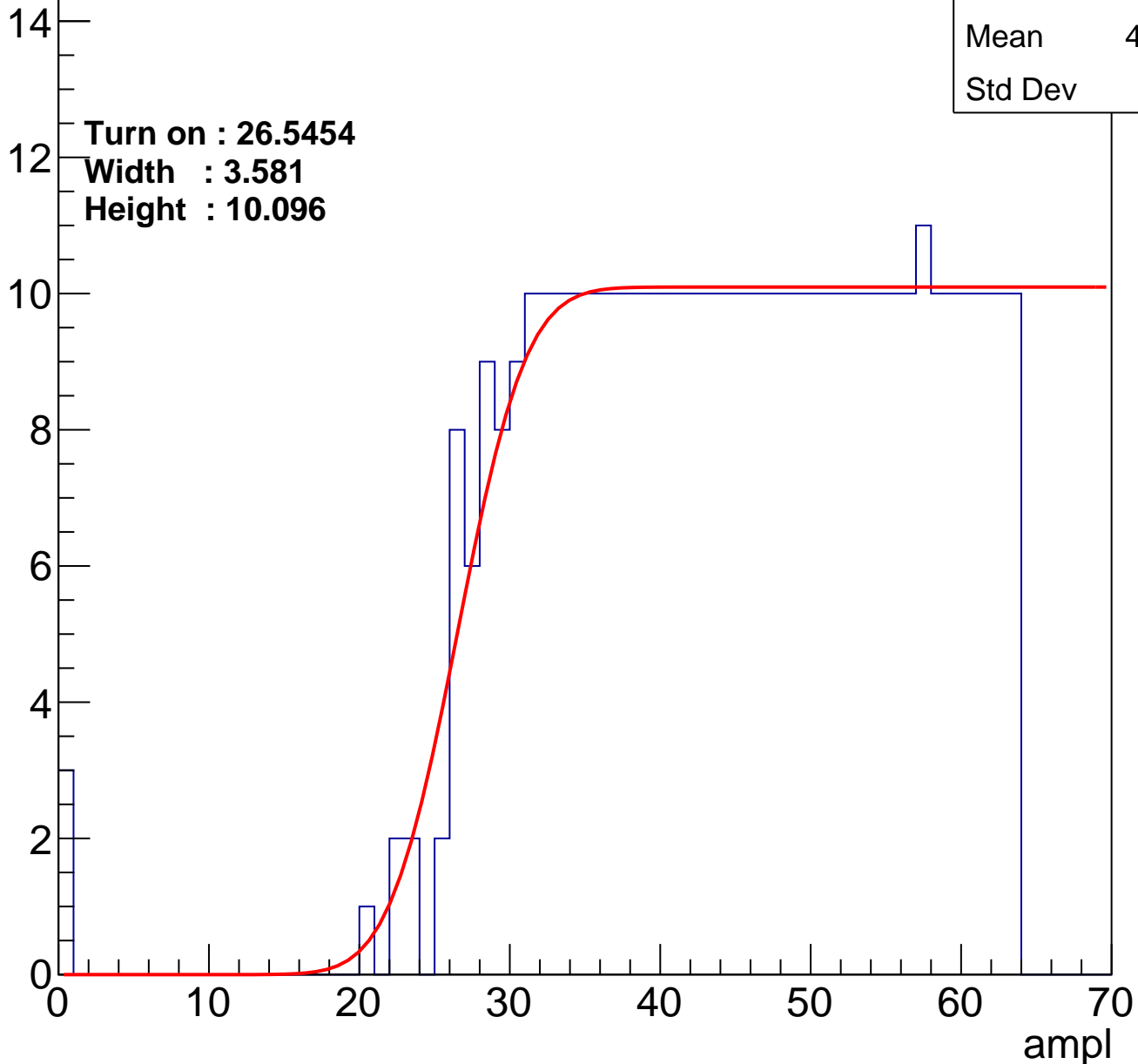
Entries	381
Mean	44.23
Std Dev	11.7

Turn on : 26.5454

Width : 3.581

Height : 10.096

Entry



B1L103S, U22-ch70

calib_packv5_042523_0143.root, FC#7, port C2

Entries	383
Mean	44.06
Std Dev	11.69

Turn on : 26.8140

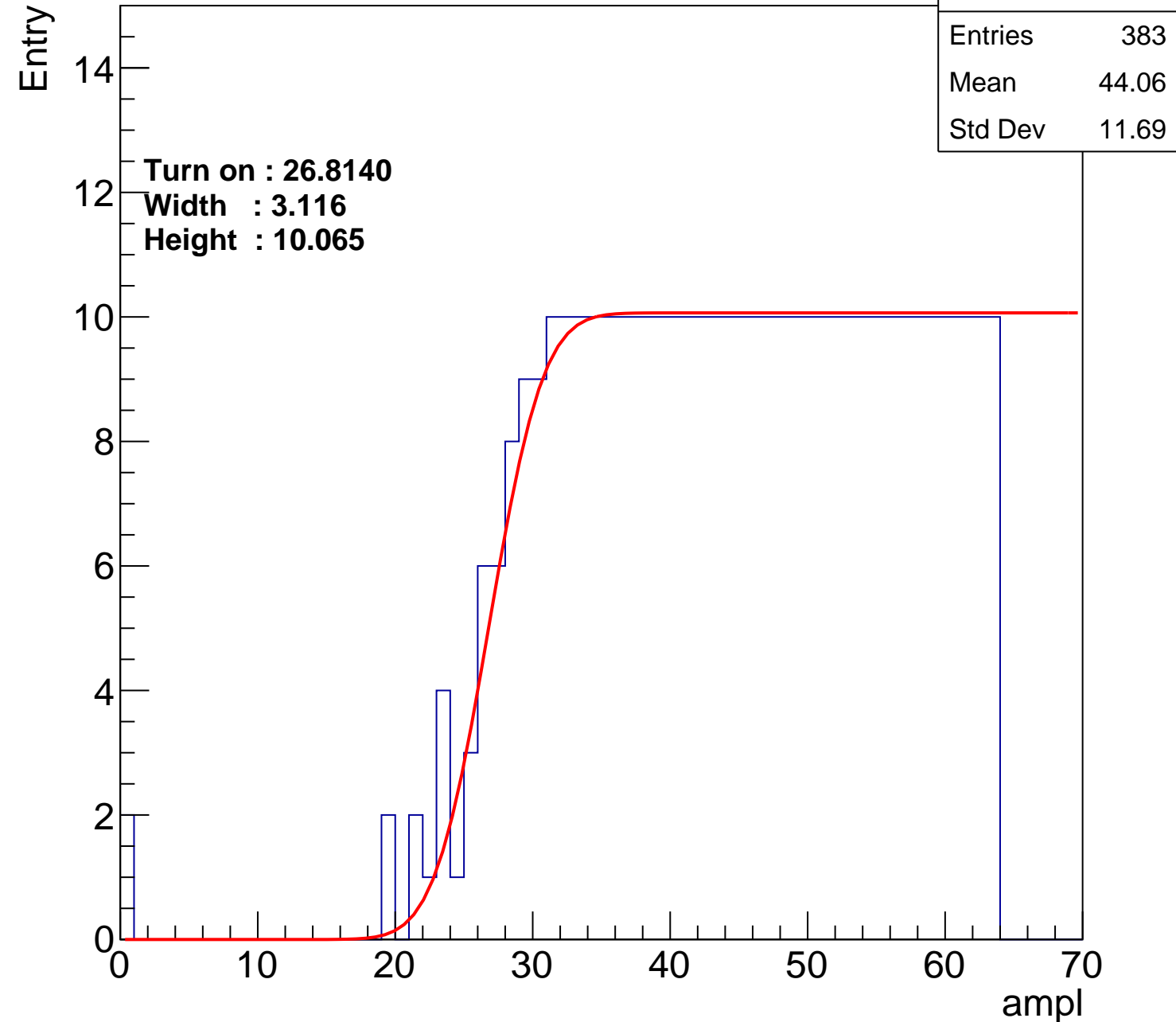
Width : 3.116

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch71

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.69
Std Dev	11.3

Turn on : 27.5827

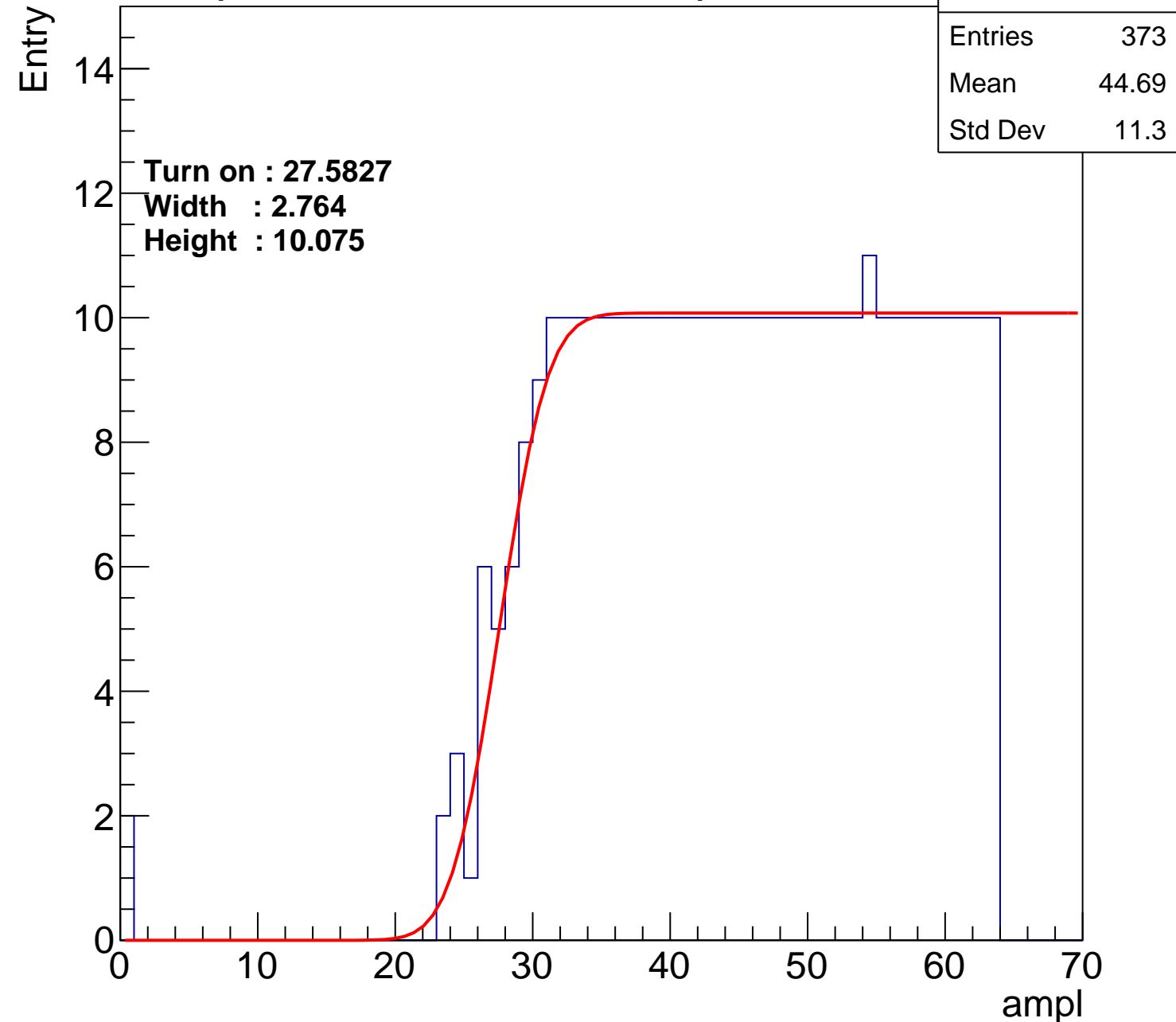
Width : 2.764

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch72

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.74
Std Dev	11.91

Turn on : 25.1735

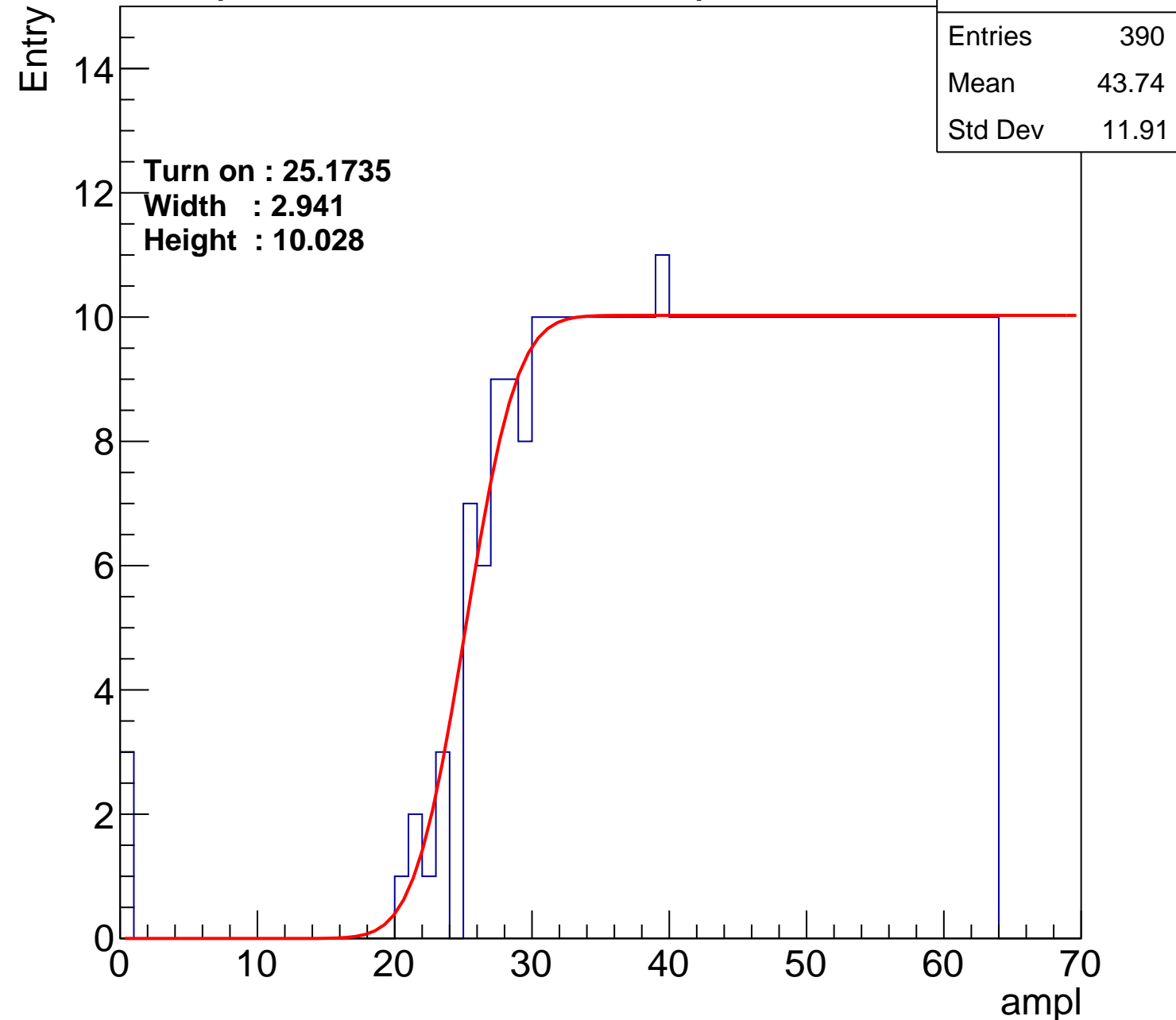
Width : 2.941

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch73

calib_packv5_042523_0143.root, FC#7, port C2

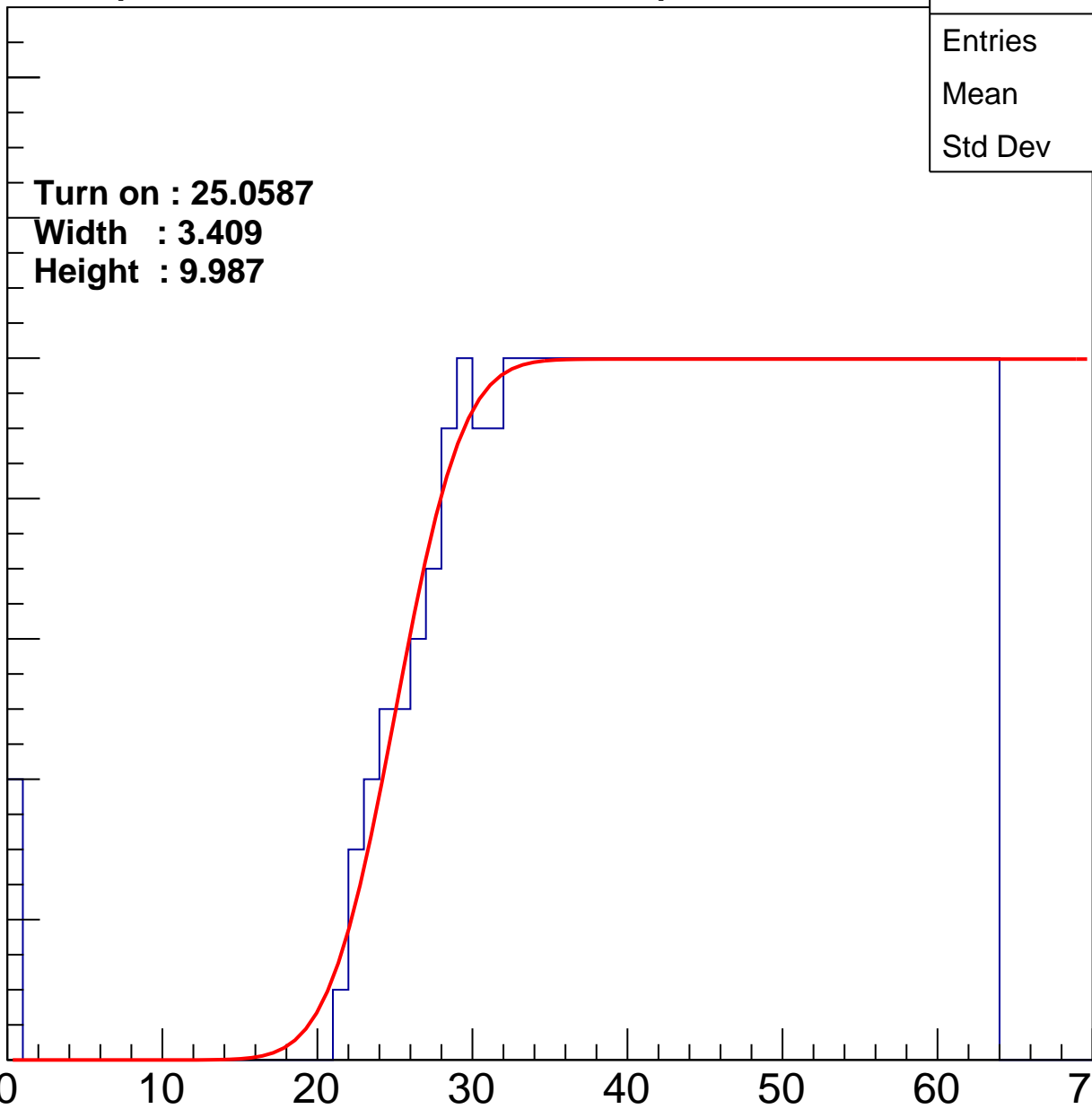
Entry

14
12
10
8
6
4
2
0

Turn on : 25.0587
Width : 3.409
Height : 9.987

Entries	392
Mean	43.52
Std Dev	12.19

ampl



B1L103S, U22-ch74

calib_packv5_042523_0143.root, FC#7, port C2

Entries	394
Mean	43.55
Std Dev	11.95

Turn on : 25.2938

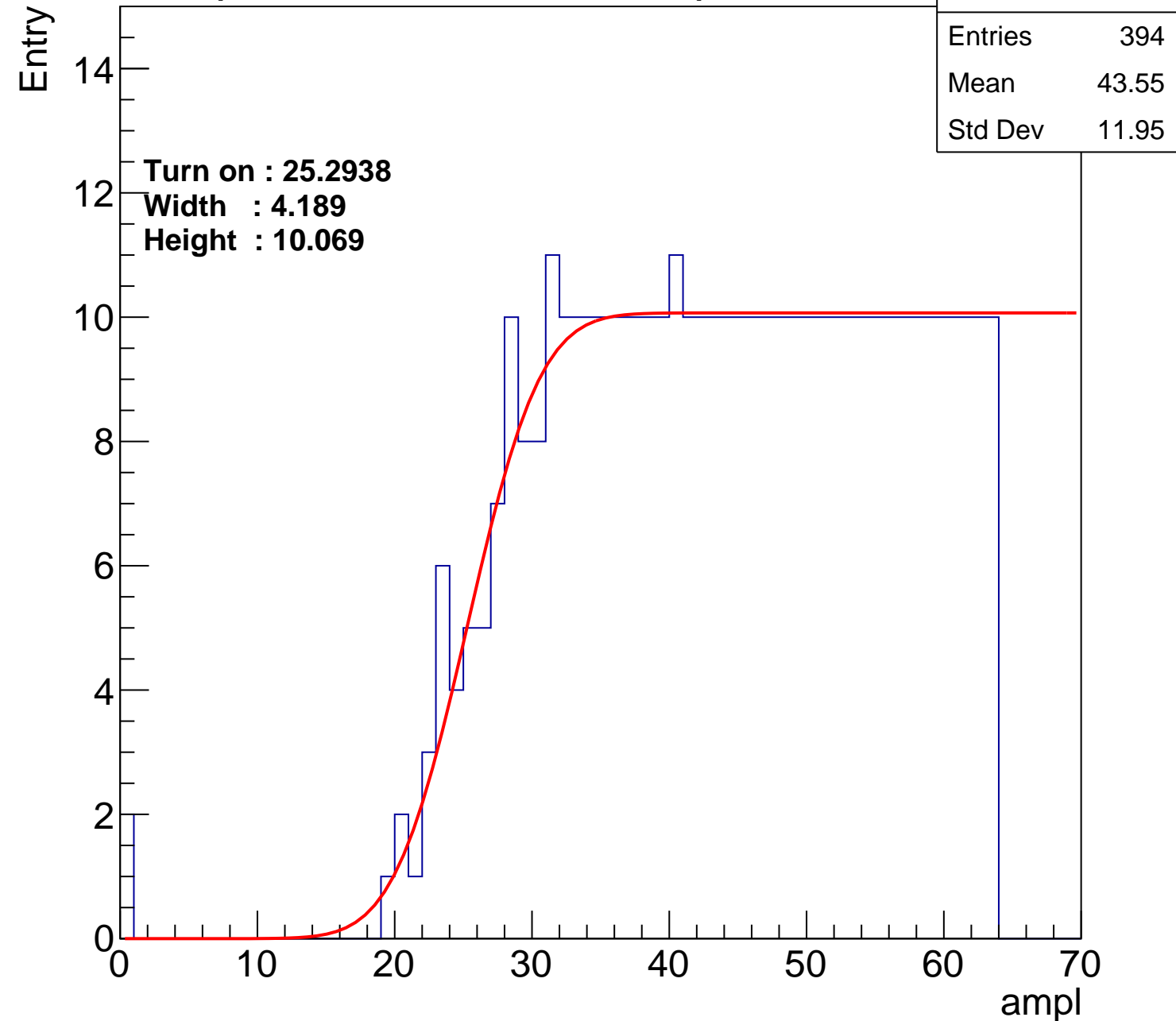
Width : 4.189

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch75

calib_packv5_042523_0143.root, FC#7, port C2

Entries	372
Mean	44.56
Std Dev	11.54

Turn on : 27.0757

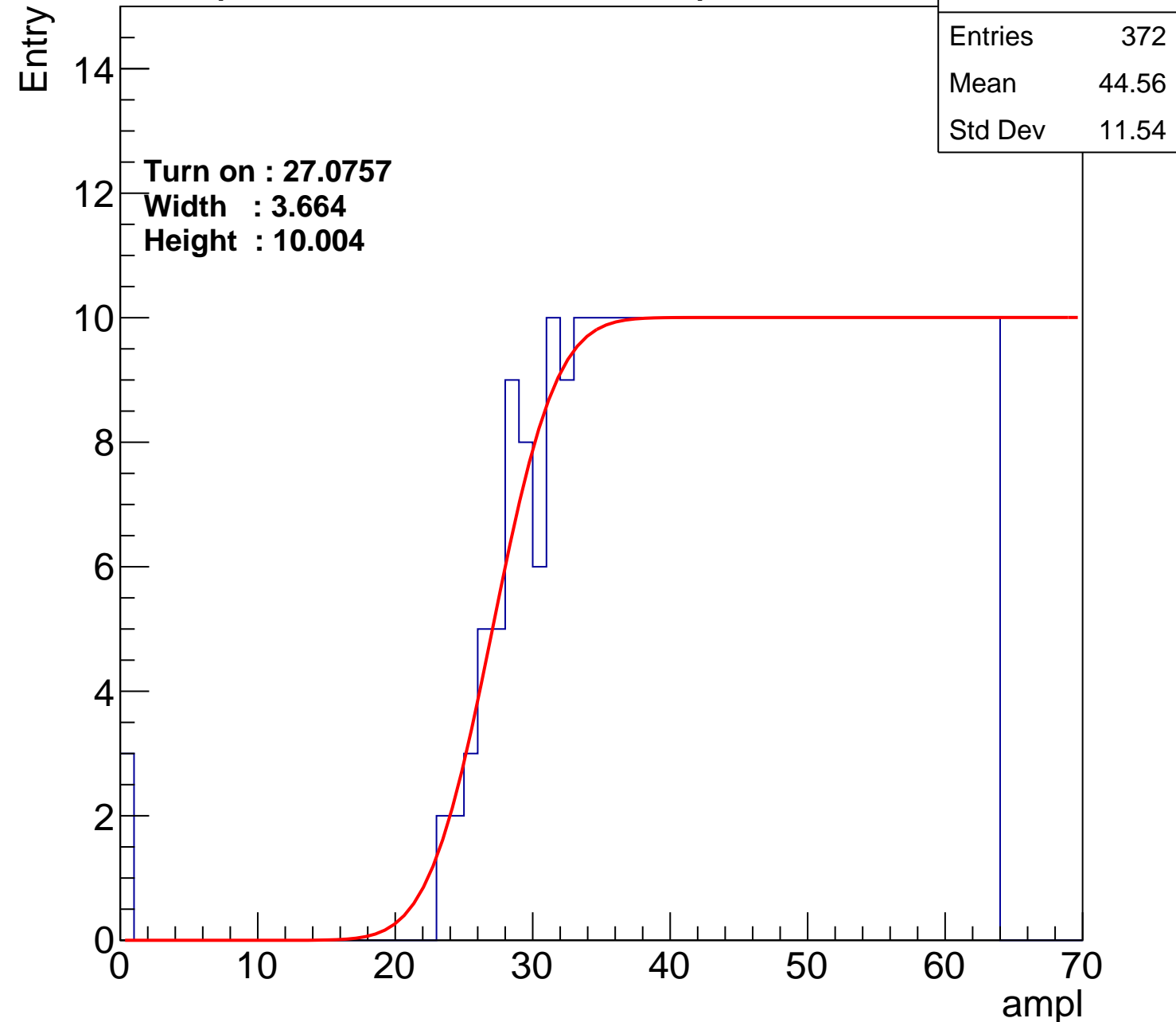
Width : 3.664

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch76

calib_packv5_042523_0143.root, FC#7, port C2

Entries	388
Mean	43.84
Std Dev	11.96

Turn on : 26.0016

Width : 3.283

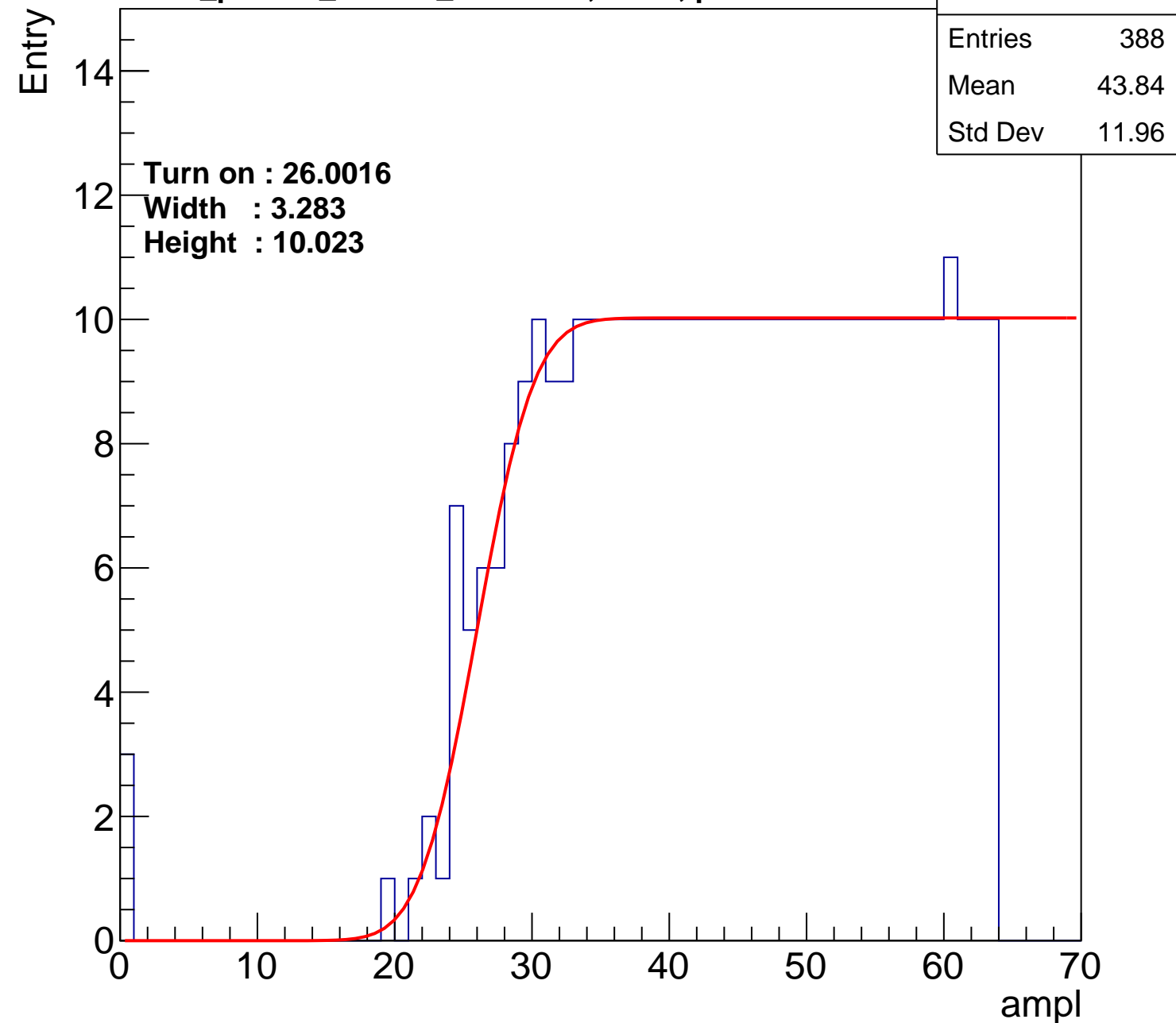
Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L103S, U22-ch77

calib_packv5_042523_0143.root, FC#7, port C2

Entries	350
Mean	45.65
Std Dev	11.01

Turn on : 29.4070

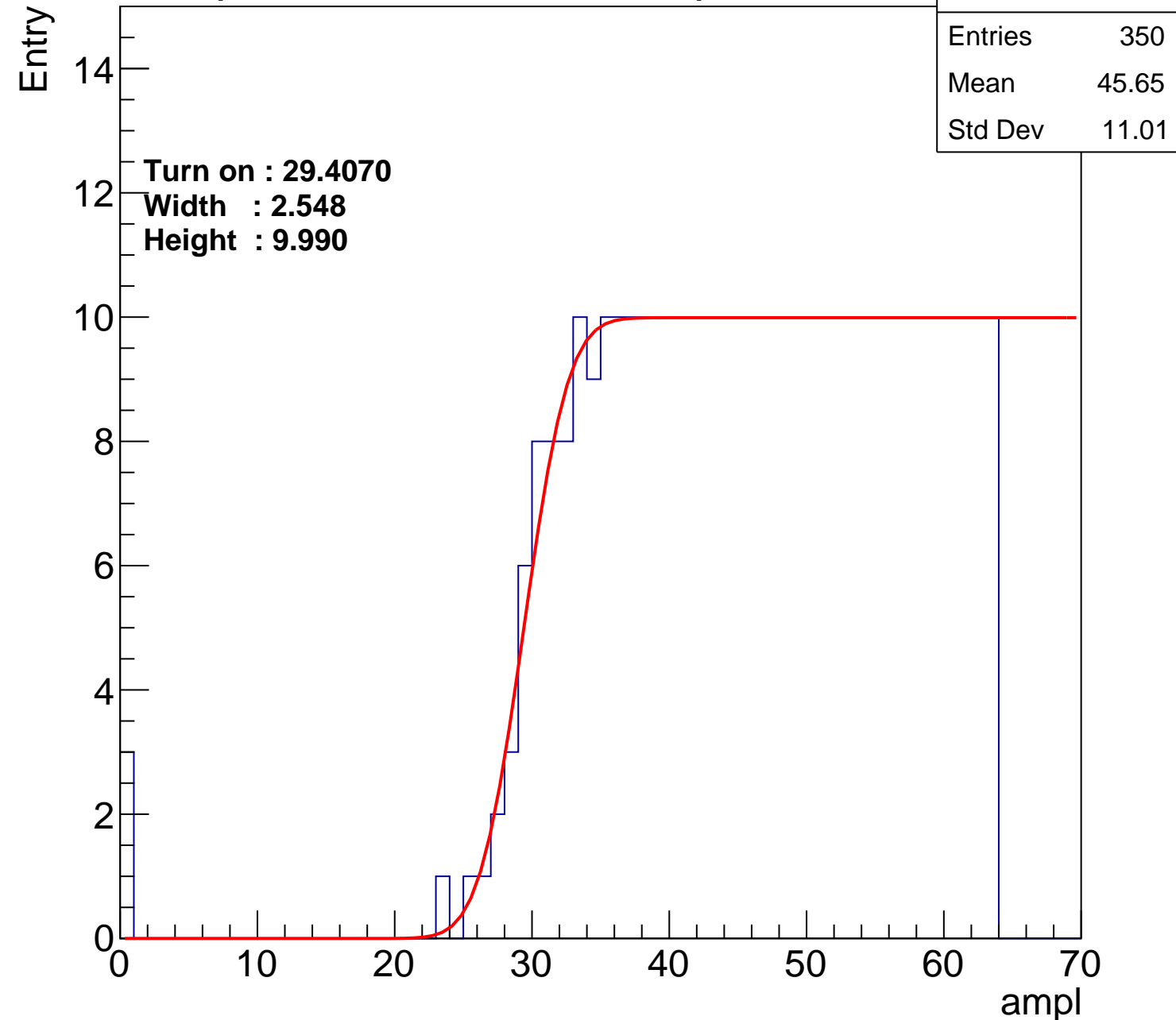
Width : 2.548

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch78

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	43.95
Std Dev	12.06

Turn on : 27.1333

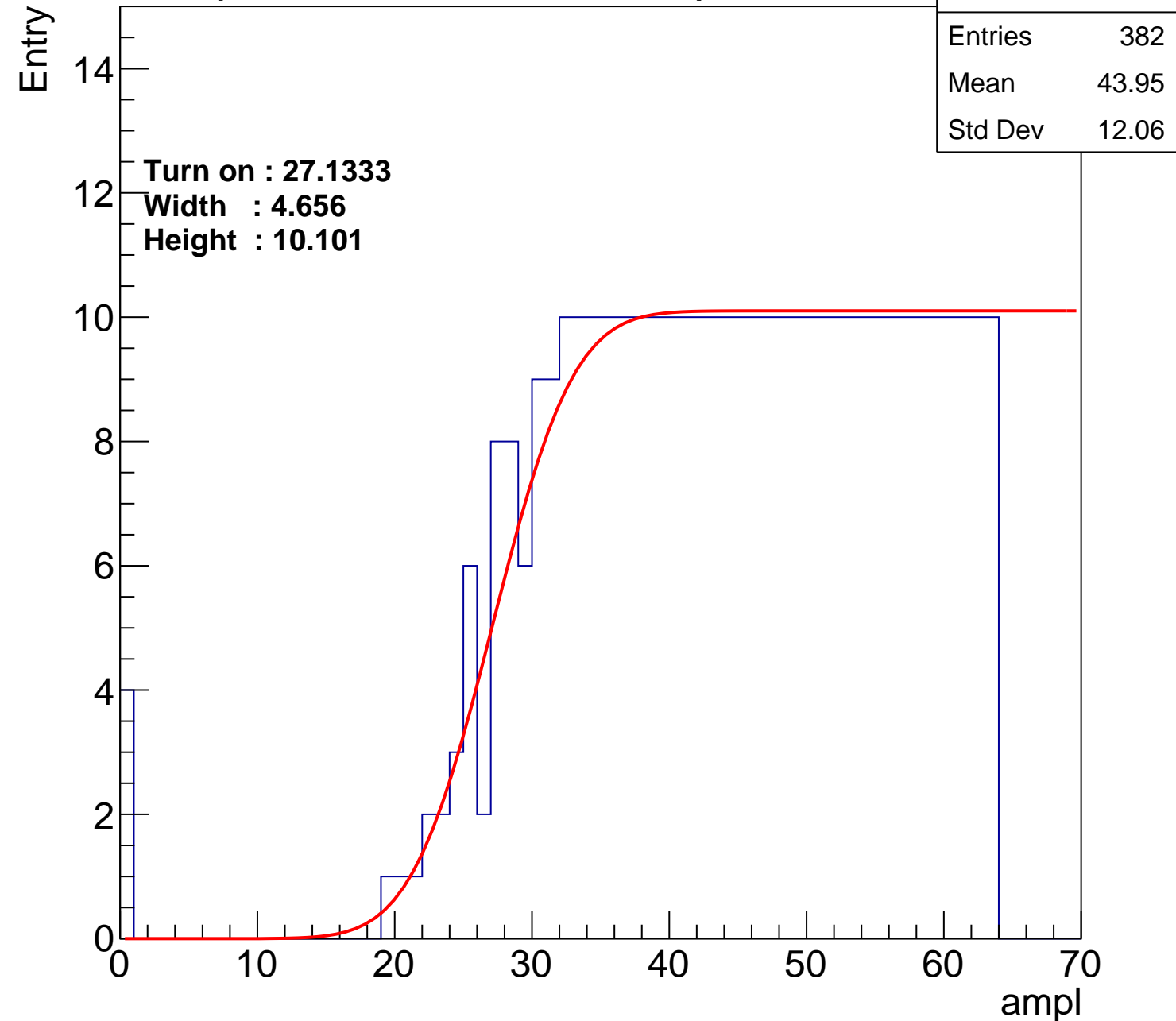
Width : 4.656

Height : 10.101

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch79

calib_packv5_042523_0143.root, FC#7, port C2

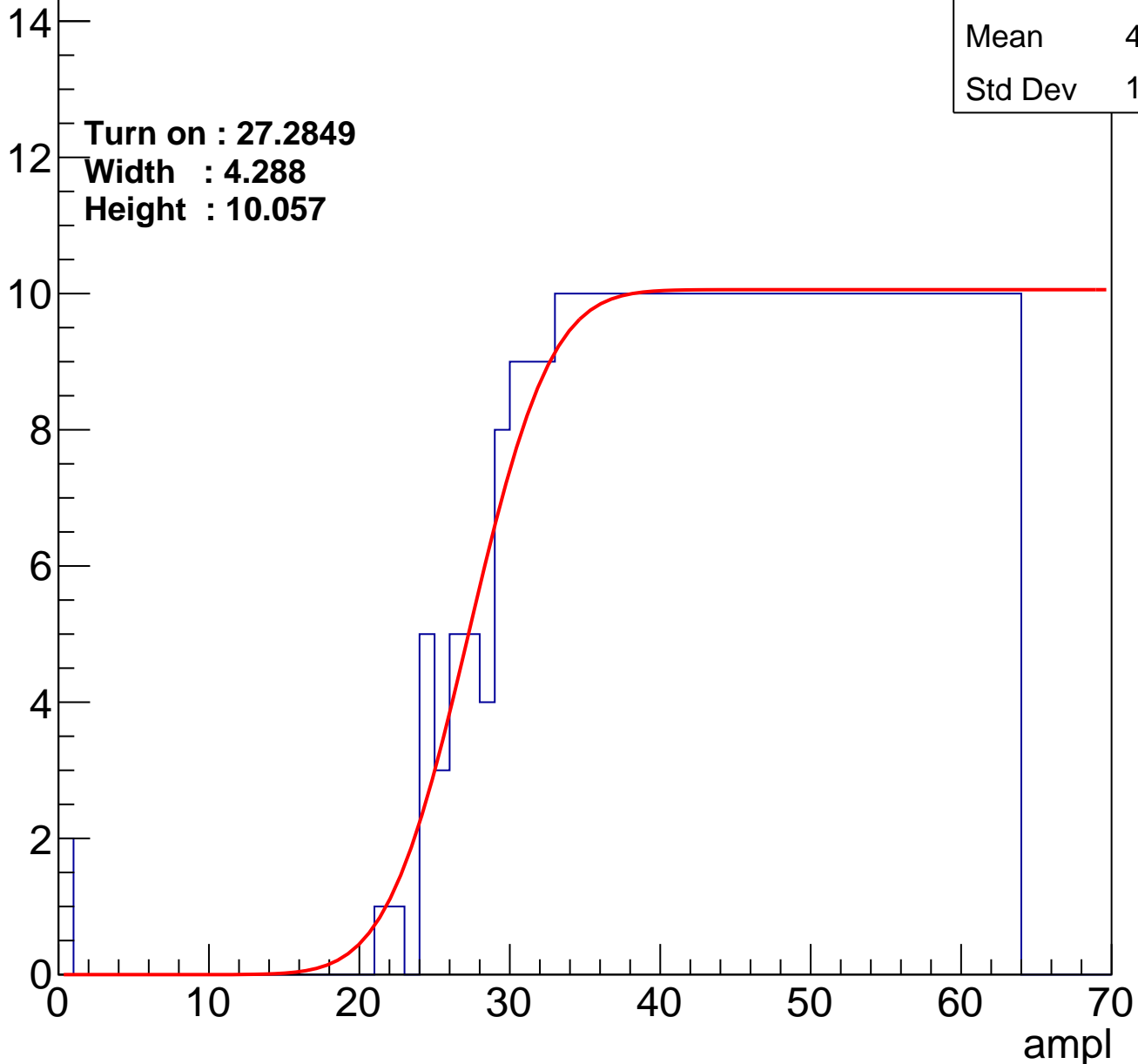
Entries	371
Mean	44.65
Std Dev	11.38

Turn on : 27.2849

Width : 4.288

Height : 10.057

Entry



B1L103S, U22-ch80

calib_packv5_042523_0143.root, FC#7, port C2

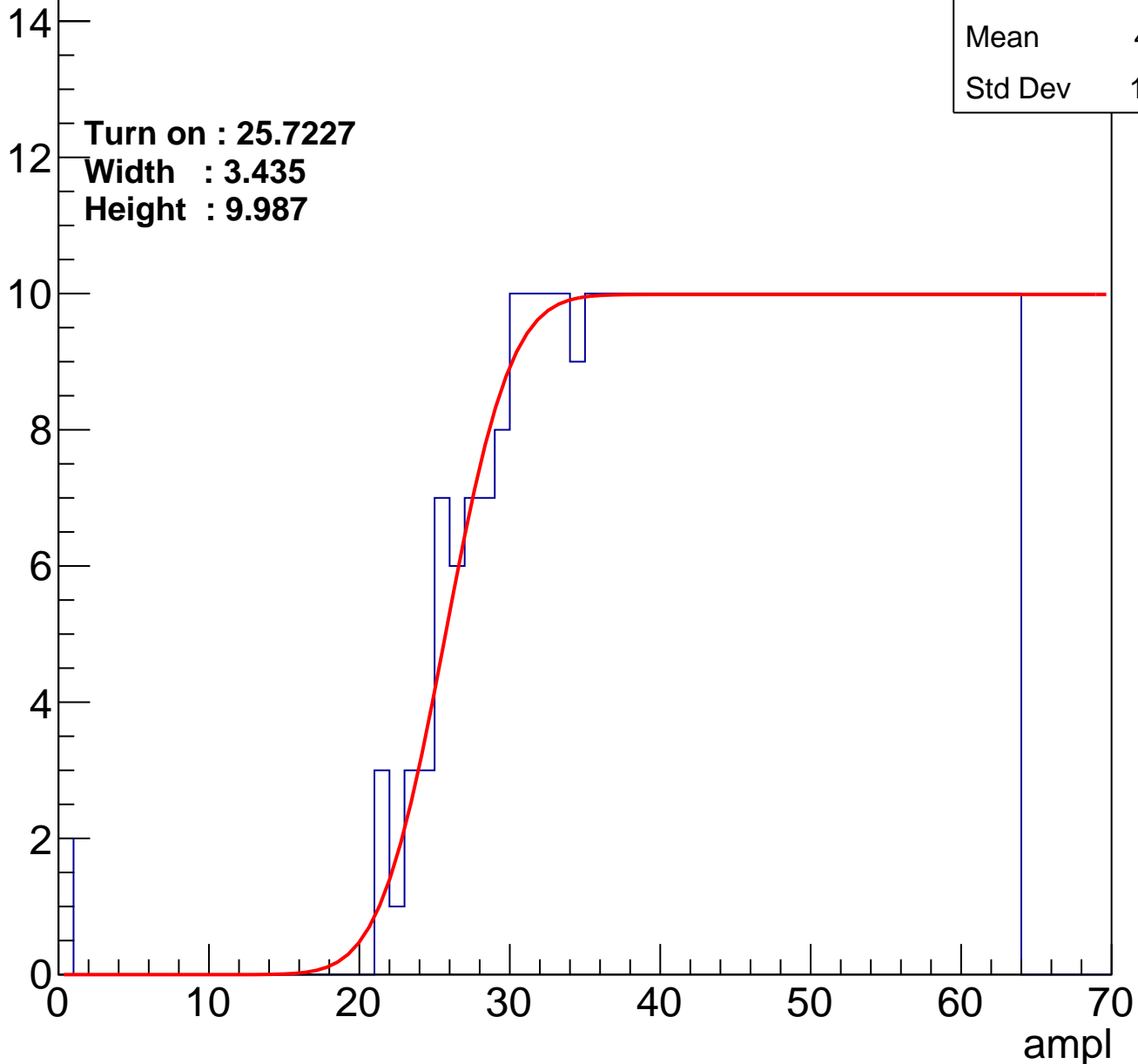
Entries	386
Mean	43.91
Std Dev	11.75

Turn on : 25.7227

Width : 3.435

Height : 9.987

Entry



B1L103S, U22-ch81

calib_packv5_042523_0143.root, FC#7, port C2

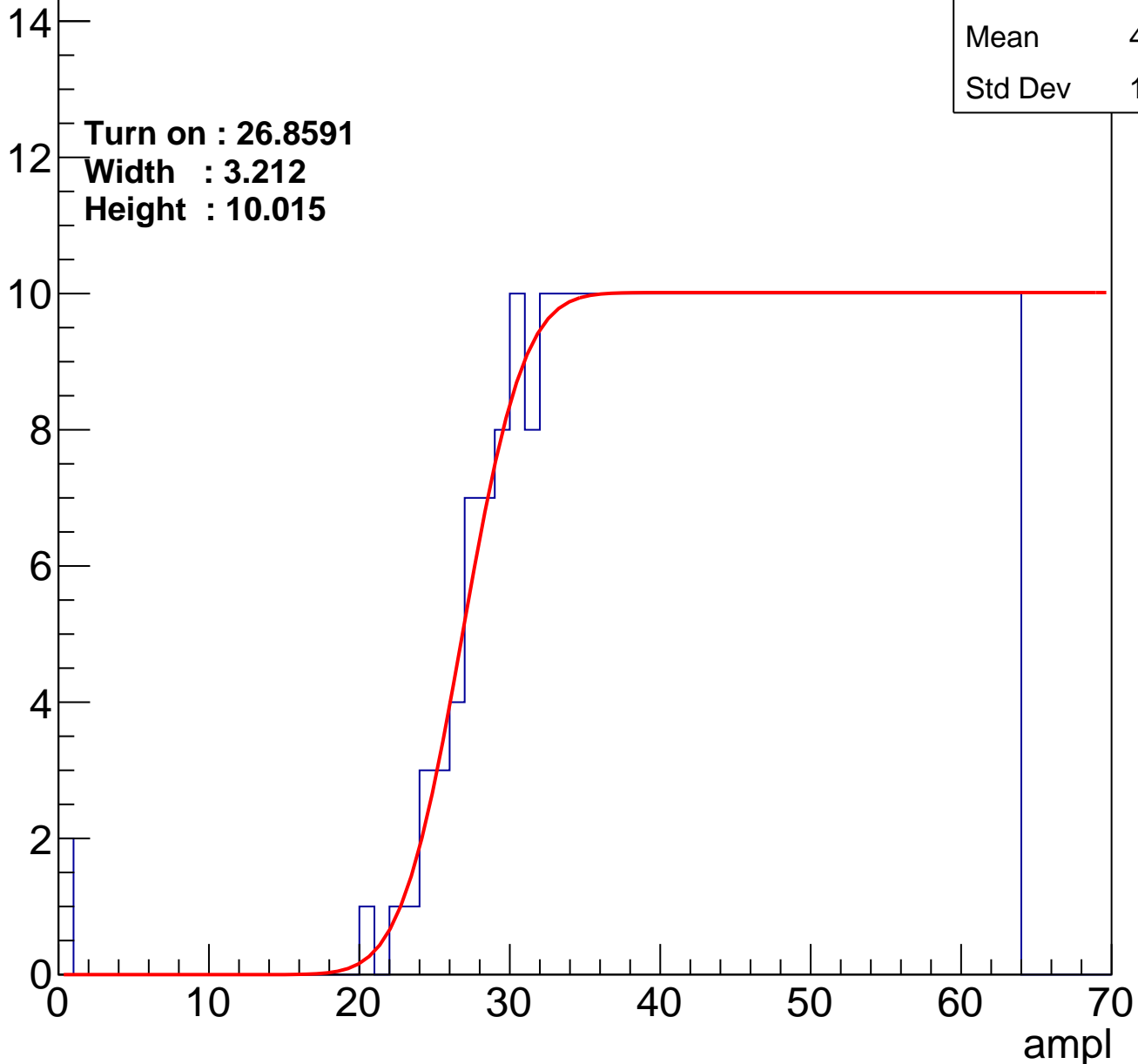
Entries	375
Mean	44.48
Std Dev	11.43

Turn on : 26.8591

Width : 3.212

Height : 10.015

Entry



B1L103S, U22-ch82

calib_packv5_042523_0143.root, FC#7, port C2

Entries	342
Mean	45.83
Std Dev	11.36

Turn on : 30.6871

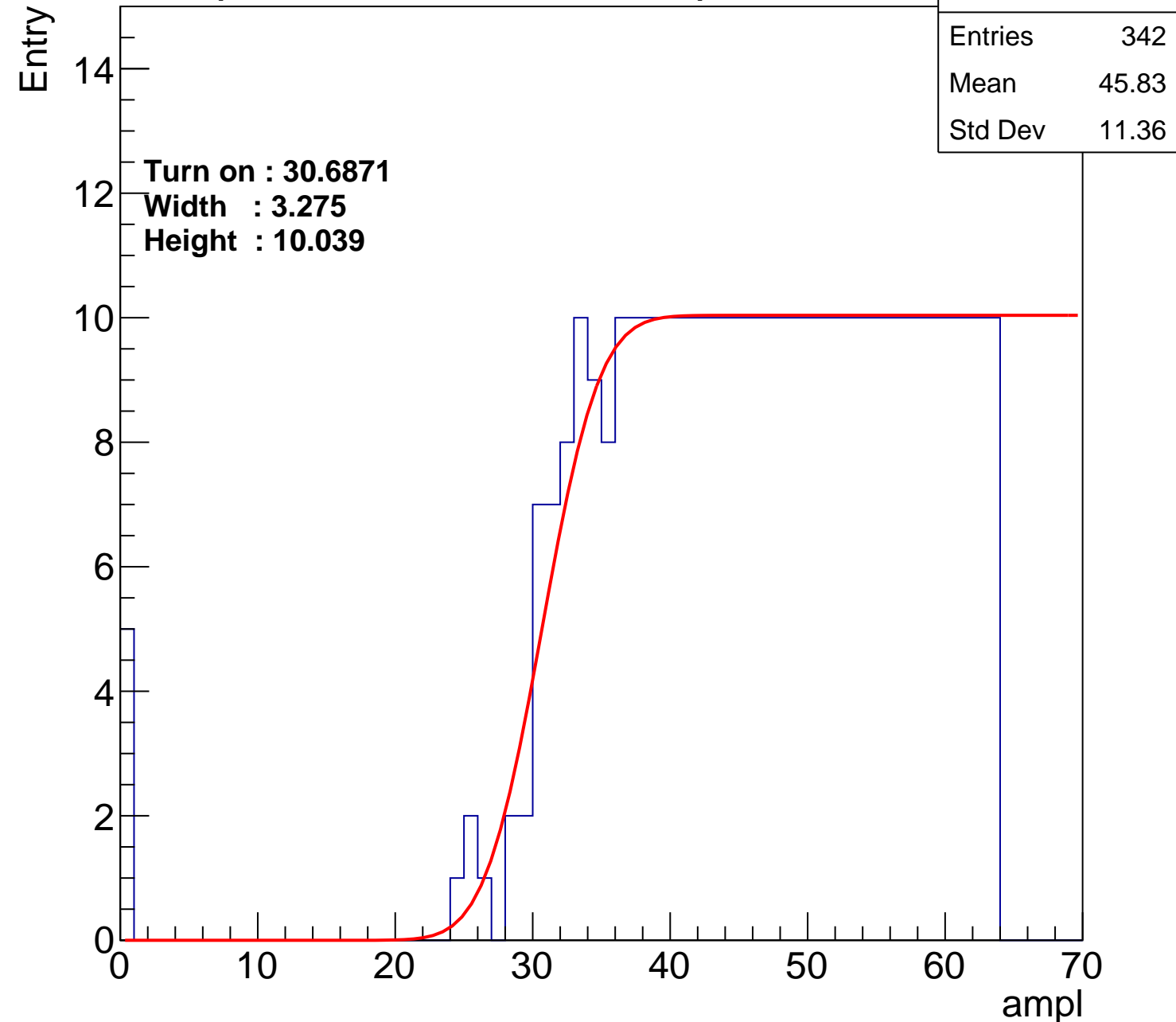
Width : 3.275

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch83

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.65
Std Dev	11.84

Turn on : 26.1479

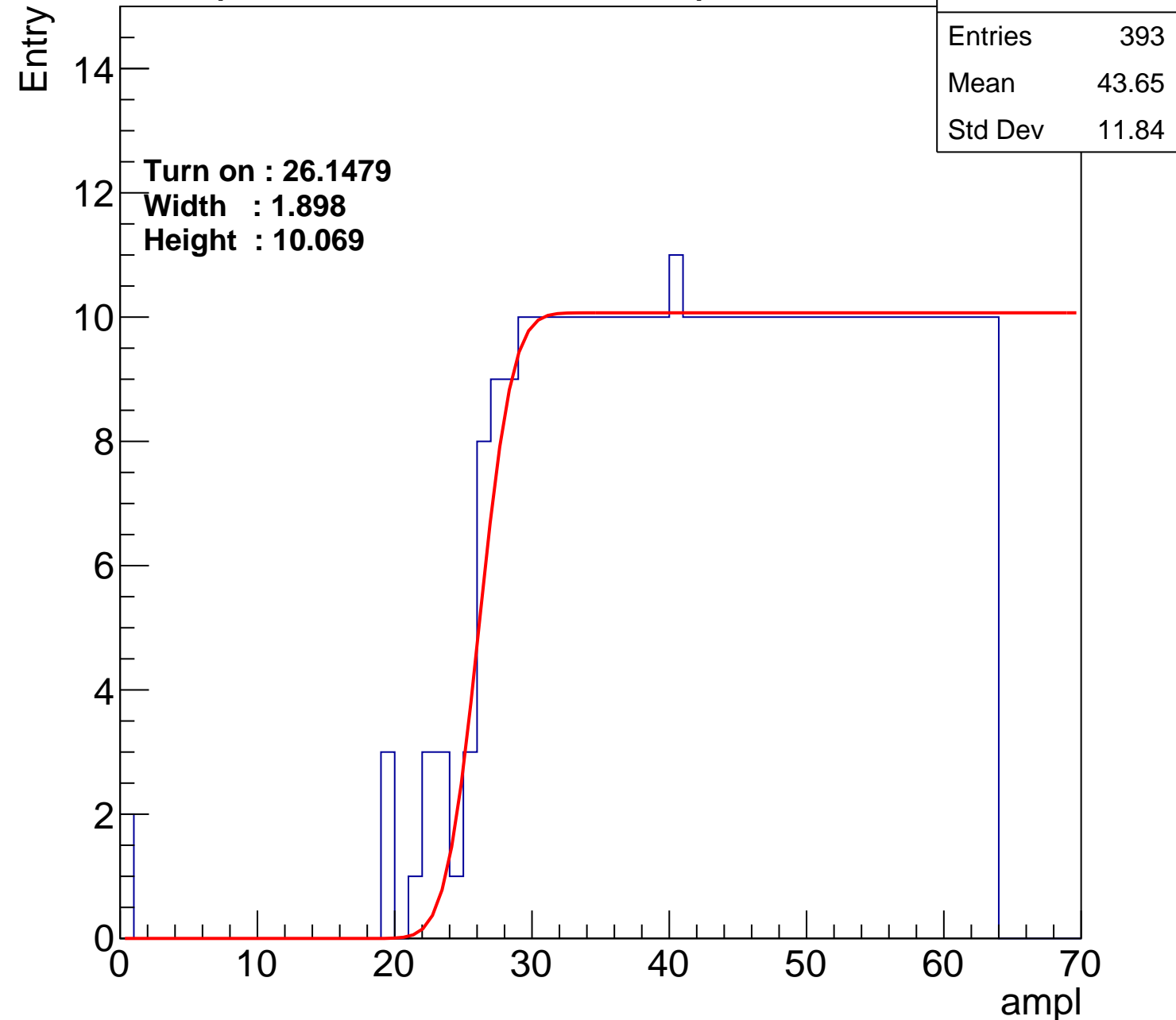
Width : 1.898

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch84

calib_packv5_042523_0143.root, FC#7, port C2

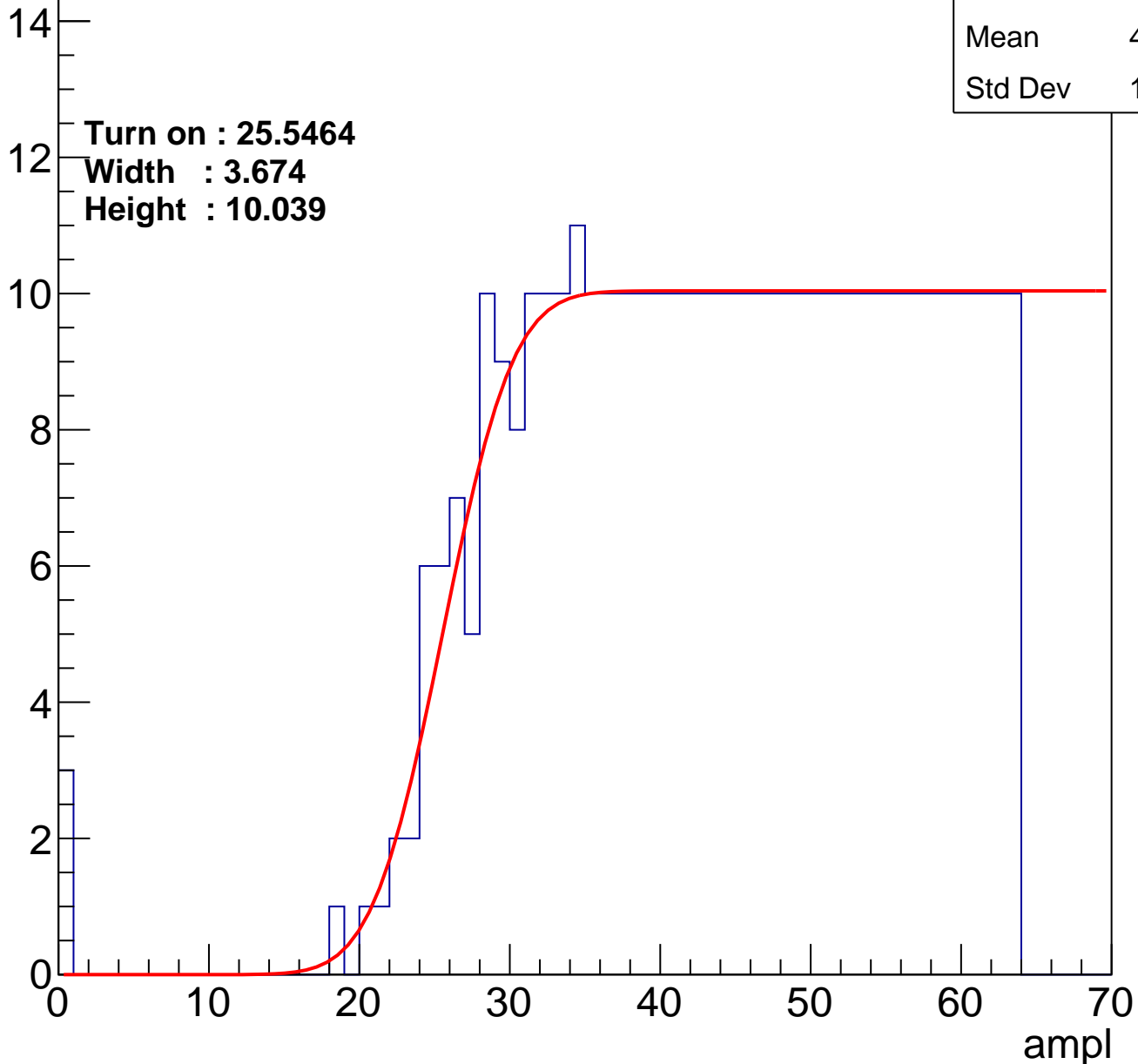
Entries	392
Mean	43.58
Std Dev	12.04

Turn on : 25.5464

Width : 3.674

Height : 10.039

Entry



B1L103S, U22-ch85

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.55
Std Dev	11.43

Turn on : 27.0603

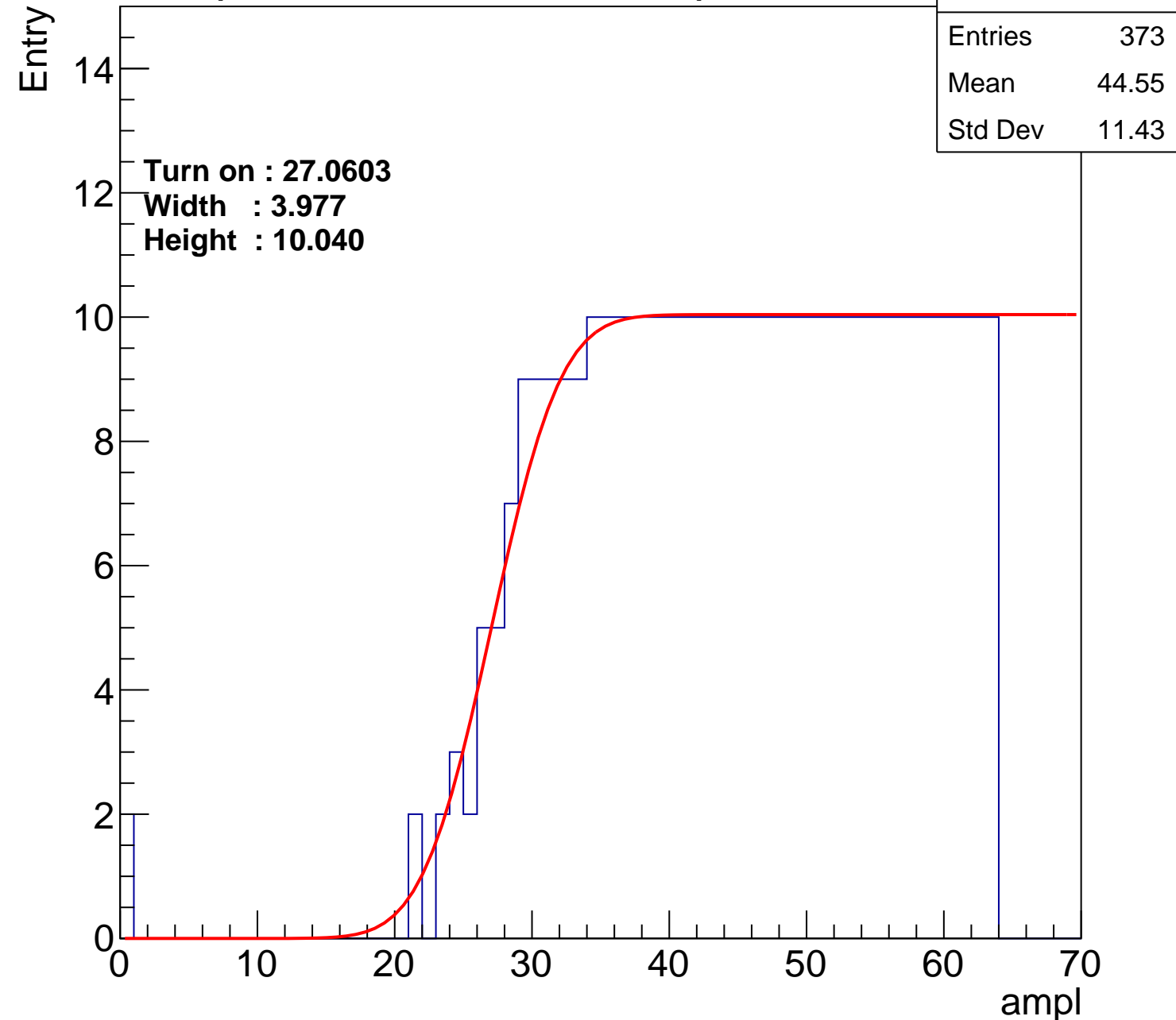
Width : 3.977

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch86

calib_packv5_042523_0143.root, FC#7, port C2

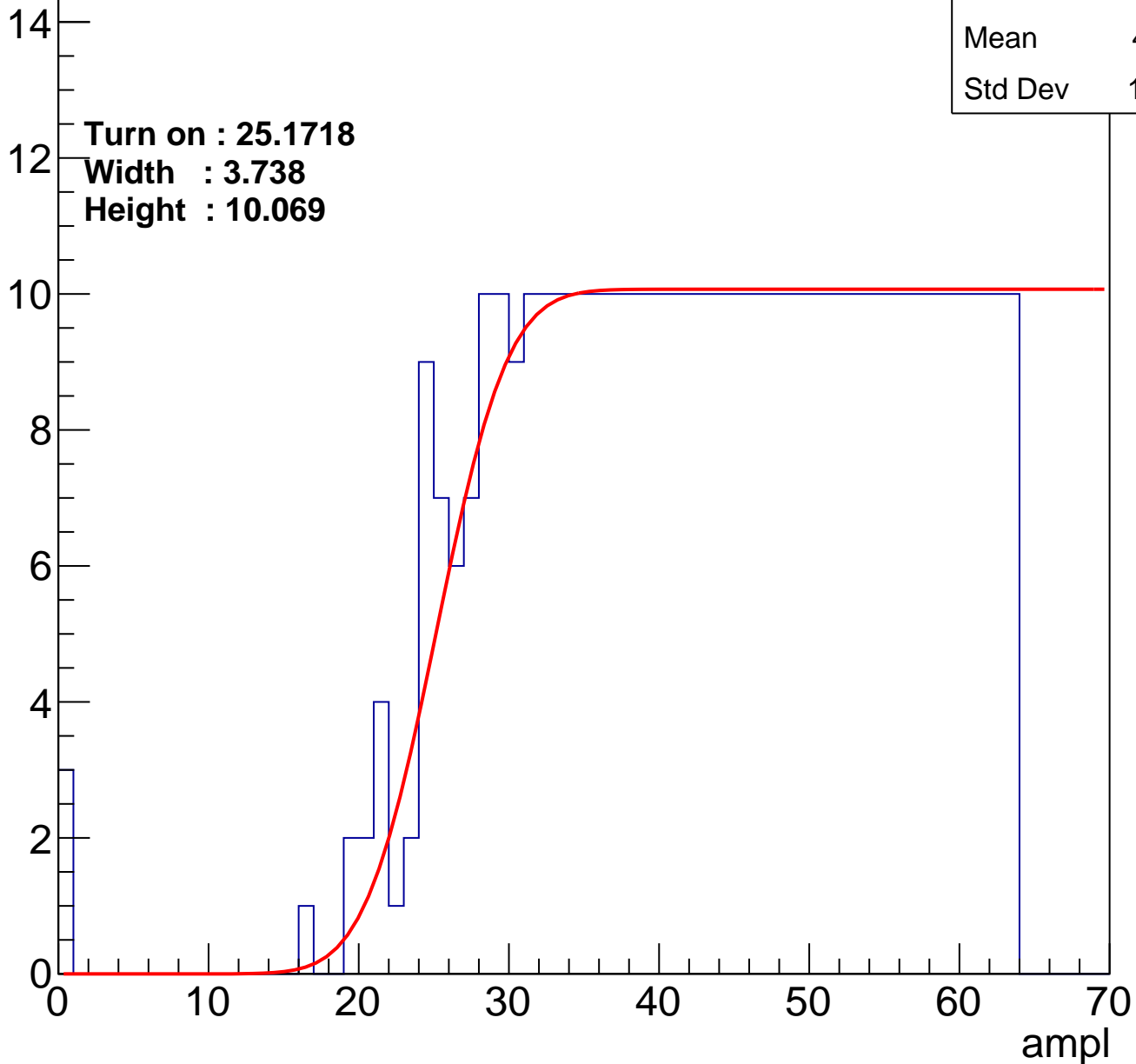
Entries	403
Mean	43.01
Std Dev	12.37

Turn on : 25.1718

Width : 3.738

Height : 10.069

Entry



B1L103S, U22-ch87

calib_packv5_042523_0143.root, FC#7, port C2

Entries	393
Mean	43.45
Std Dev	12.38

Turn on : 25.7640

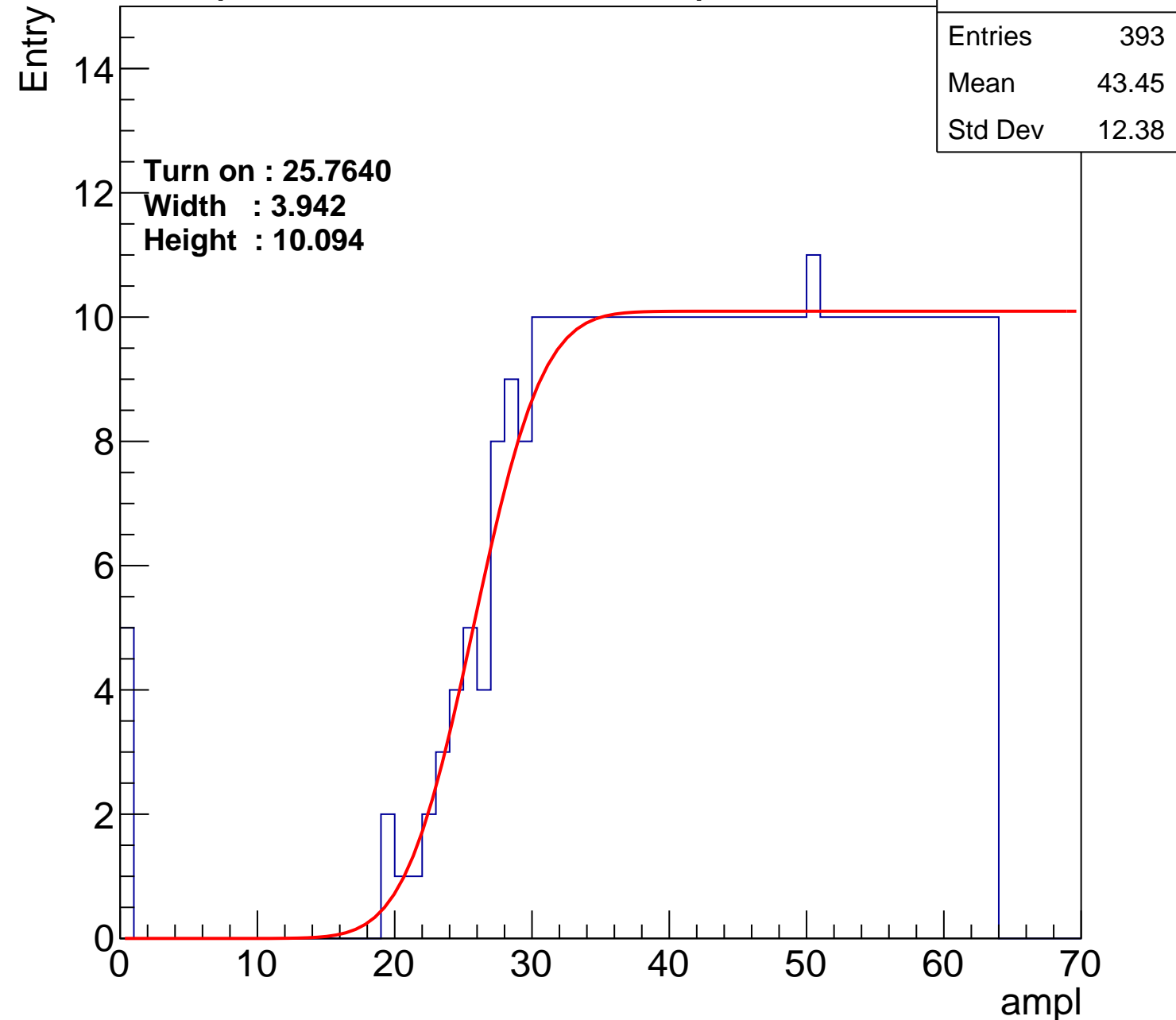
Width : 3.942

Height : 10.094

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch88

calib_packv5_042523_0143.root, FC#7, port C2

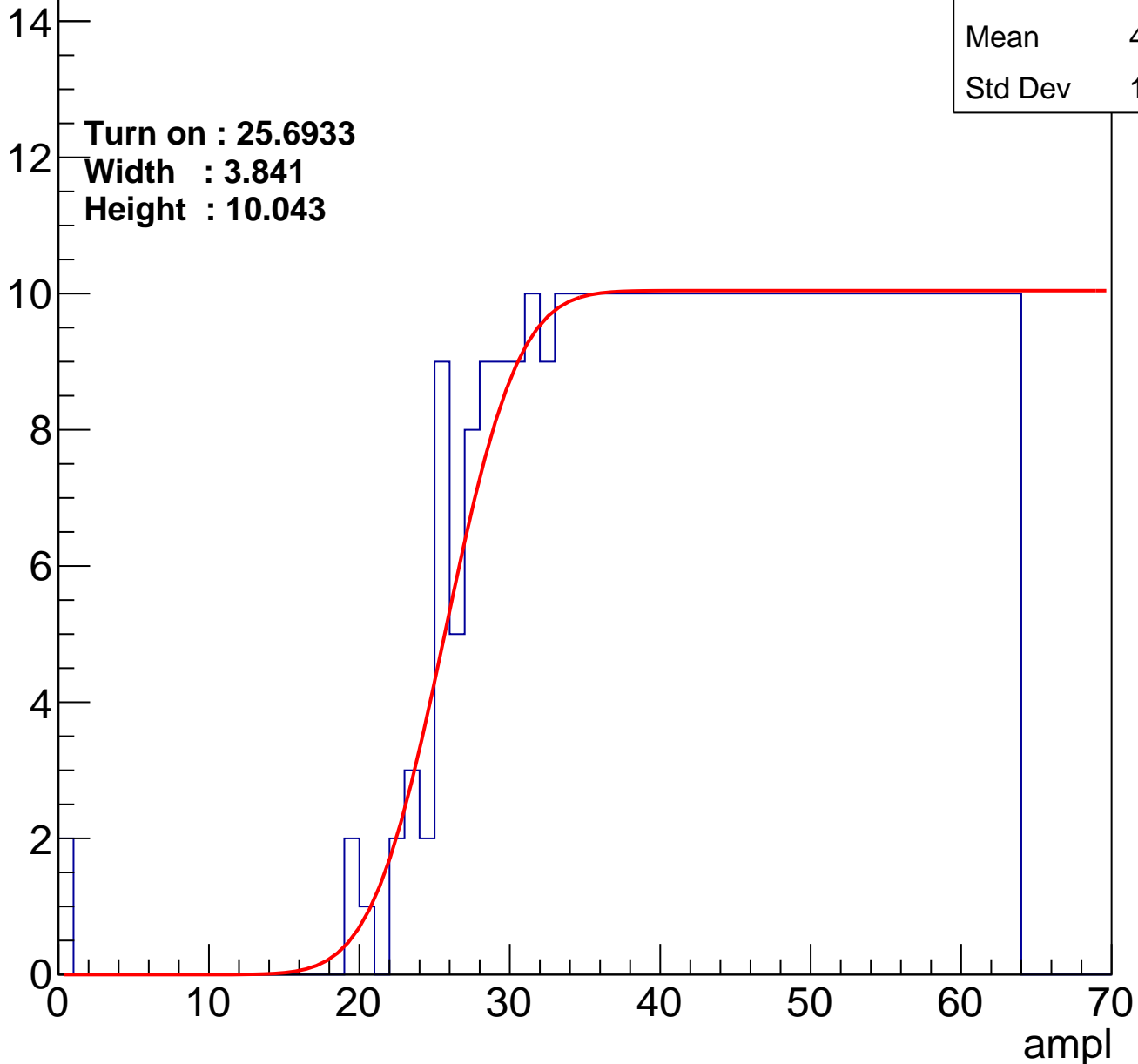
Entries	390
Mean	43.72
Std Dev	11.85

Turn on : 25.6933

Width : 3.841

Height : 10.043

Entry



B1L103S, U22-ch89

calib_packv5_042523_0143.root, FC#7, port C2

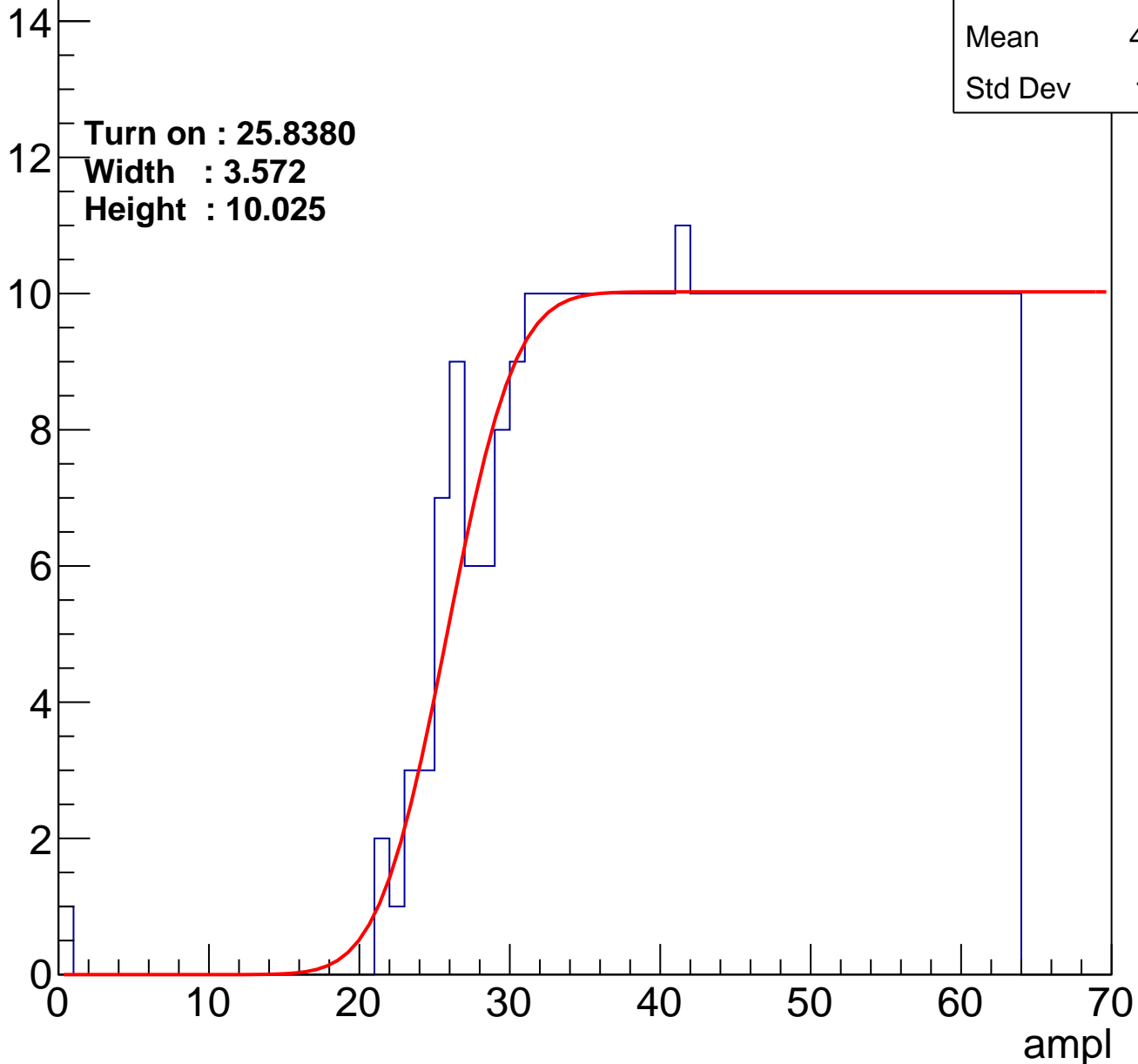
Entries	386
Mean	44.03
Std Dev	11.51

Turn on : 25.8380

Width : 3.572

Height : 10.025

Entry



B1L103S, U22-ch90

calib_packv5_042523_0143.root, FC#7, port C2

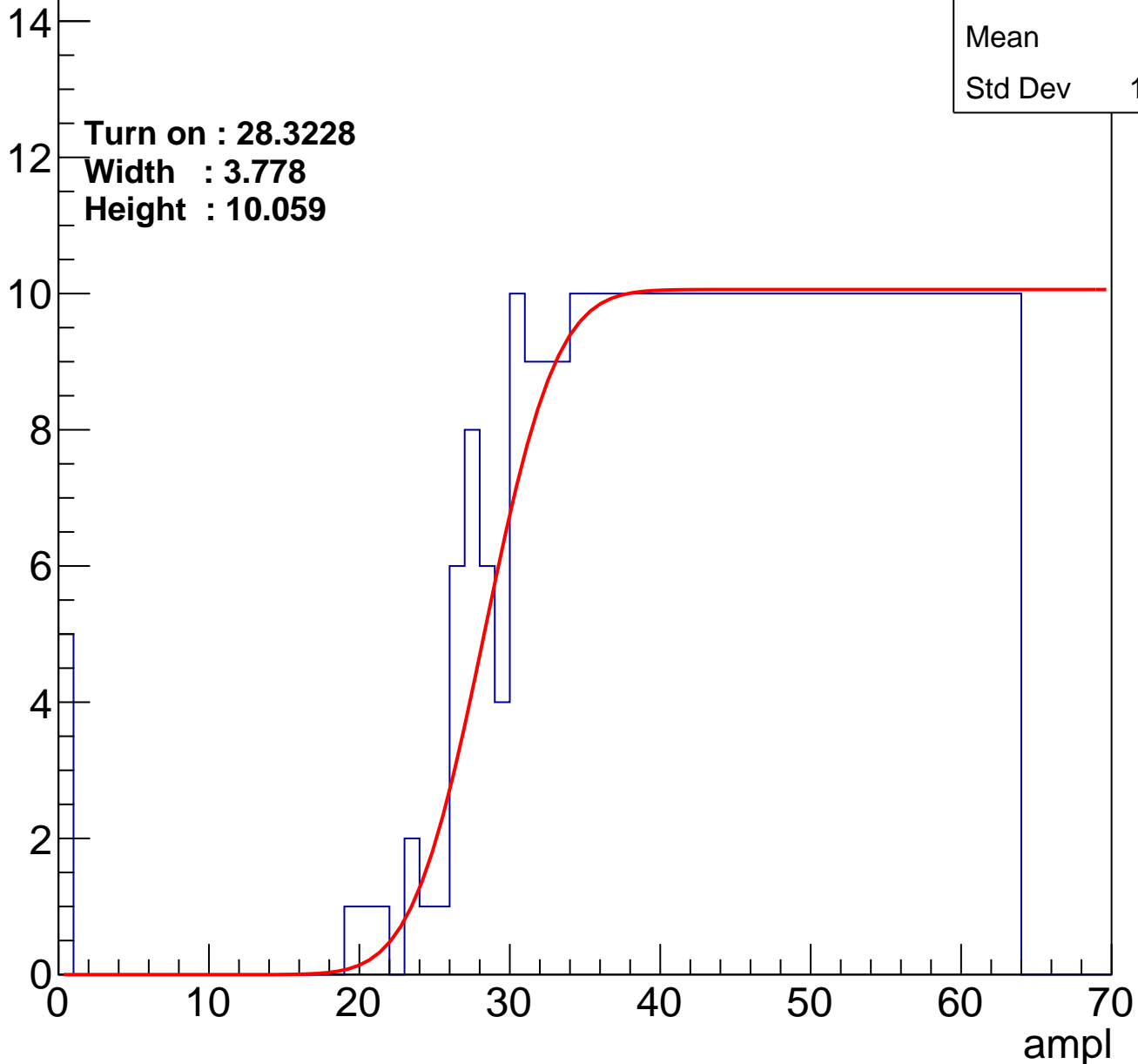
Entries	373
Mean	44.3
Std Dev	12.05

Turn on : 28.3228

Width : 3.778

Height : 10.059

Entry



B1L103S, U22-ch91

calib_packv5_042523_0143.root, FC#7, port C2

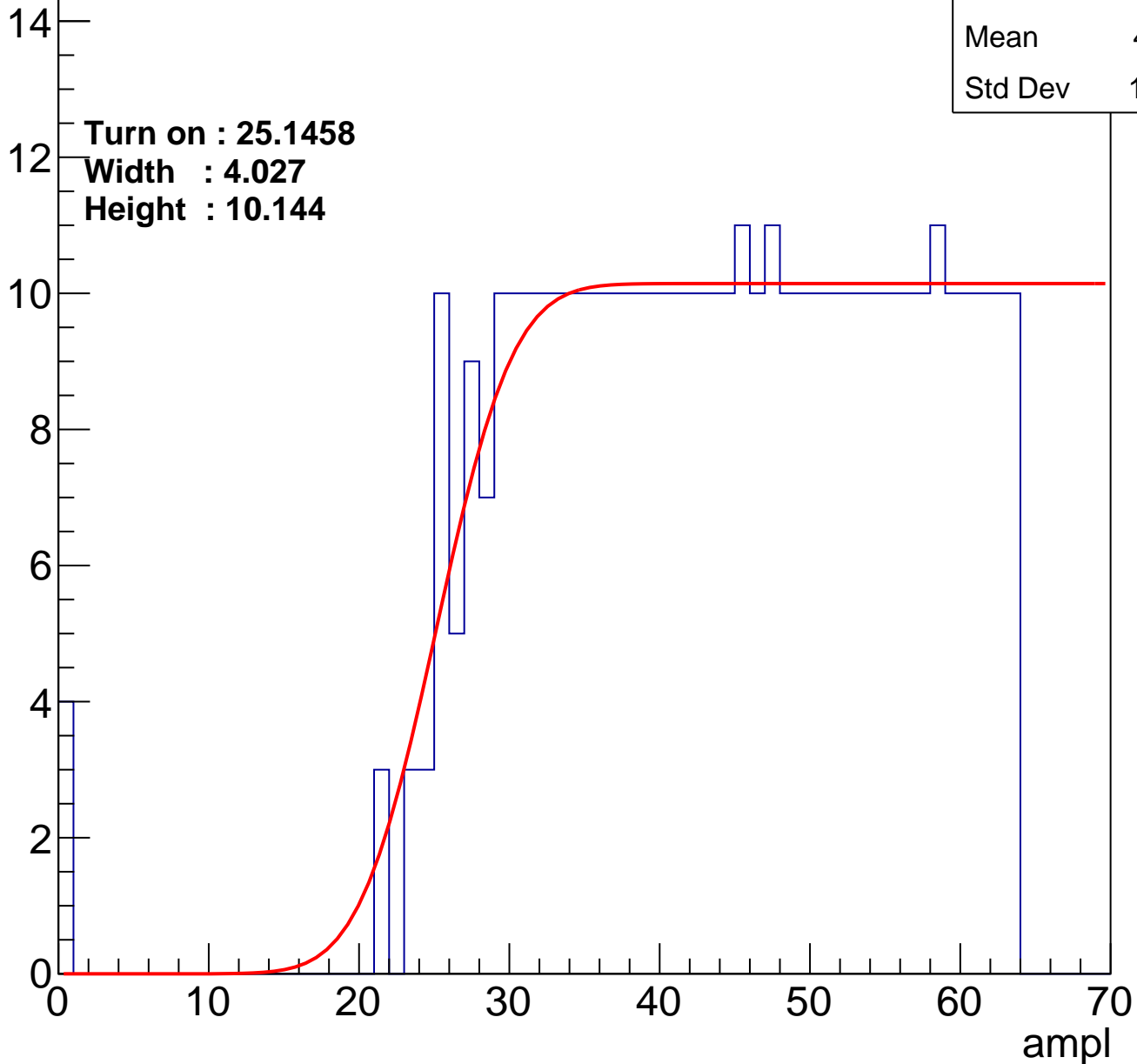
Entries	397
Mean	43.51
Std Dev	12.16

Turn on : 25.1458

Width : 4.027

Height : 10.144

Entry



B1L103S, U22-ch92

calib_packv5_042523_0143.root, FC#7, port C2

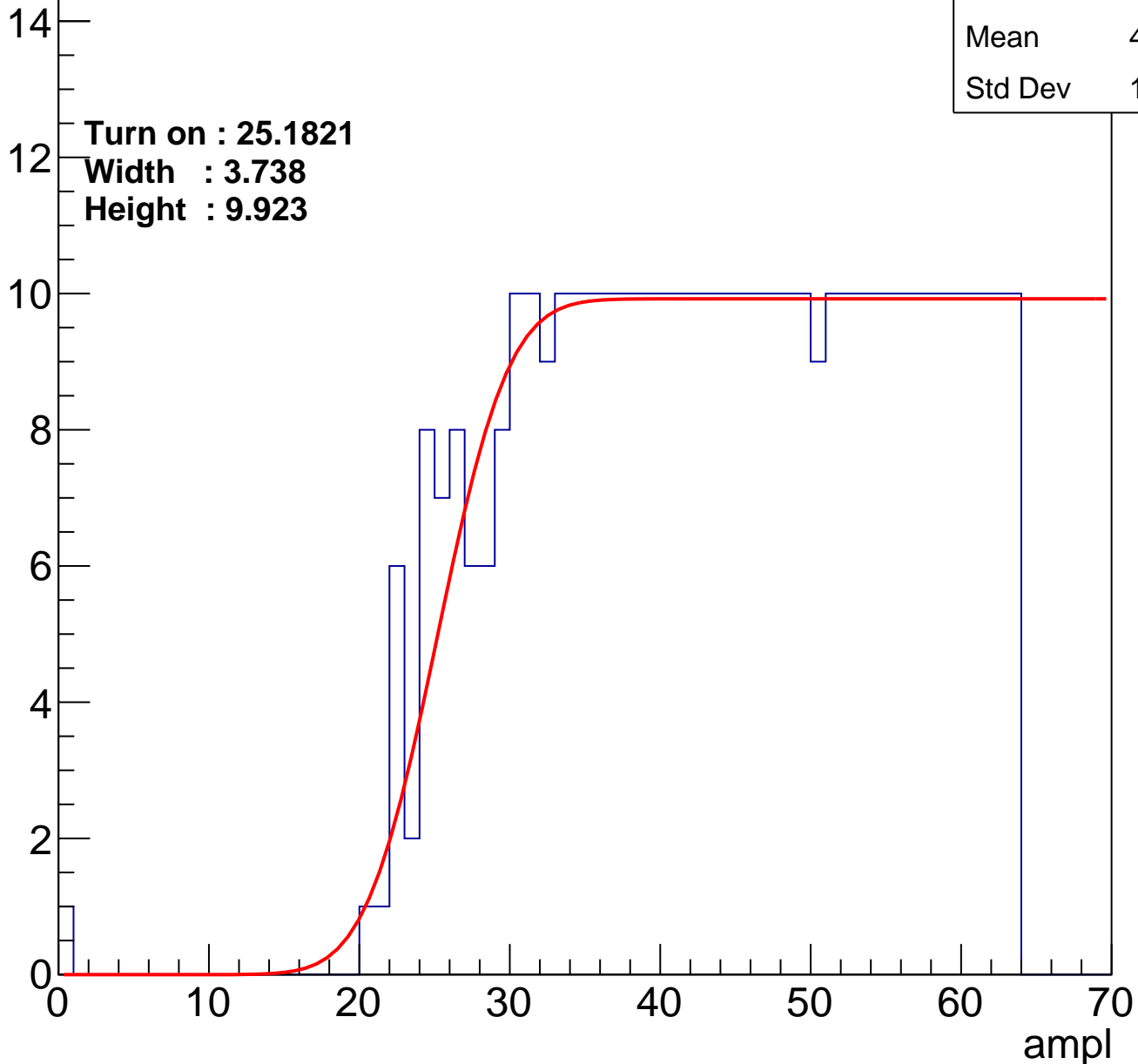
Entries	392
Mean	43.58
Std Dev	11.83

Turn on : 25.1821

Width : 3.738

Height : 9.923

Entry



B1L103S, U22-ch93

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.65
Std Dev	11.67

Turn on : 28.0428

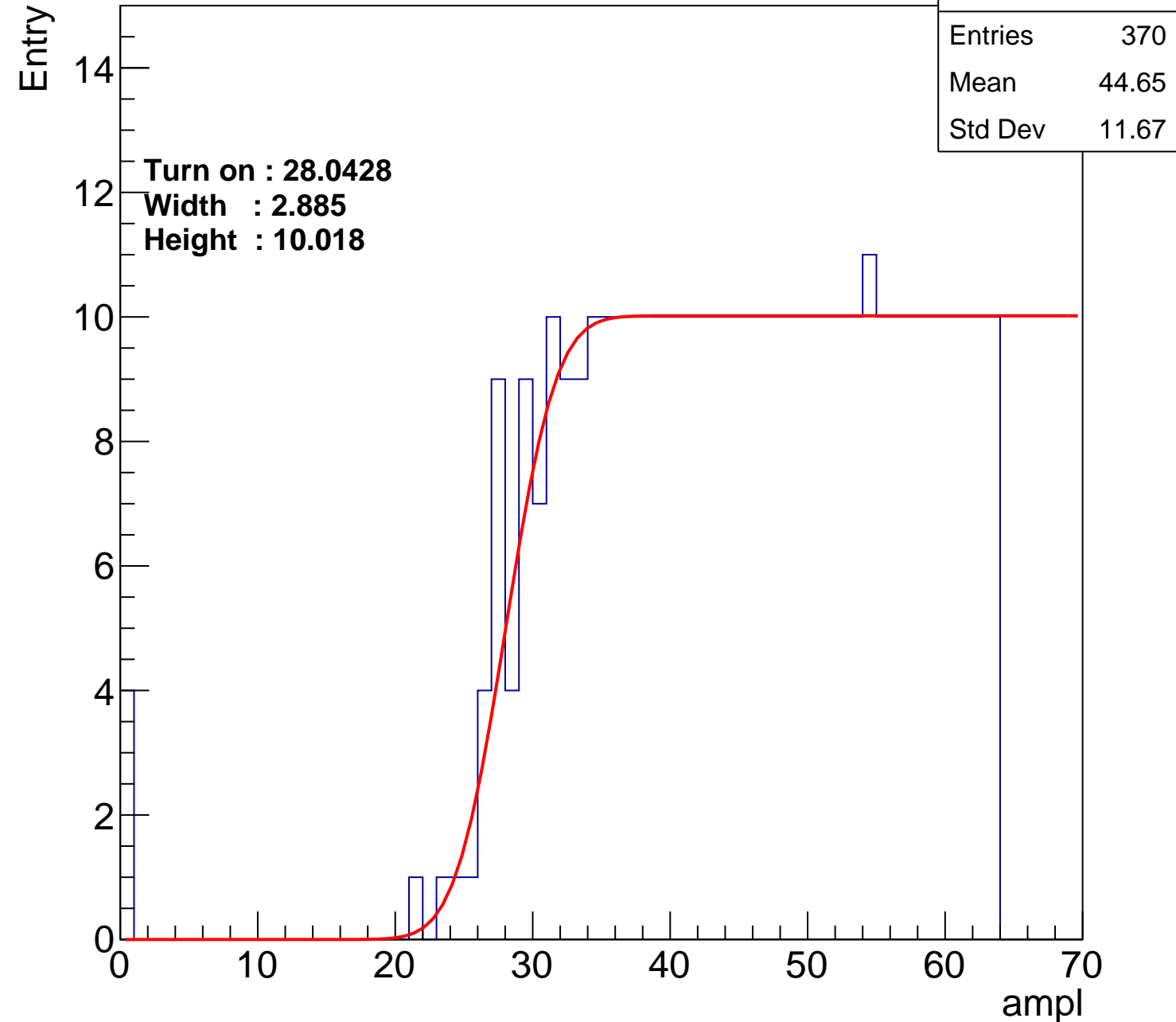
Width : 2.885

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch94

calib_packv5_042523_0143.root, FC#7, port C2

Entries	402
Mean	43.06
Std Dev	12.33

Turn on : 24.5442

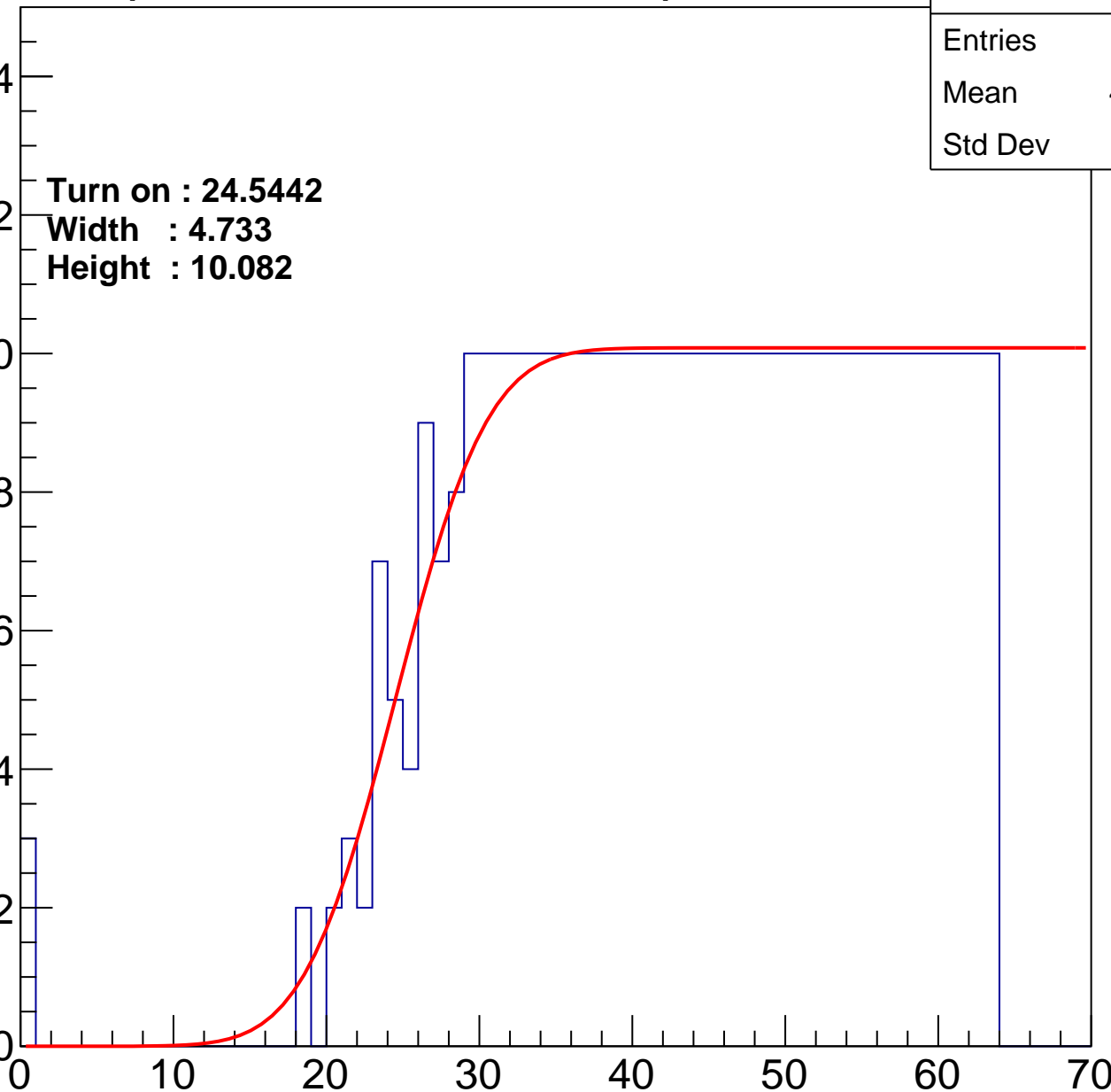
Width : 4.733

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch95

calib_packv5_042523_0143.root, FC#7, port C2

Entries	369
Mean	44.72
Std Dev	11.46

Turn on : 27.9452

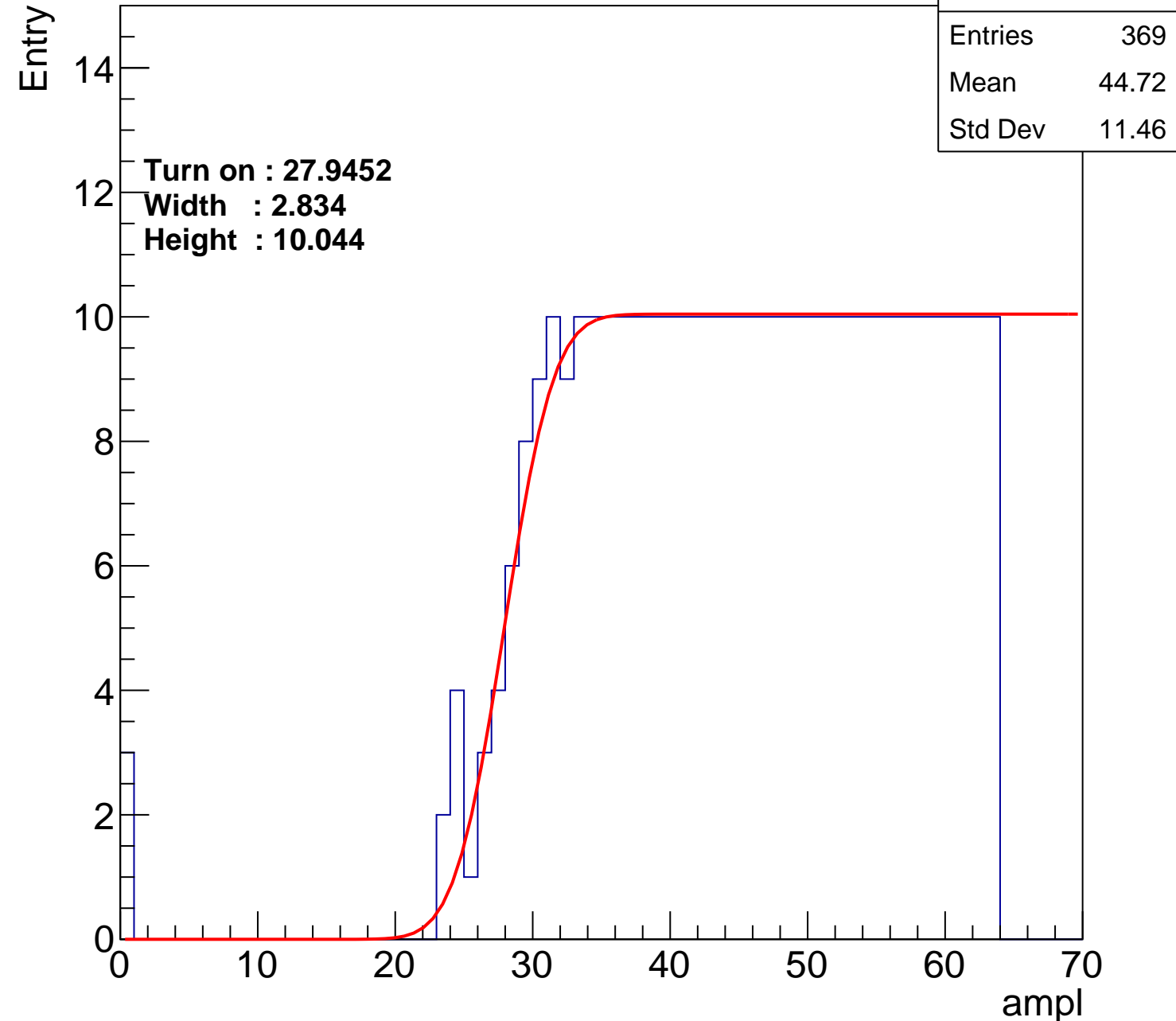
Width : 2.834

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch96

calib_packv5_042523_0143.root, FC#7, port C2

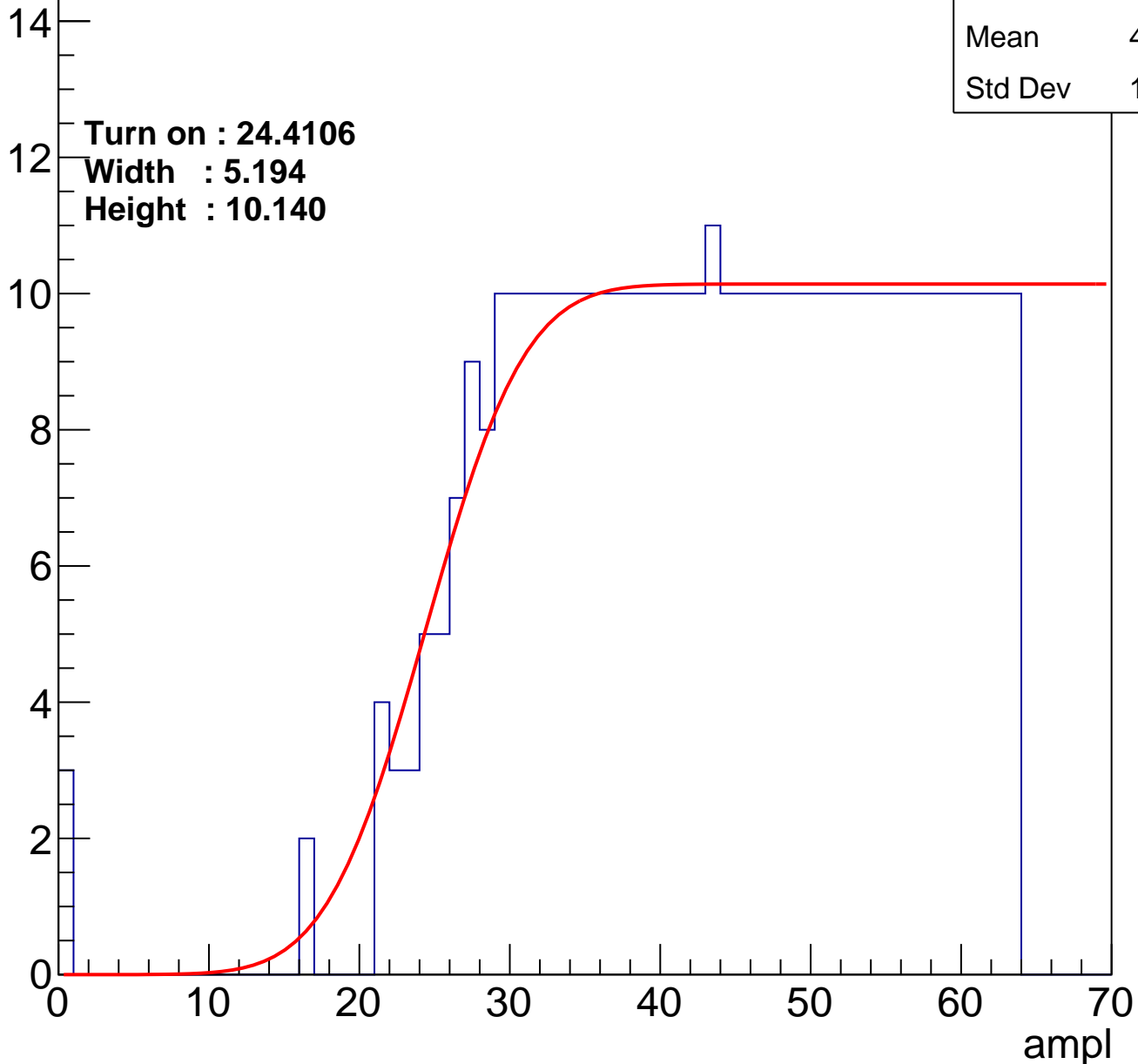
Entries	400
Mean	43.22
Std Dev	12.23

Turn on : 24.4106

Width : 5.194

Height : 10.140

Entry



B1L103S, U22-ch97

calib_packv5_042523_0143.root, FC#7, port C2

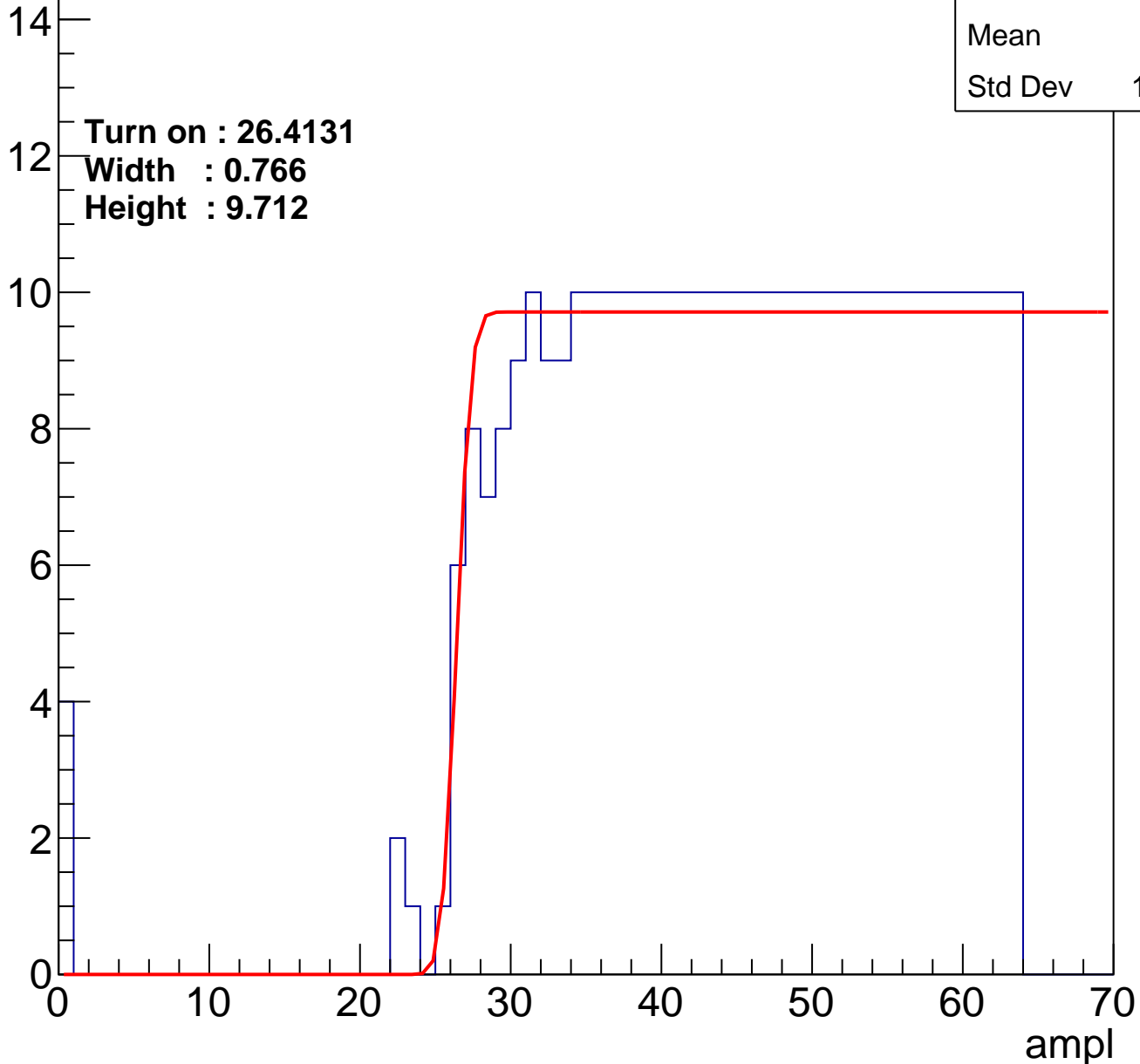
Entries	374
Mean	44.4
Std Dev	11.76

Turn on : 26.4131

Width : 0.766

Height : 9.712

Entry



B1L103S, U22-ch98

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	44.05
Std Dev	11.69

Turn on : 26.1673

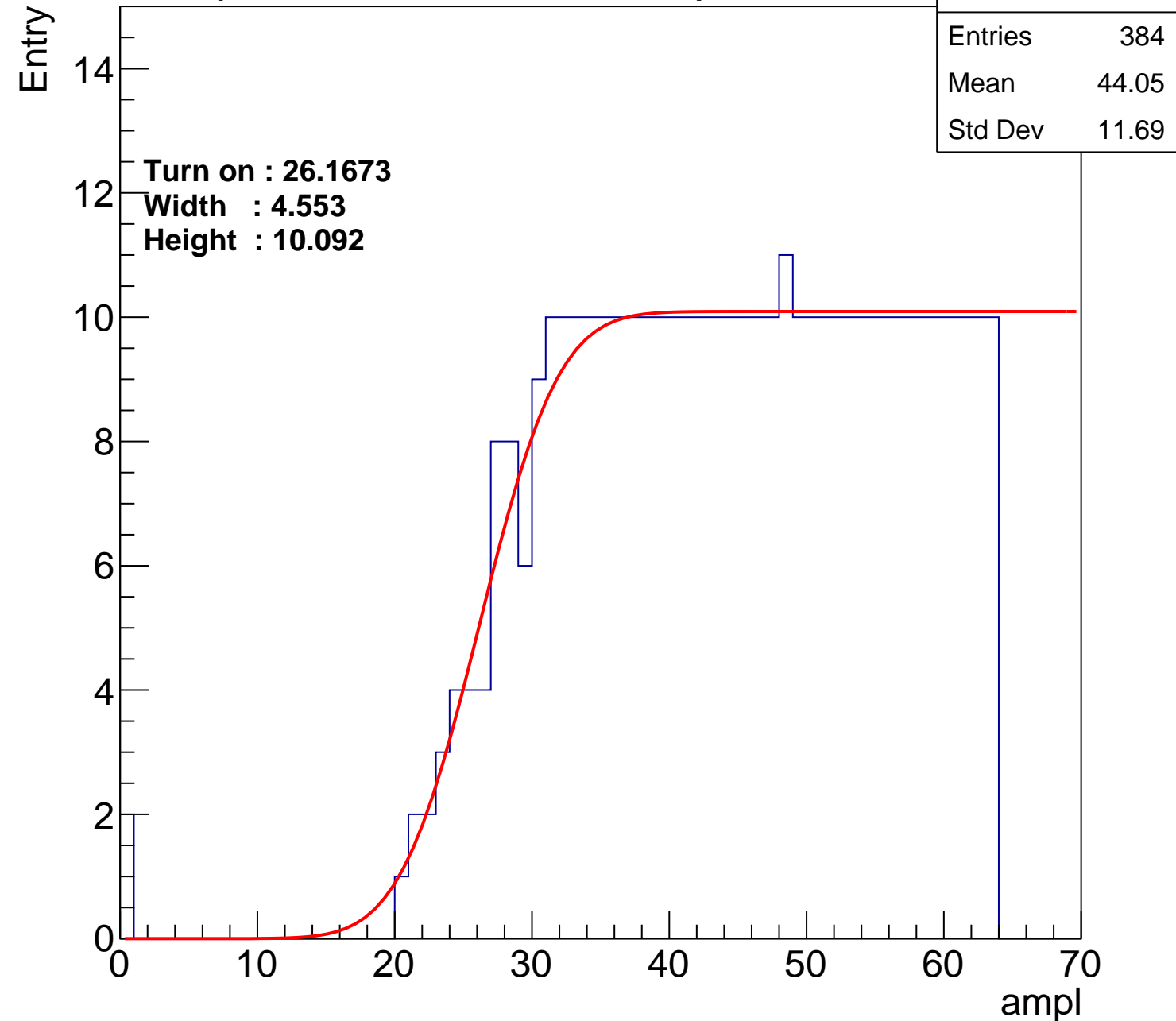
Width : 4.553

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch99

calib_packv5_042523_0143.root, FC#7, port C2

Entries	373
Mean	44.53
Std Dev	11.44

Turn on : 26.2988

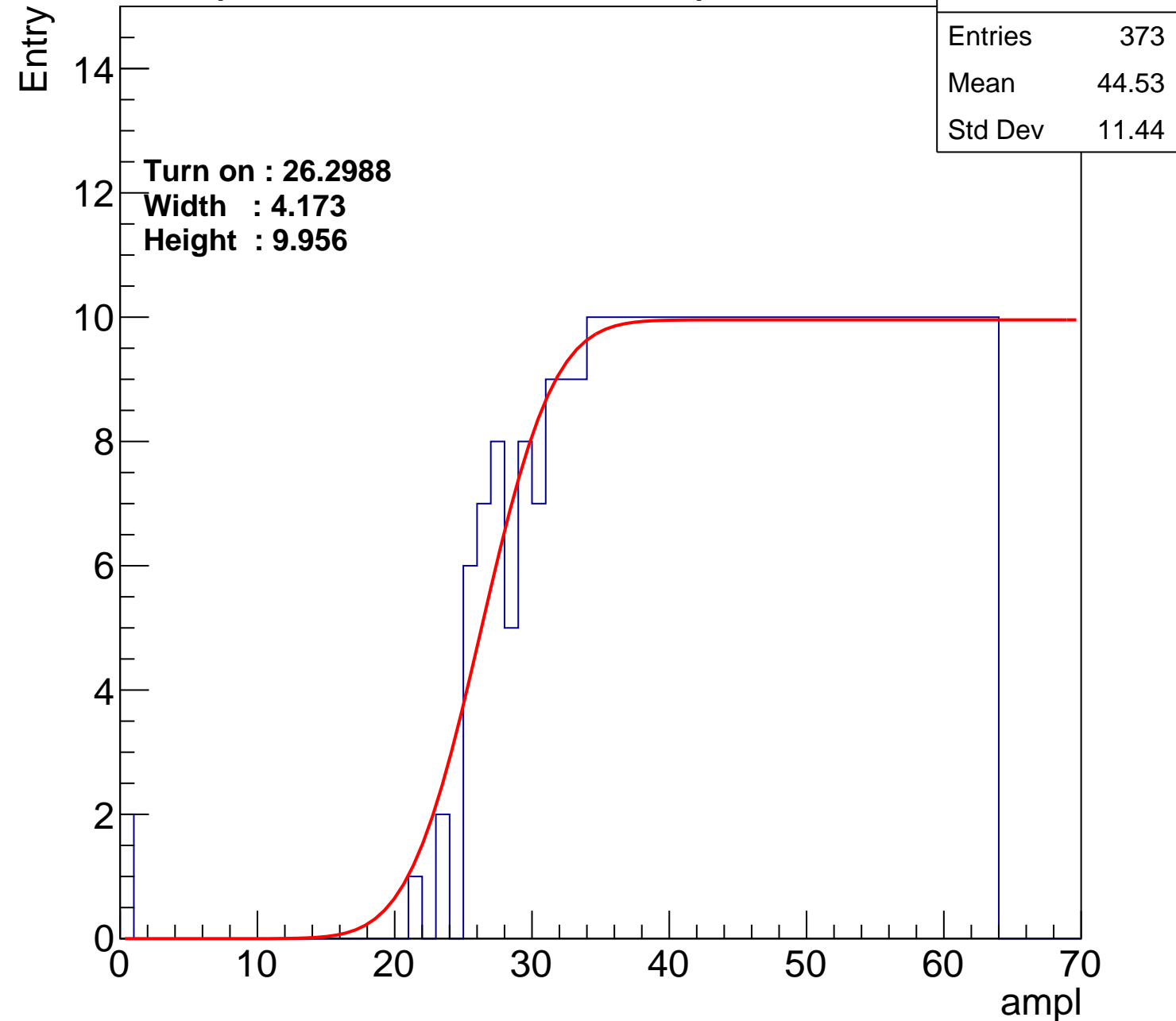
Width : 4.173

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch100

calib_packv5_042523_0143.root, FC#7, port C2

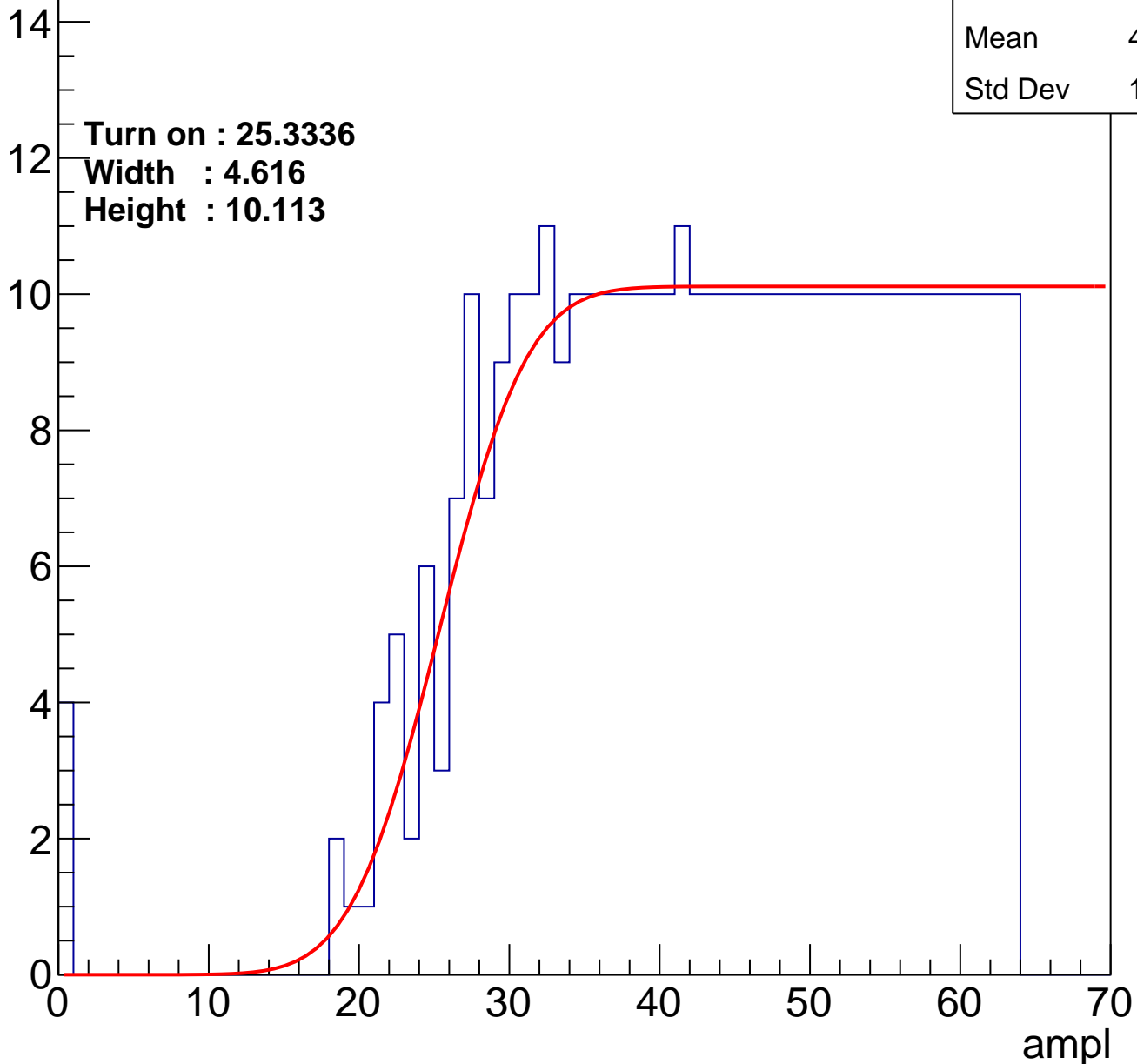
Entries	402
Mean	43.02
Std Dev	12.49

Turn on : 25.3336

Width : 4.616

Height : 10.113

Entry



B1L103S, U22-ch101

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.17
Std Dev	12.59

Turn on : 24.9316

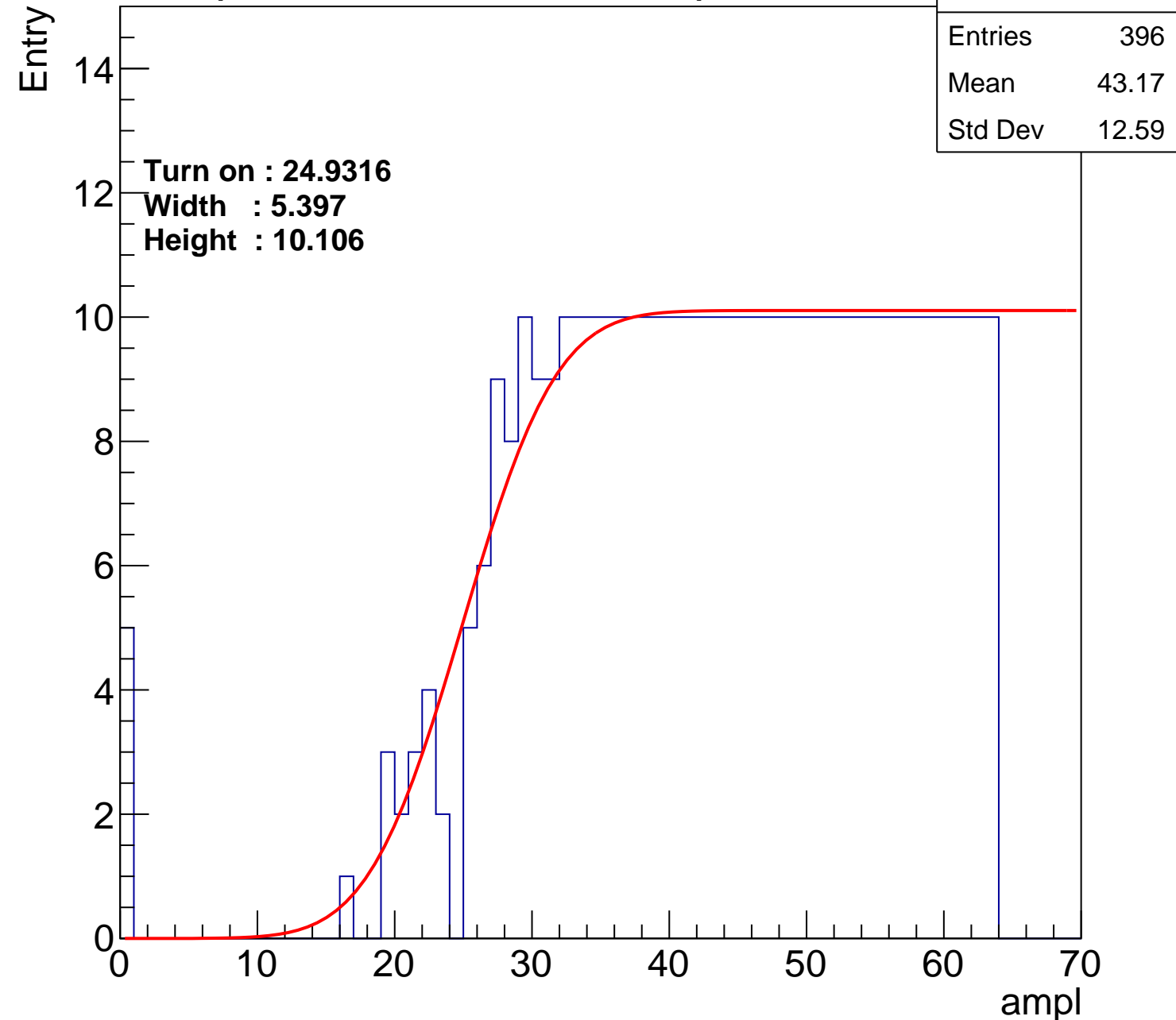
Width : 5.397

Height : 10.106

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch102

calib_packv5_042523_0143.root, FC#7, port C2

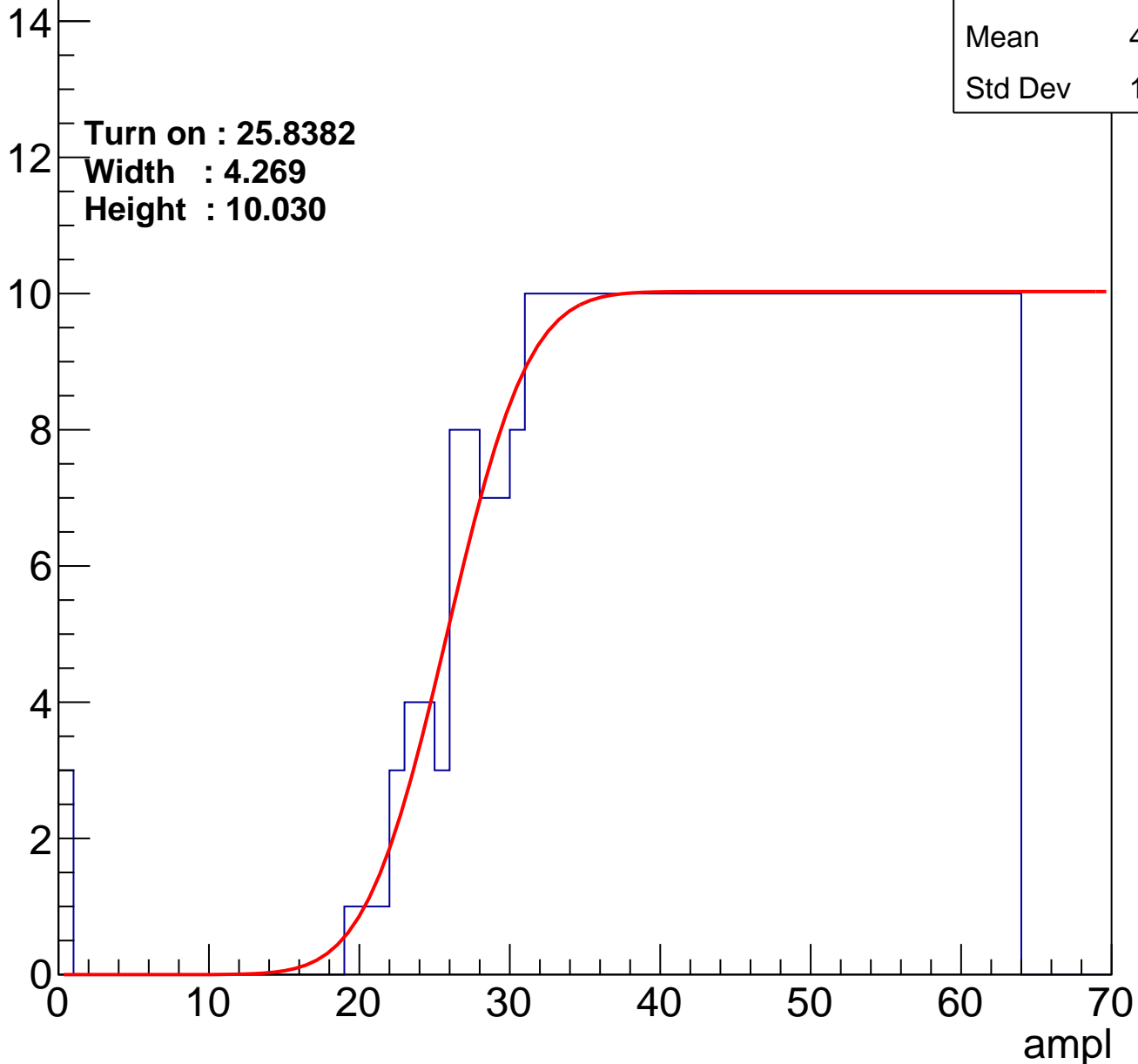
Entries	388
Mean	43.72
Std Dev	12.03

Turn on : 25.8382

Width : 4.269

Height : 10.030

Entry



B1L103S, U22-ch103

calib_packv5_042523_0143.root, FC#7, port C2

Entries	367
Mean	44.85
Std Dev	11.27

Turn on : 27.6072

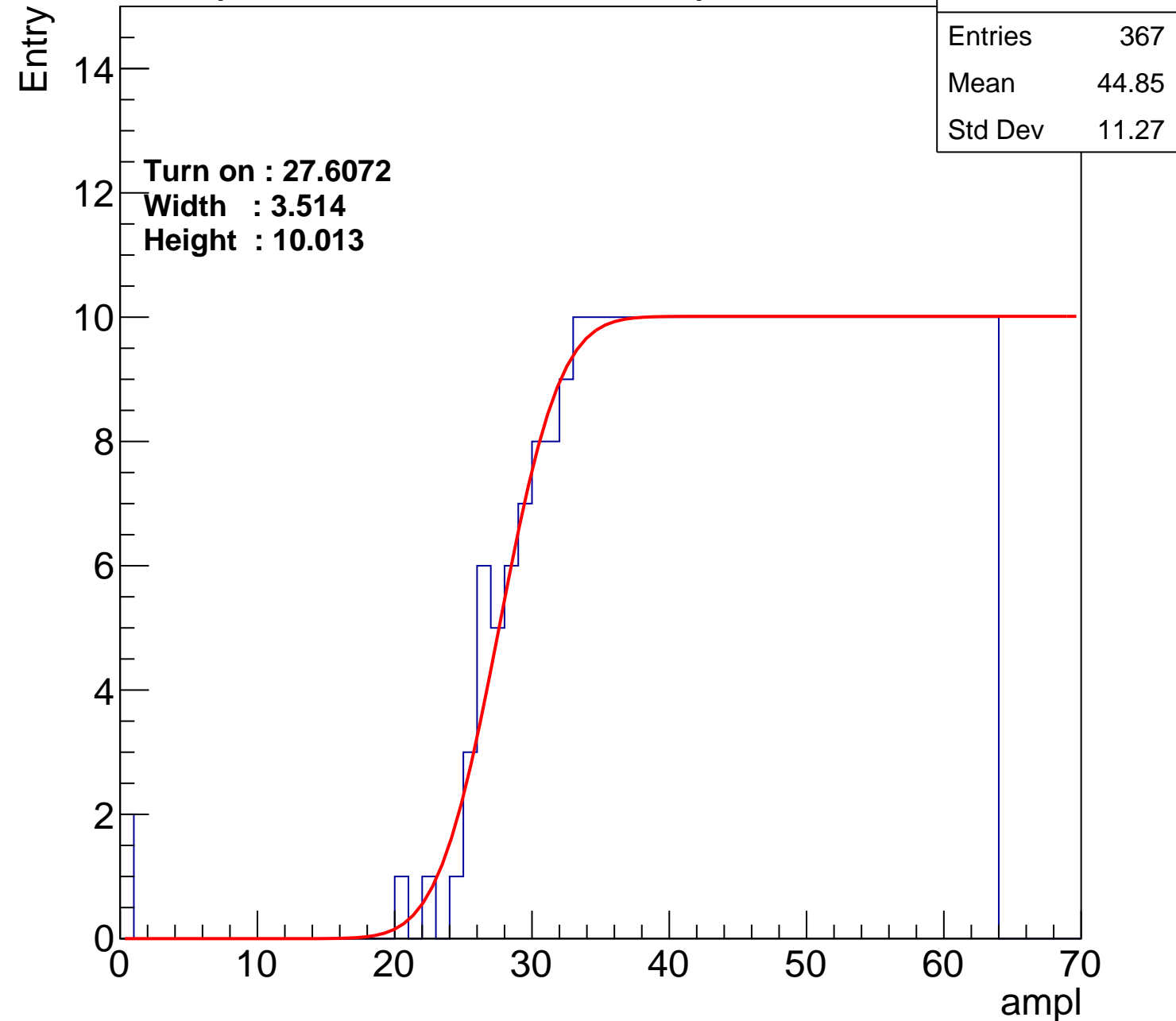
Width : 3.514

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch104

calib_packv5_042523_0143.root, FC#7, port C2

Entries	417
Mean	42.27
Std Dev	12.89

Turn on : 23.3623

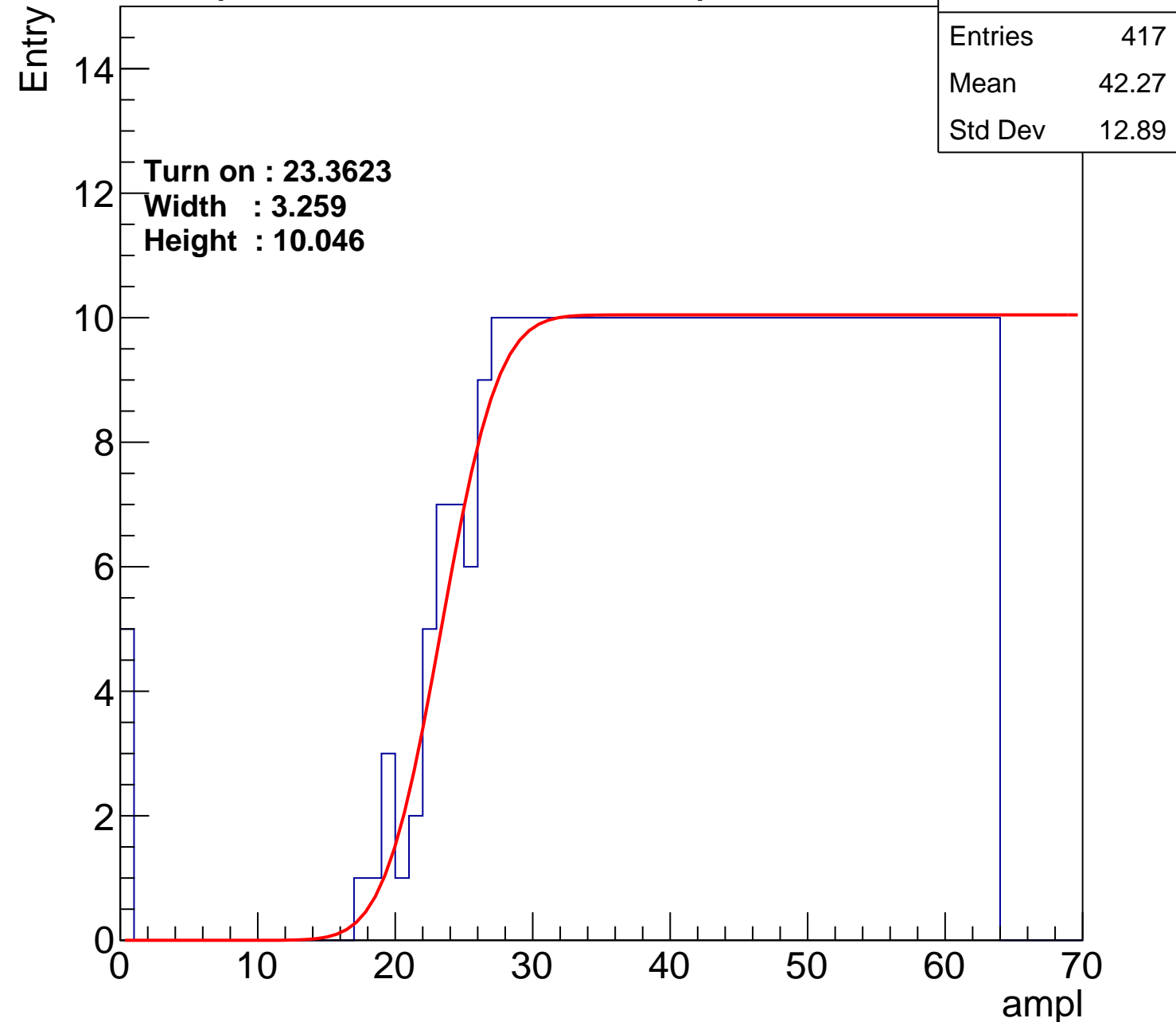
Width : 3.259

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch105

calib_packv5_042523_0143.root, FC#7, port C2

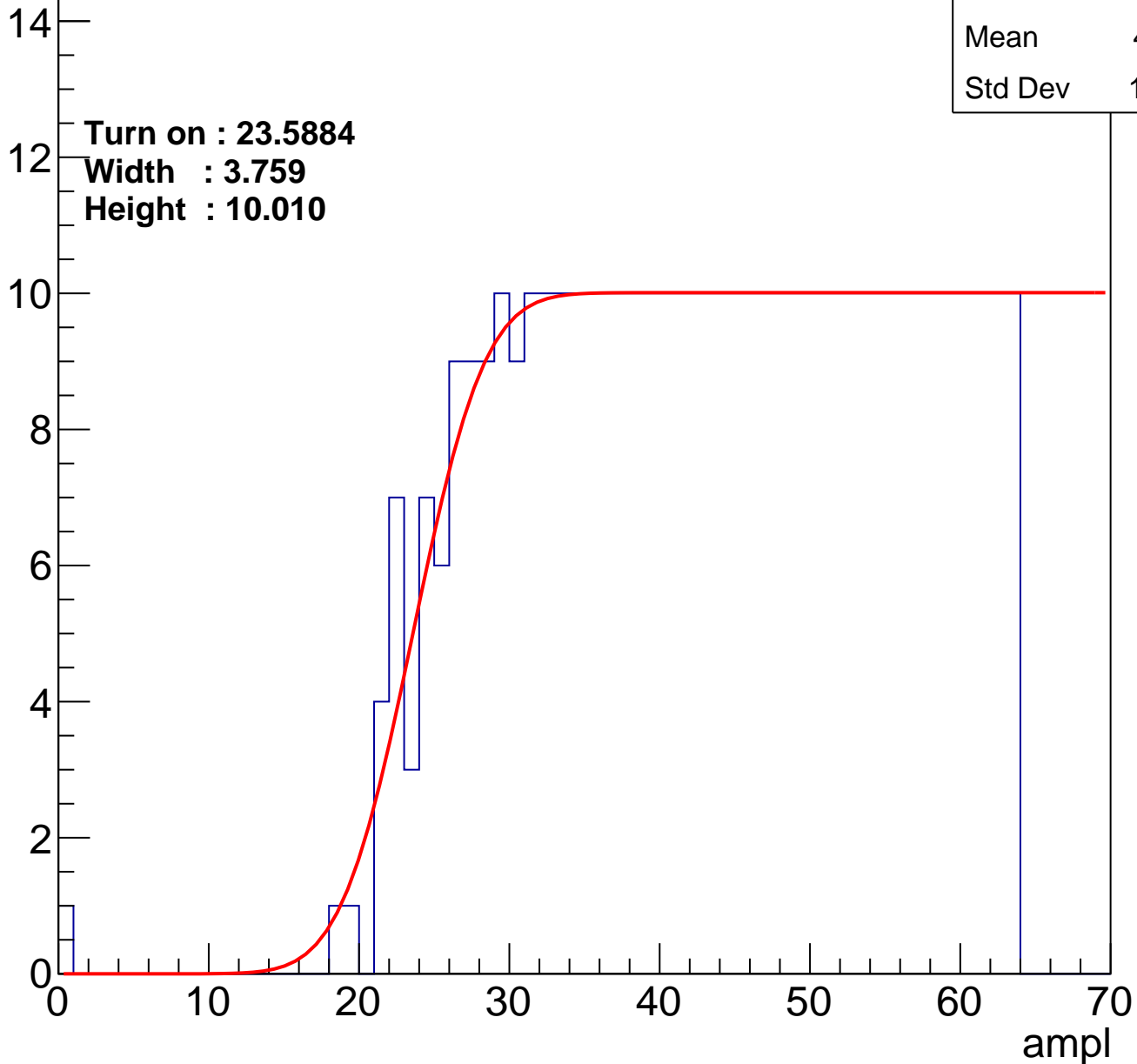
Entries	406
Mean	43.01
Std Dev	12.09

Turn on : 23.5884

Width : 3.759

Height : 10.010

Entry



B1L103S, U22-ch106

calib_packv5_042523_0143.root, FC#7, port C2

Entries	399
Mean	42.95
Std Dev	12.86

Turn on : 25.0270

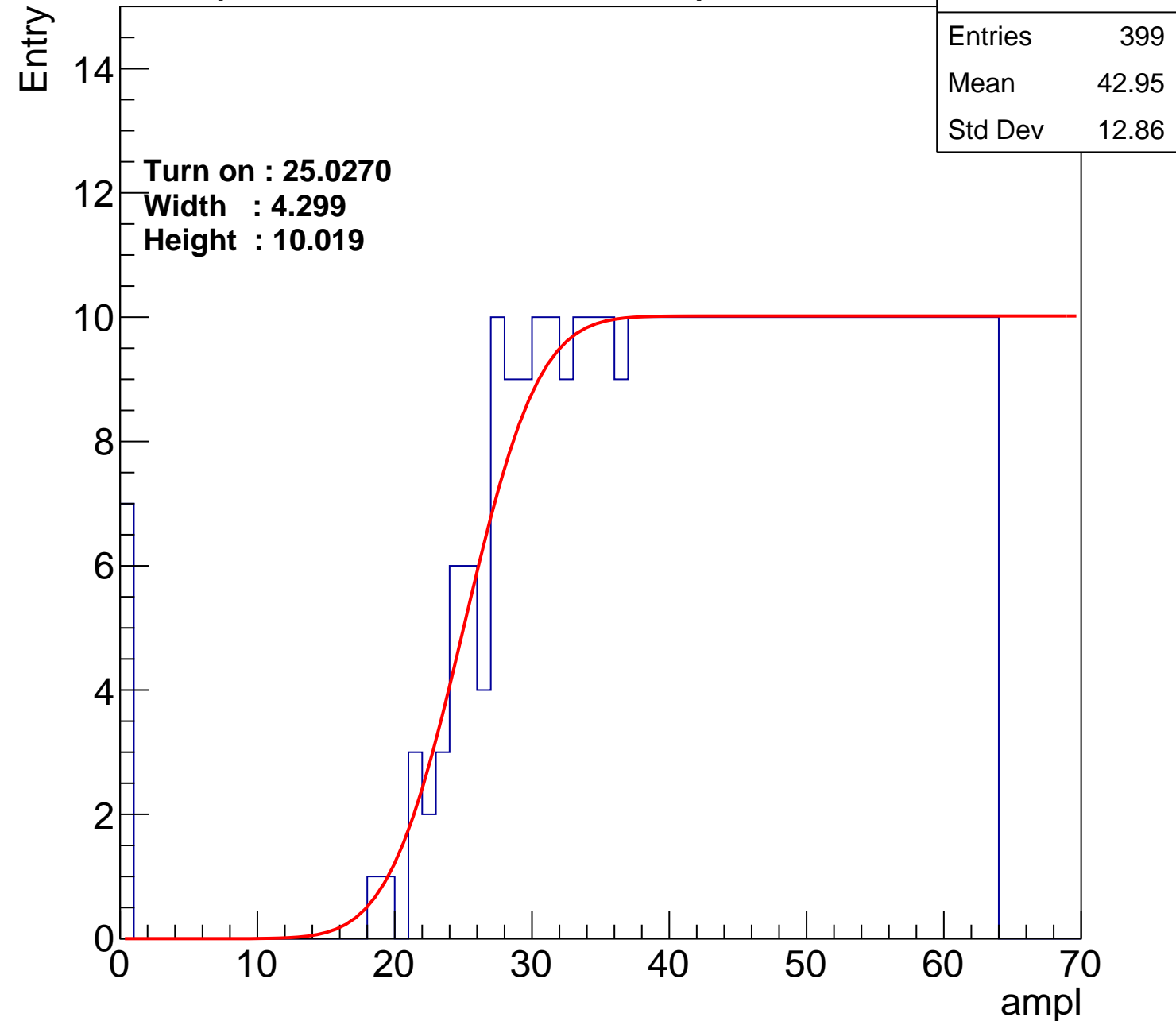
Width : 4.299

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch107

calib_packv5_042523_0143.root, FC#7, port C2

Entries	382
Mean	43.98
Std Dev	12.01

Turn on : 26.5212

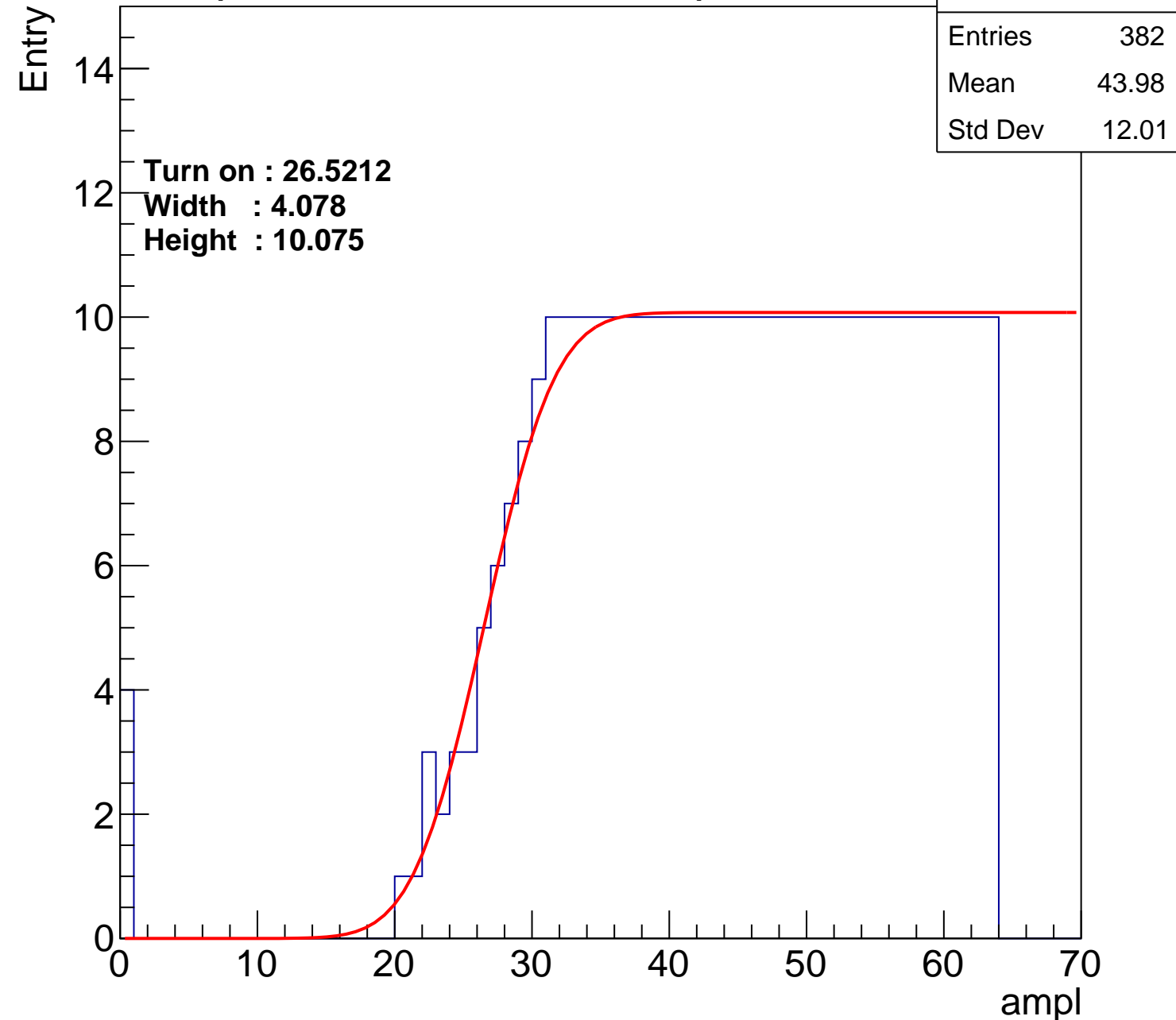
Width : 4.078

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch108

calib_packv5_042523_0143.root, FC#7, port C2

Entries	411
Mean	42.74
Std Dev	12.31

Turn on : 23.5378

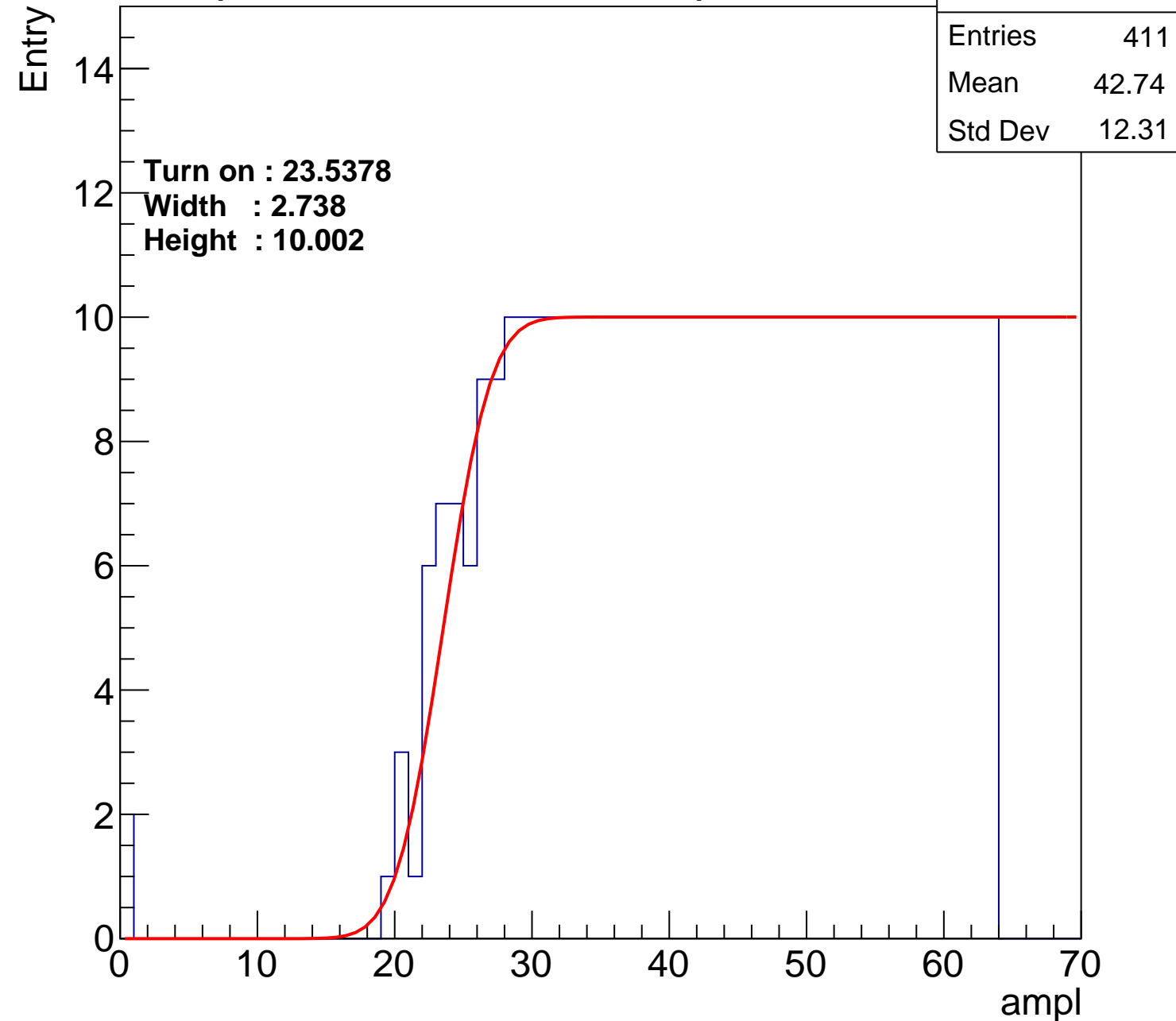
Width : 2.738

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch109

calib_packv5_042523_0143.root, FC#7, port C2

Entries	385
Mean	44.04
Std Dev	11.56

Turn on : 26.5307

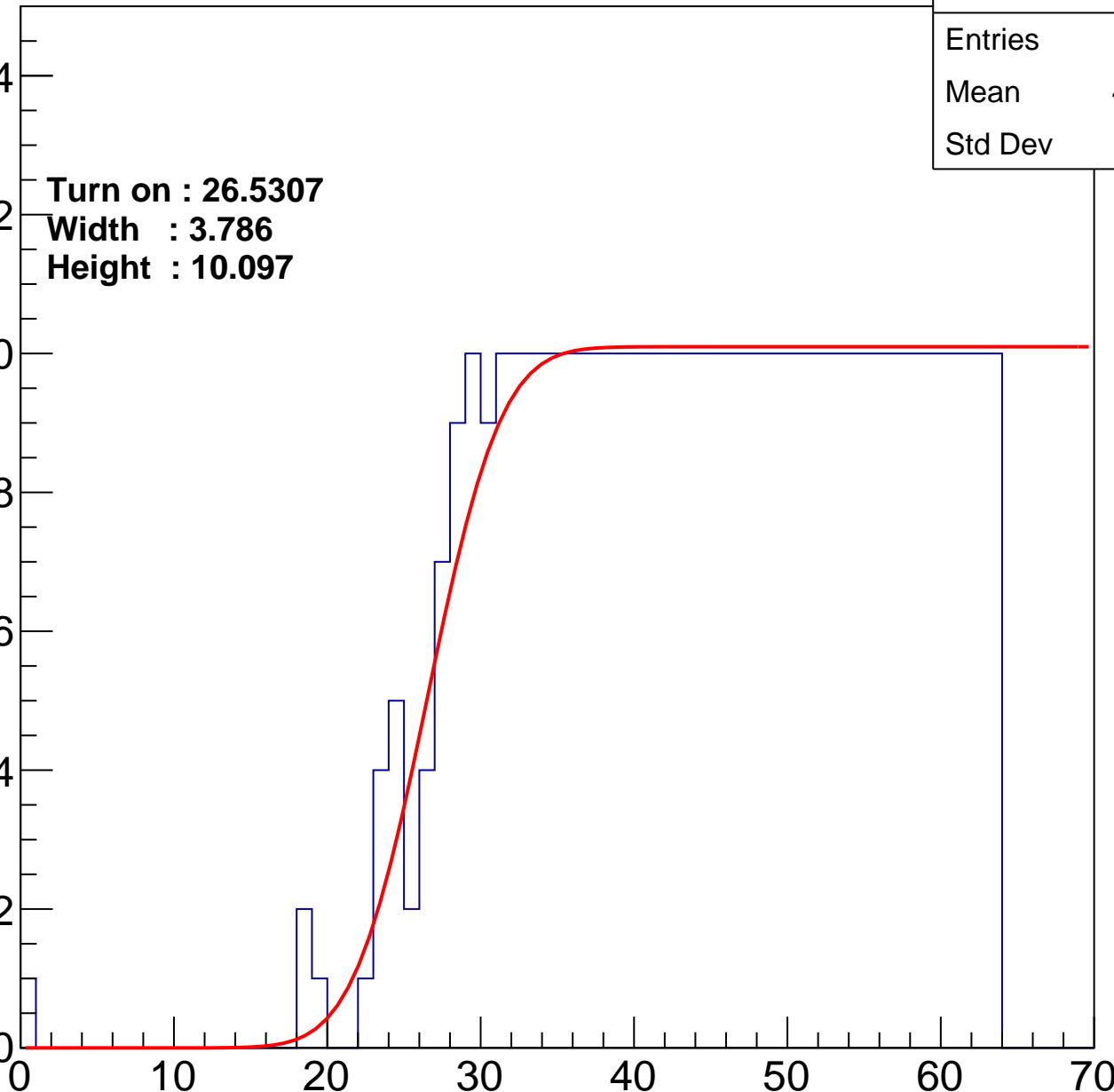
Width : 3.786

Height : 10.097

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch110

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	43.97
Std Dev	11.73

Turn on : 26.4957

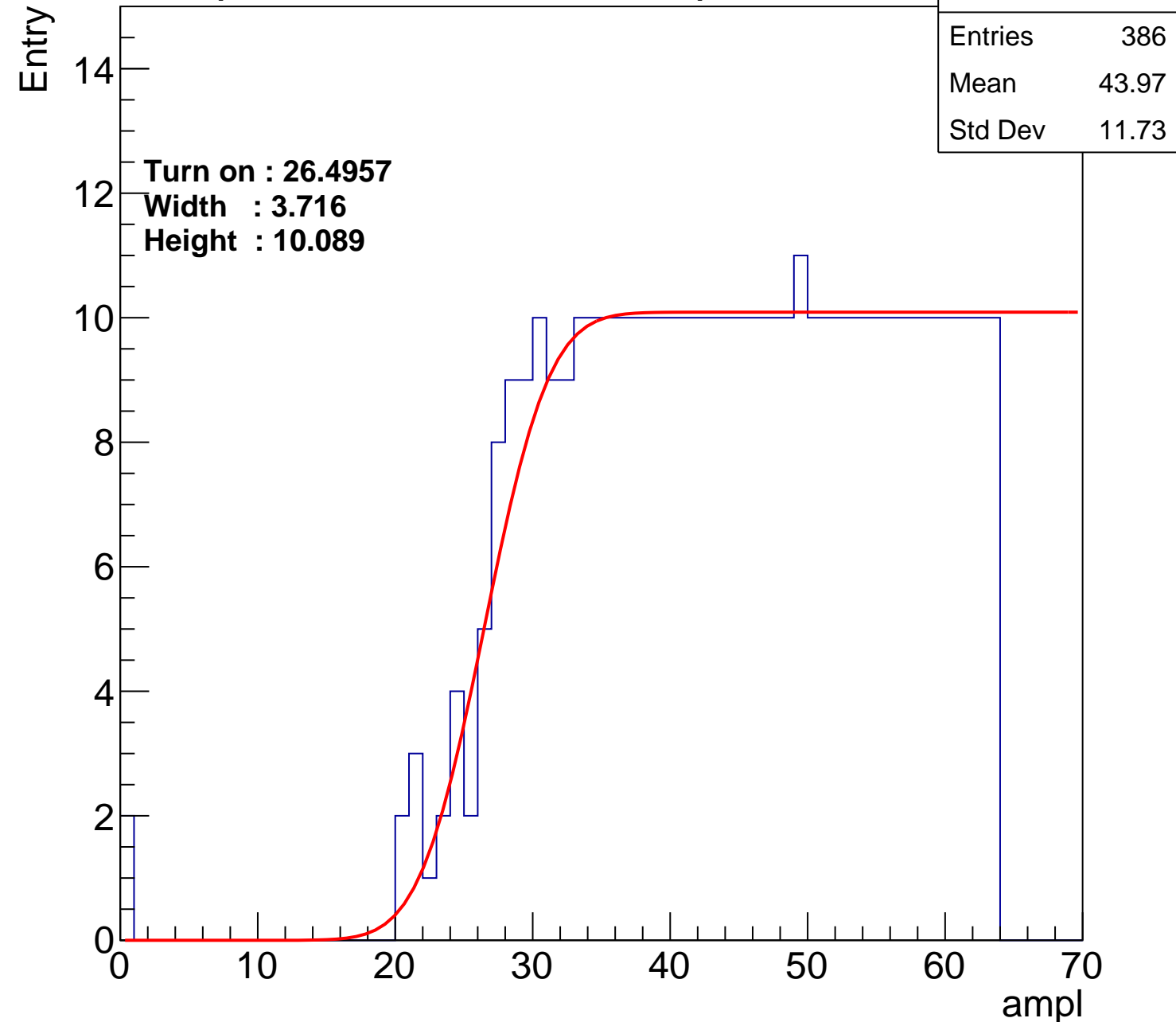
Width : 3.716

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch111

calib_packv5_042523_0143.root, FC#7, port C2

Entries	384
Mean	43.81
Std Dev	12.3

Turn on : 26.6808

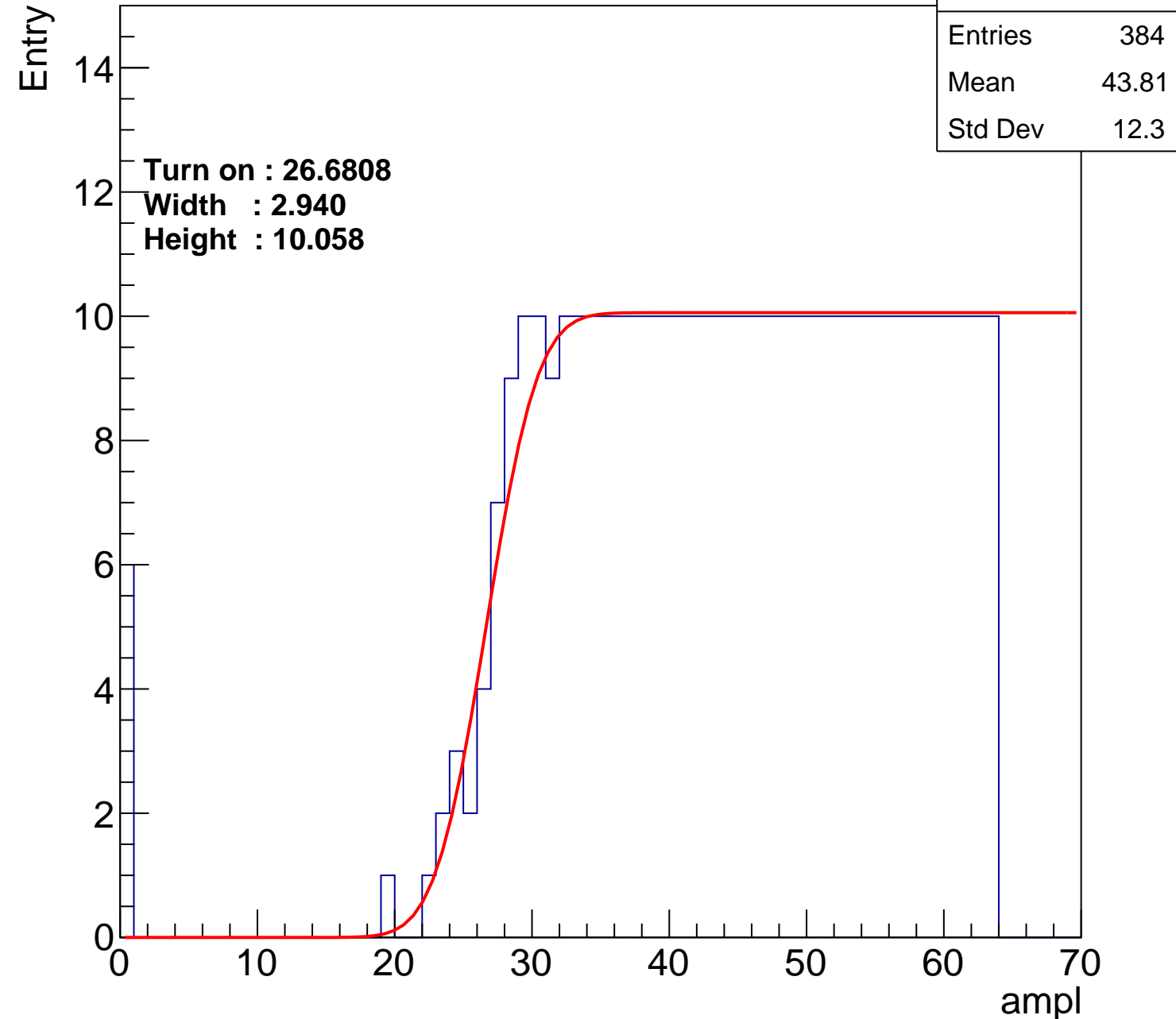
Width : 2.940

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch112

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.76
Std Dev	11.78

Turn on : 24.9149

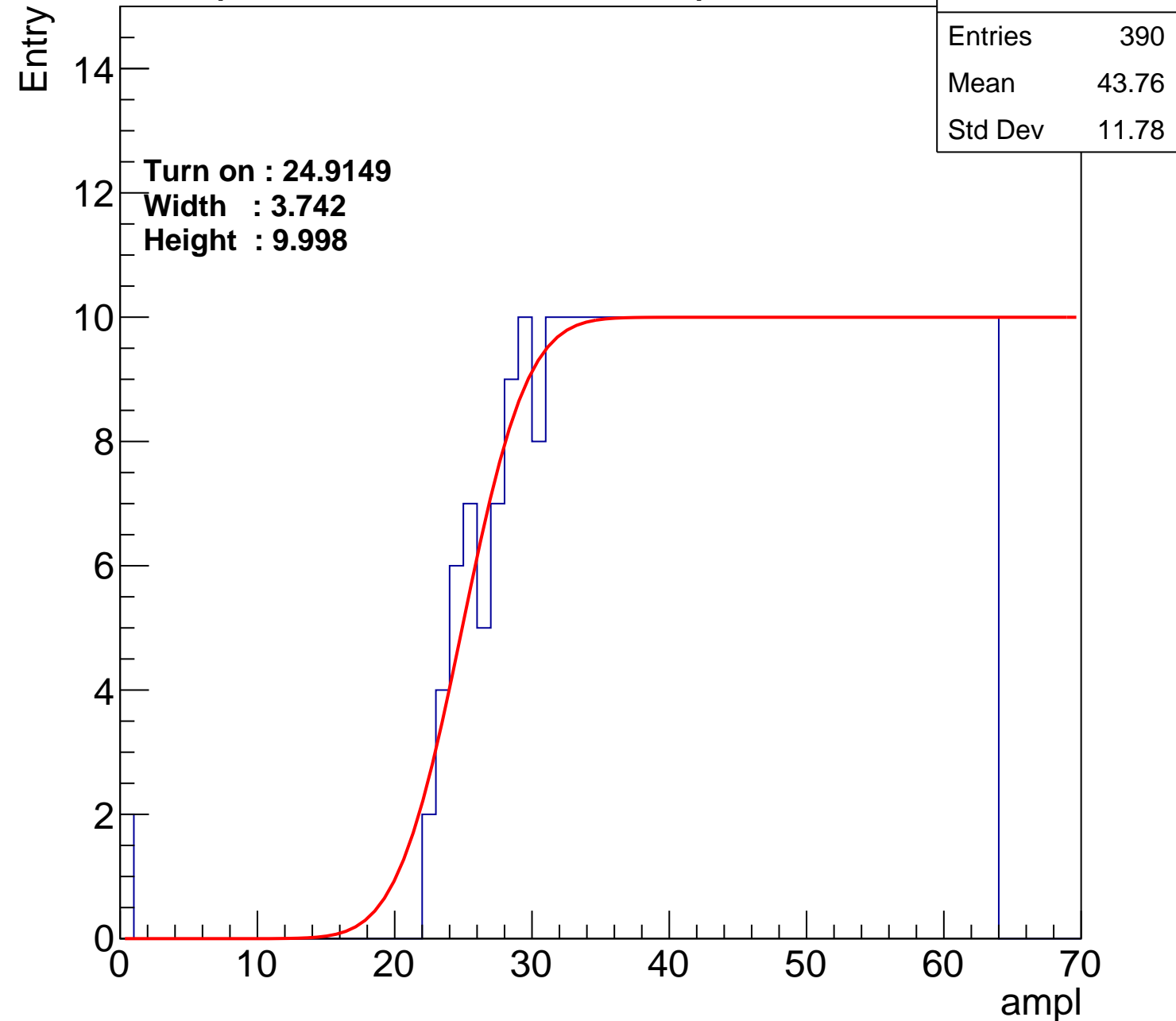
Width : 3.742

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch113

calib_packv5_042523_0143.root, FC#7, port C2

Entries	370
Mean	44.64
Std Dev	11.54

Turn on : 27.2904

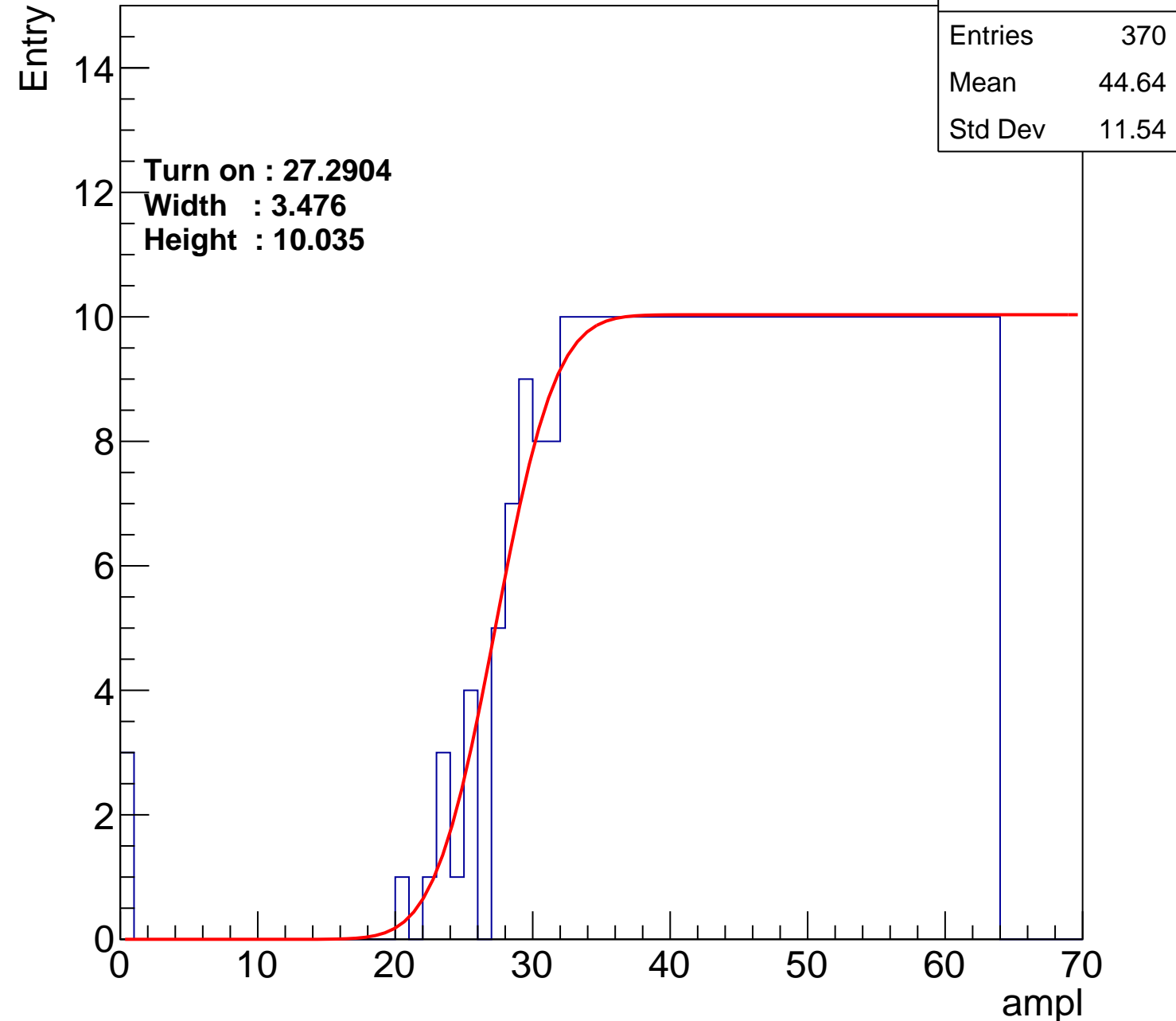
Width : 3.476

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch114

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.51
Std Dev	12.07

Turn on : 24.8643

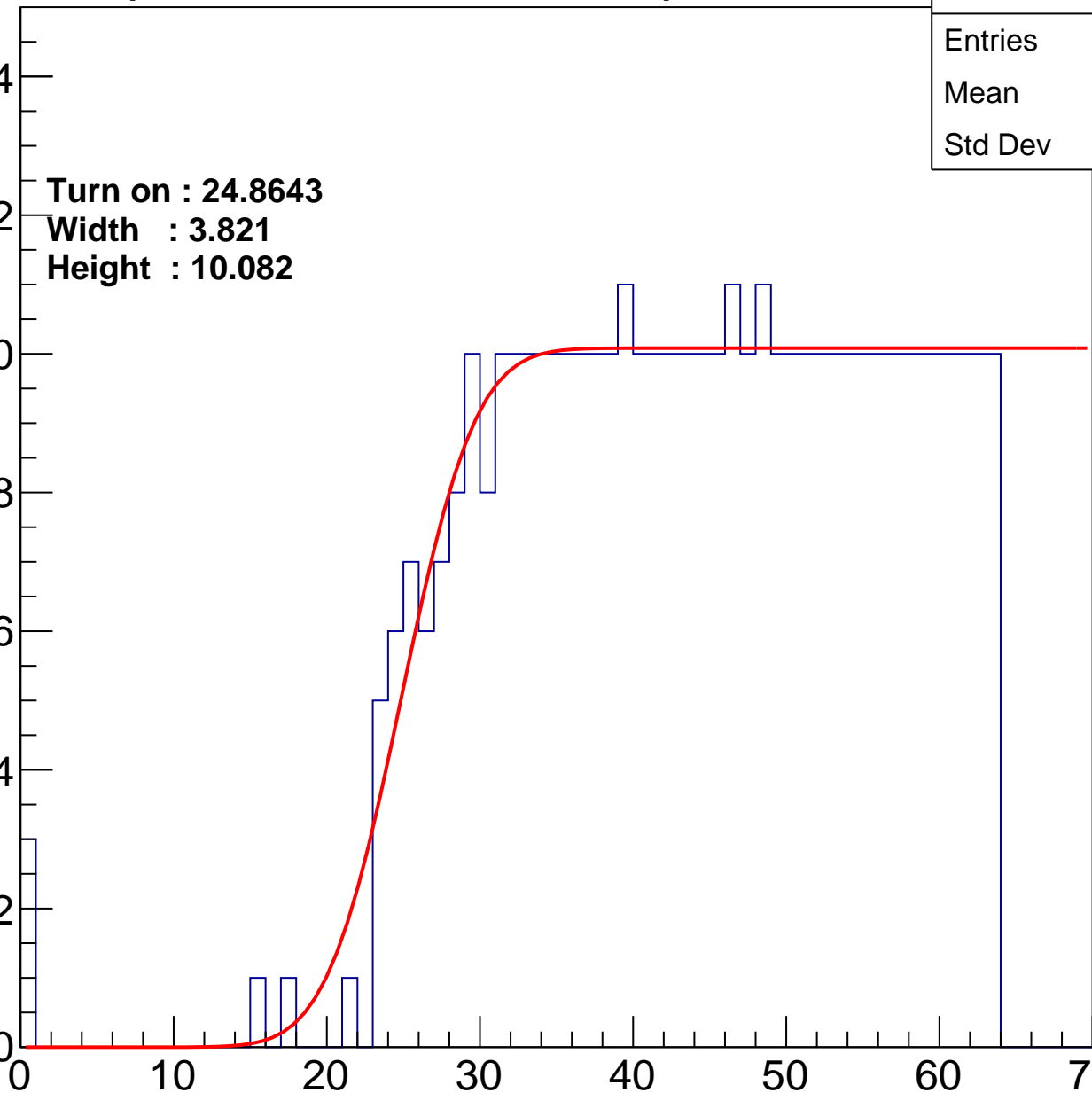
Width : 3.821

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch115

calib_packv5_042523_0143.root, FC#7, port C2

Entries	386
Mean	44.18
Std Dev	11.59

Turn on : 25.9605

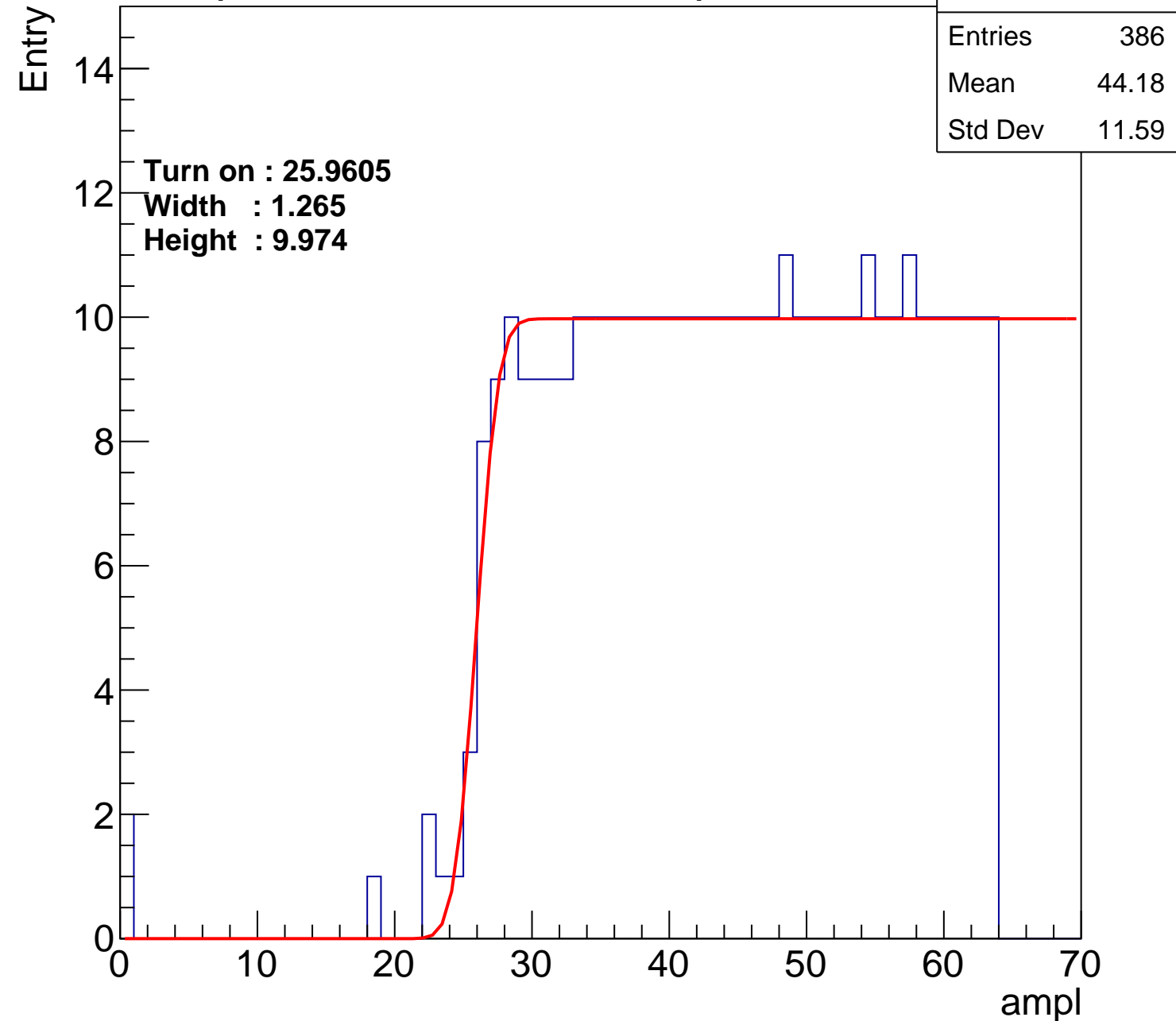
Width : 1.265

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch116

calib_packv5_042523_0143.root, FC#7, port C2

Entries	417
Mean	42.44
Std Dev	12.57

Turn on : 23.0697

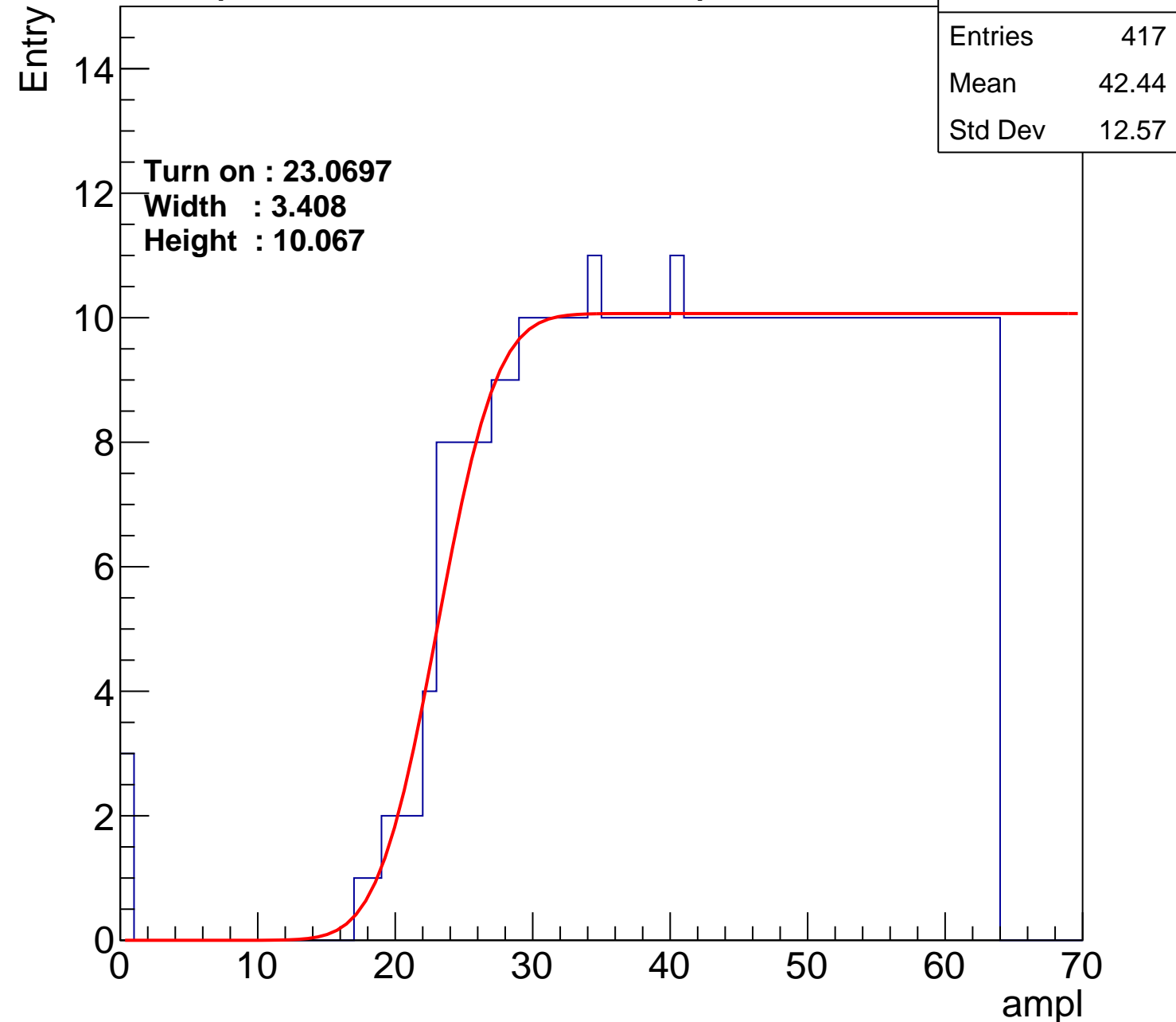
Width : 3.408

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch117

calib_packv5_042523_0143.root, FC#7, port C2

Entries	396
Mean	43.24
Std Dev	12.54

Turn on : 26.5755

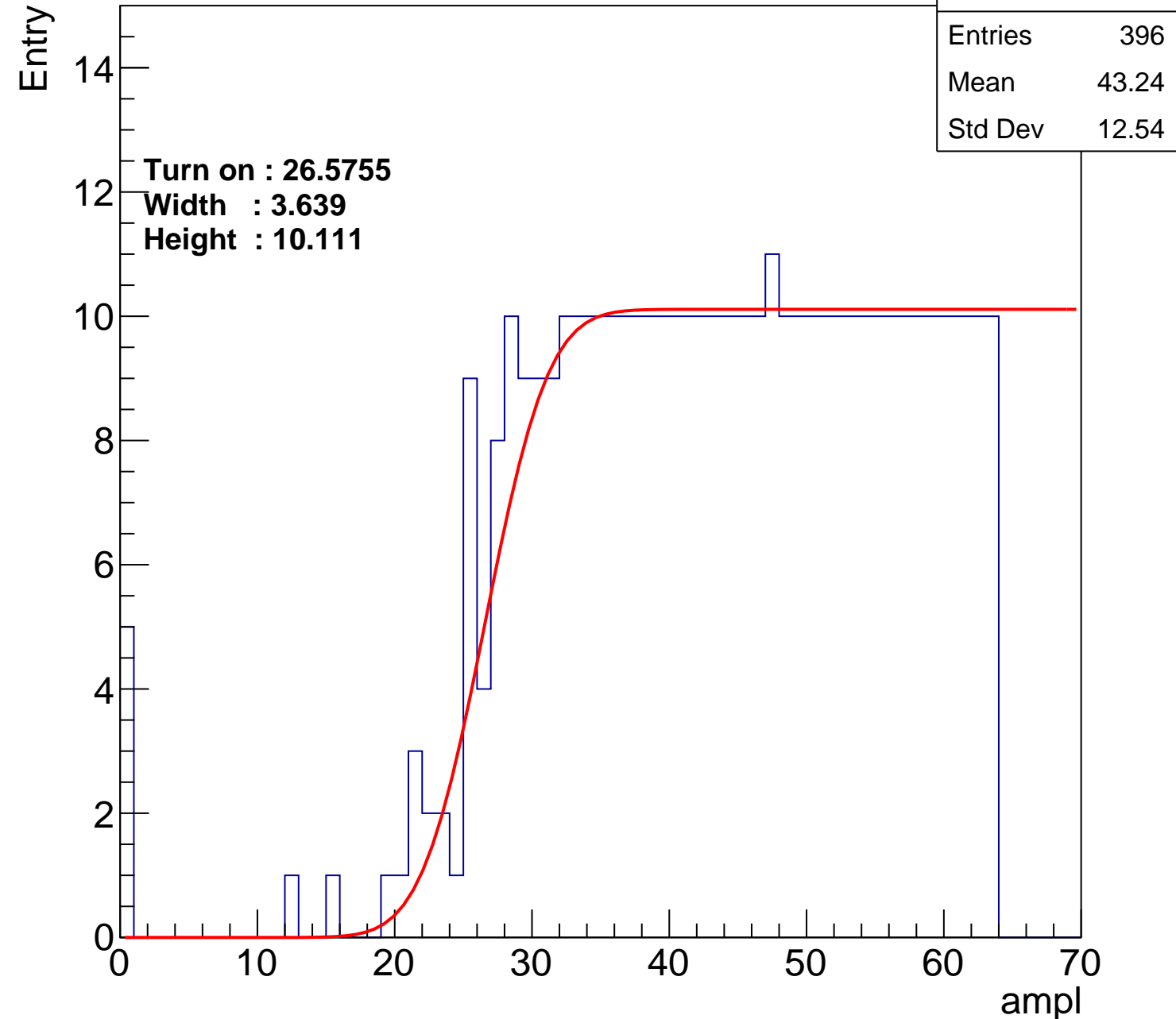
Width : 3.639

Height : 10.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch118

calib_packv5_042523_0143.root, FC#7, port C2

Entries	400
Mean	43.2
Std Dev	12.17

Turn on : 25.0687

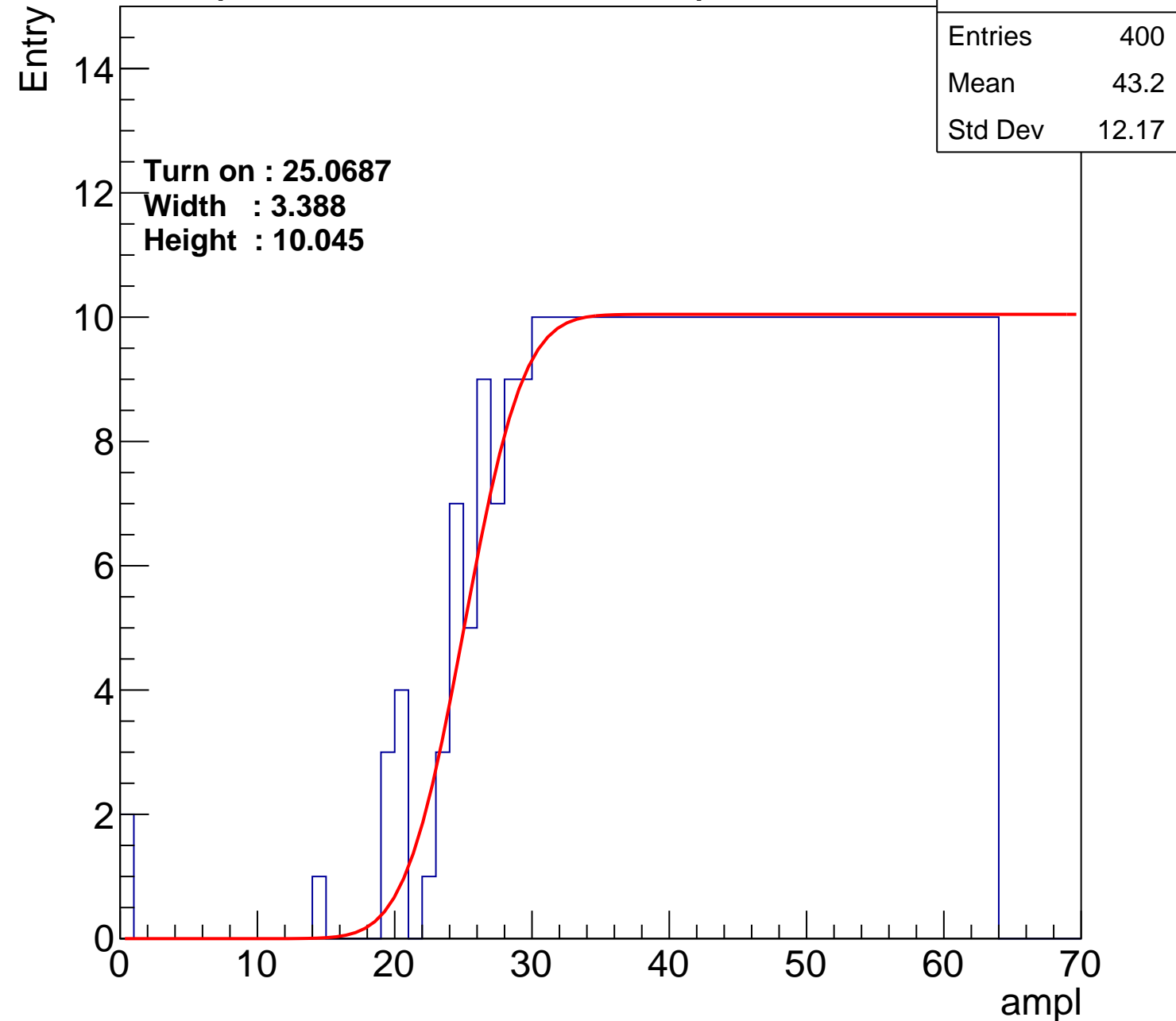
Width : 3.388

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch119

calib_packv5_042523_0143.root, FC#7, port C2

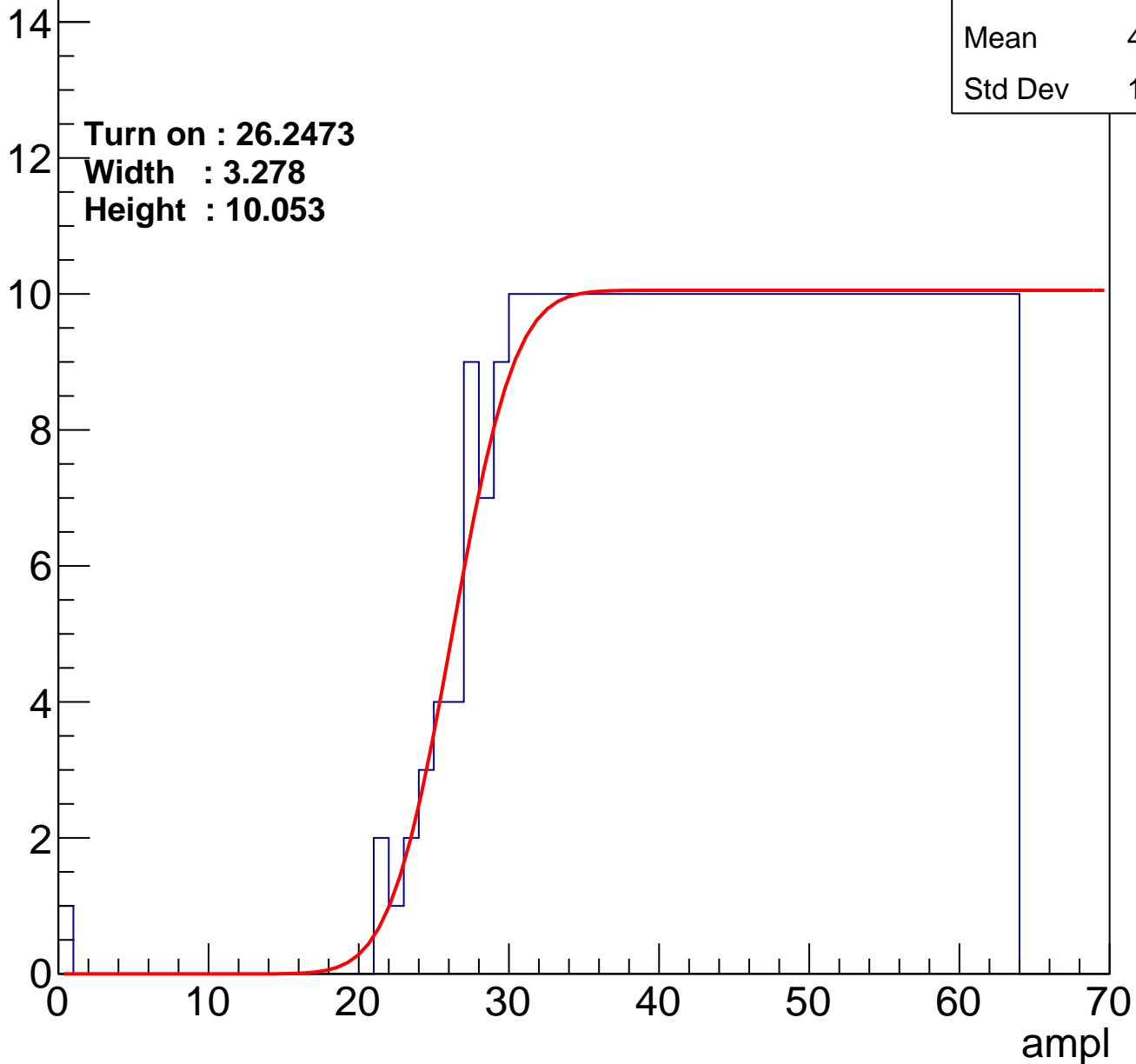
Entries	382
Mean	44.23
Std Dev	11.39

Turn on : 26.2473

Width : 3.278

Height : 10.053

Entry



B1L103S, U22-ch120

calib_packv5_042523_0143.root, FC#7, port C2

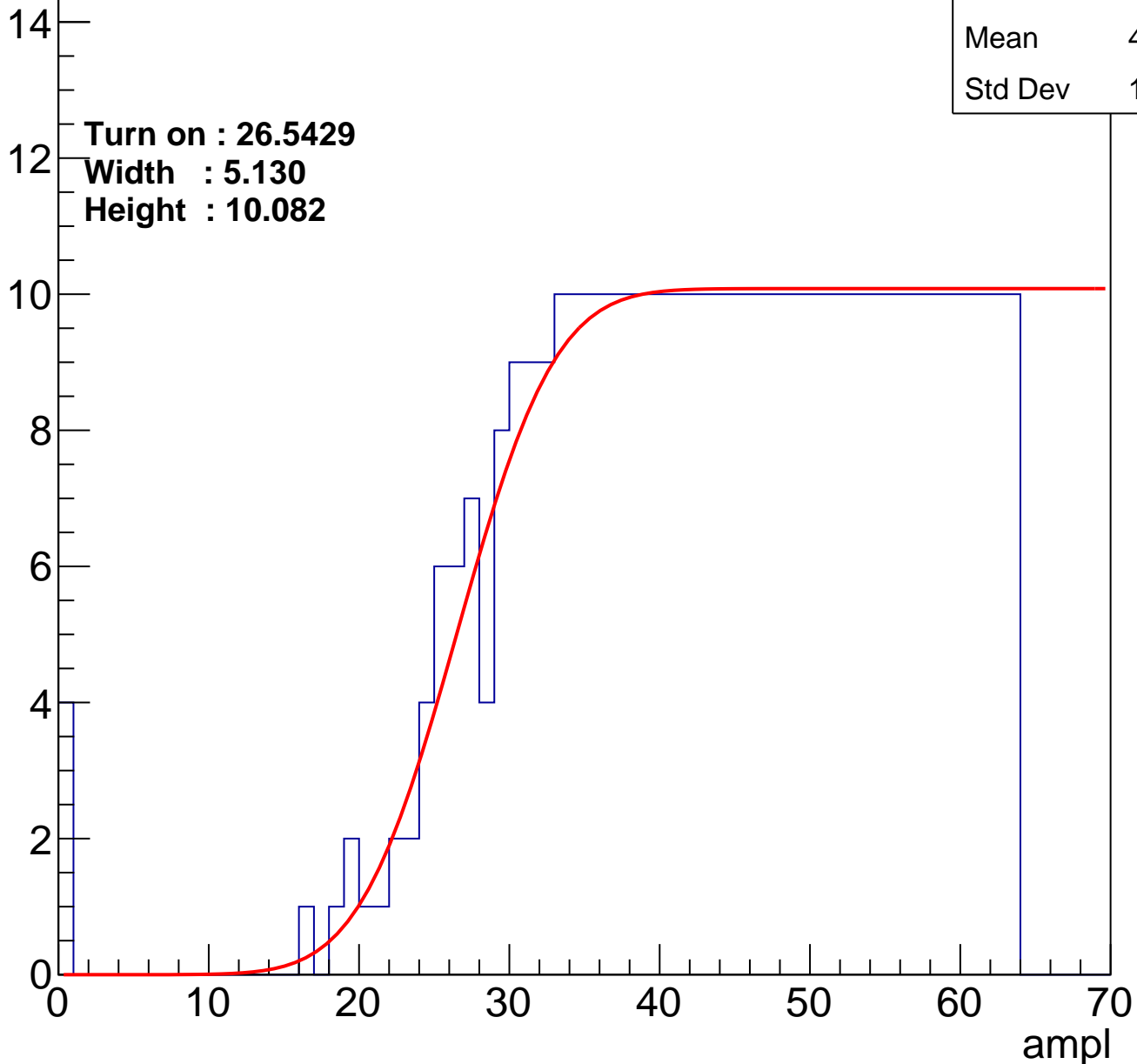
Entries	386
Mean	43.67
Std Dev	12.29

Turn on : 26.5429

Width : 5.130

Height : 10.082

Entry



B1L103S, U22-ch121

calib_packv5_042523_0143.root, FC#7, port C2

Entries	390
Mean	43.68
Std Dev	11.97

Turn on : 25.1992

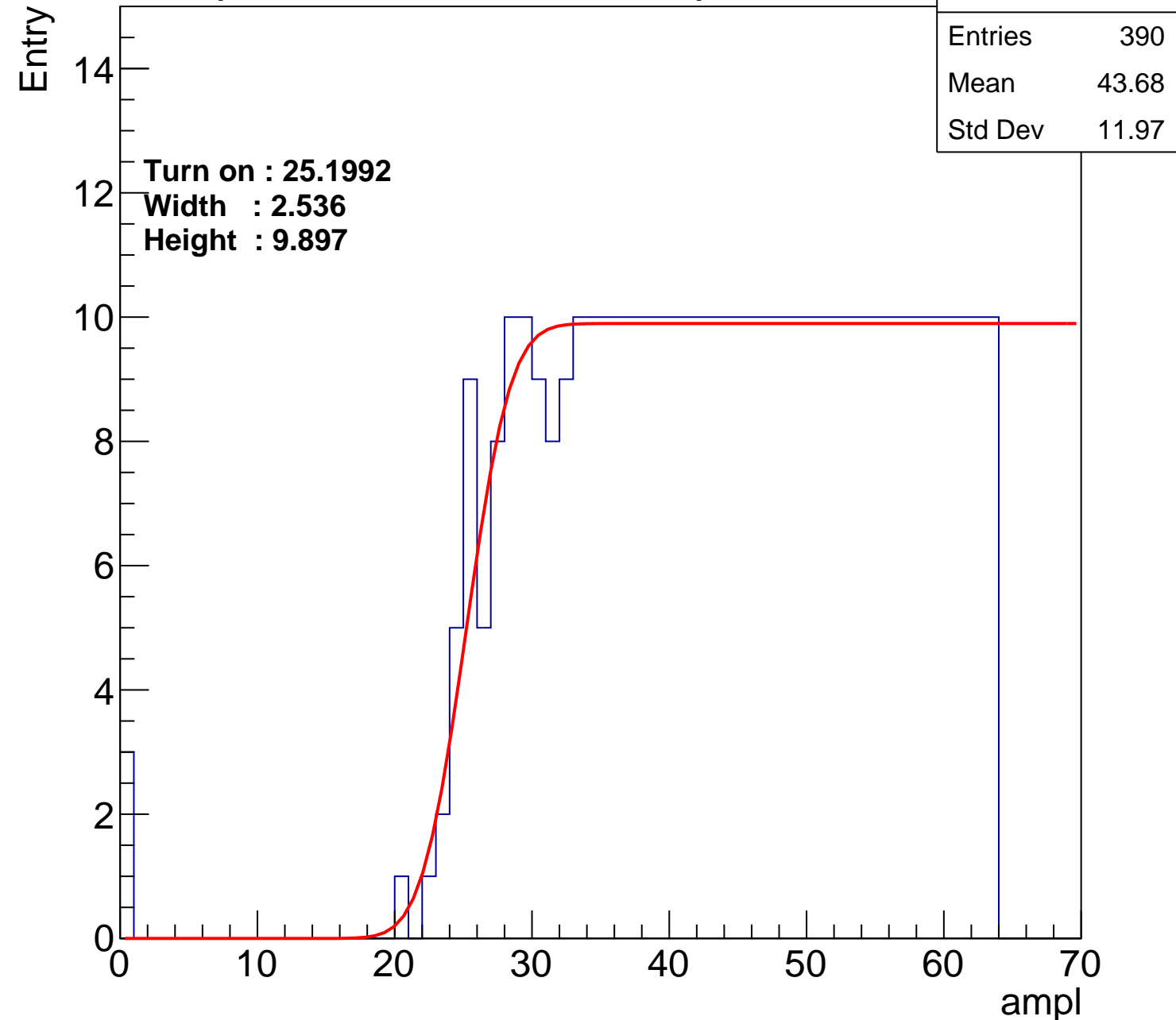
Width : 2.536

Height : 9.897

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch122

calib_packv5_042523_0143.root, FC#7, port C2

Entries	397
Mean	43.24
Std Dev	12.42

Turn on : 25.2857

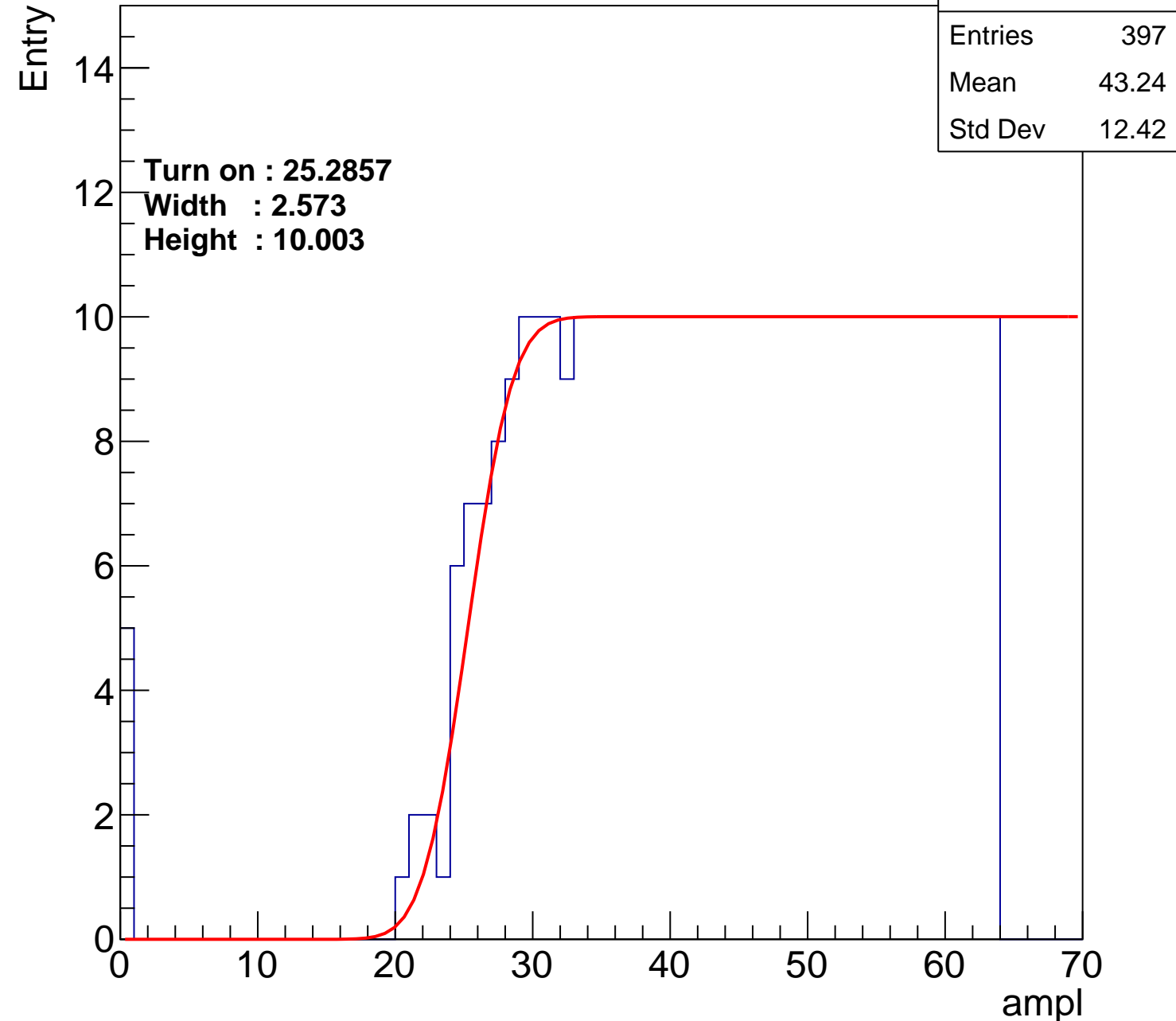
Width : 2.573

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch123

calib_packv5_042523_0143.root, FC#7, port C2

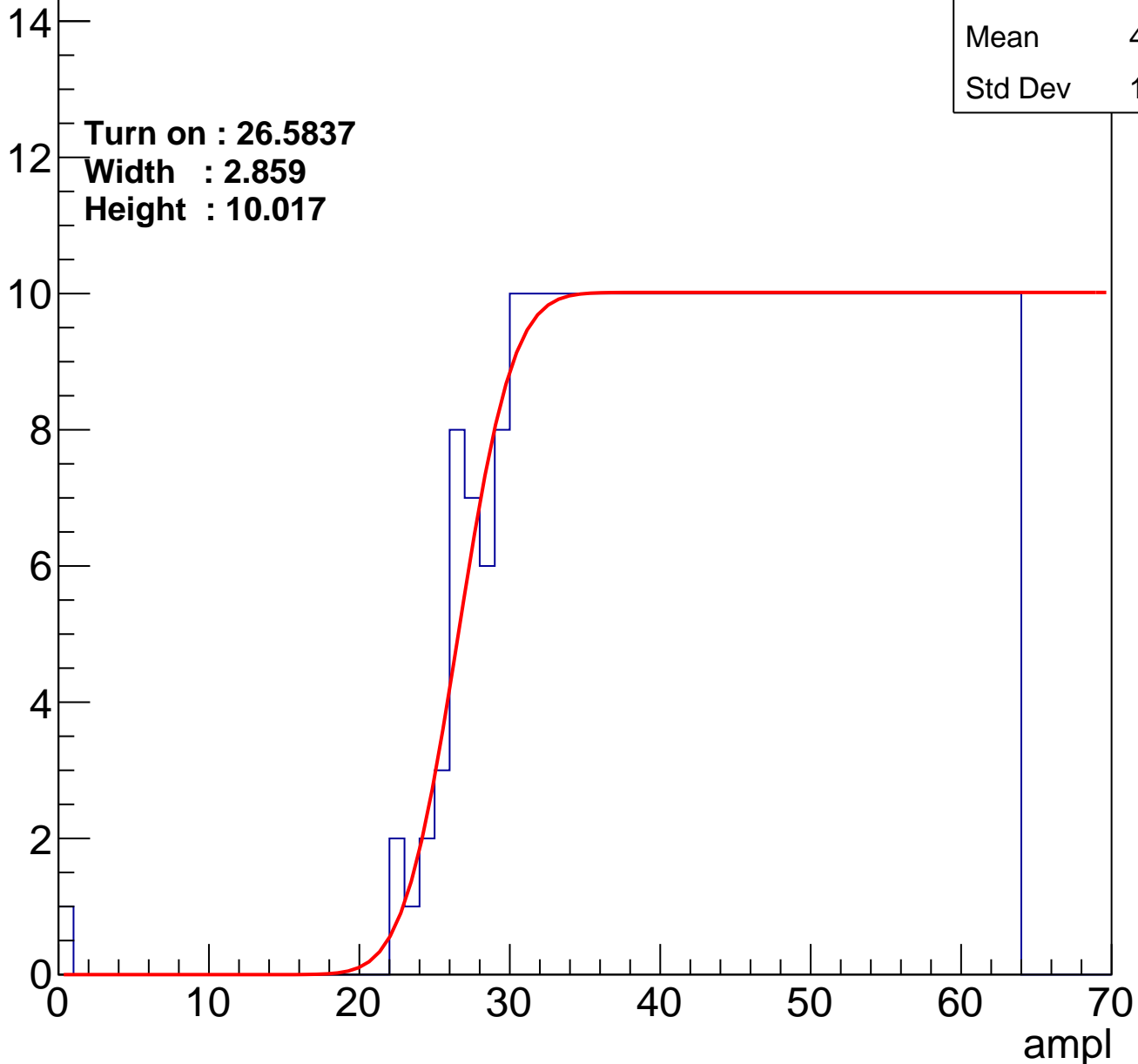
Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.5837

Width : 2.859

Height : 10.017

Entry



B1L103S, U22-ch124

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	42.89
Std Dev	12.41

Turn on : 24.5770

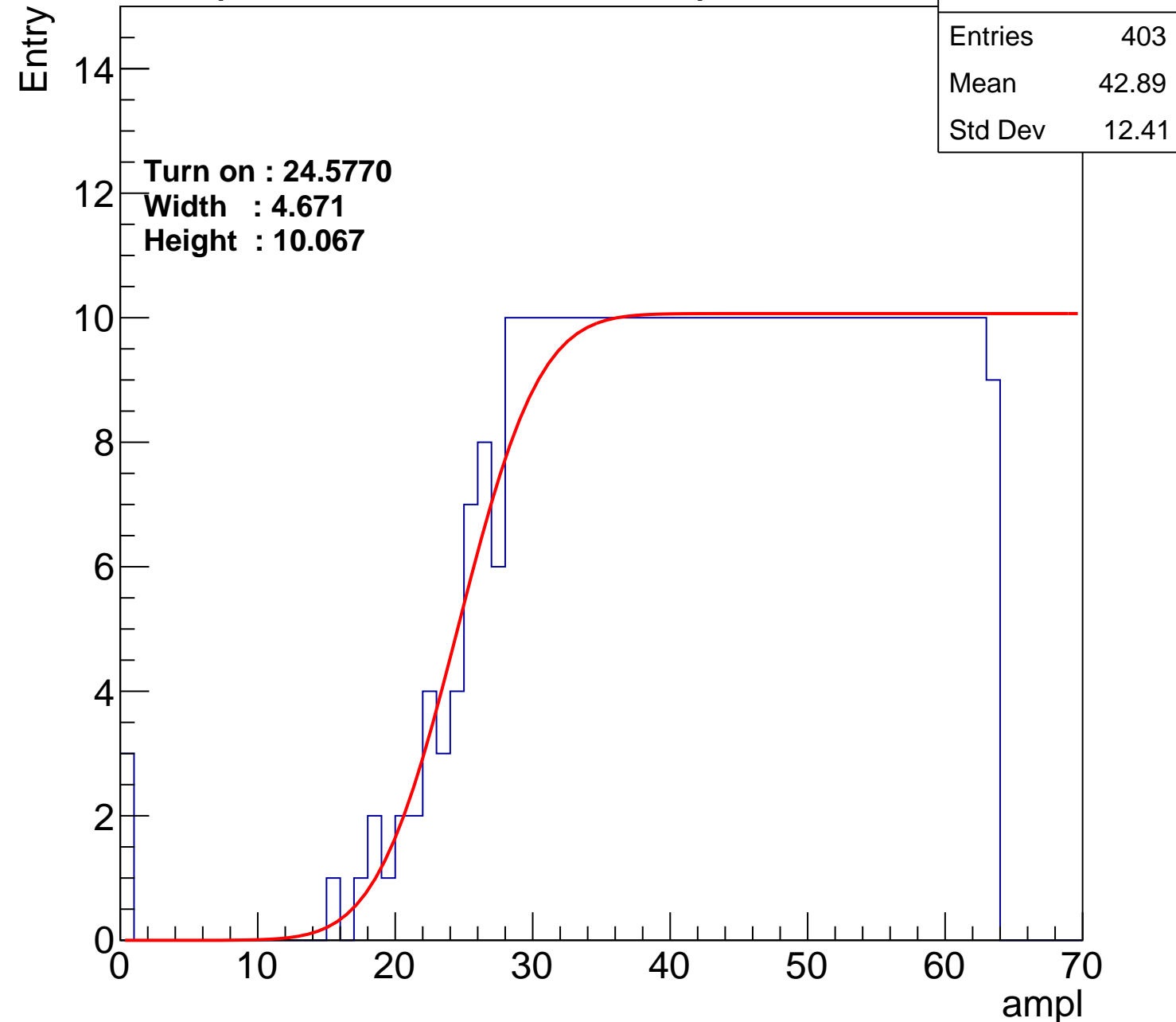
Width : 4.671

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch125

calib_packv5_042523_0143.root, FC#7, port C2

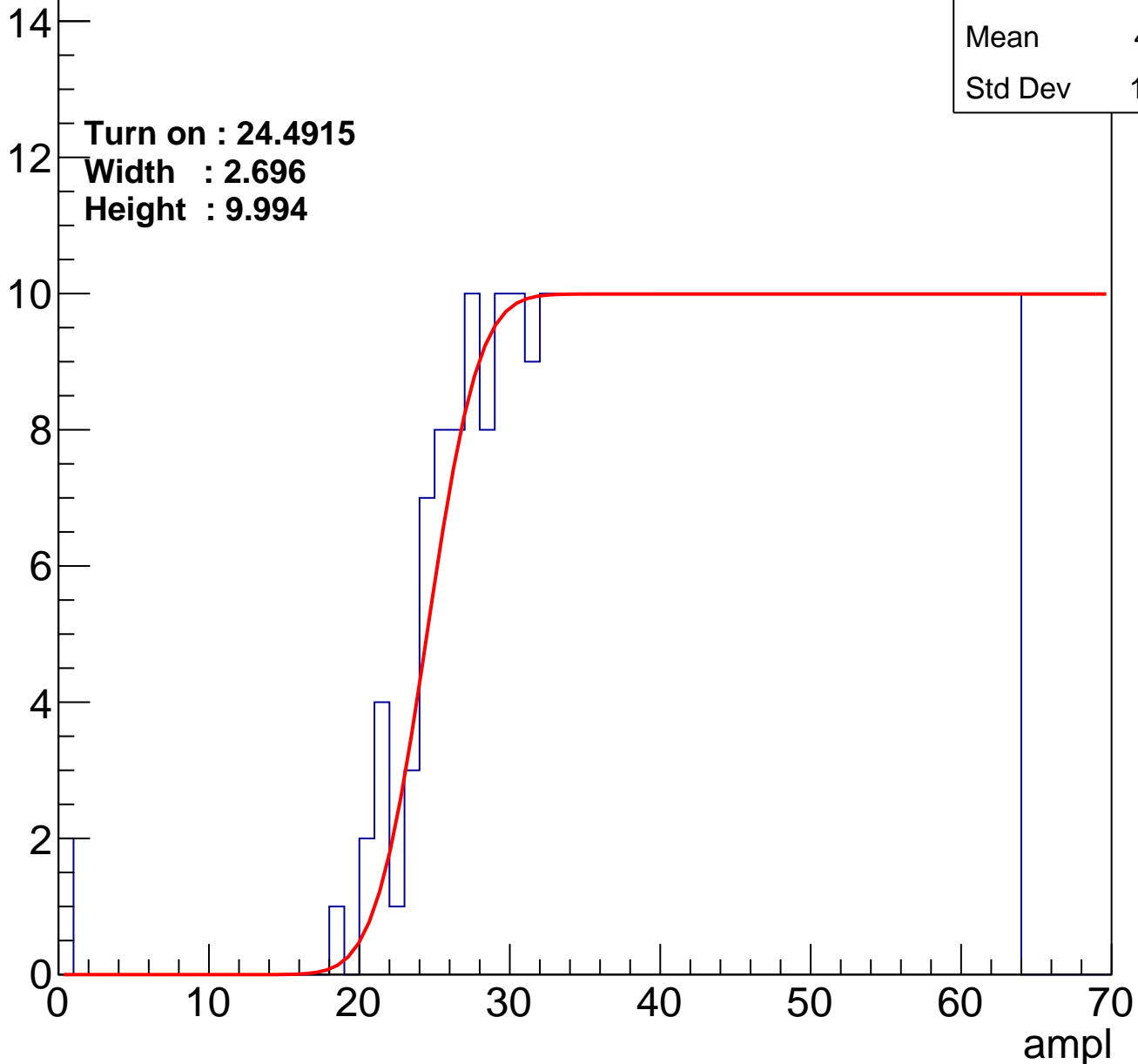
Entries	403
Mean	43.11
Std Dev	12.15

Turn on : 24.4915

Width : 2.696

Height : 9.994

Entry



B1L103S, U22-ch126

calib_packv5_042523_0143.root, FC#7, port C2

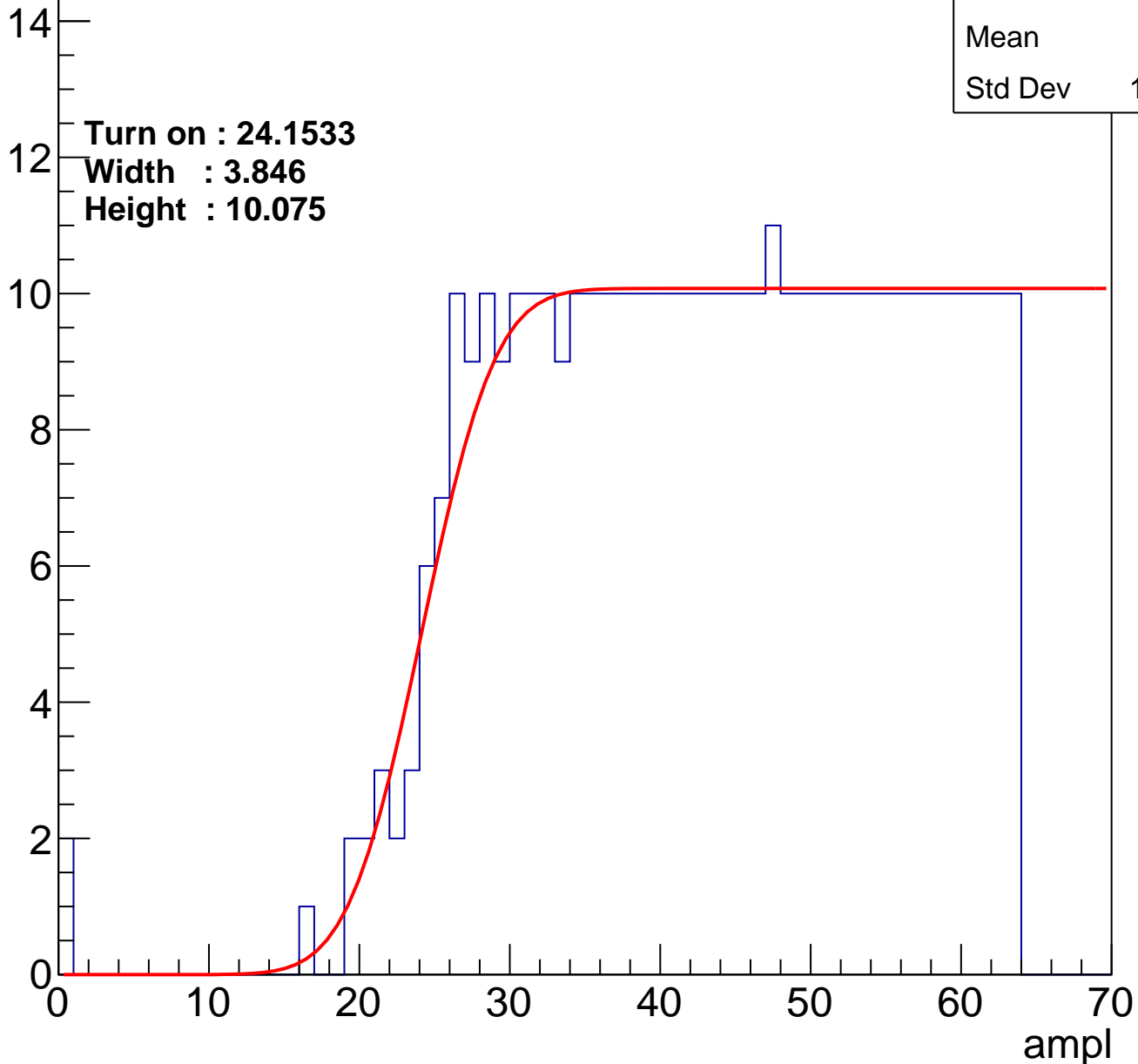
Entries	406
Mean	43
Std Dev	12.22

Turn on : 24.1533

Width : 3.846

Height : 10.075

Entry



B1L103S, U22-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43.18
Std Dev	12.1

Turn on : 24.6835

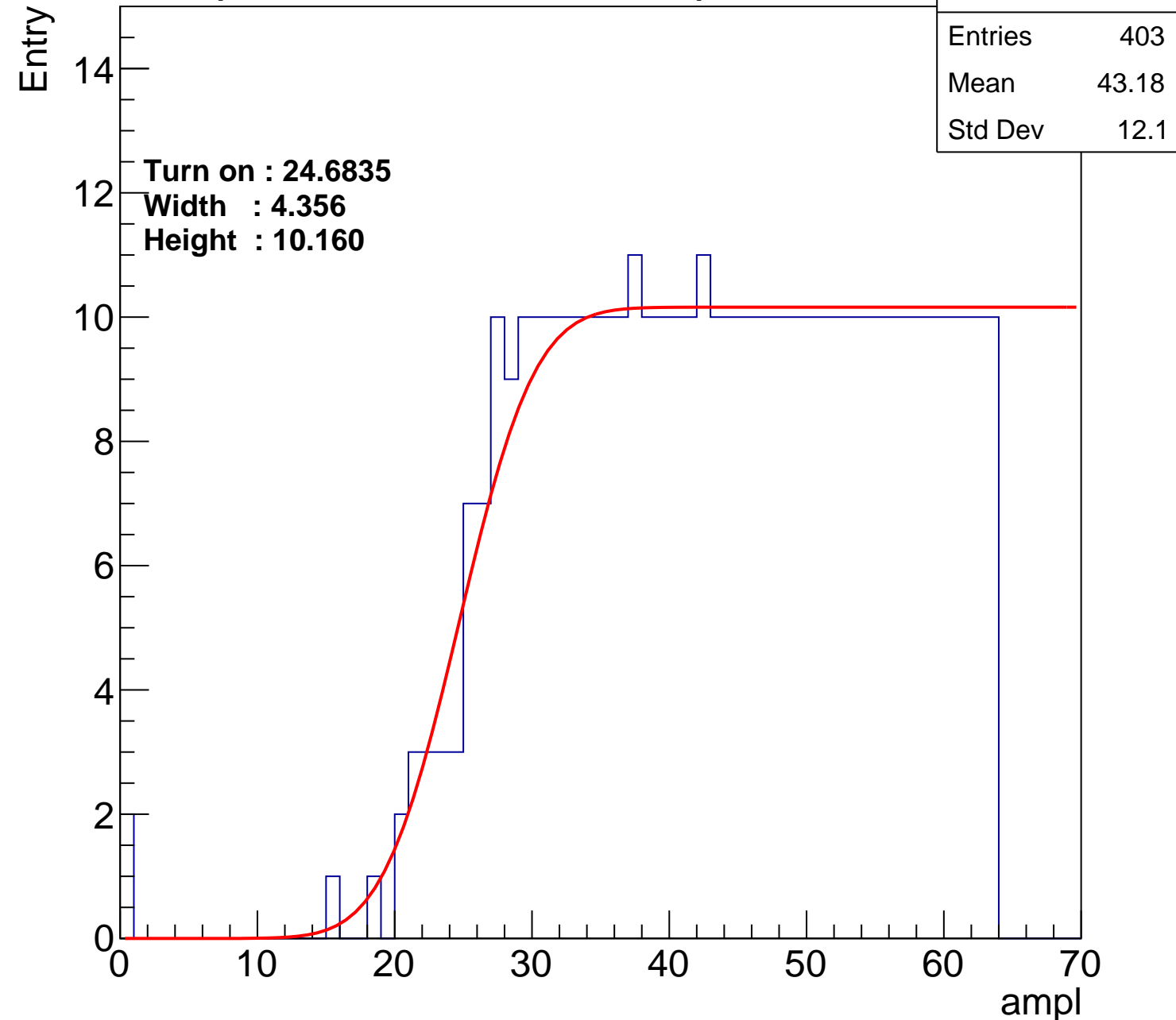
Width : 4.356

Height : 10.160

Entry

14
12
10
8
6
4
2
0

ampl



B1L103S, U22-ch127

calib_packv5_042523_0143.root, FC#7, port C2

Entries	403
Mean	43.18
Std Dev	12.1

Turn on : 24.6835

Width : 4.356

Height : 10.160

Entry

