

B0L101S, U19-ch0

calib_packv5_042523_0143.root, FC#1, port C1

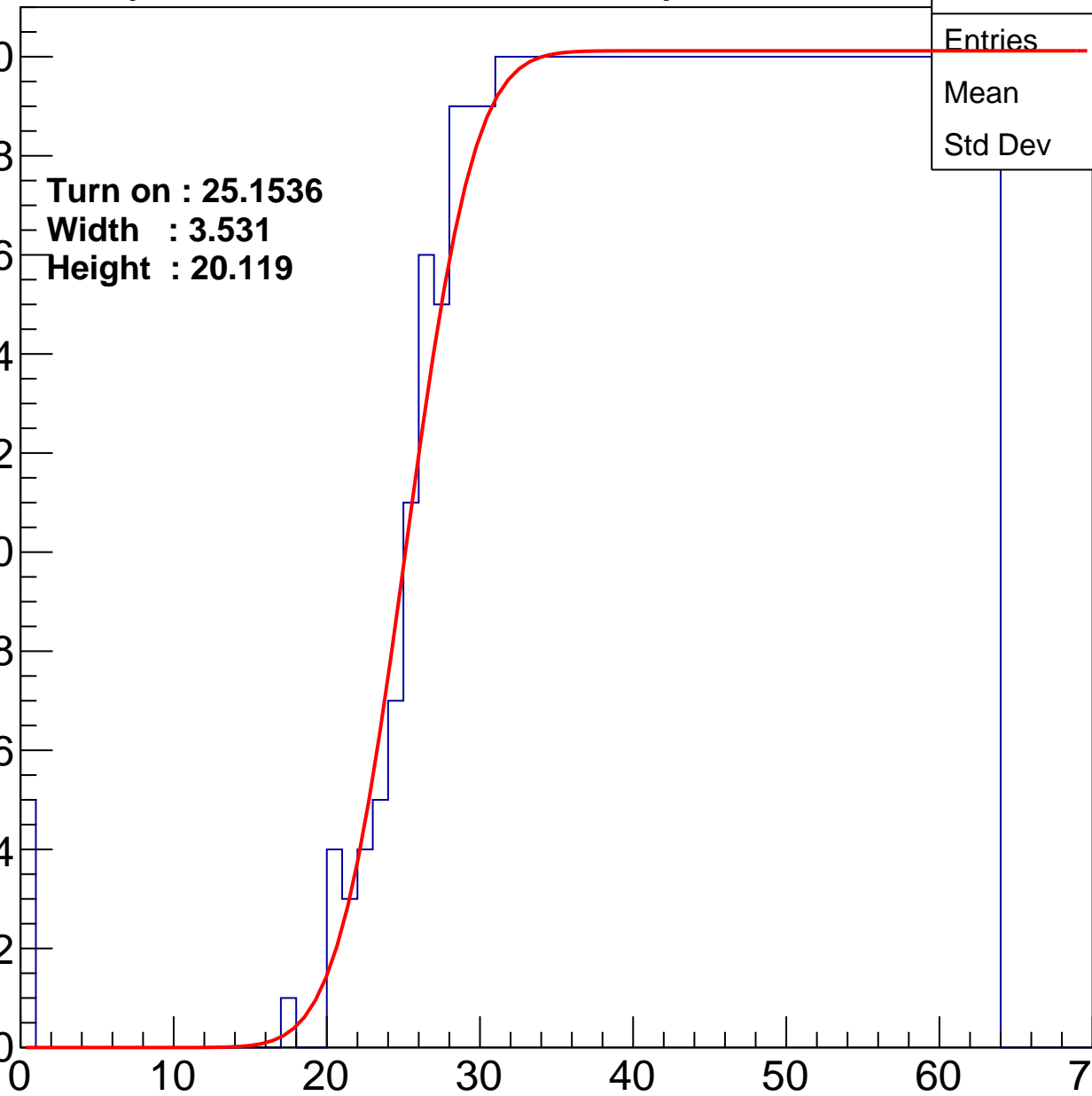
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1536
Width : 3.531
Height : 20.119

Entries	788
Mean	43.53
Std Dev	11.99

ampl



B0L101S, U19-ch1

calib_packv5_042523_0143.root, FC#1, port C1

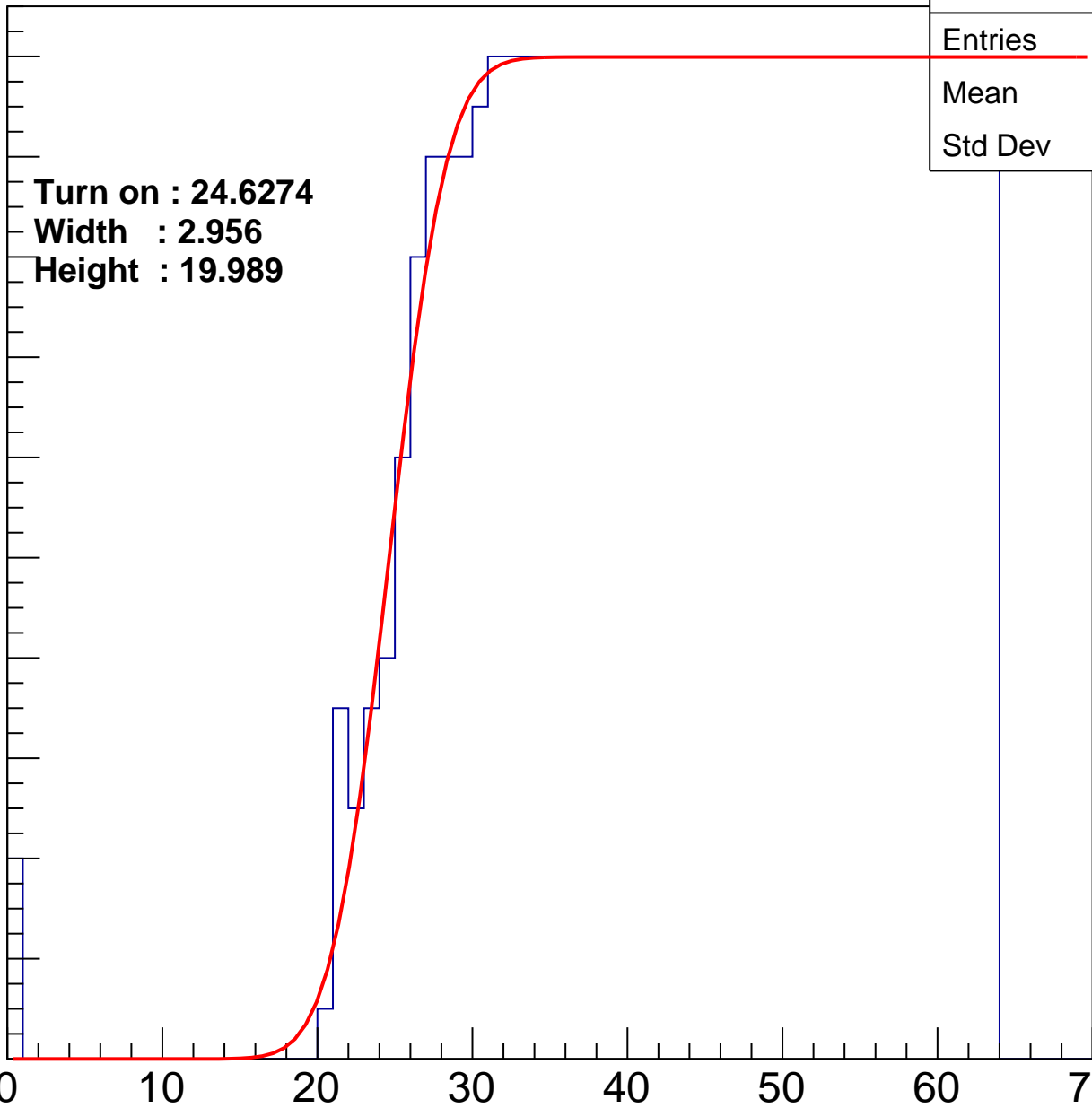
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6274
Width : 2.956
Height : 19.989

Entries	793
Mean	43.44
Std Dev	11.96

ampl



B0L101S, U19-ch2

calib_packv5_042523_0143.root, FC#1, port C1

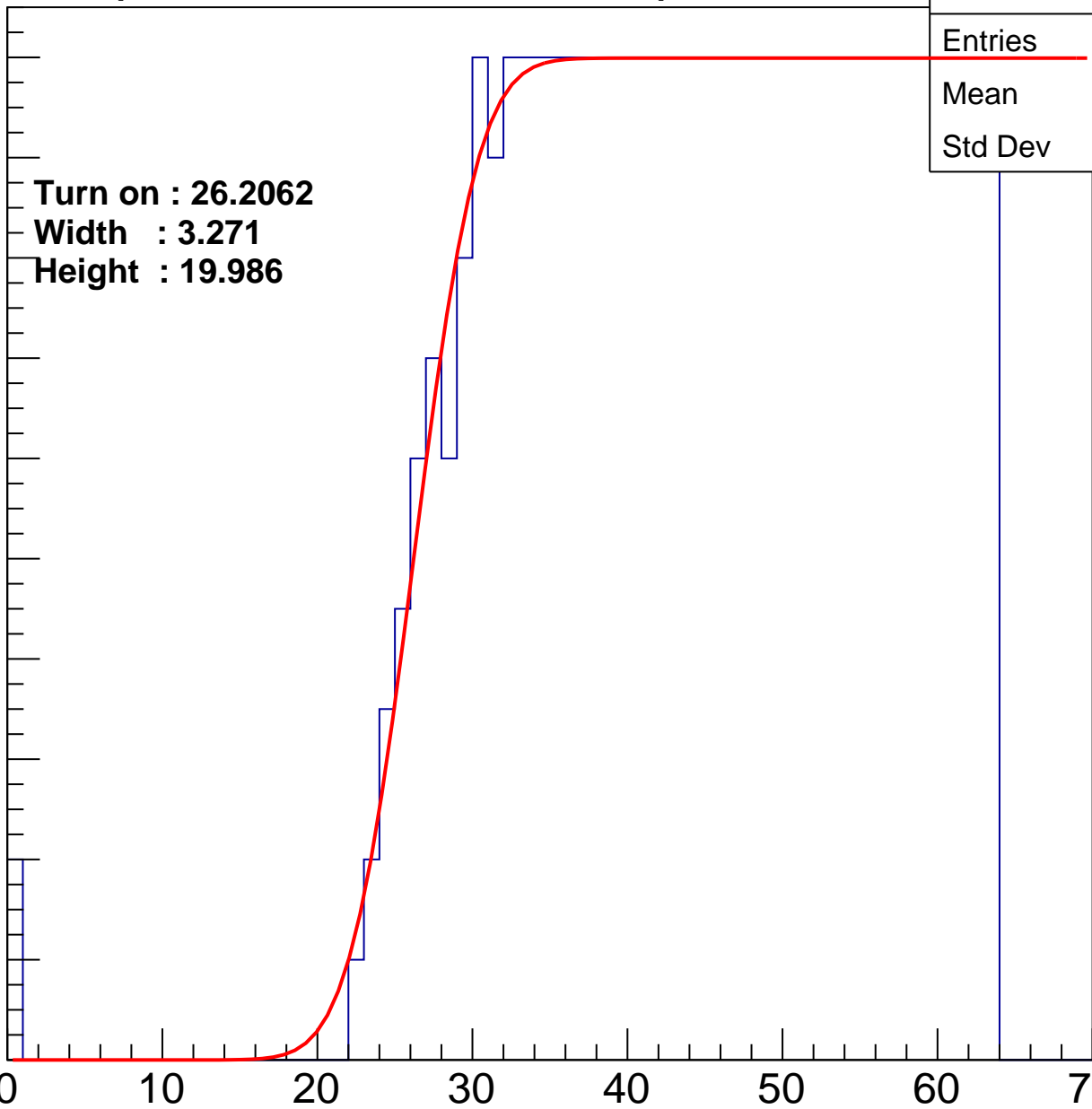
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2062
Width : 3.271
Height : 19.986

Entries	758
Mean	44.3
Std Dev	11.51

ampl



B0L101S, U19-ch3

calib_packv5_042523_0143.root, FC#1, port C1

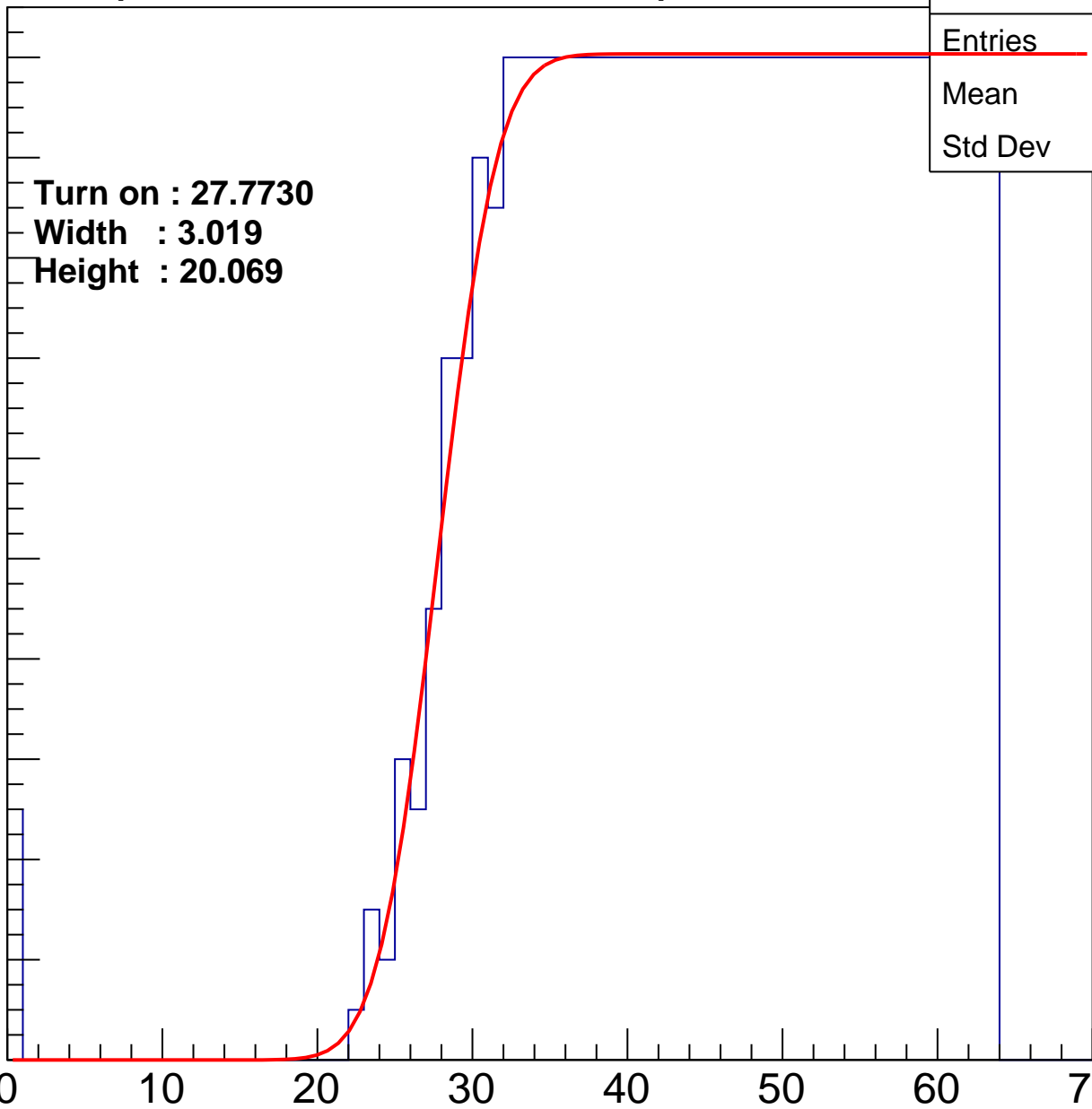
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7730
Width : 3.019
Height : 20.069

Entries	734
Mean	44.86
Std Dev	11.3

ampl



B0L101S, U19-ch4

calib_packv5_042523_0143.root, FC#1, port C1

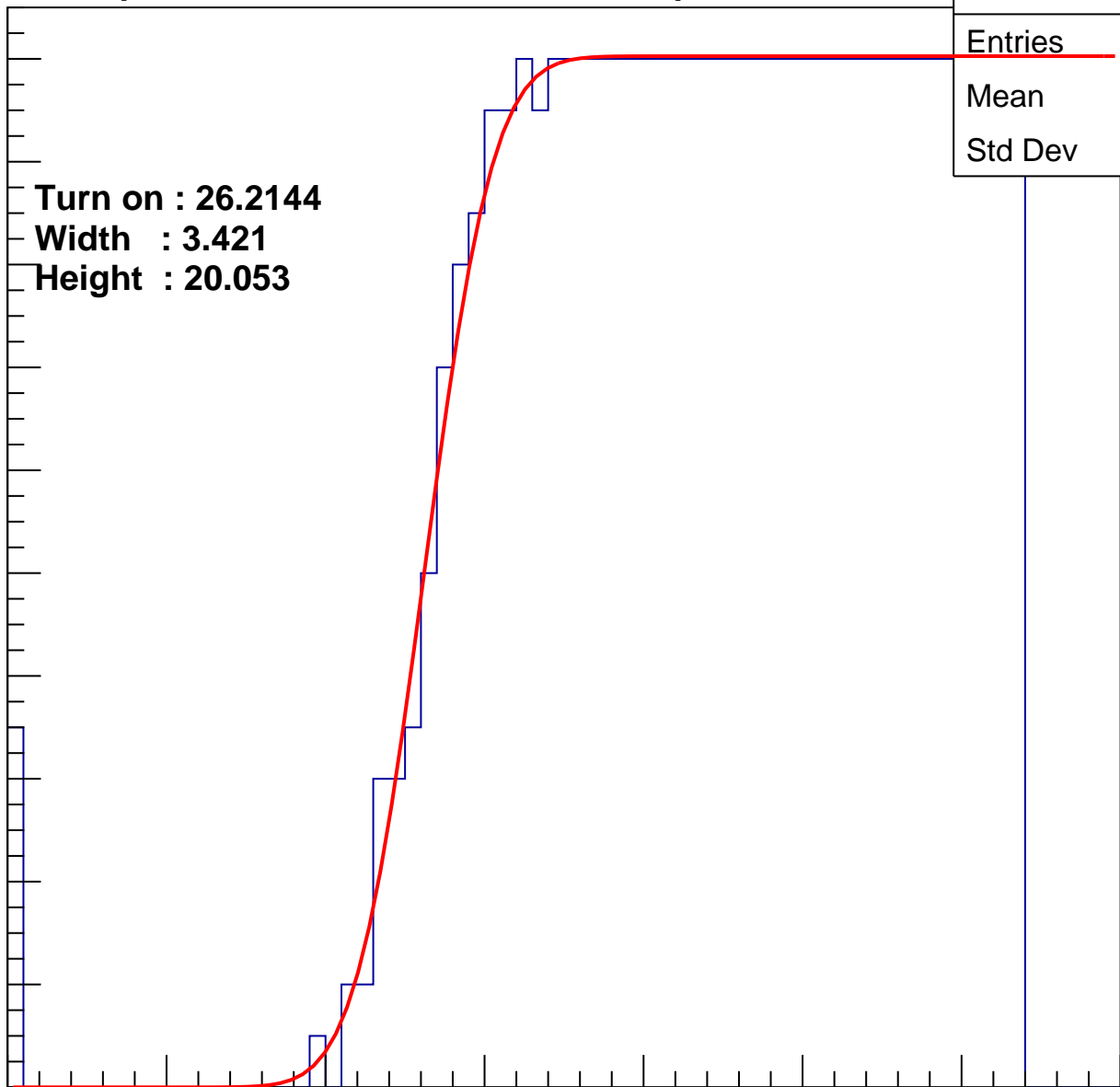
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2144
Width : 3.421
Height : 20.053

Entries	765
Mean	44.01
Std Dev	11.89

ampl



B0L101S, U19-ch5

calib_packv5_042523_0143.root, FC#1, port C1

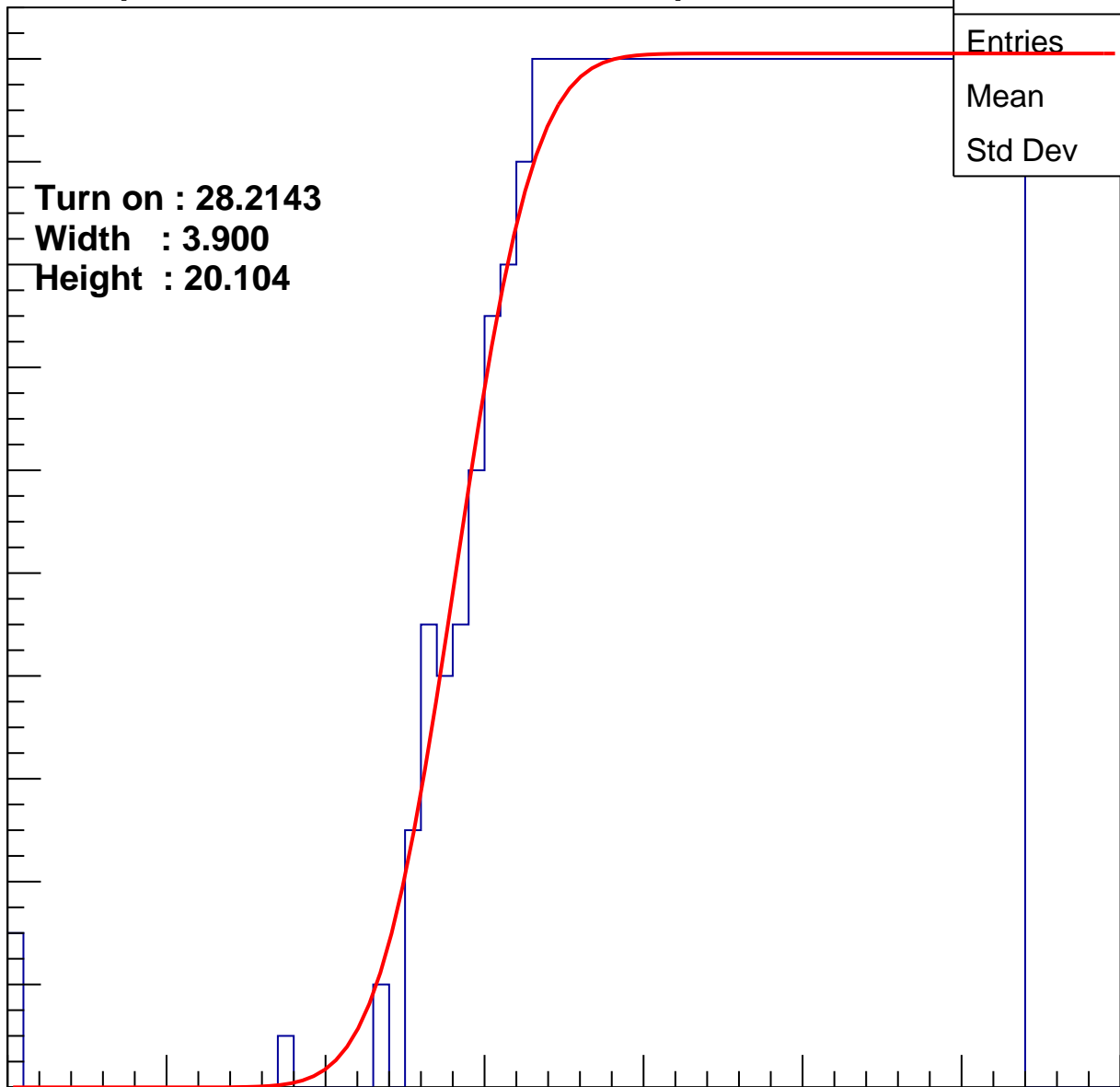
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2143
Width : 3.900
Height : 20.104

Entries	718
Mean	45.29
Std Dev	10.95

ampl



B0L101S, U19-ch6

calib_packv5_042523_0143.root, FC#1, port C1

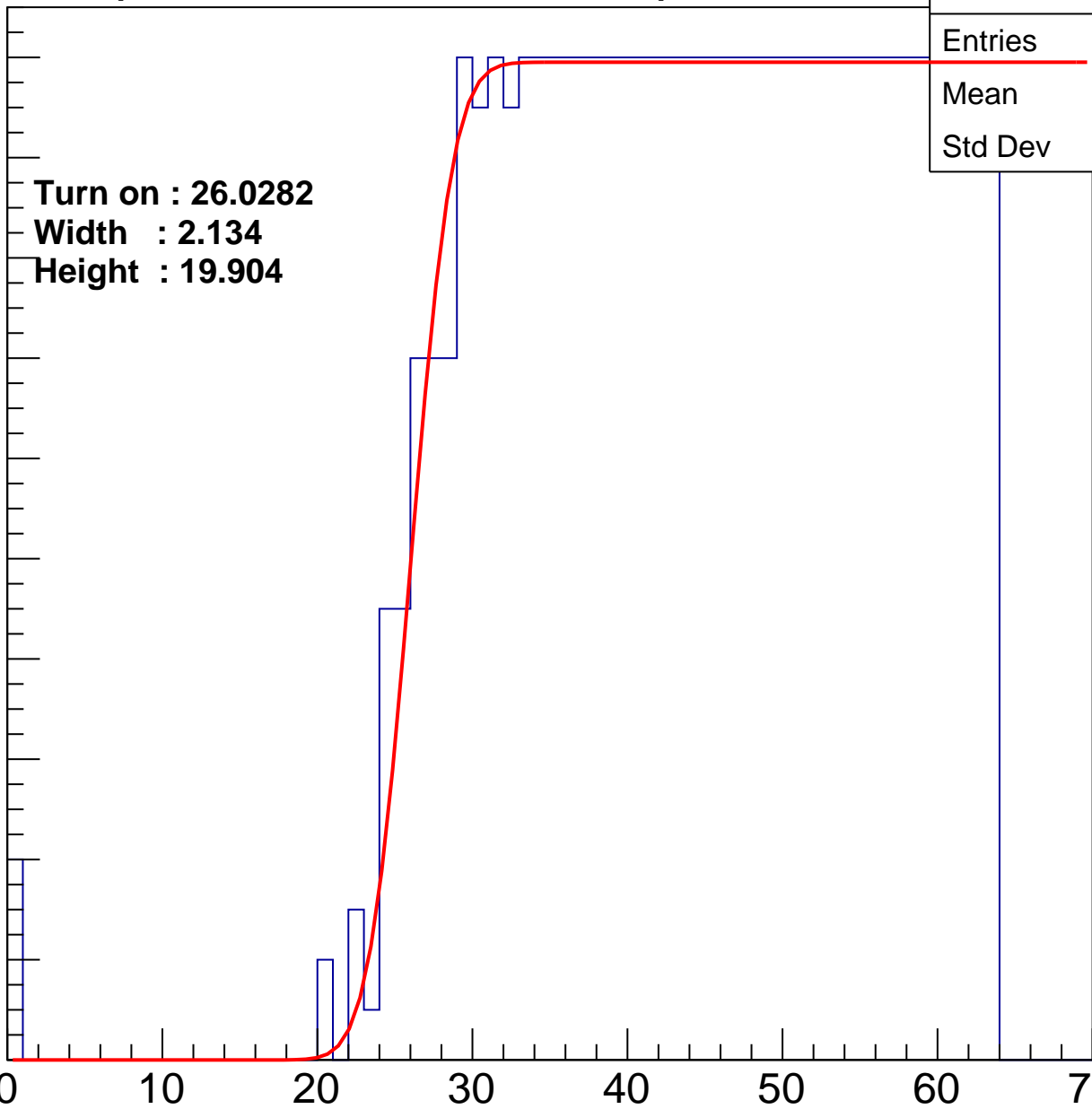
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0282
Width : 2.134
Height : 19.904

Entries	768
Mean	44.07
Std Dev	11.62

ampl



B0L101S, U19-ch7

calib_packv5_042523_0143.root, FC#1, port C1

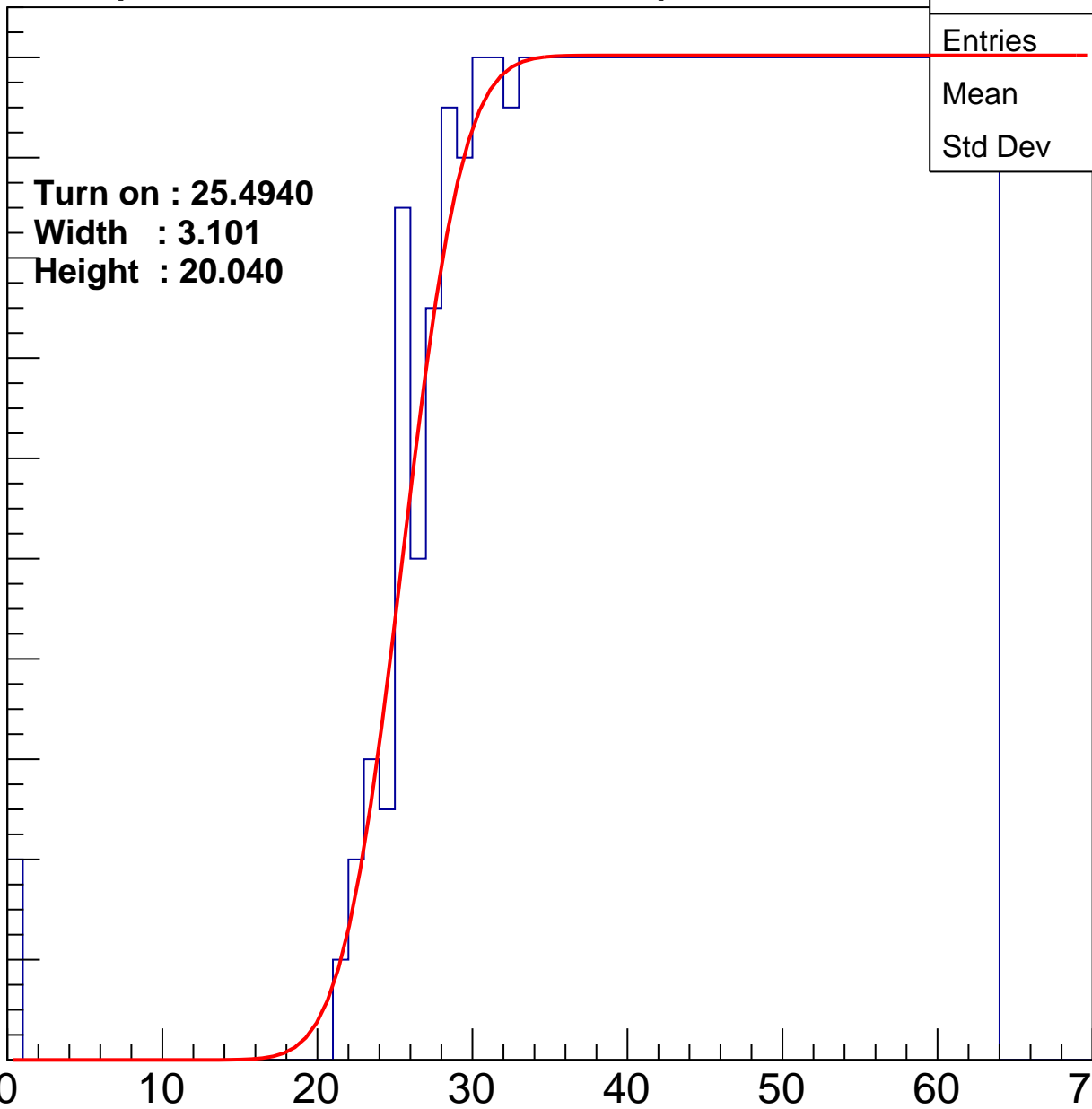
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4940
Width : 3.101
Height : 20.040

Entries	779
Mean	43.8
Std Dev	11.75

ampl



B0L101S, U19-ch8

calib_packv5_042523_0143.root, FC#1, port C1

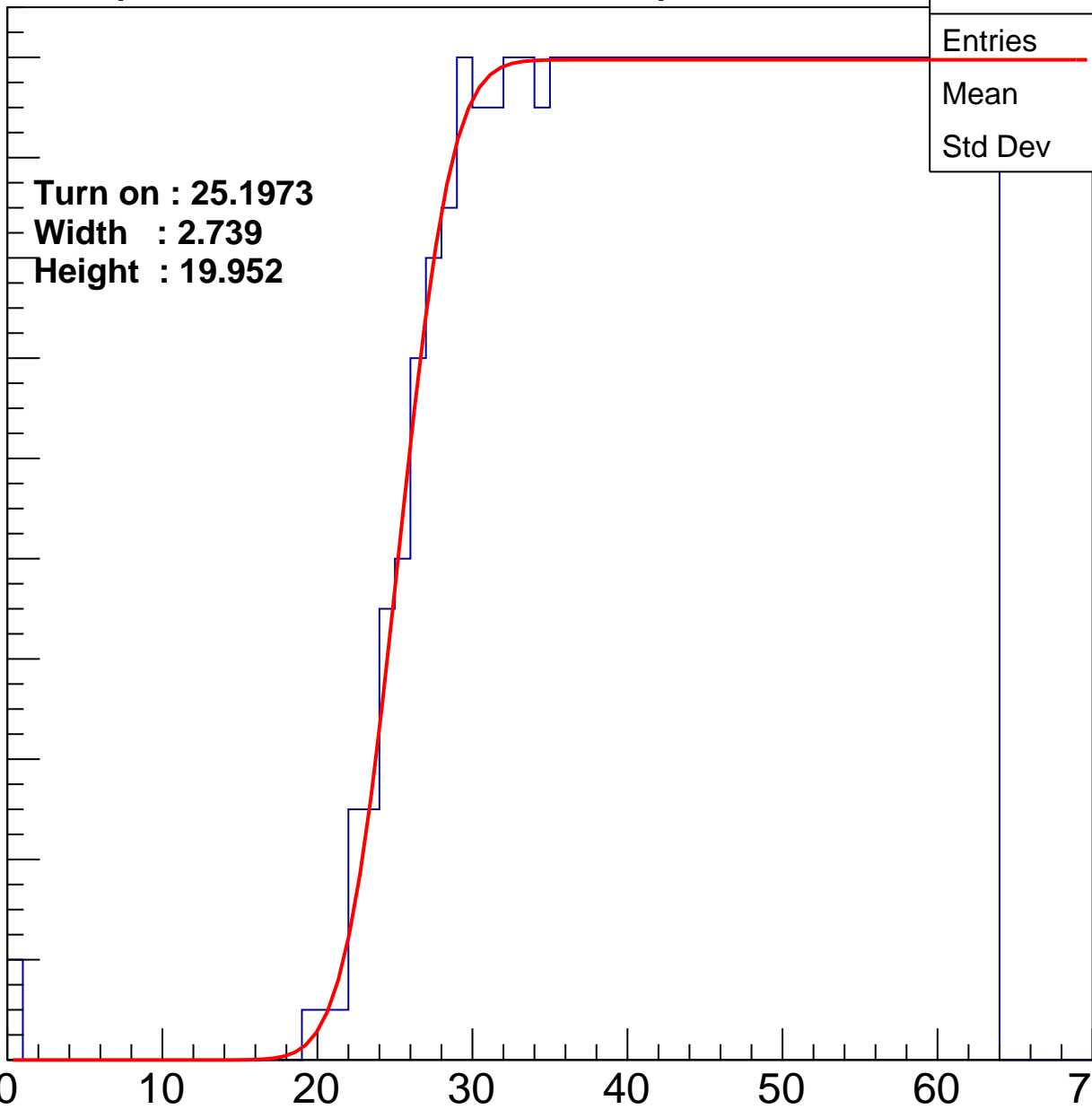
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1973
Width : 2.739
Height : 19.952

Entries	778
Mean	43.87
Std Dev	11.6

ampl



B0L101S, U19-ch9

calib_packv5_042523_0143.root, FC#1, port C1

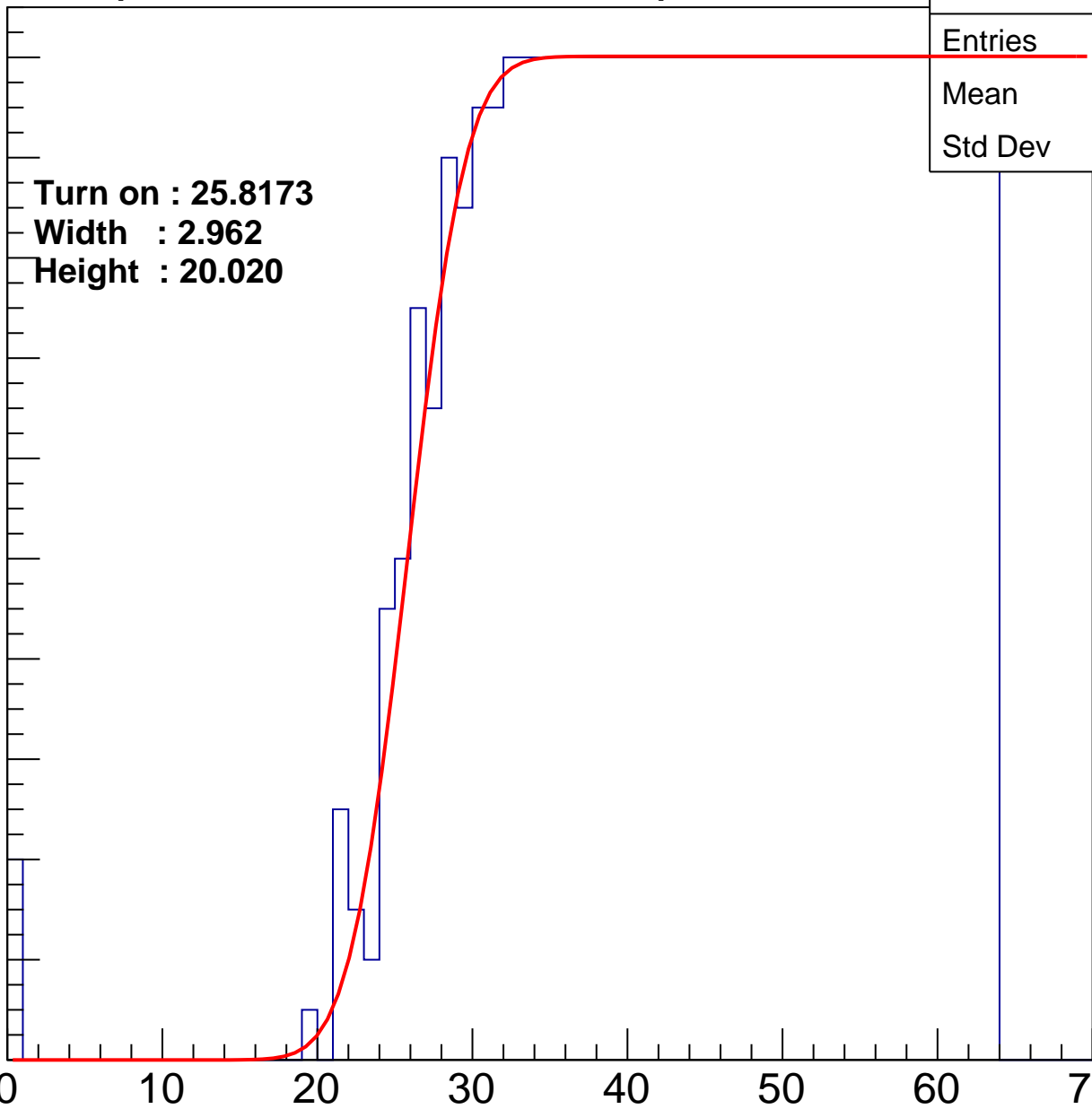
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8173
Width : 2.962
Height : 20.020

Entries	775
Mean	43.87
Std Dev	11.75

ampl



B0L101S, U19-ch10

calib_packv5_042523_0143.root, FC#1, port C1

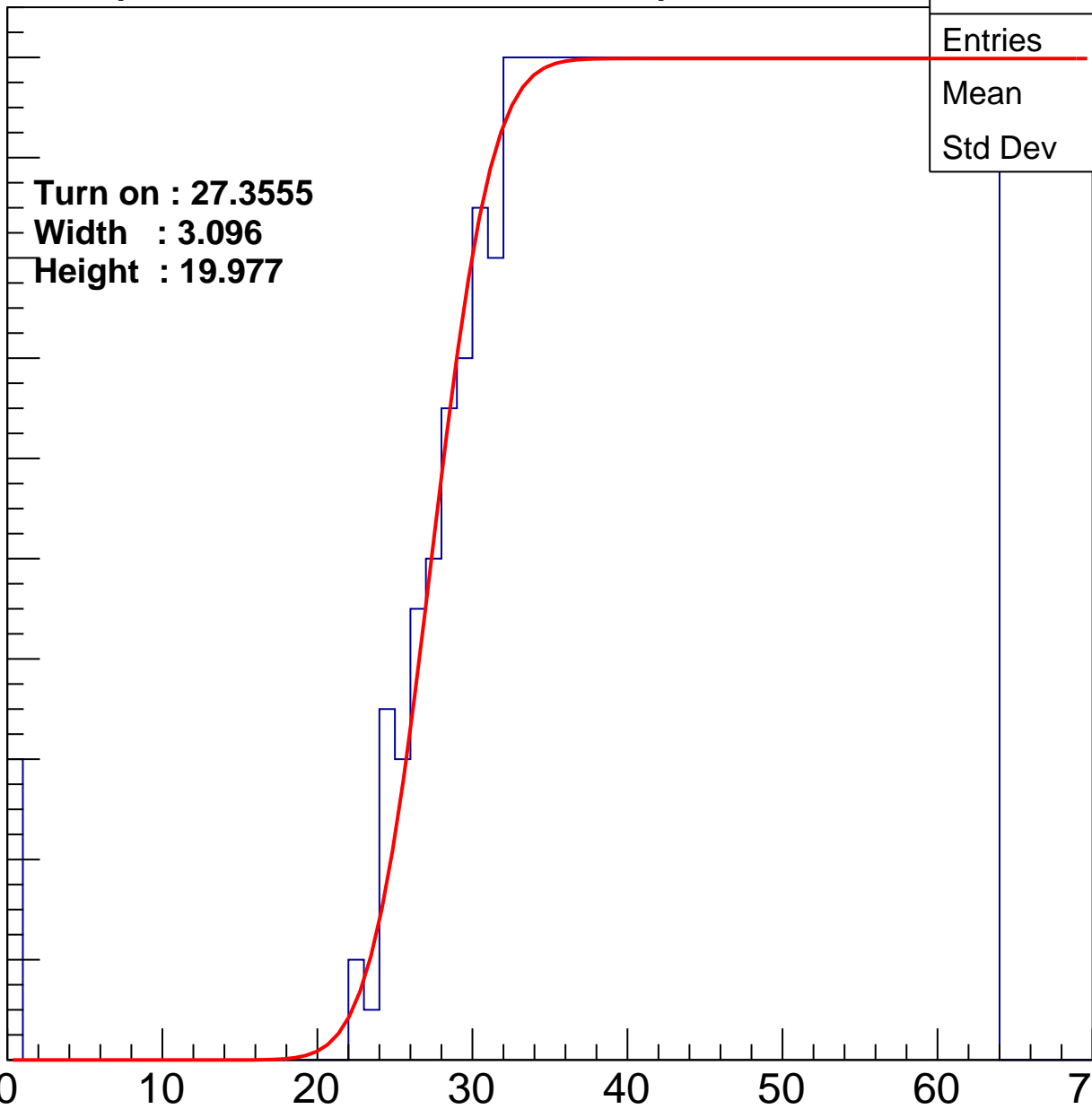
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3555
Width : 3.096
Height : 19.977

Entries	741
Mean	44.62
Std Dev	11.53

ampl



B0L101S, U19-ch11

calib_packv5_042523_0143.root, FC#1, port C1

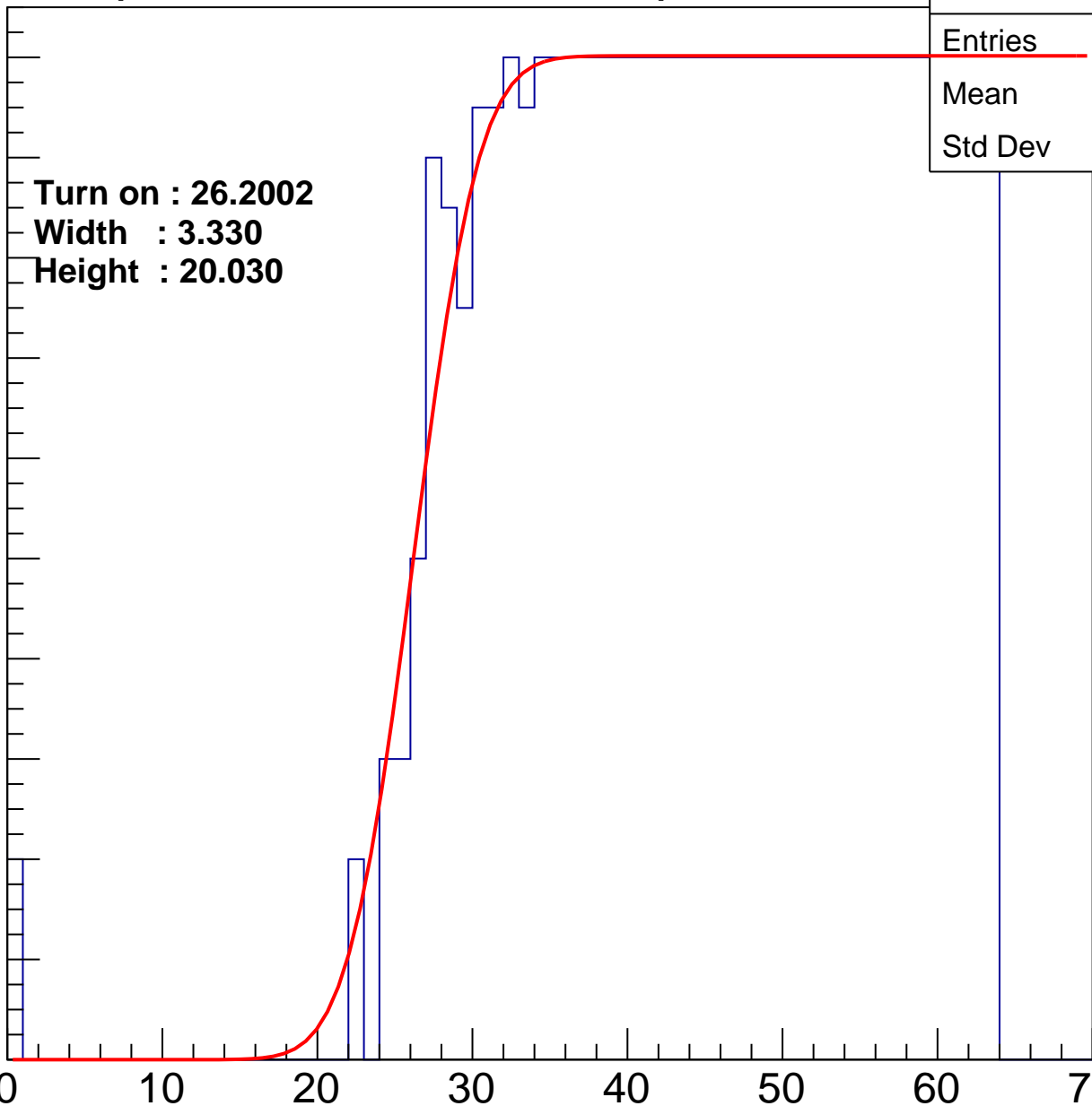
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2002
Width : 3.330
Height : 20.030

Entries	757
Mean	44.34
Std Dev	11.47

ampl



B0L101S, U19-ch12

calib_packv5_042523_0143.root, FC#1, port C1

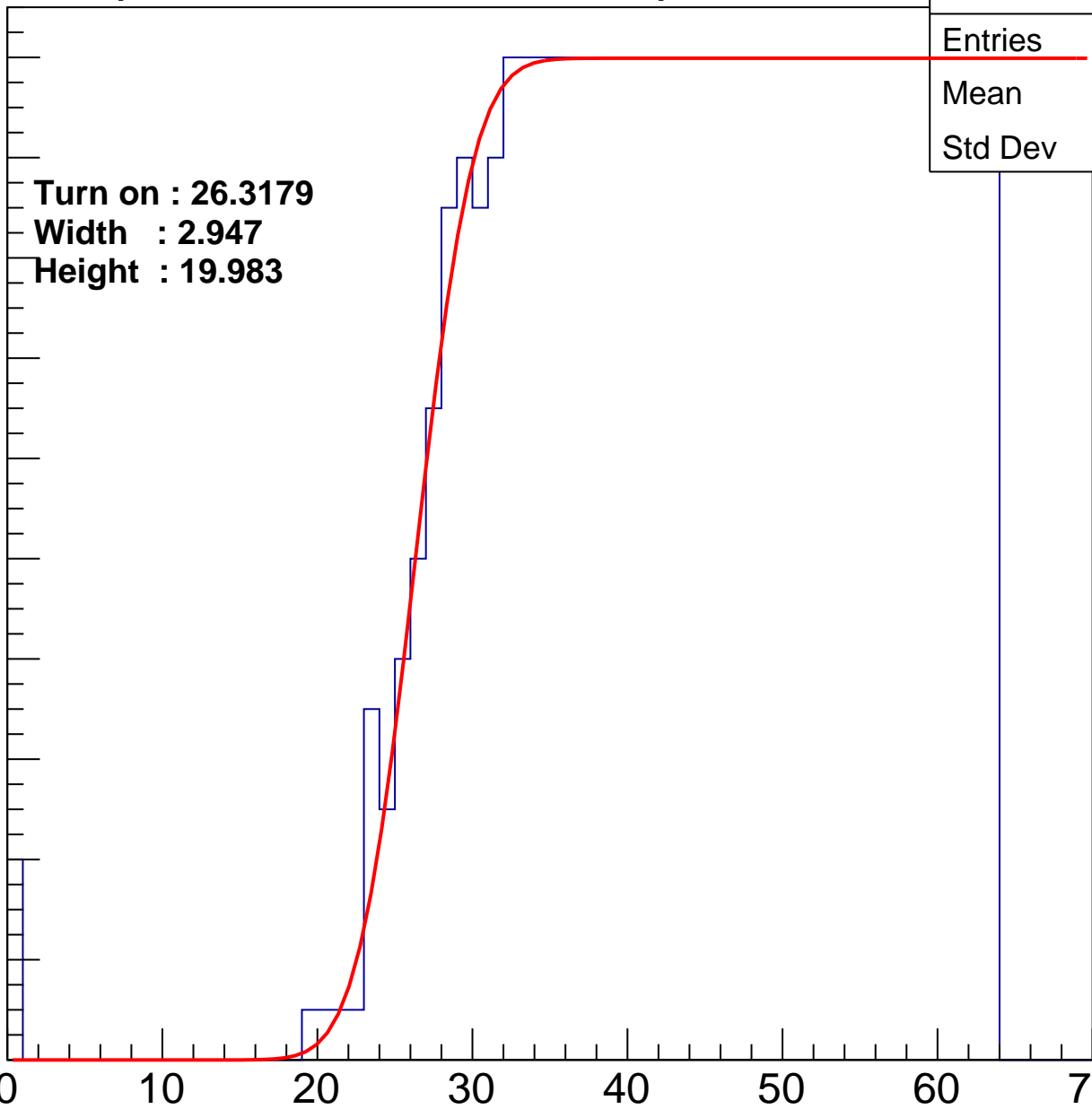
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3179
Width : 2.947
Height : 19.983

Entries	761
Mean	44.2
Std Dev	11.58

ampl



B0L101S, U19-ch13

calib_packv5_042523_0143.root, FC#1, port C1

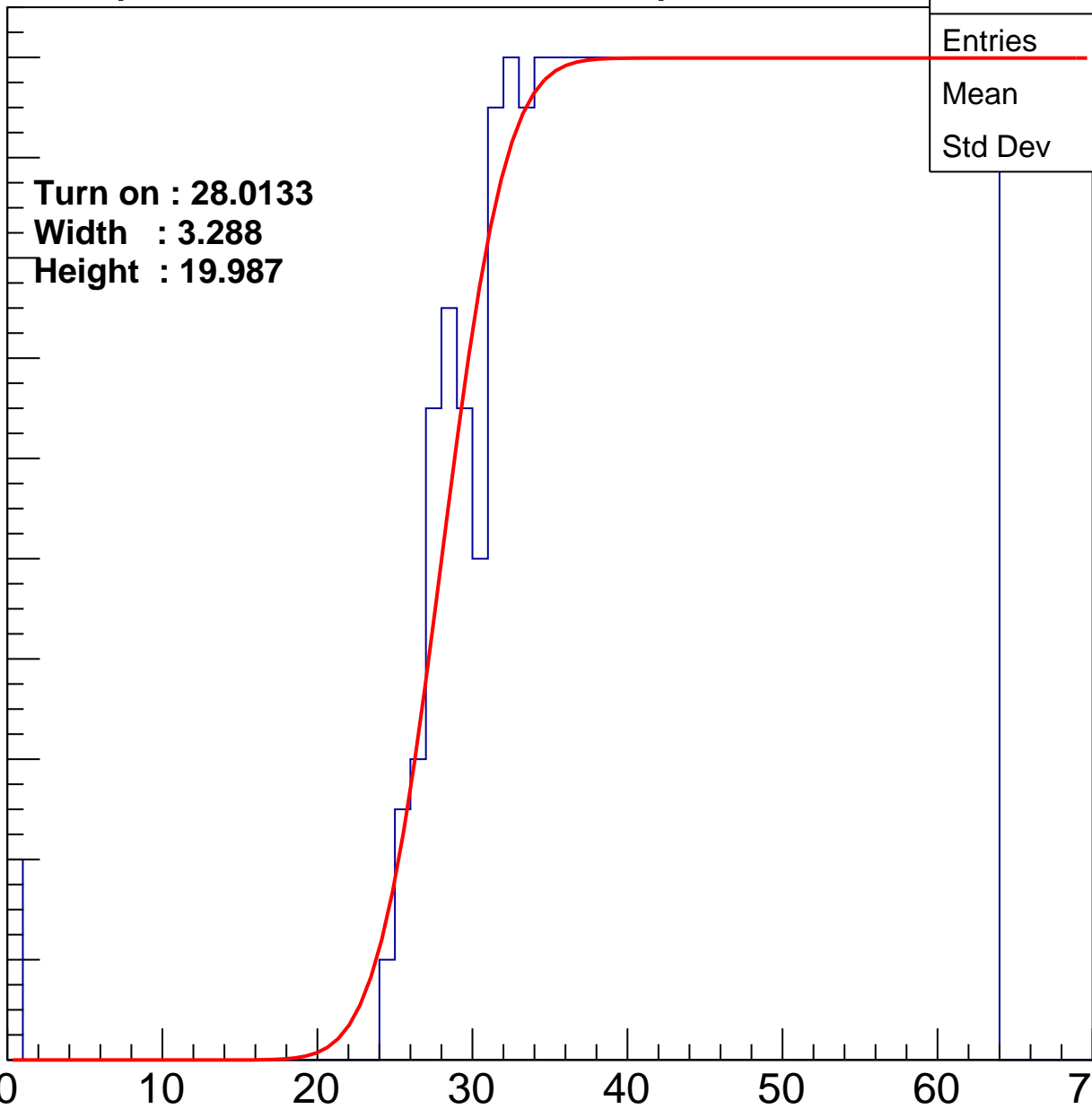
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0133
Width : 3.288
Height : 19.987

Entries	726
Mean	45.09
Std Dev	11.1

ampl



B0L101S, U19-ch14

calib_packv5_042523_0143.root, FC#1, port C1

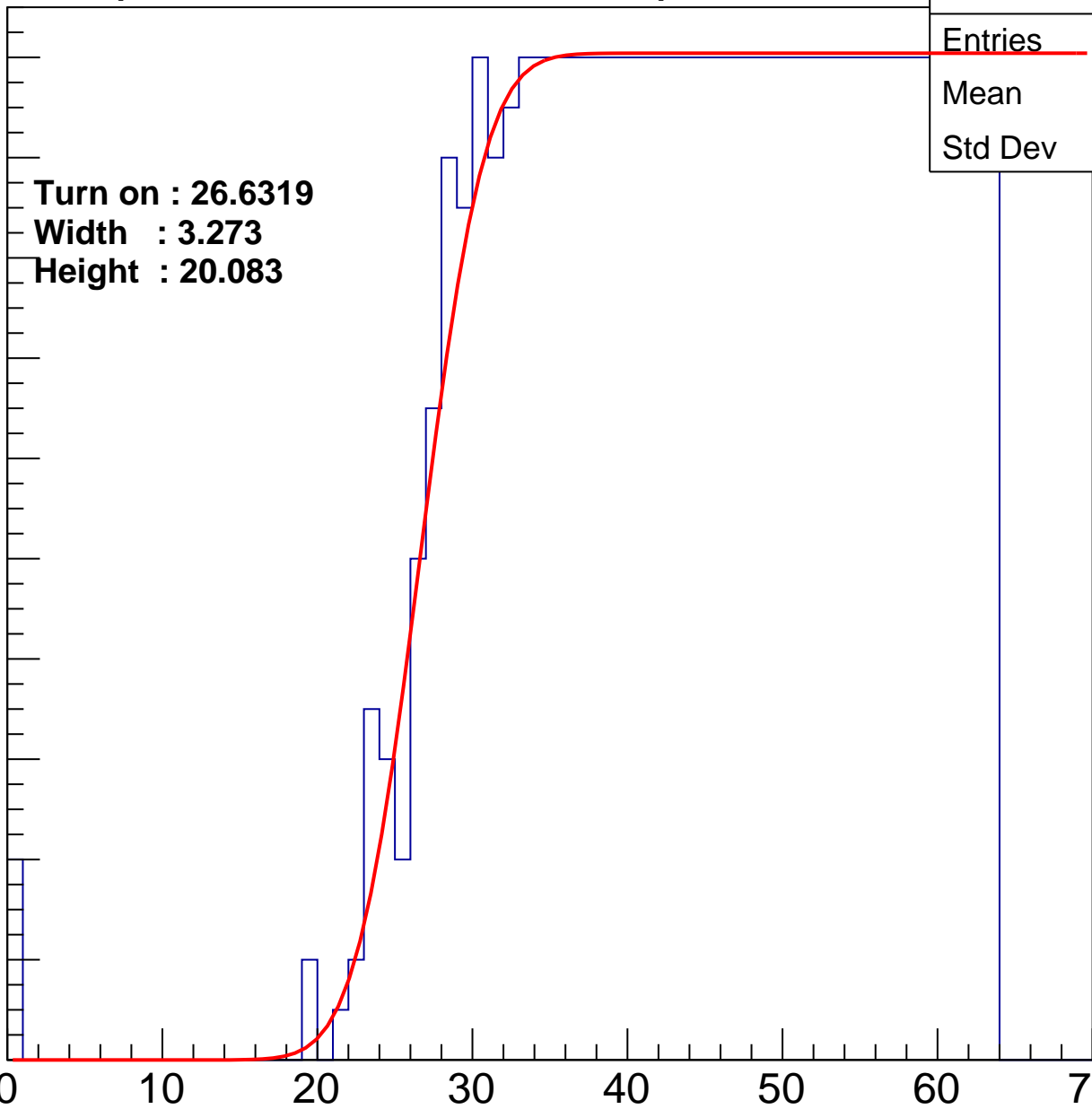
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6319
Width : 3.273
Height : 20.083

Entries	761
Mean	44.21
Std Dev	11.58

ampl



B0L101S, U19-ch15

calib_packv5_042523_0143.root, FC#1, port C1

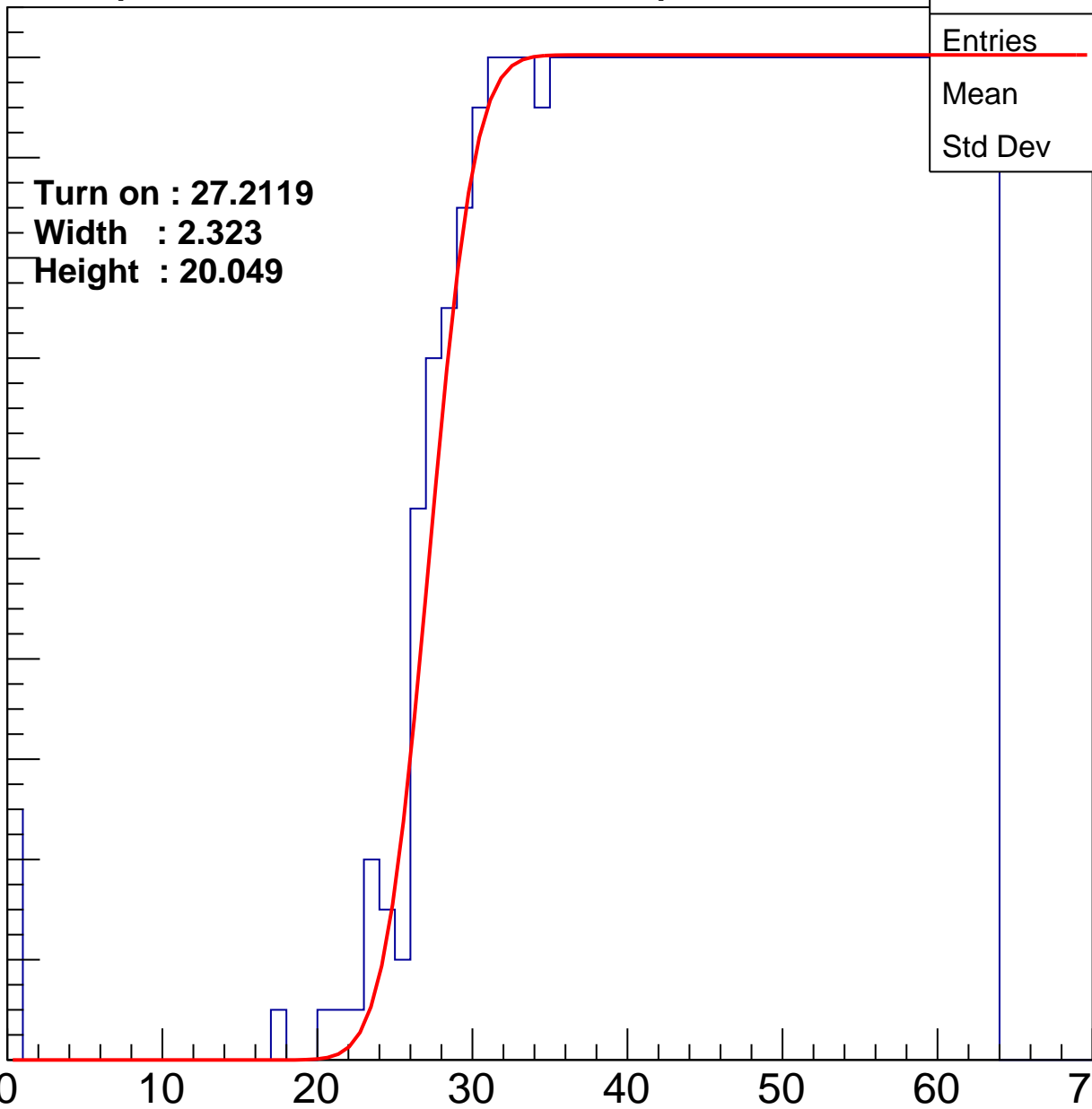
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2119
Width : 2.323
Height : 20.049

Entries	753
Mean	44.39
Std Dev	11.54

ampl



B0L101S, U19-ch16

calib_packv5_042523_0143.root, FC#1, port C1

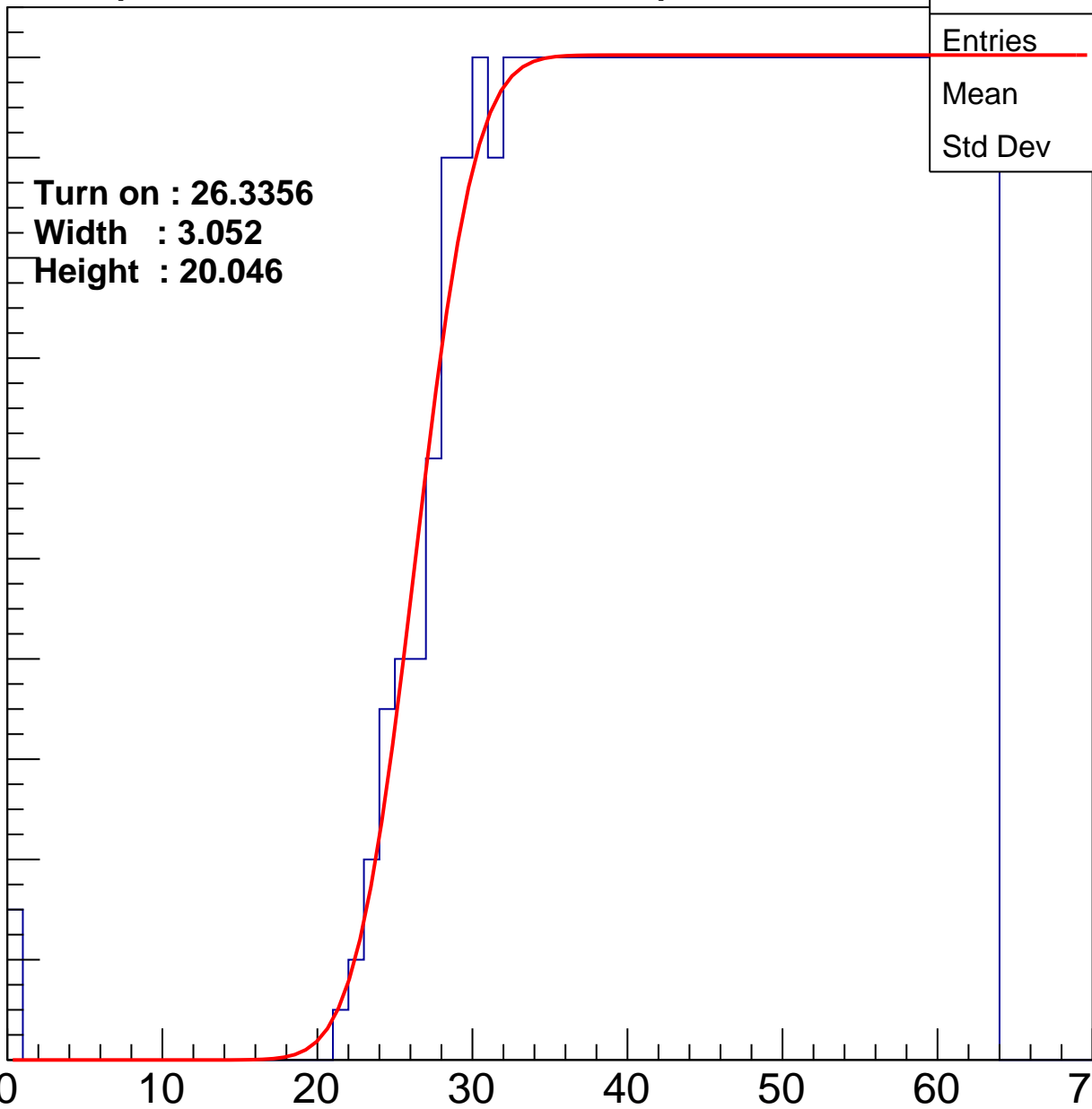
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3356
Width : 3.052
Height : 20.046

Entries	759
Mean	44.32
Std Dev	11.41

ampl



B0L101S, U19-ch17

calib_packv5_042523_0143.root, FC#1, port C1

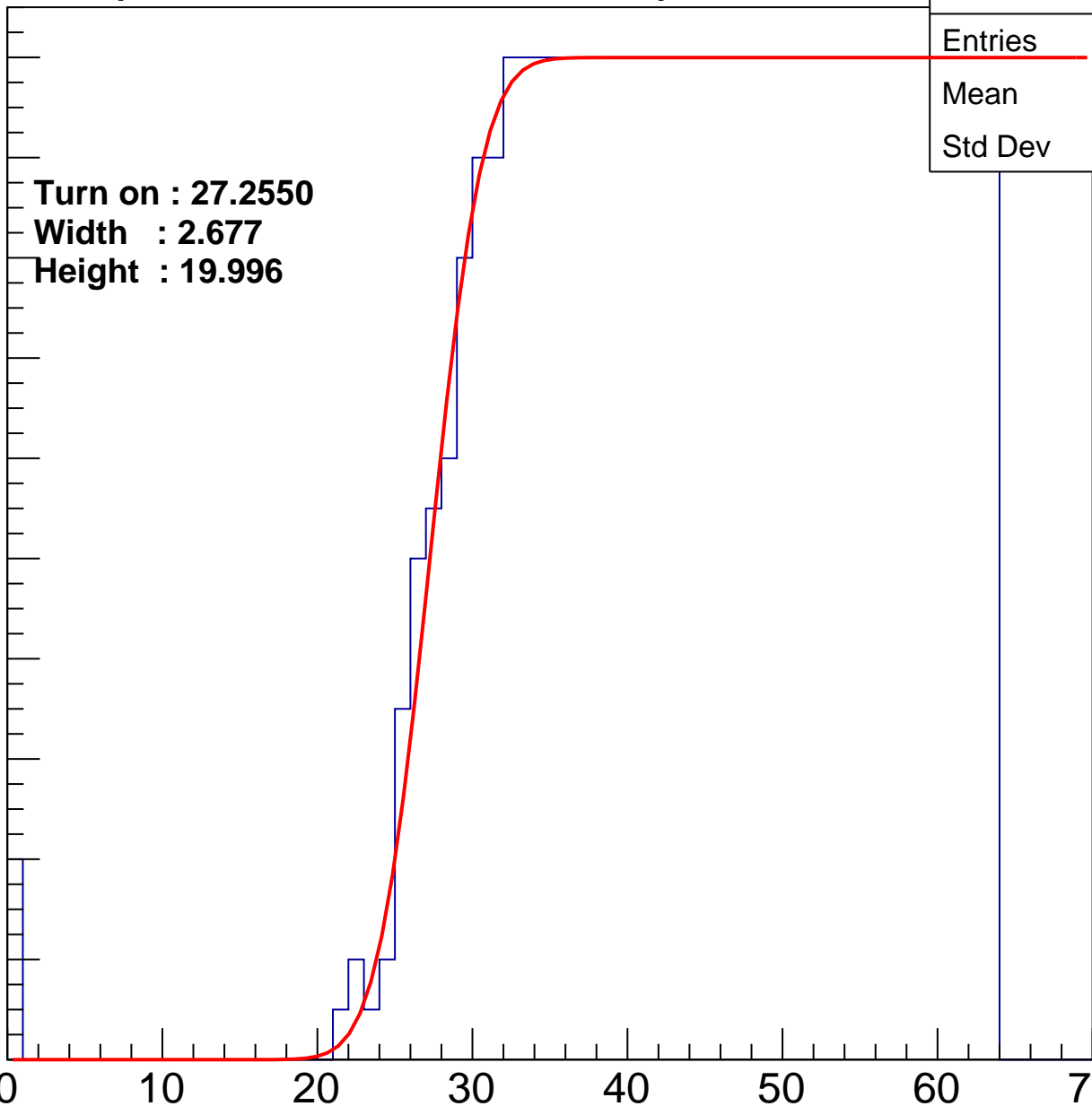
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2550
Width : 2.677
Height : 19.996

Entries	742
Mean	44.7
Std Dev	11.3

ampl



B0L101S, U19-ch18

calib_packv5_042523_0143.root, FC#1, port C1

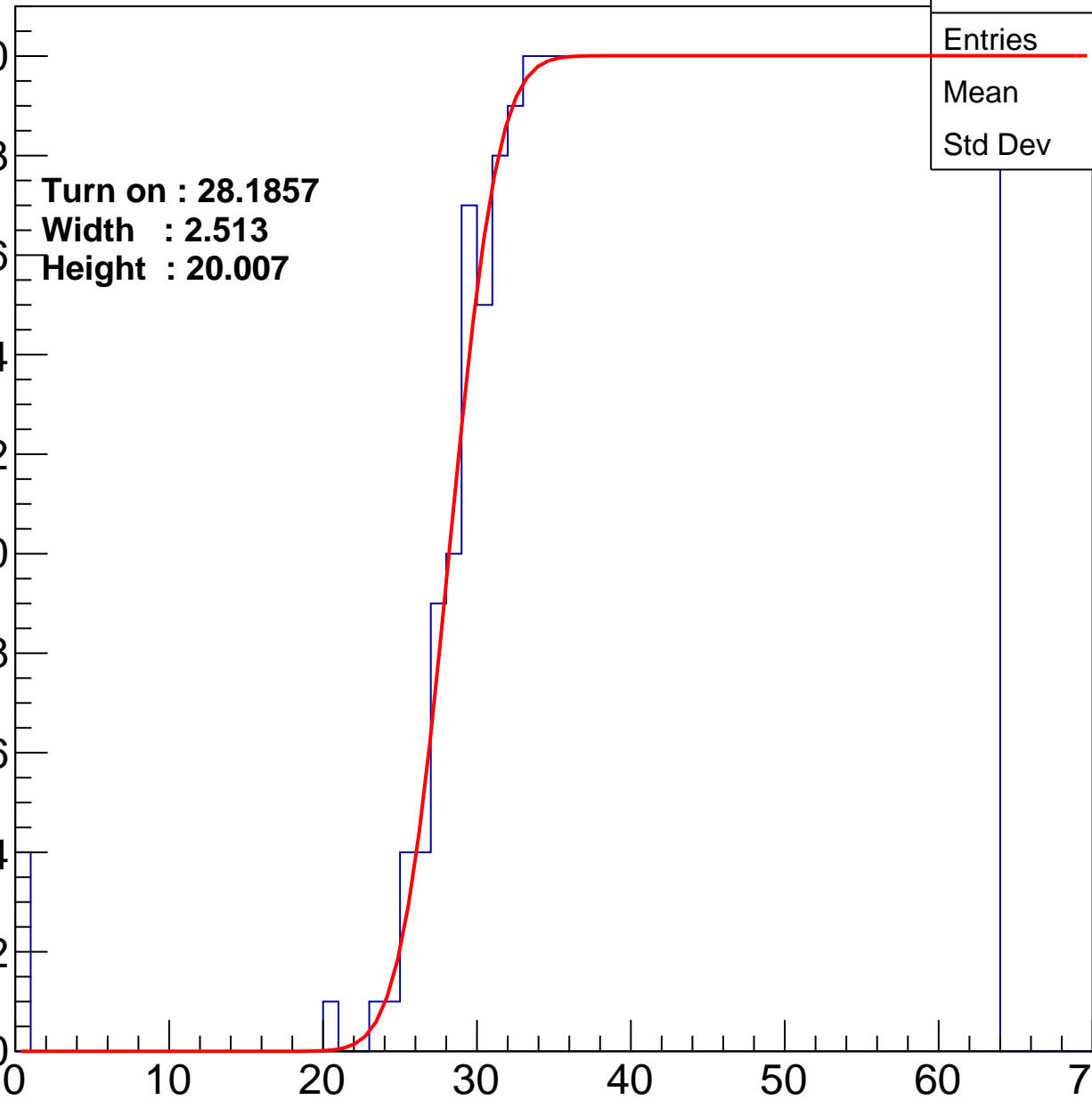
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1857
Width : 2.513
Height : 20.007

Entries	723
Mean	45.18
Std Dev	11.04

ampl



B0L101S, U19-ch19

calib_packv5_042523_0143.root, FC#1, port C1

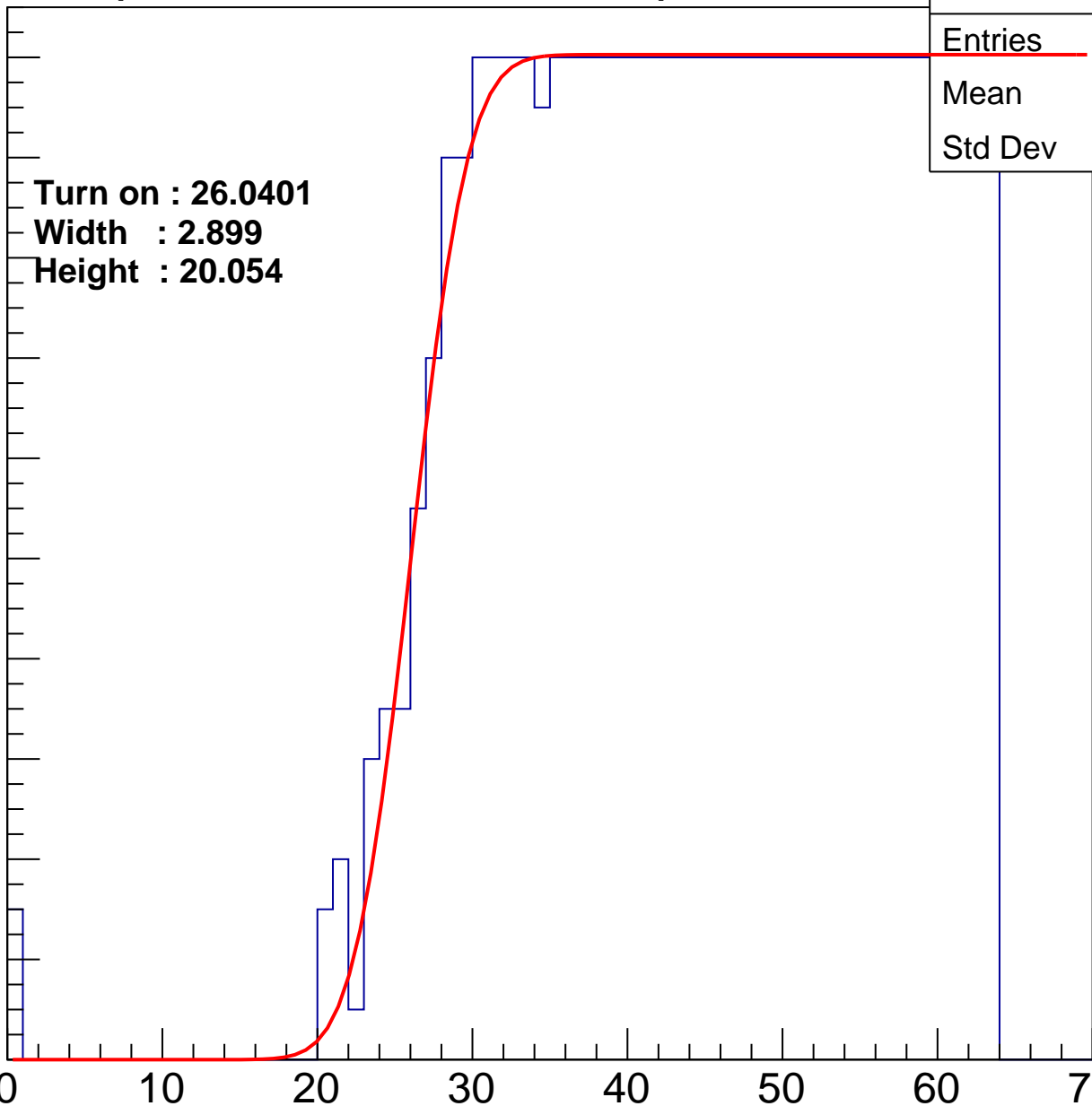
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0401
Width : 2.899
Height : 20.054

Entries	771
Mean	44
Std Dev	11.61

ampl



B0L101S, U19-ch20

calib_packv5_042523_0143.root, FC#1, port C1

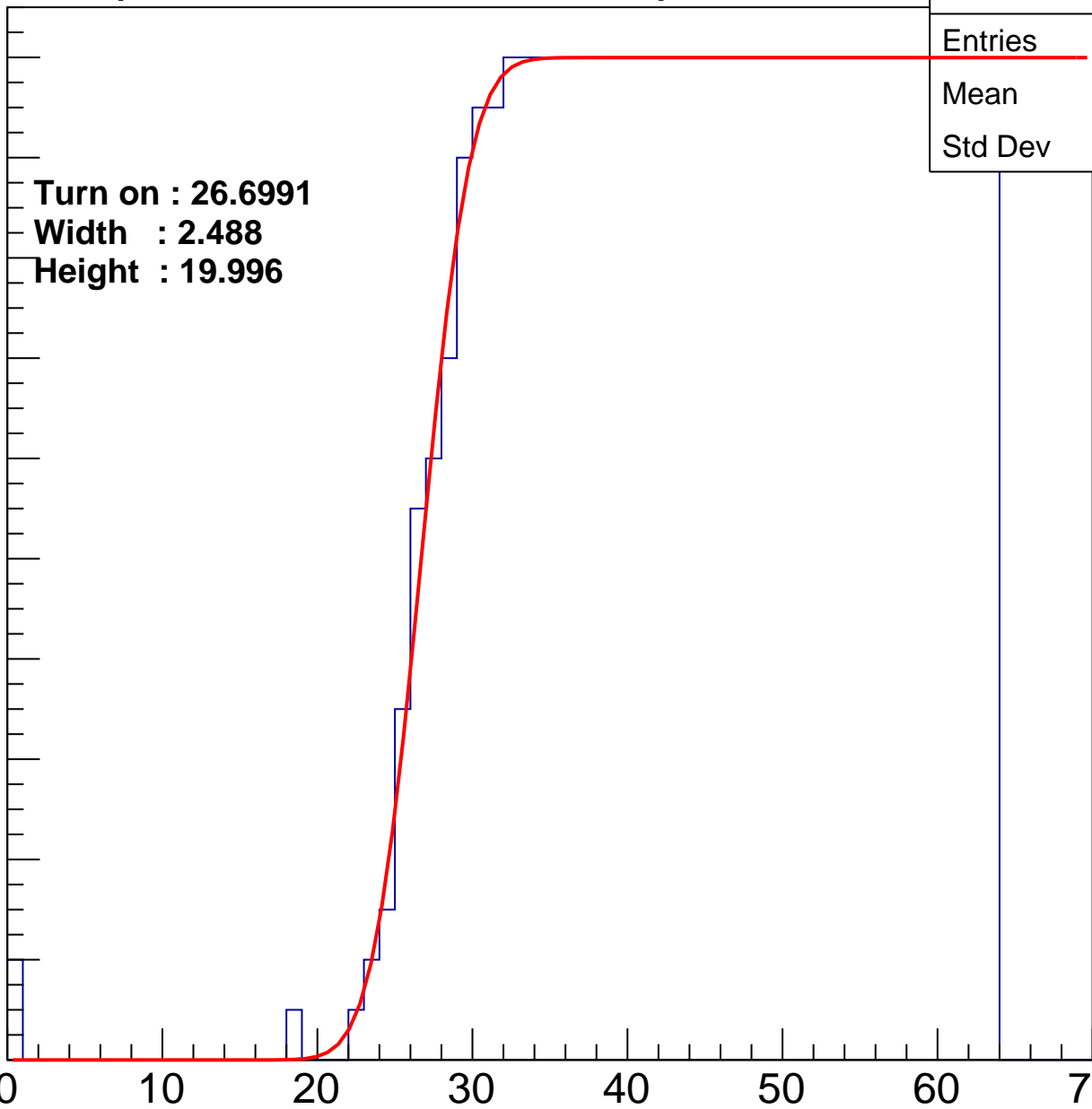
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6991
Width : 2.488
Height : 19.996

Entries	749
Mean	44.61
Std Dev	11.16

ampl



B0L101S, U19-ch21

calib_packv5_042523_0143.root, FC#1, port C1

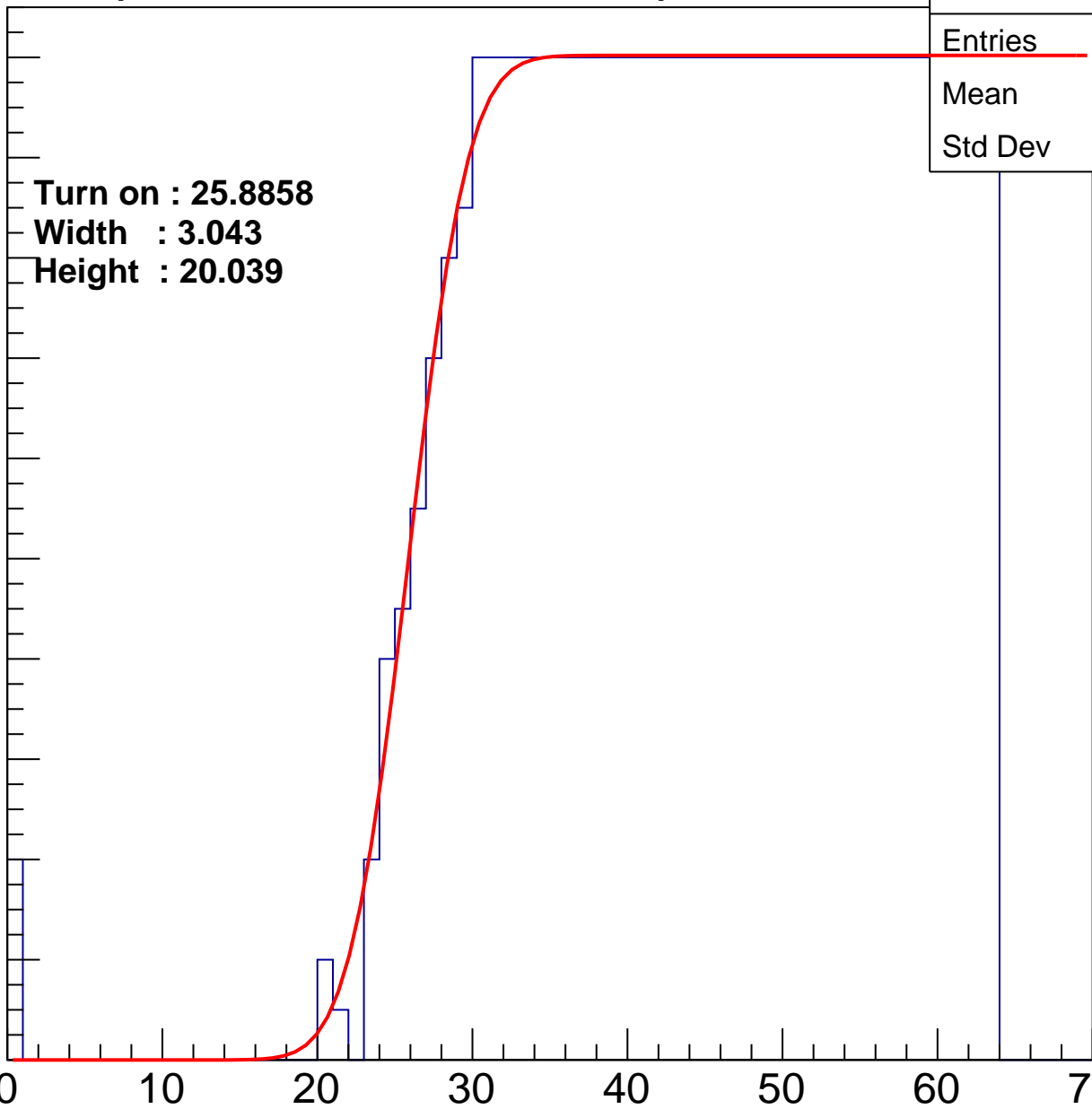
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8858
Width : 3.043
Height : 20.039

Entries	766
Mean	44.12
Std Dev	11.59

ampl



B0L101S, U19-ch22

calib_packv5_042523_0143.root, FC#1, port C1

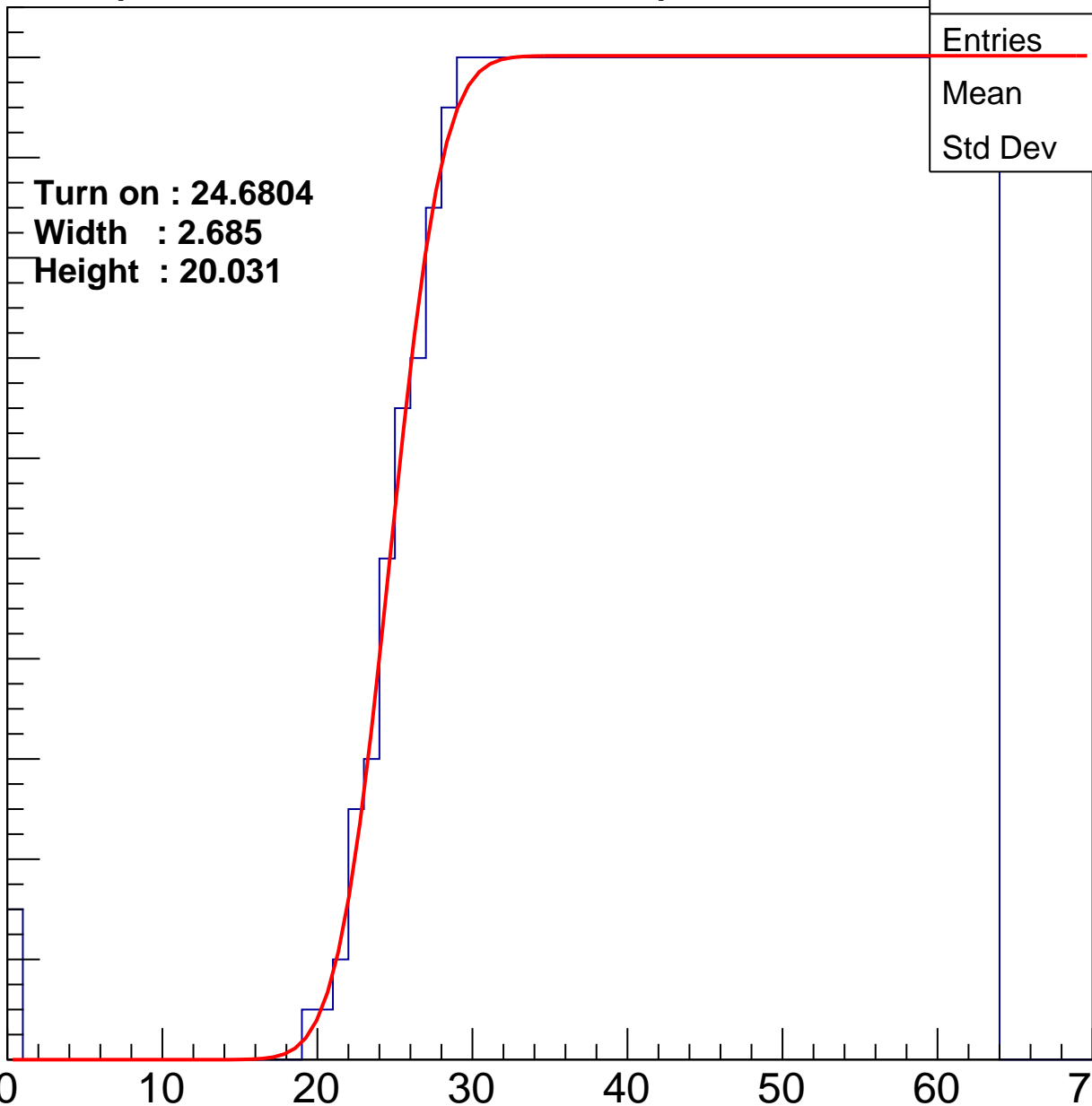
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6804
Width : 2.685
Height : 20.031

Entries	791
Mean	43.55
Std Dev	11.8

ampl



B0L101S, U19-ch23

calib_packv5_042523_0143.root, FC#1, port C1

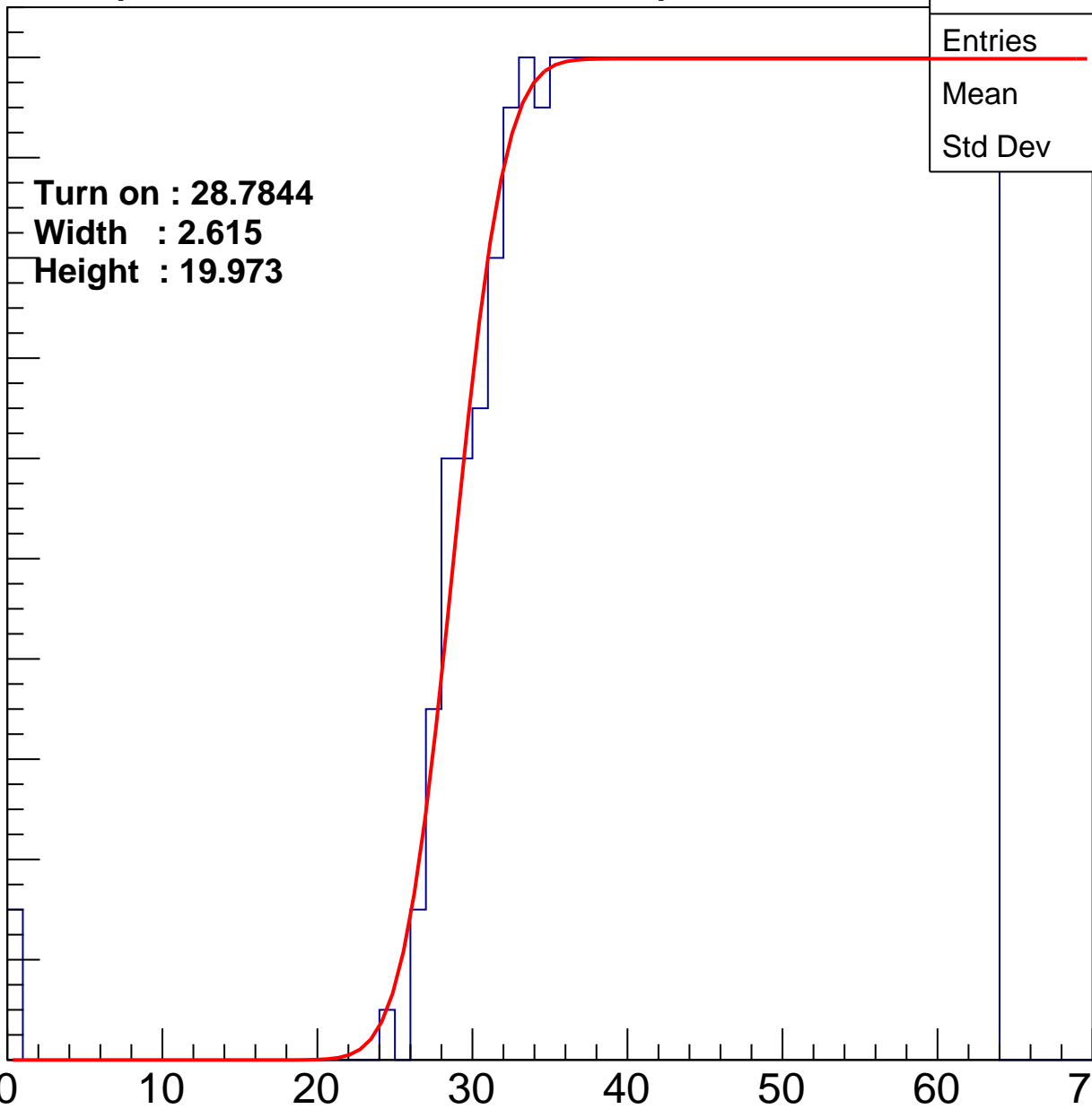
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7844
Width : 2.615
Height : 19.973

Entries	705
Mean	45.67
Std Dev	10.68

ampl



B0L101S, U19-ch24

calib_packv5_042523_0143.root, FC#1, port C1

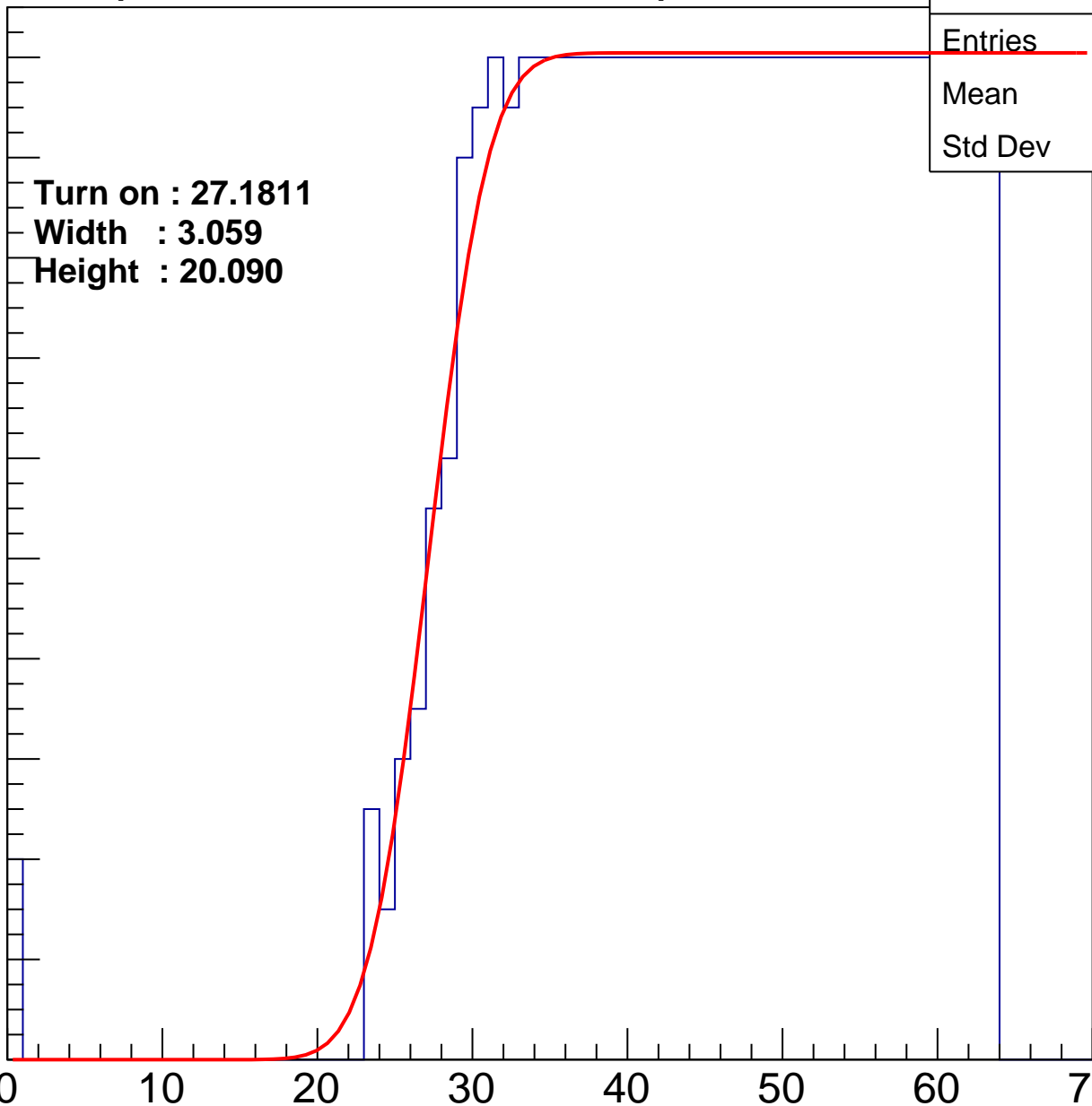
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1811
Width : 3.059
Height : 20.090

Entries	744
Mean	44.67
Std Dev	11.29

ampl



B0L101S, U19-ch25

calib_packv5_042523_0143.root, FC#1, port C1

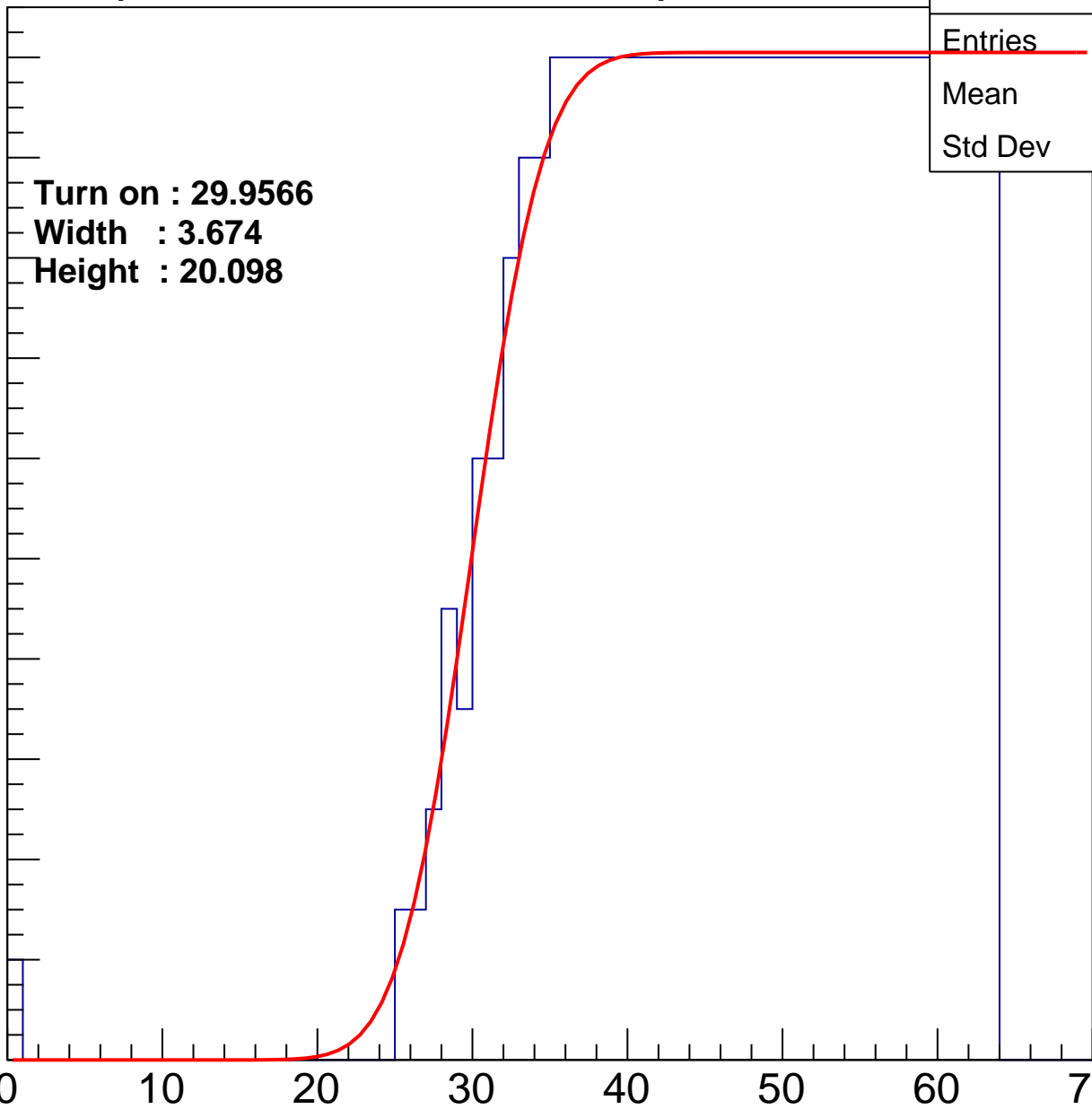
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.9566
Width : 3.674
Height : 20.098

Entries	685
Mean	46.15
Std Dev	10.38

ampl



B0L101S, U19-ch26

calib_packv5_042523_0143.root, FC#1, port C1

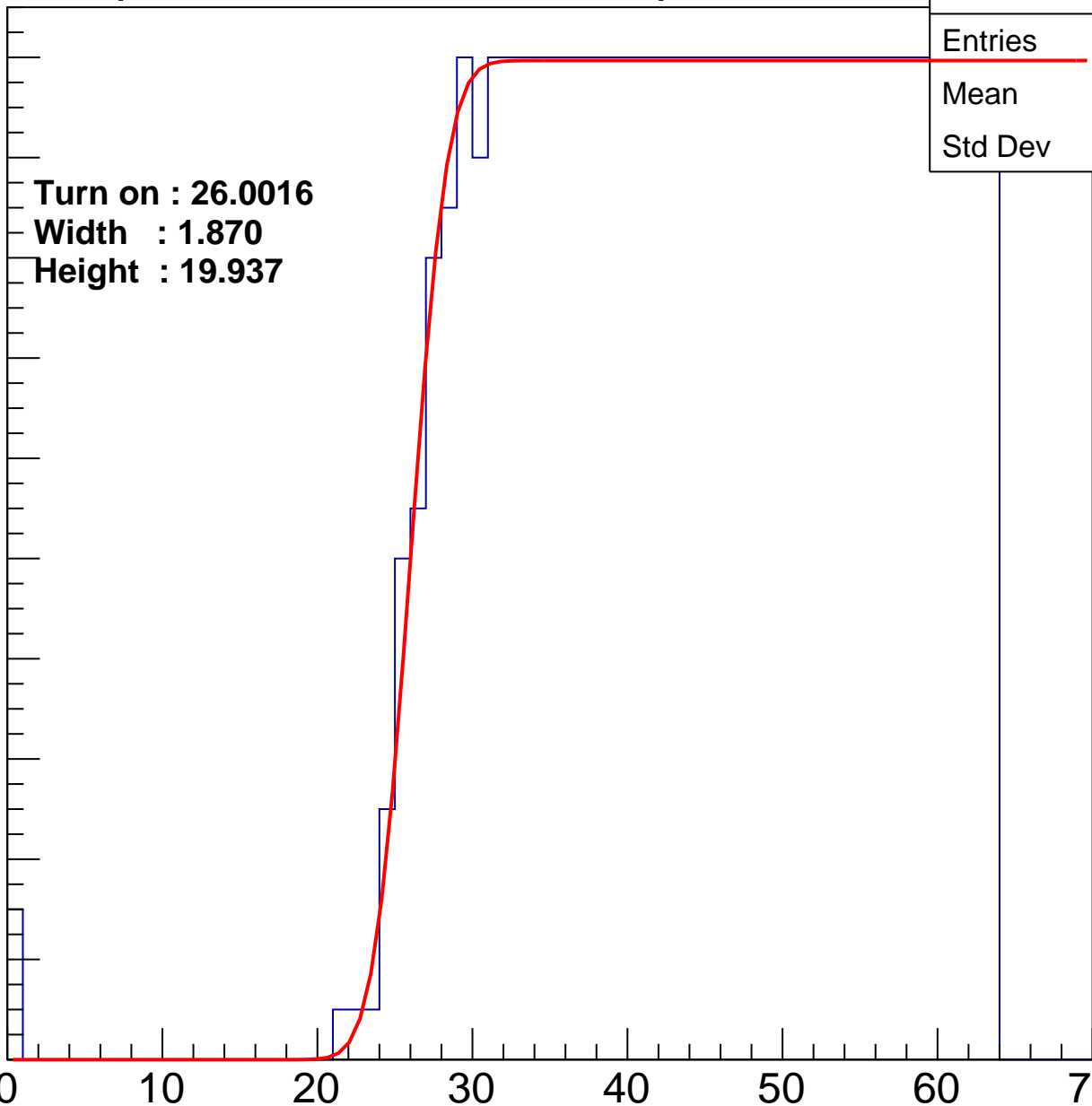
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0016
Width : 1.870
Height : 19.937

Entries	763
Mean	44.26
Std Dev	11.4

ampl



B0L101S, U19-ch27

calib_packv5_042523_0143.root, FC#1, port C1

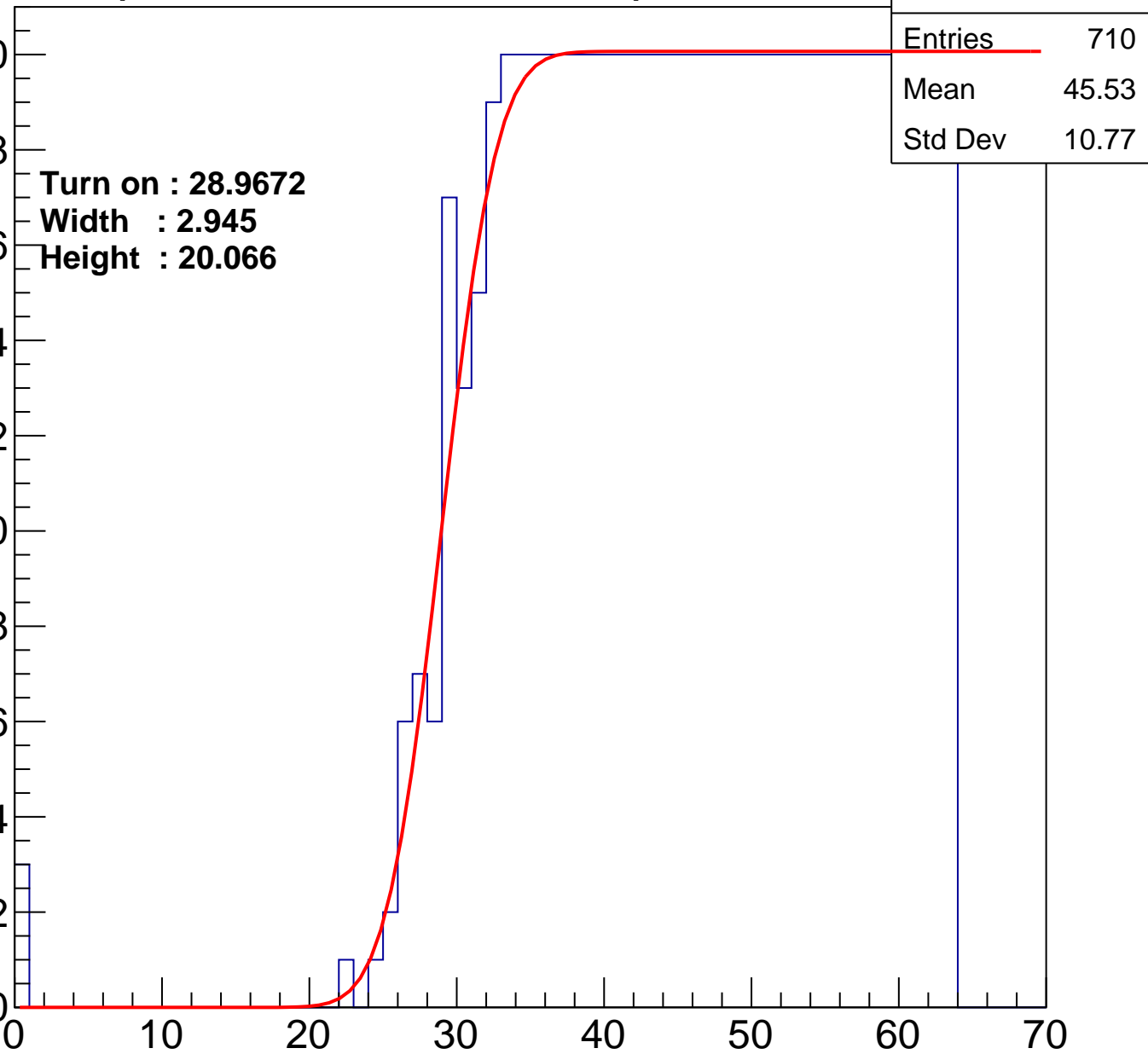
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.9672
Width : 2.945
Height : 20.066

Entries	710
Mean	45.53
Std Dev	10.77

ampl



B0L101S, U19-ch28

calib_packv5_042523_0143.root, FC#1, port C1

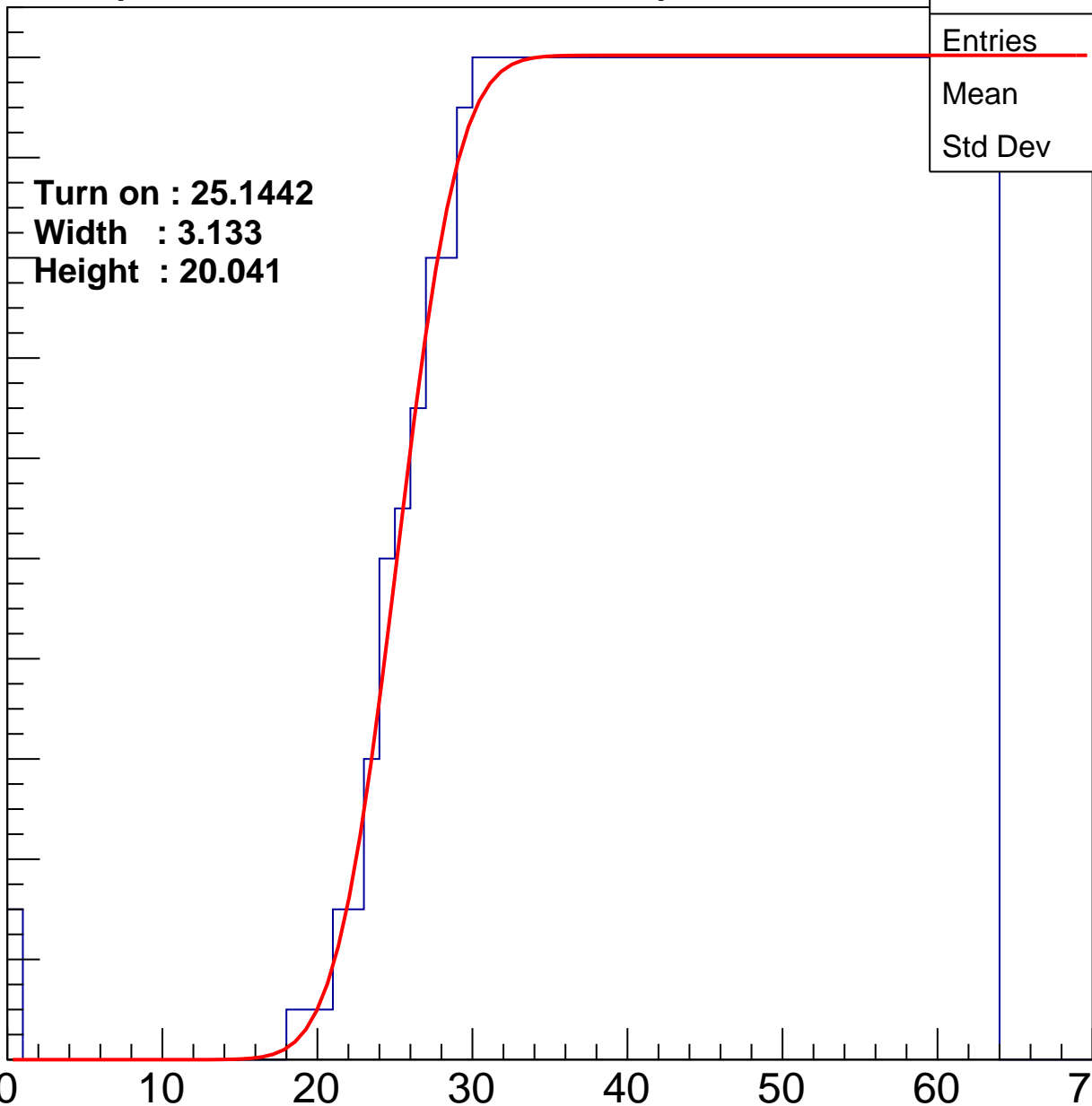
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1442
Width : 3.133
Height : 20.041

Entries	783
Mean	43.71
Std Dev	11.75

ampl



B0L101S, U19-ch29

calib_packv5_042523_0143.root, FC#1, port C1

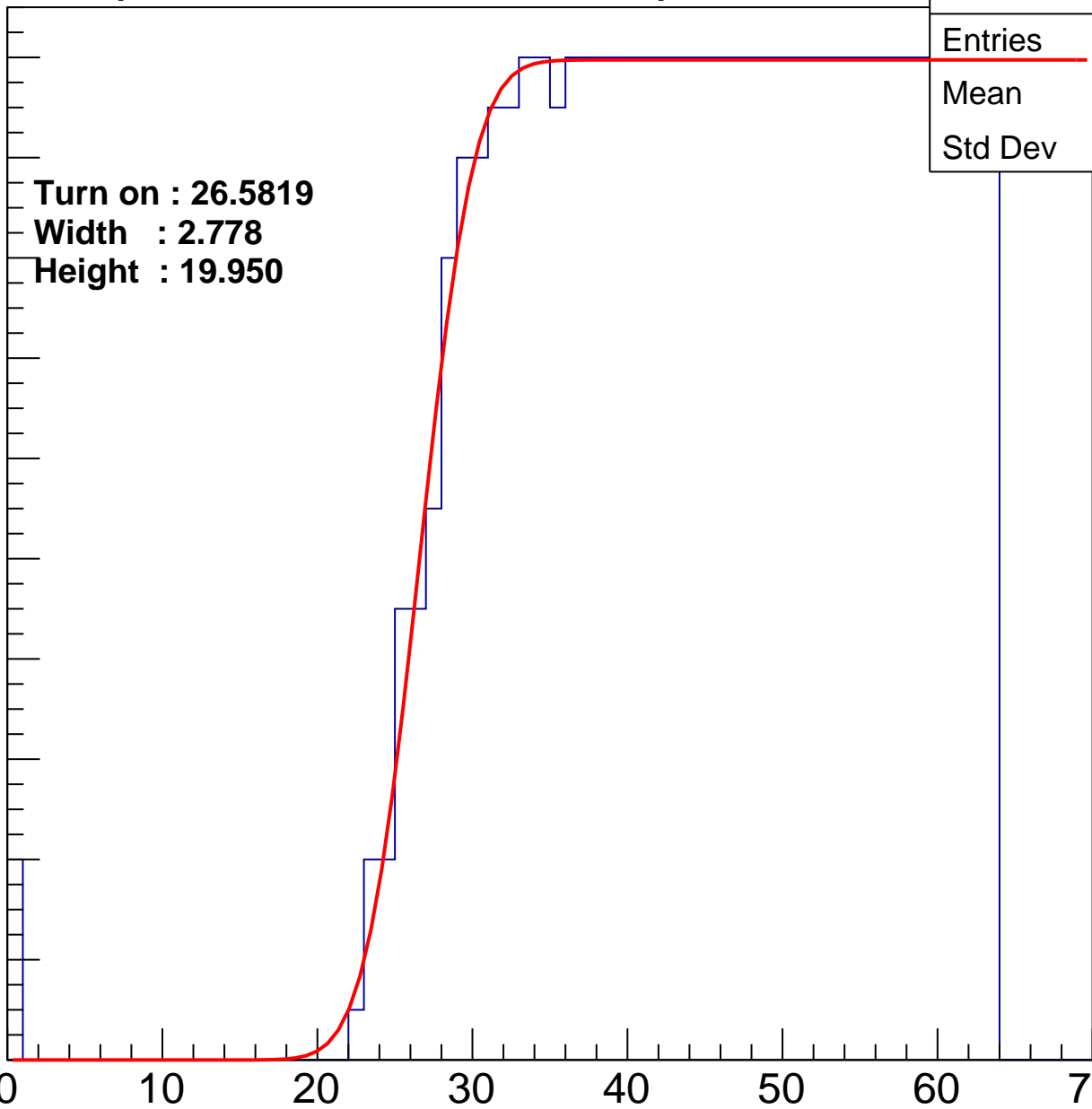
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5819
Width : 2.778
Height : 19.950

Entries	751
Mean	44.47
Std Dev	11.41

ampl



B0L101S, U19-ch30

calib_packv5_042523_0143.root, FC#1, port C1

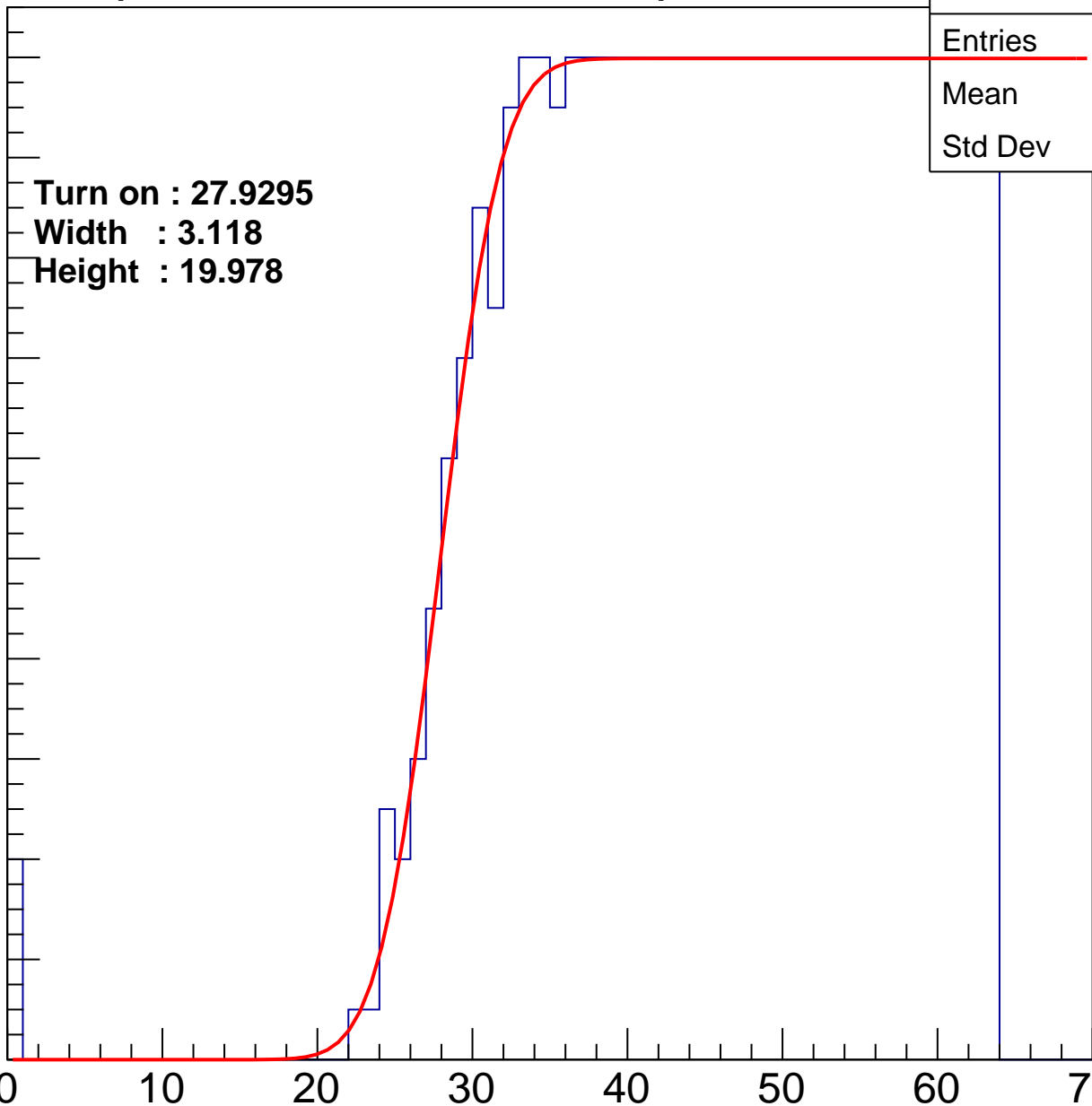
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9295
Width : 3.118
Height : 19.978

Entries	726
Mean	45.06
Std Dev	11.14

ampl



B0L101S, U19-ch31

calib_packv5_042523_0143.root, FC#1, port C1

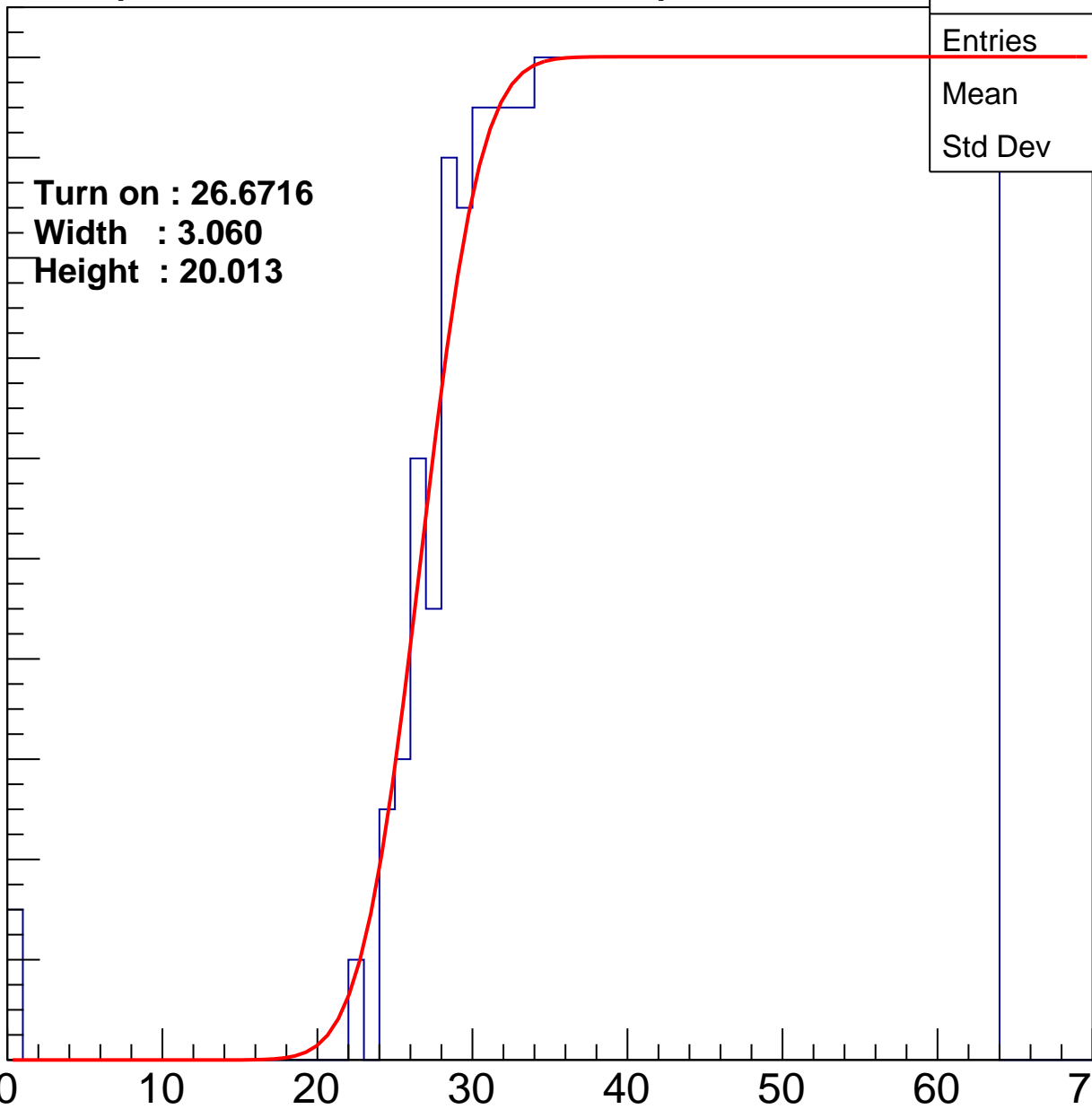
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6716
Width : 3.060
Height : 20.013

Entries	748
Mean	44.6
Std Dev	11.25

ampl



B0L101S, U19-ch32

calib_packv5_042523_0143.root, FC#1, port C1

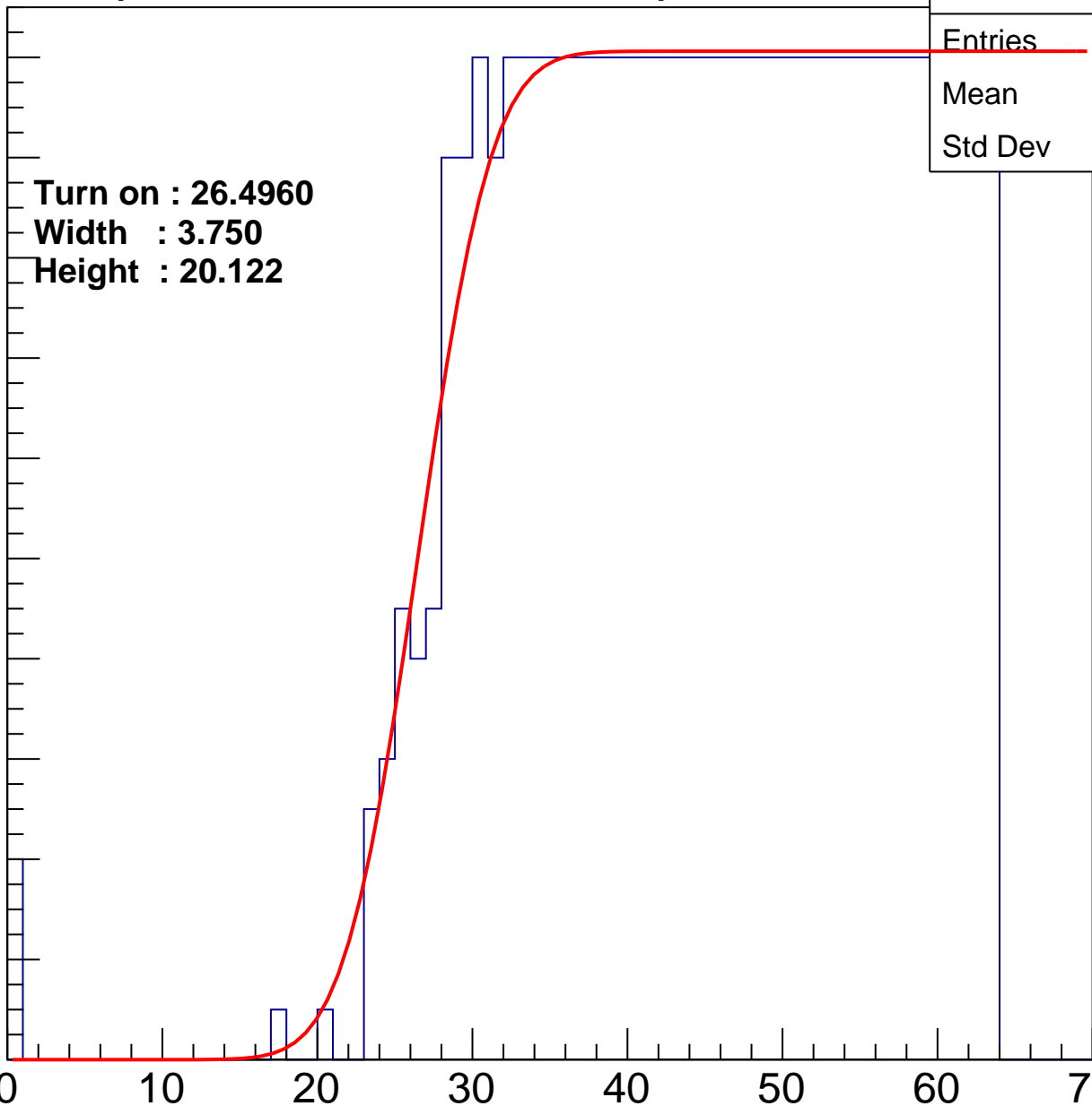
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4960
Width : 3.750
Height : 20.122

Entries	757
Mean	44.33
Std Dev	11.5

ampl



B0L101S, U19-ch33

calib_packv5_042523_0143.root, FC#1, port C1

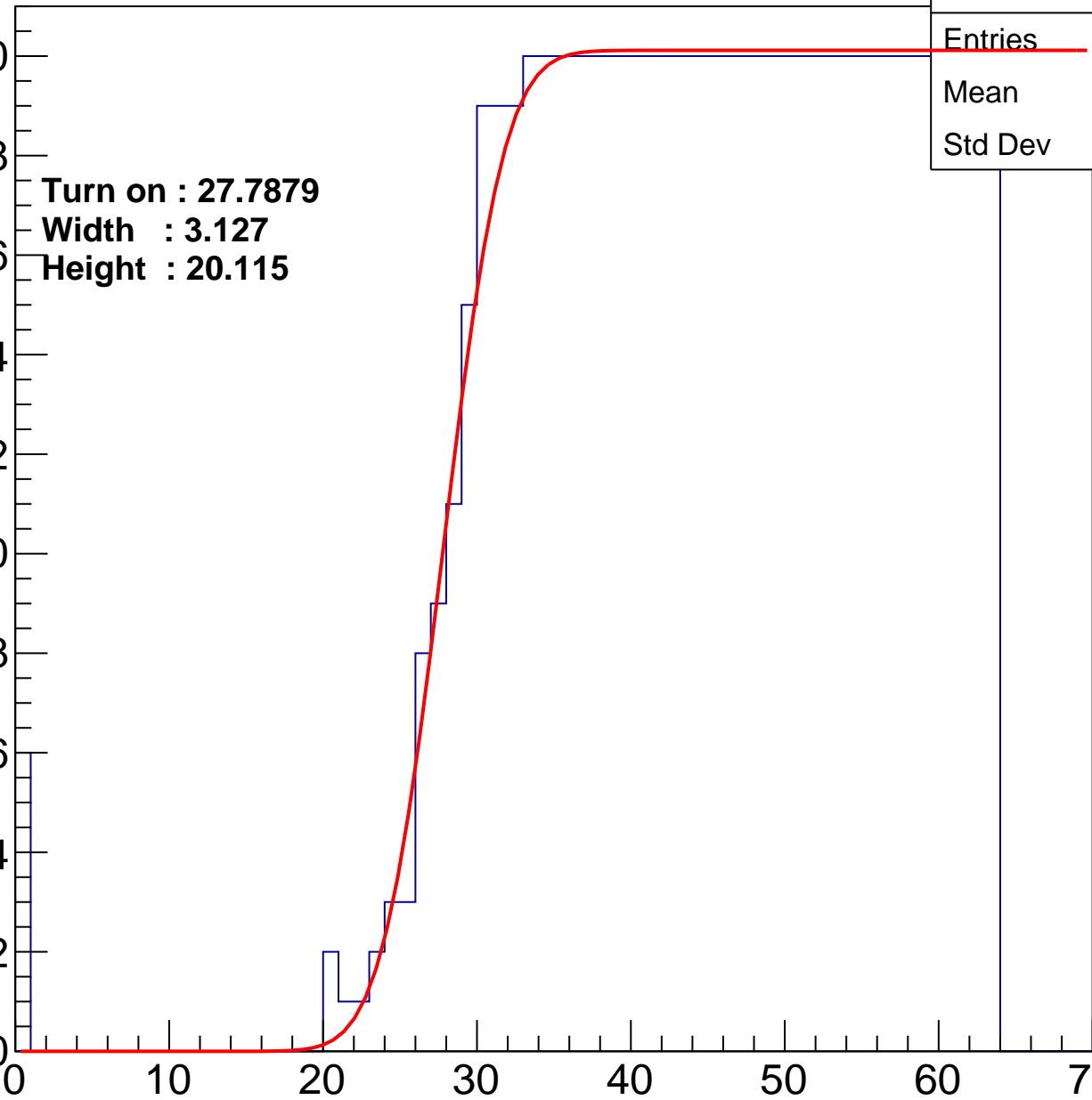
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7879
Width : 3.127
Height : 20.115

Entries	738
Mean	44.71
Std Dev	11.47

ampl



B0L101S, U19-ch34

calib_packv5_042523_0143.root, FC#1, port C1

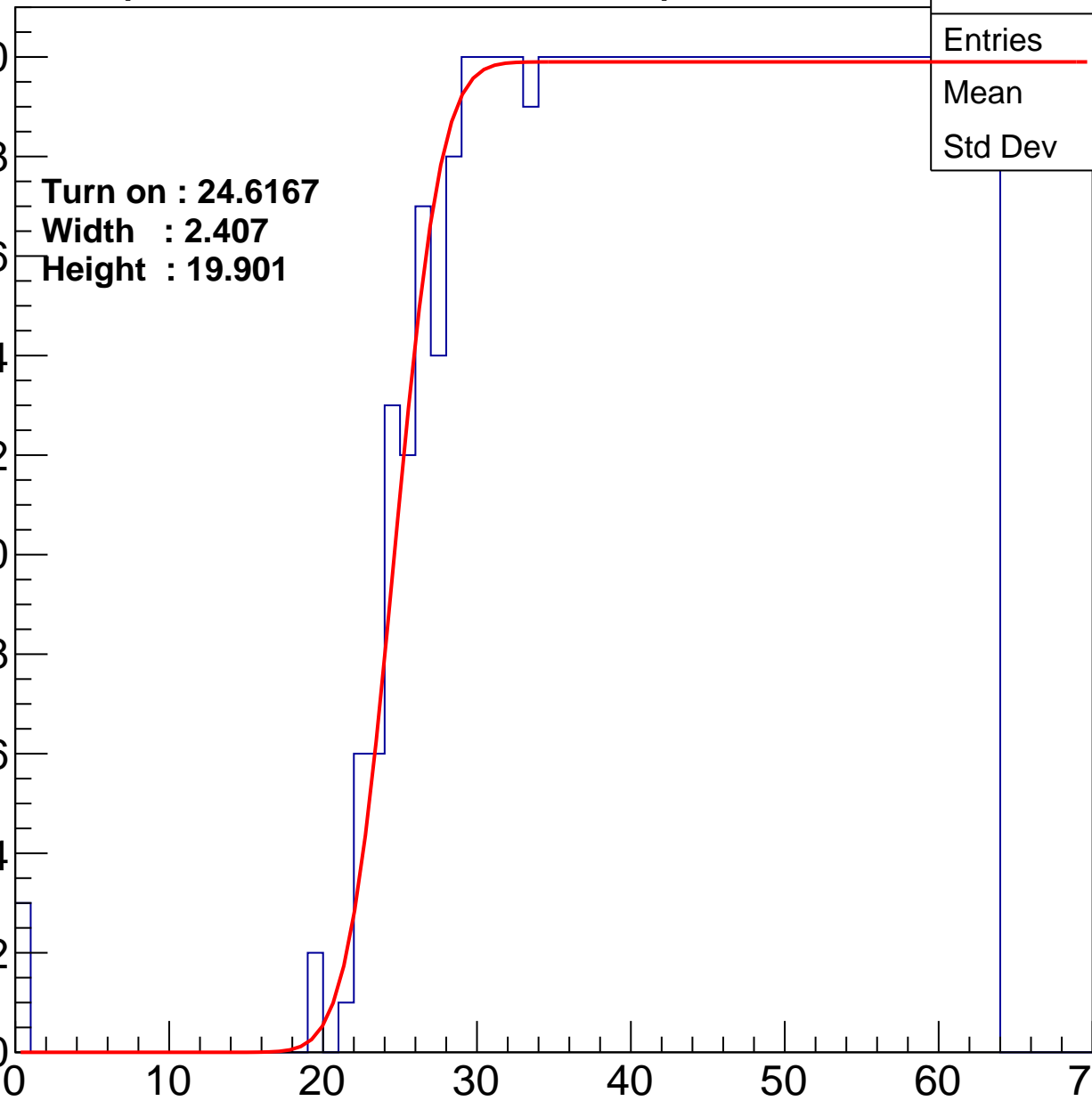
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6167
Width : 2.407
Height : 19.901

Entries	791
Mean	43.53
Std Dev	11.83

ampl



B0L101S, U19-ch35

calib_packv5_042523_0143.root, FC#1, port C1

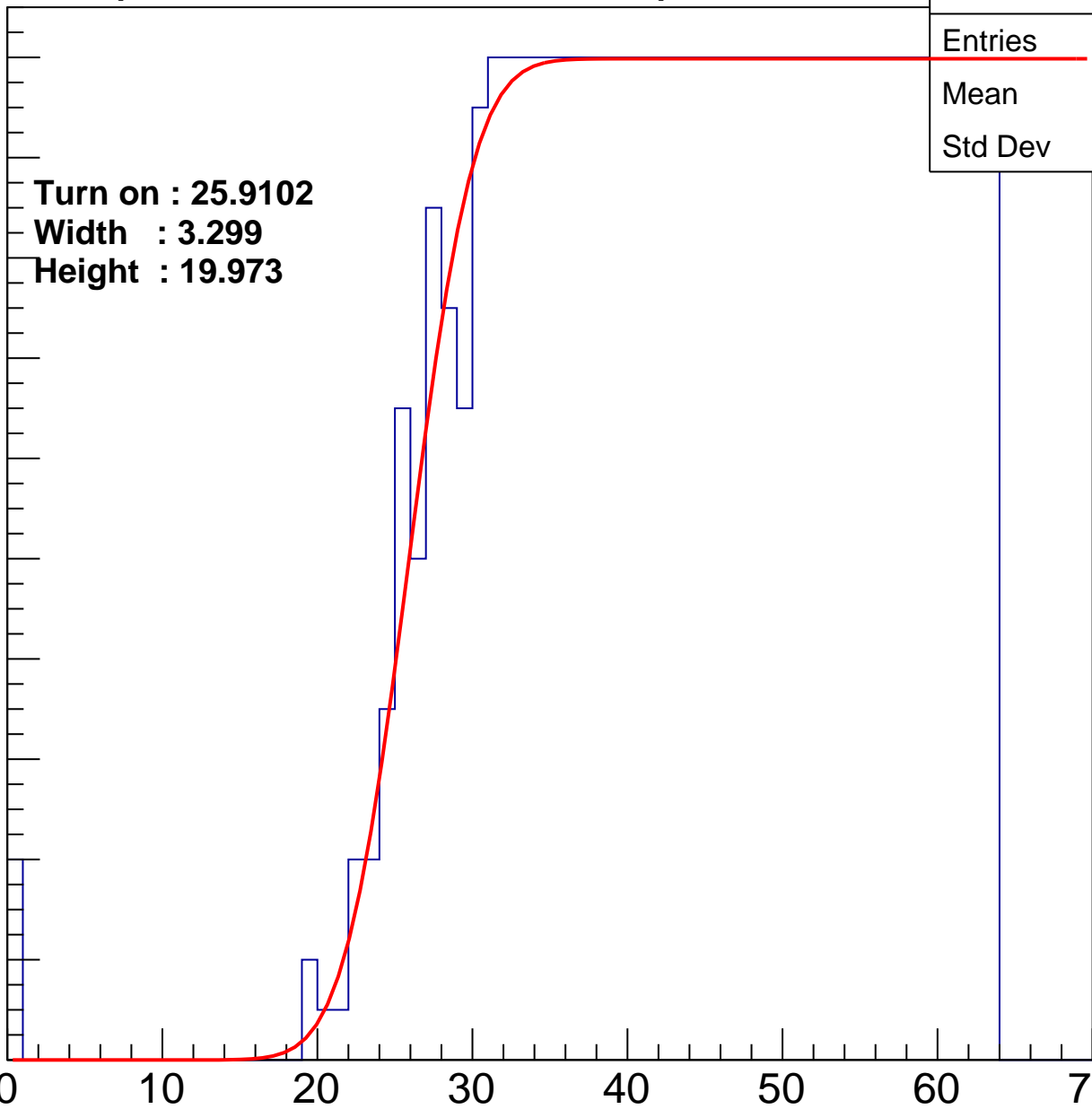
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9102
Width : 3.299
Height : 19.973

Entries	770
Mean	43.97
Std Dev	11.72

ampl



B0L101S, U19-ch36

calib_packv5_042523_0143.root, FC#1, port C1

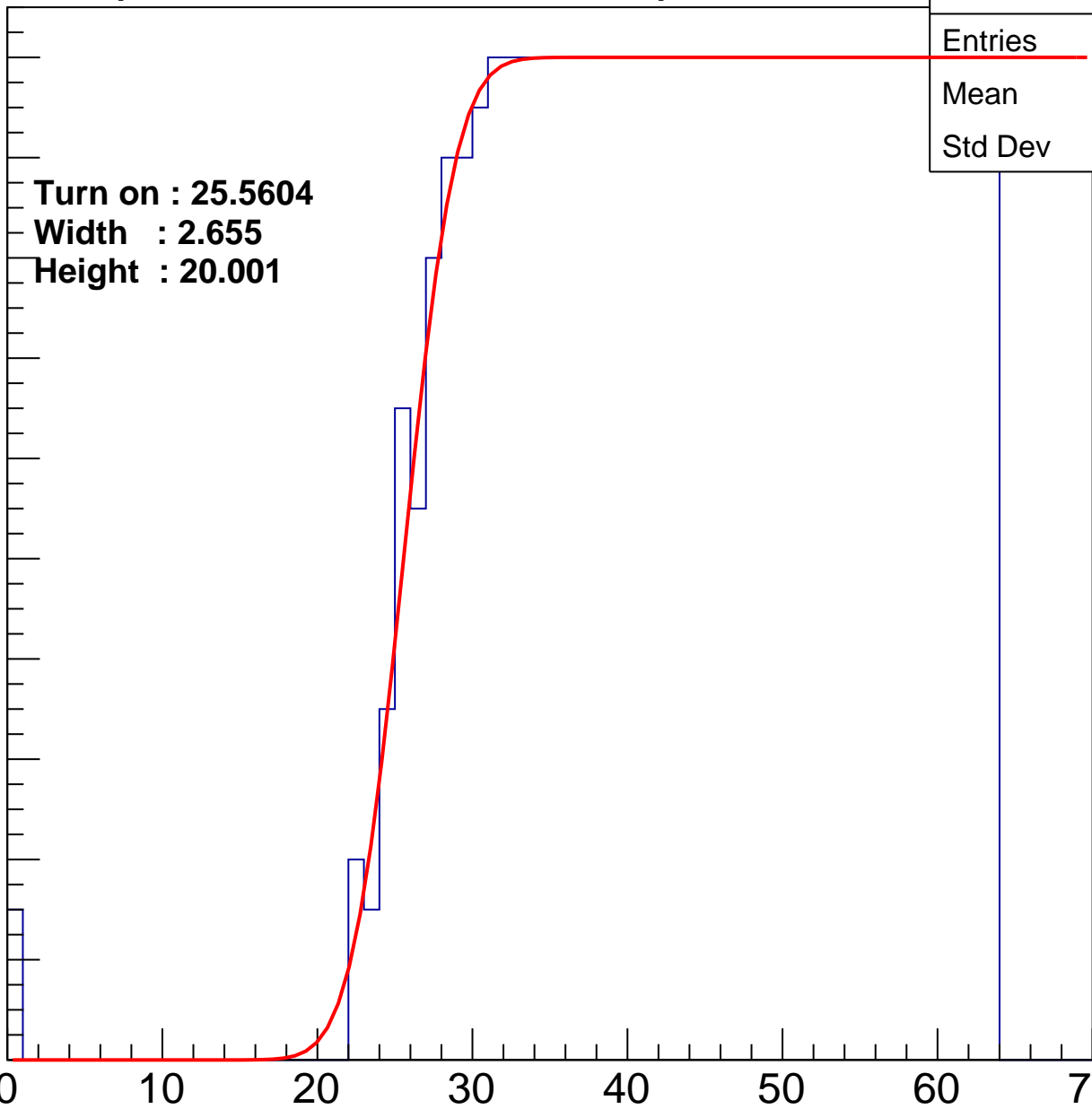
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5604
Width : 2.655
Height : 20.001

Entries	772
Mean	44.02
Std Dev	11.55

ampl



B0L101S, U19-ch37

calib_packv5_042523_0143.root, FC#1, port C1

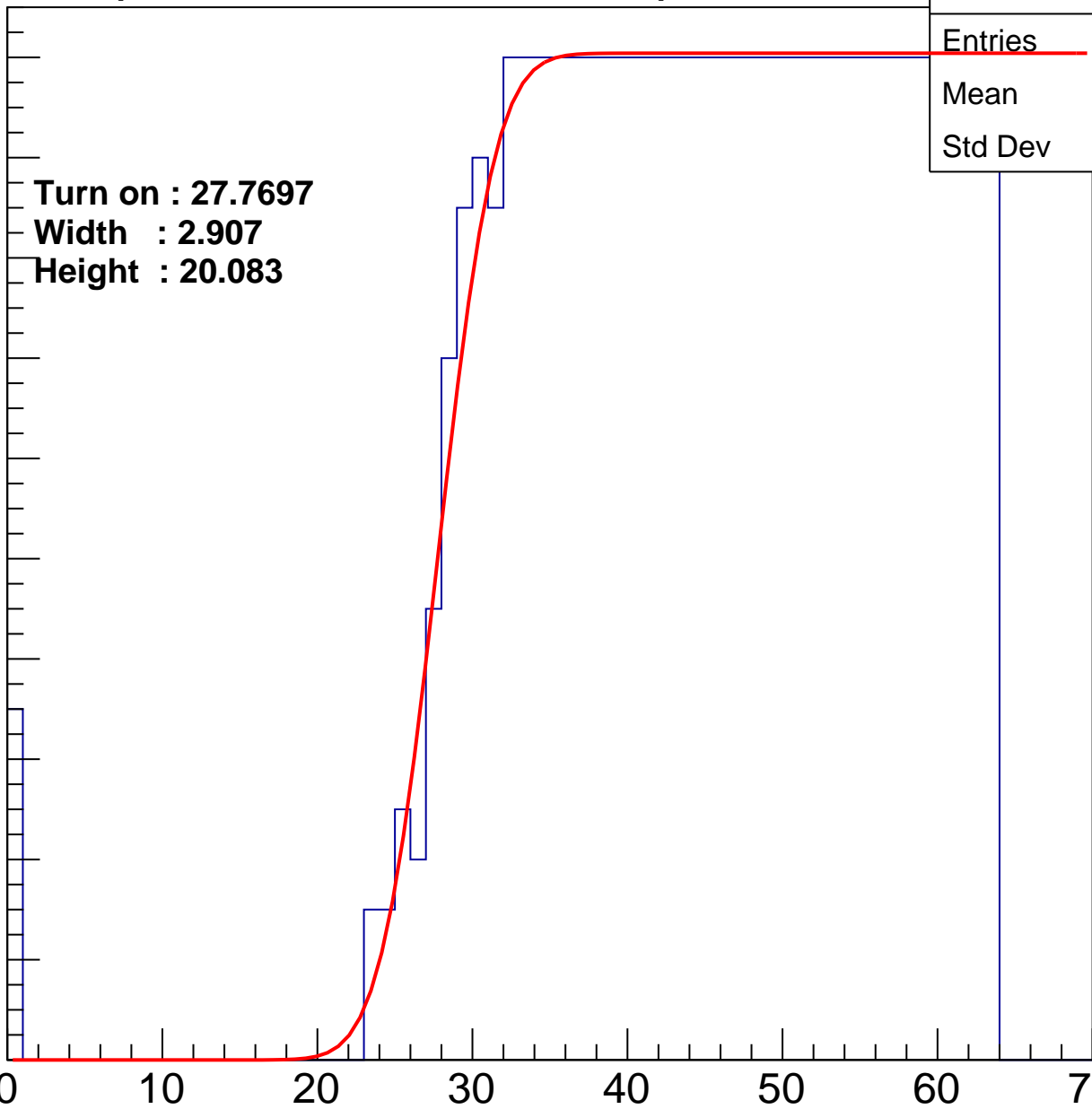
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7697
Width : 2.907
Height : 20.083

Entries	737
Mean	44.73
Std Dev	11.51

ampl



B0L101S, U19-ch38

calib_packv5_042523_0143.root, FC#1, port C1

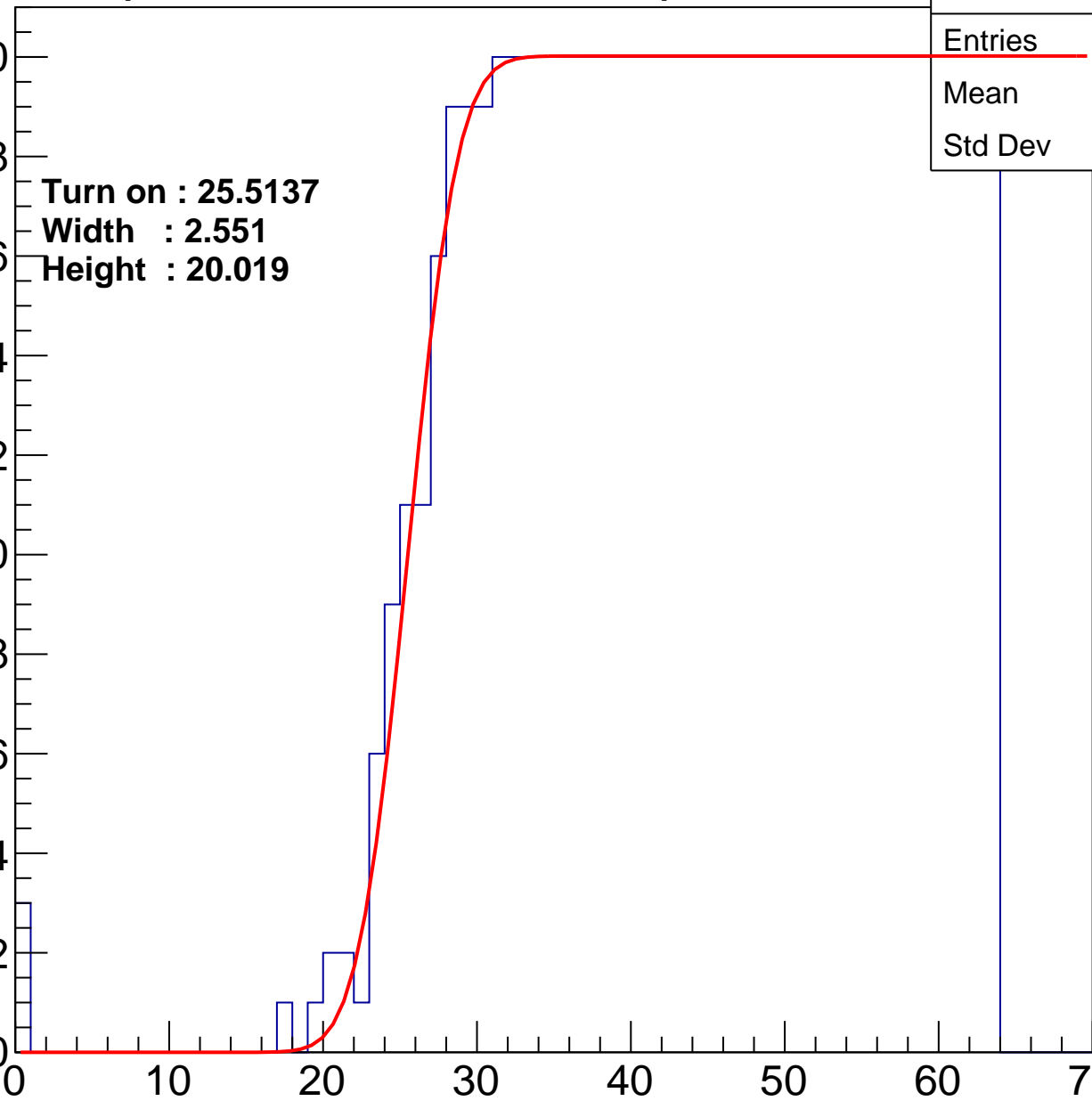
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5137
Width : 2.551
Height : 20.019

Entries	780
Mean	43.79
Std Dev	11.71

ampl



B0L101S, U19-ch39

calib_packv5_042523_0143.root, FC#1, port C1

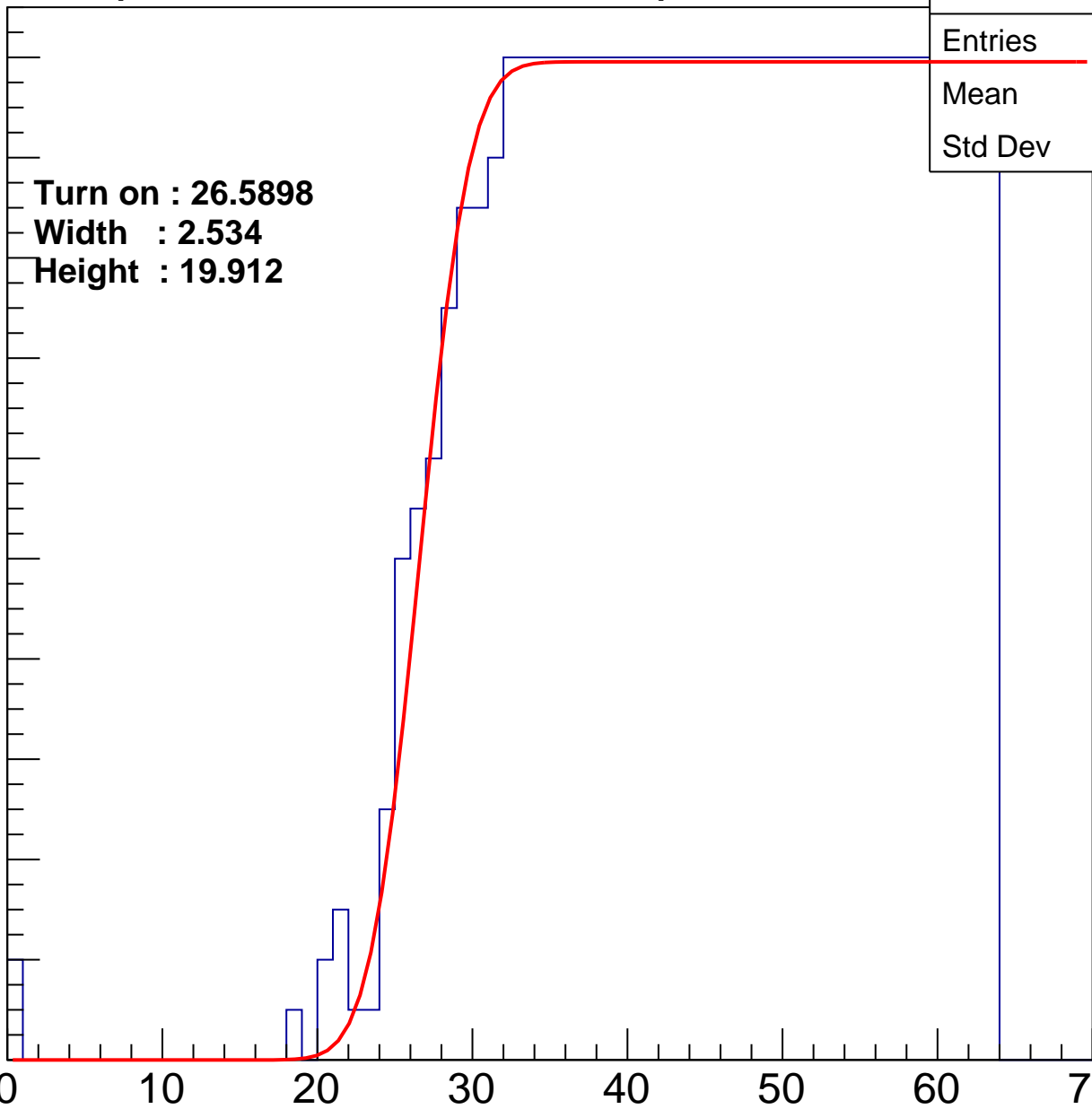
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5898
Width : 2.534
Height : 19.912

Entries	755
Mean	44.41
Std Dev	11.34

ampl



B0L101S, U19-ch40

calib_packv5_042523_0143.root, FC#1, port C1

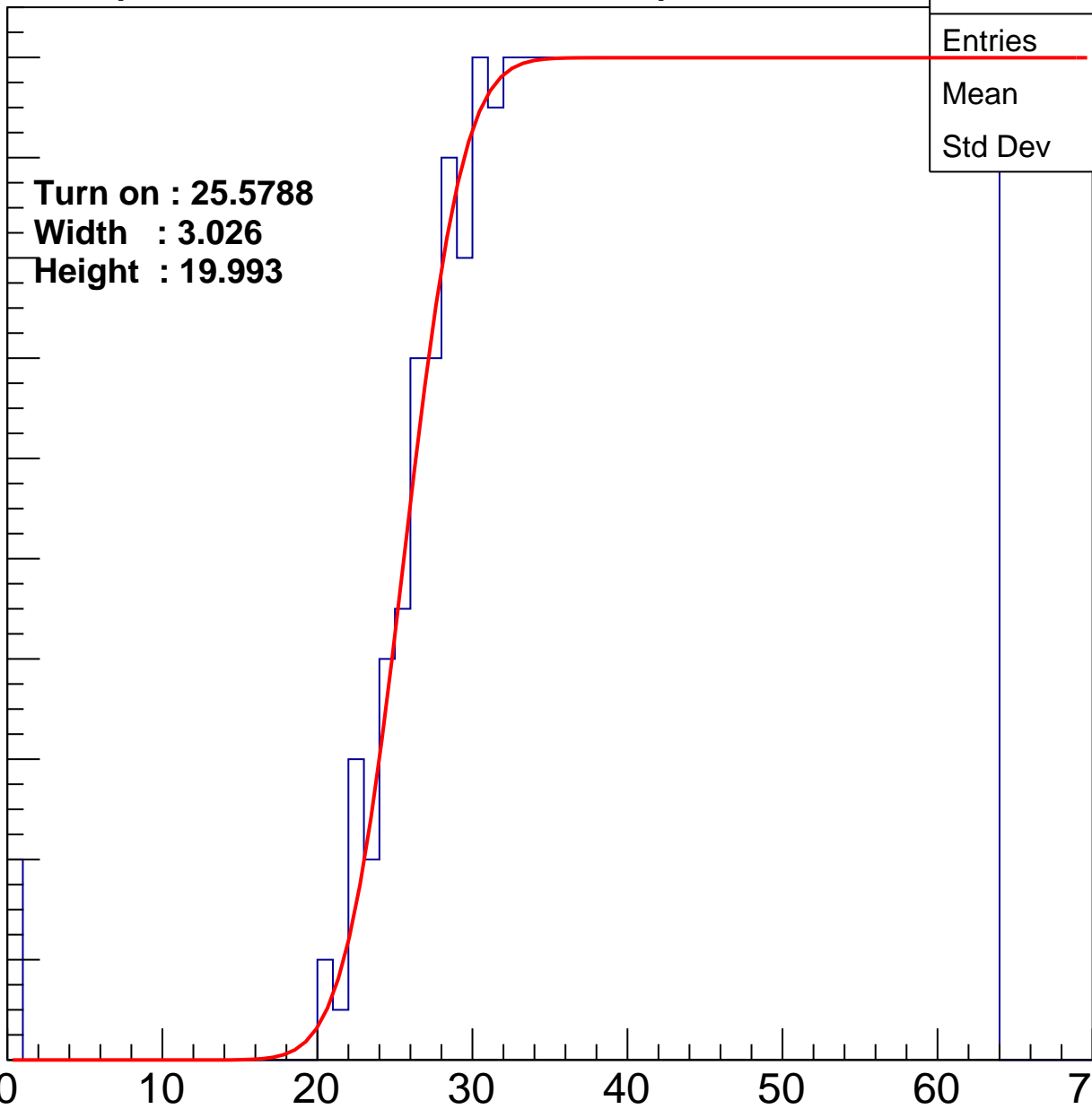
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5788
Width : 3.026
Height : 19.993

Entries	775
Mean	43.87
Std Dev	11.74

ampl



B0L101S, U19-ch41

calib_packv5_042523_0143.root, FC#1, port C1

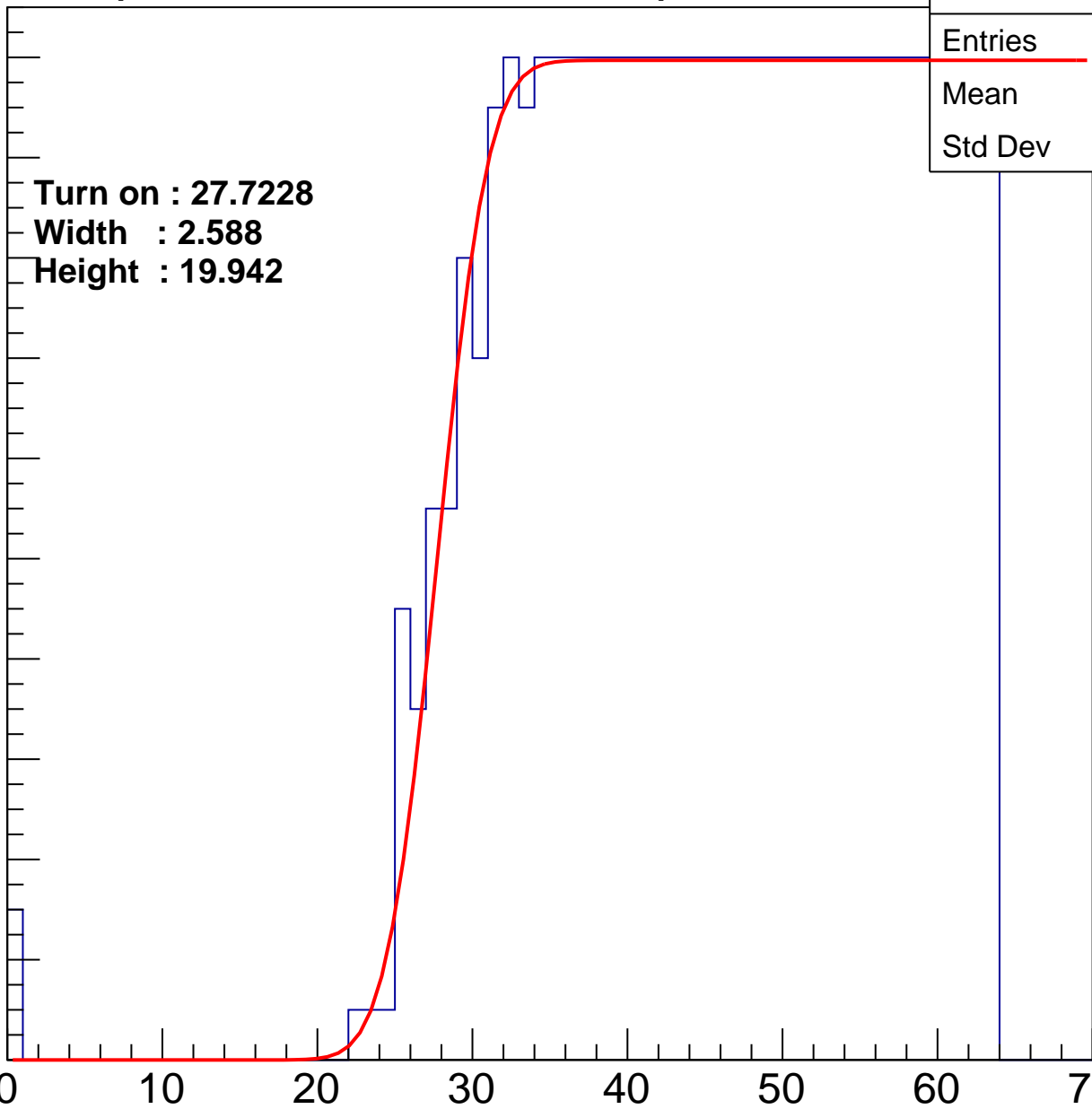
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7228
Width : 2.588
Height : 19.942

Entries	732
Mean	44.97
Std Dev	11.08

ampl



B0L101S, U19-ch42

calib_packv5_042523_0143.root, FC#1, port C1

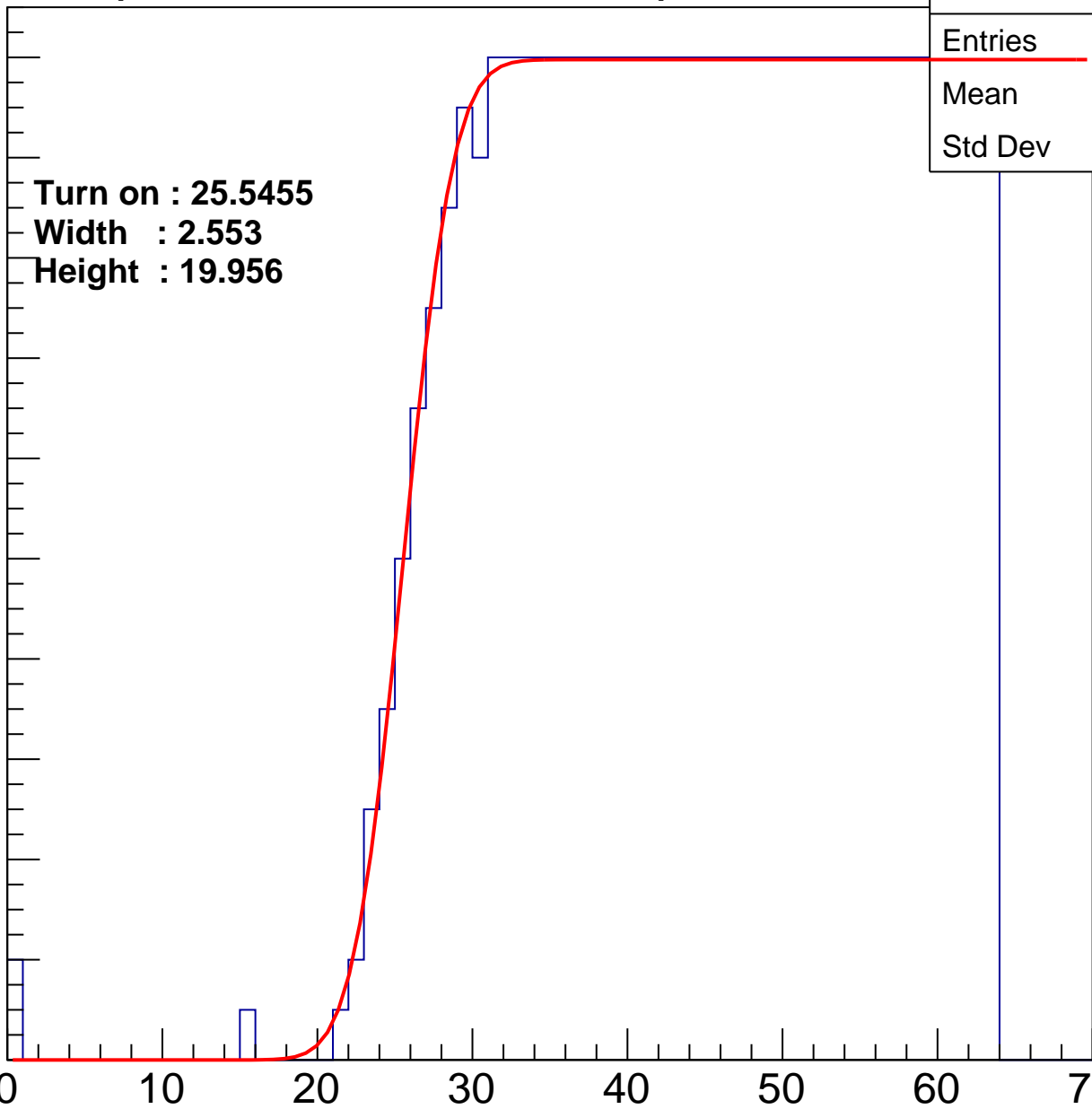
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5455
Width : 2.553
Height : 19.956

Entries	770
Mean	44.08
Std Dev	11.47

ampl



B0L101S, U19-ch43

calib_packv5_042523_0143.root, FC#1, port C1

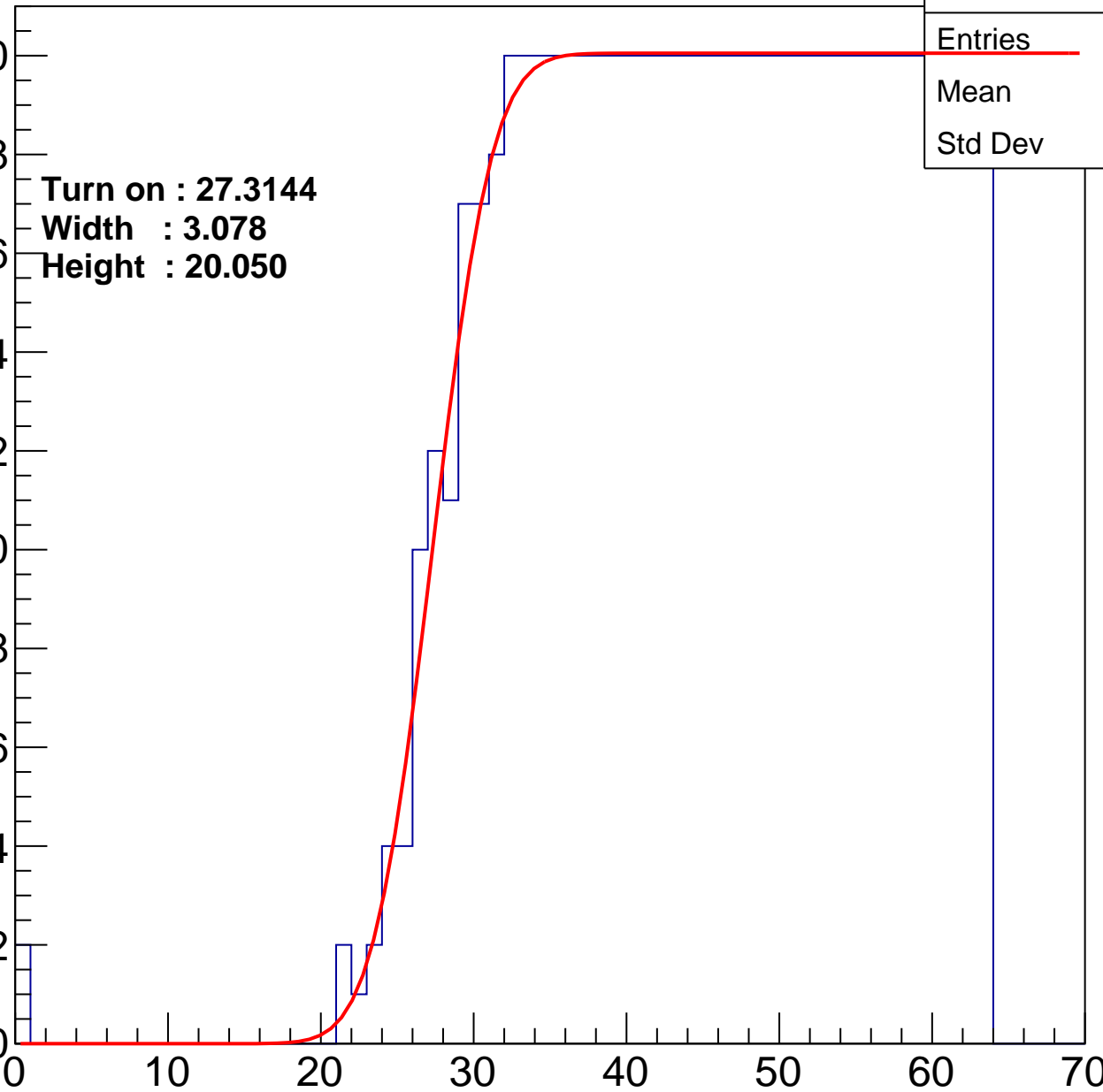
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3144
Width : 3.078
Height : 20.050

Entries	740
Mean	44.81
Std Dev	11.09

ampl



B0L101S, U19-ch44

calib_packv5_042523_0143.root, FC#1, port C1

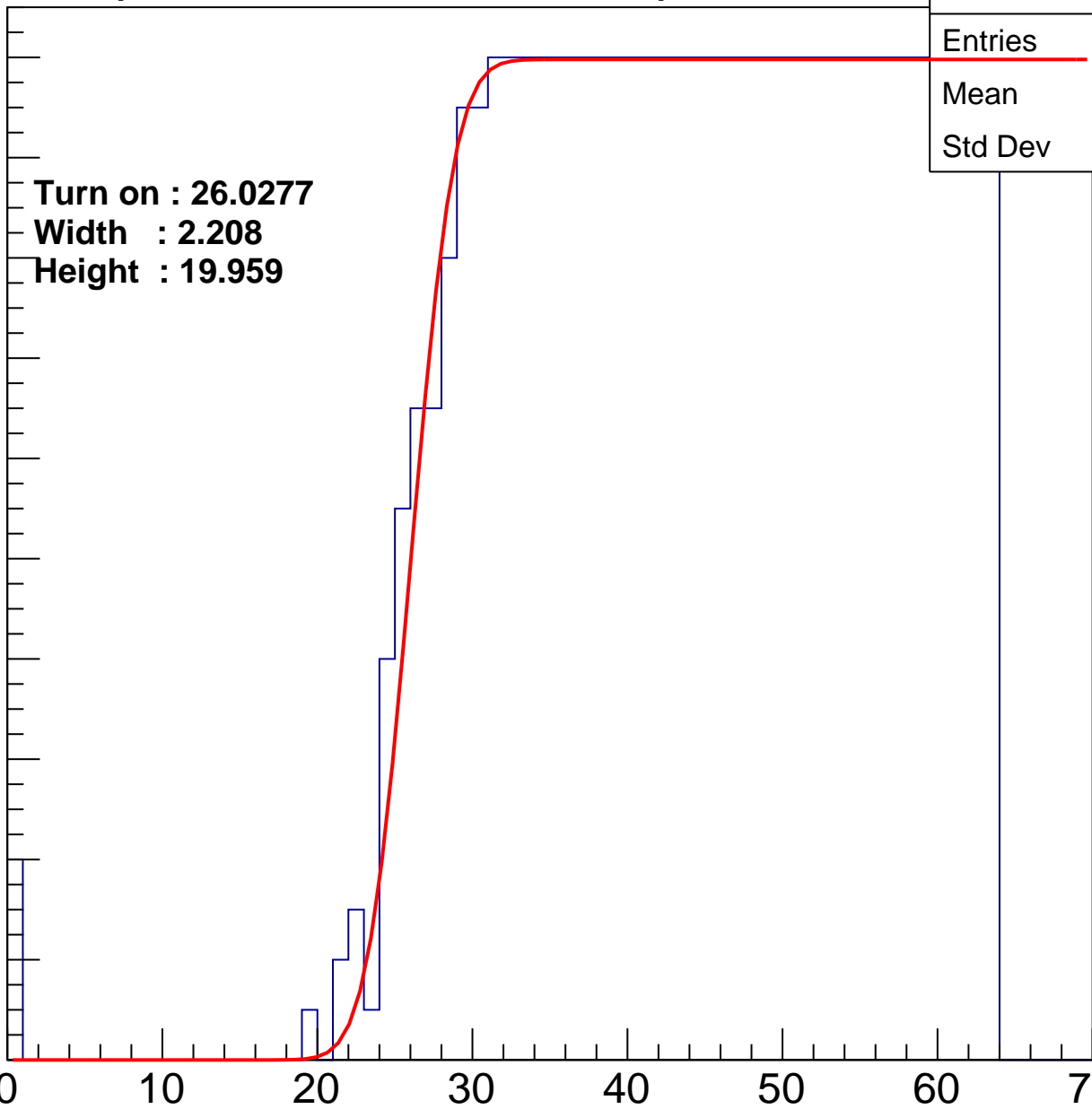
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0277
Width : 2.208
Height : 19.959

Entries	770
Mean	44.02
Std Dev	11.64

ampl



B0L101S, U19-ch45

calib_packv5_042523_0143.root, FC#1, port C1

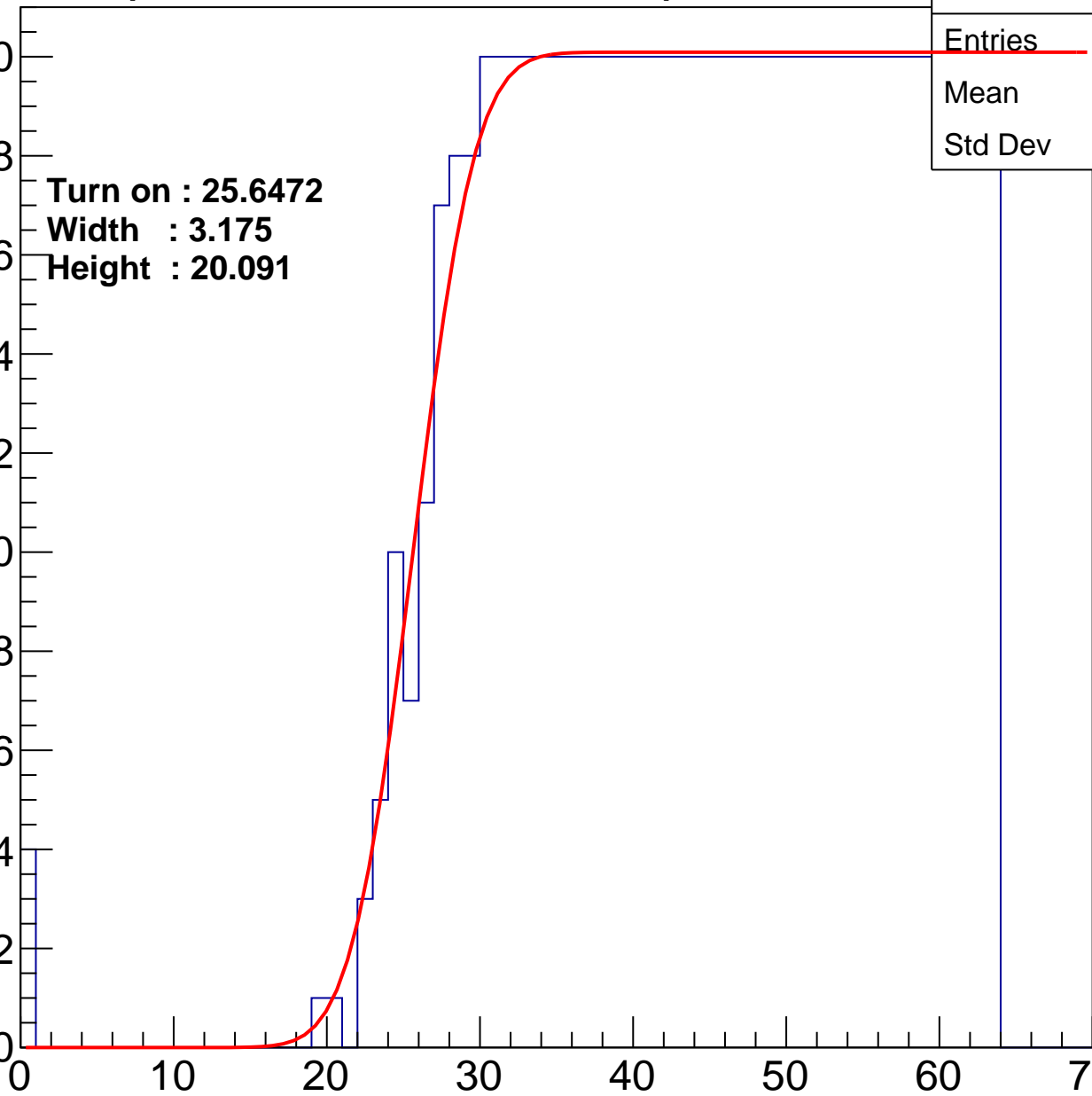
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6472
Width : 3.175
Height : 20.091

Entries	775
Mean	43.9
Std Dev	11.69

ampl



B0L101S, U19-ch46

calib_packv5_042523_0143.root, FC#1, port C1

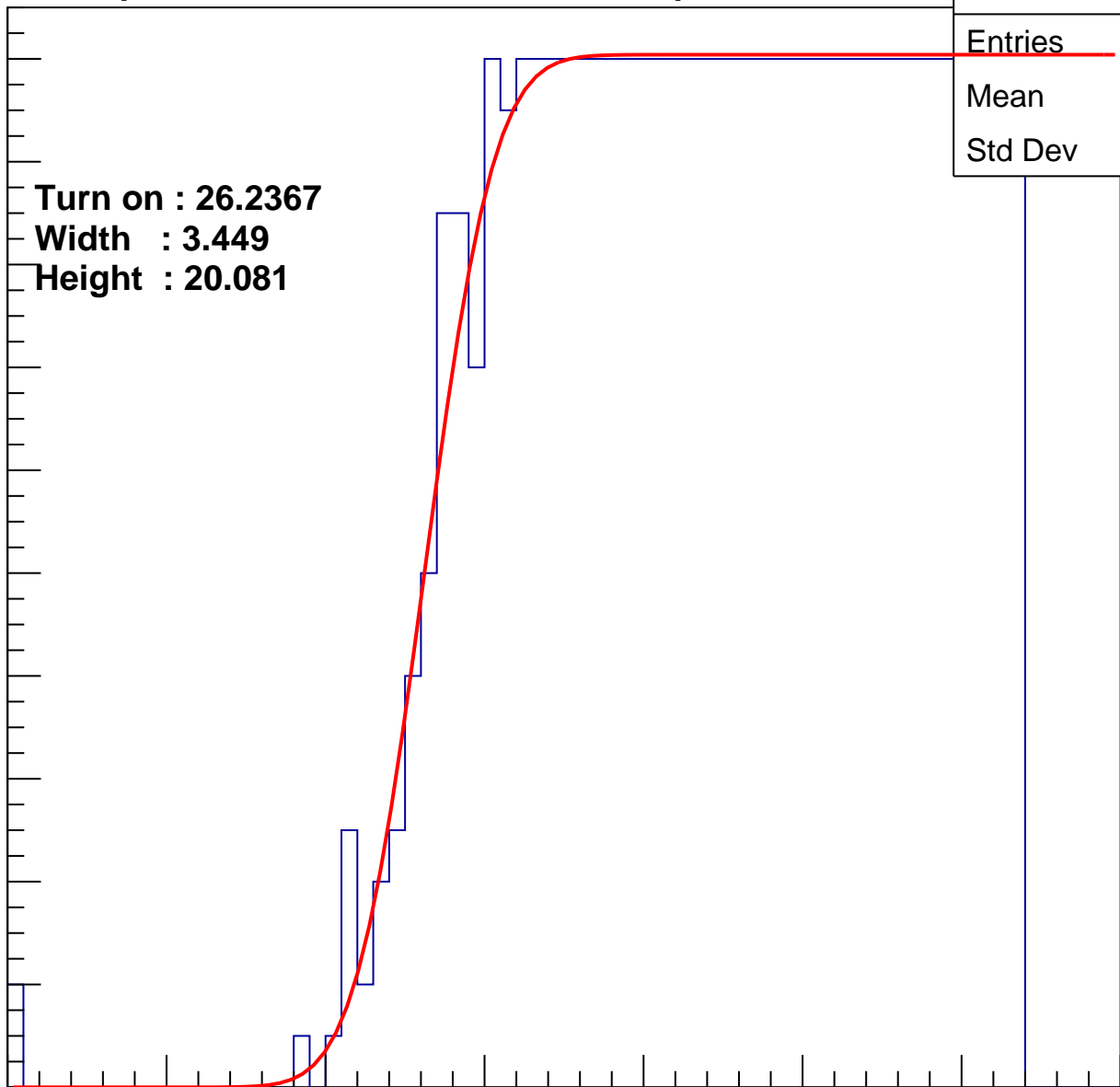
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2367
Width : 3.449
Height : 20.081

Entries	765
Mean	44.17
Std Dev	11.46

ampl



B0L101S, U19-ch47

calib_packv5_042523_0143.root, FC#1, port C1

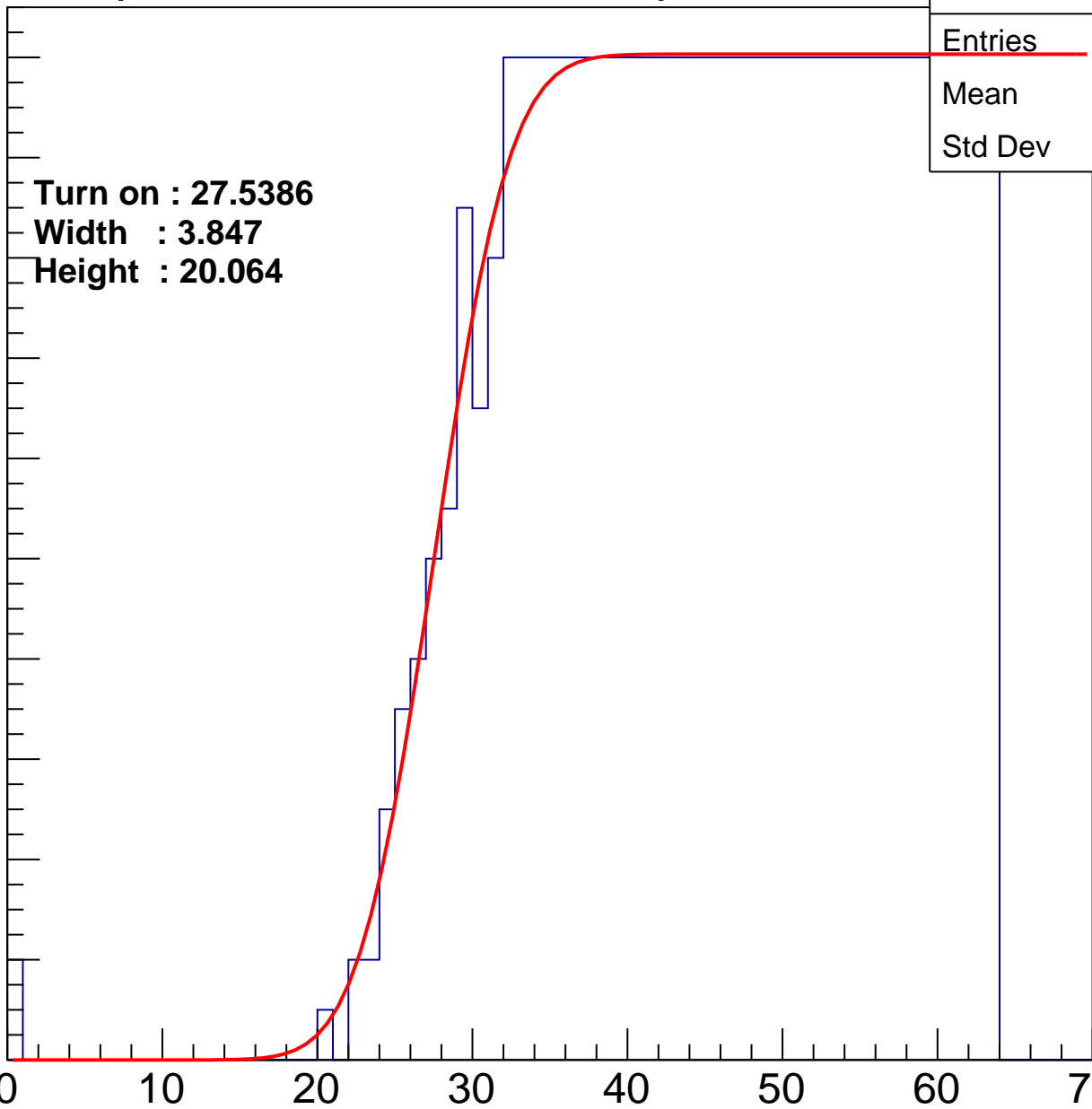
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5386
Width : 3.847
Height : 20.064

Entries	734
Mean	44.92
Std Dev	11.07

ampl



B0L101S, U19-ch48

calib_packv5_042523_0143.root, FC#1, port C1

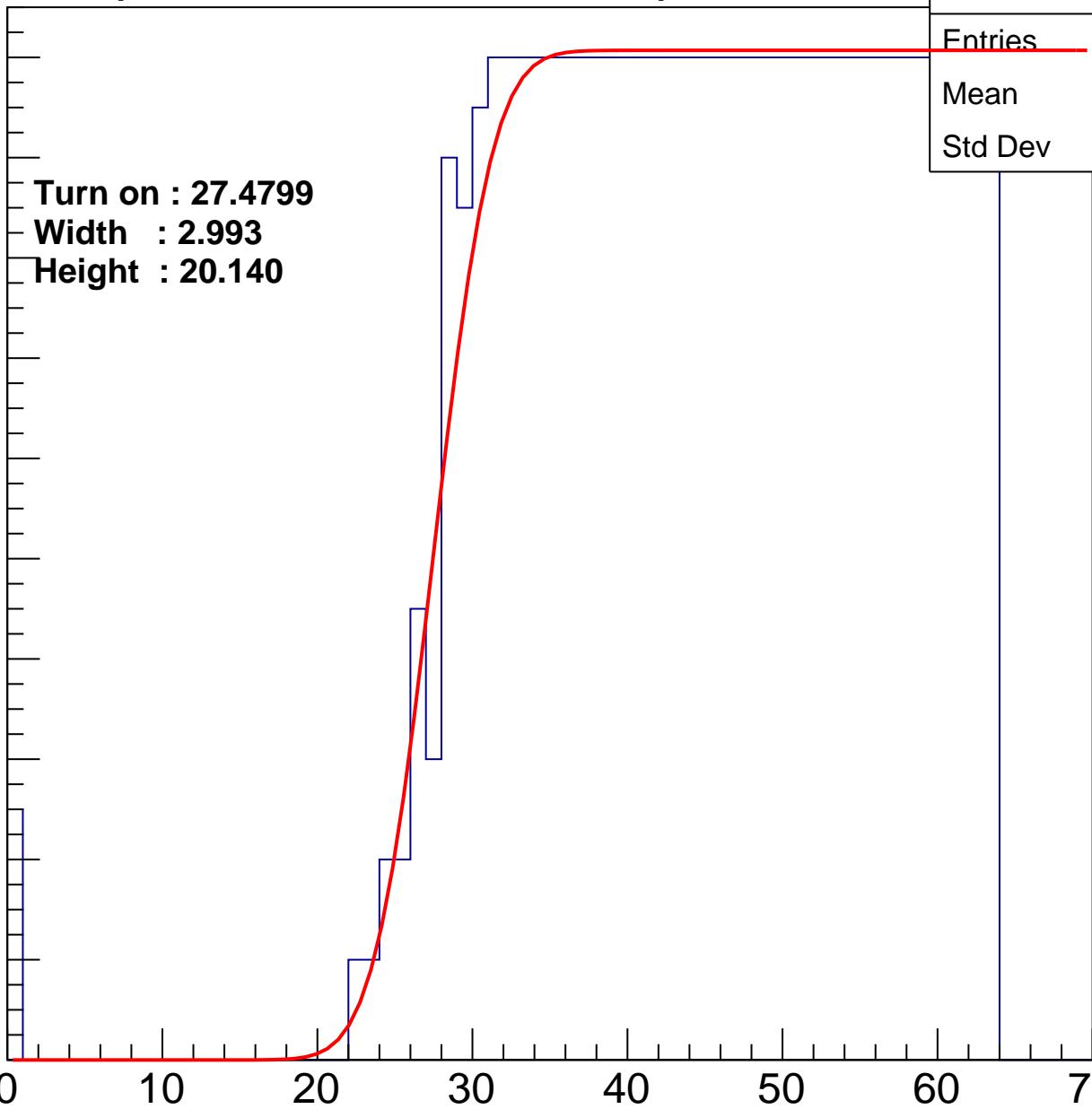
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4799
Width : 2.993
Height : 20.140

Entries	746
Mean	44.6
Std Dev	11.4

ampl



B0L101S, U19-ch49

calib_packv5_042523_0143.root, FC#1, port C1

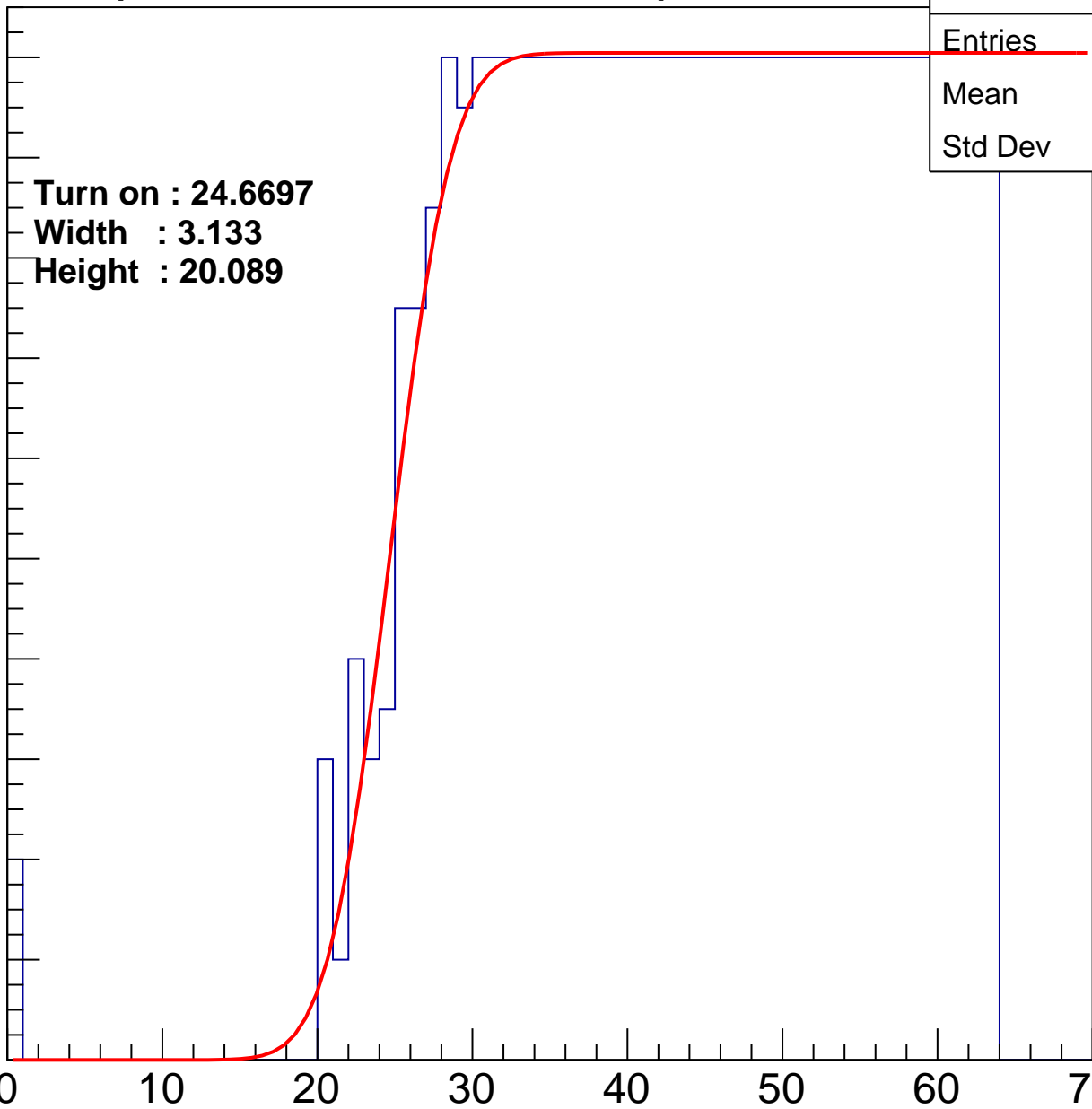
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.6697
Width : 3.133
Height : 20.089

Entries	799
Mean	43.3
Std Dev	12.02

ampl



B0L101S, U19-ch50

calib_packv5_042523_0143.root, FC#1, port C1

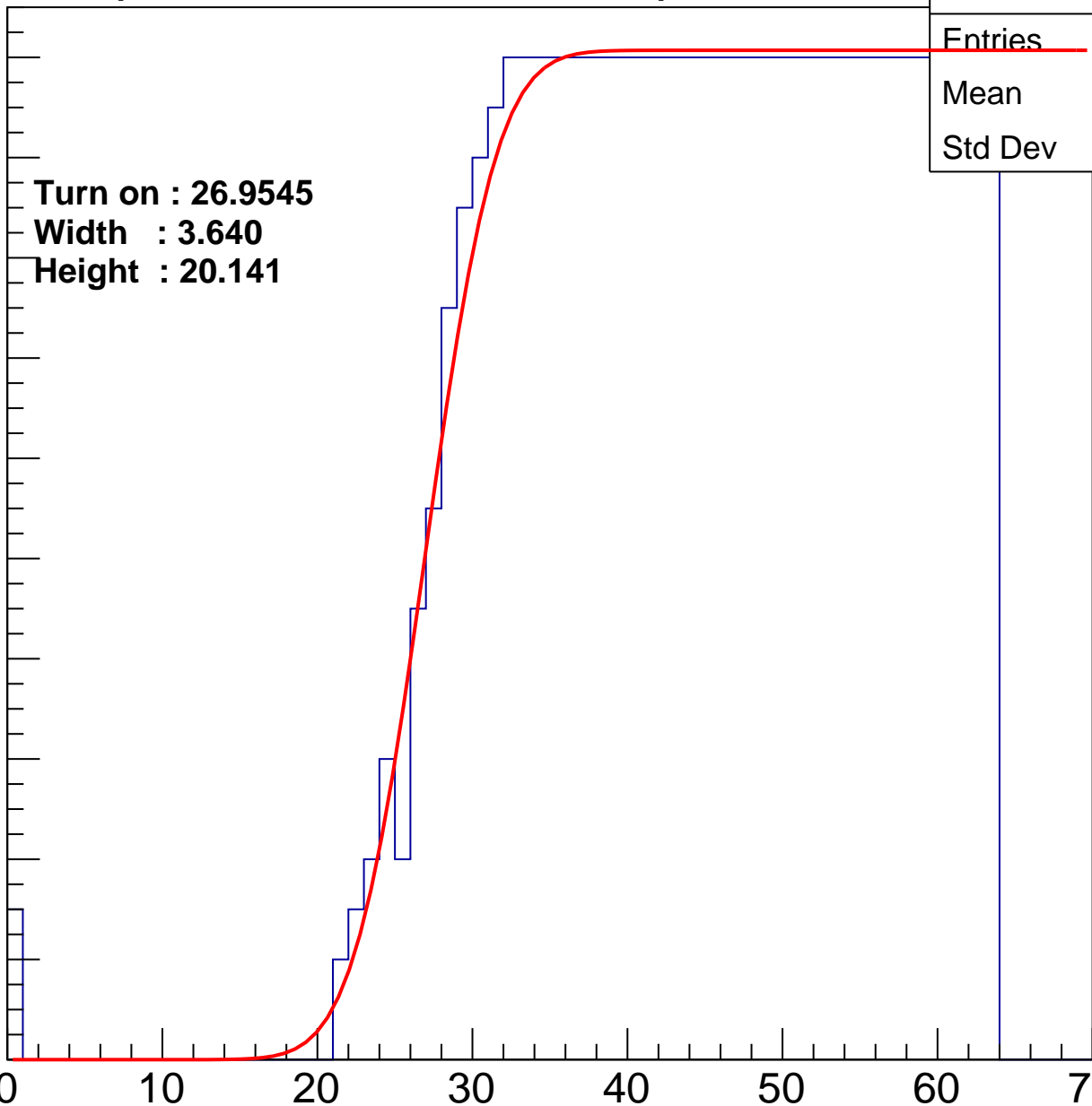
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9545
Width : 3.640
Height : 20.141

Entries	751
Mean	44.5
Std Dev	11.34

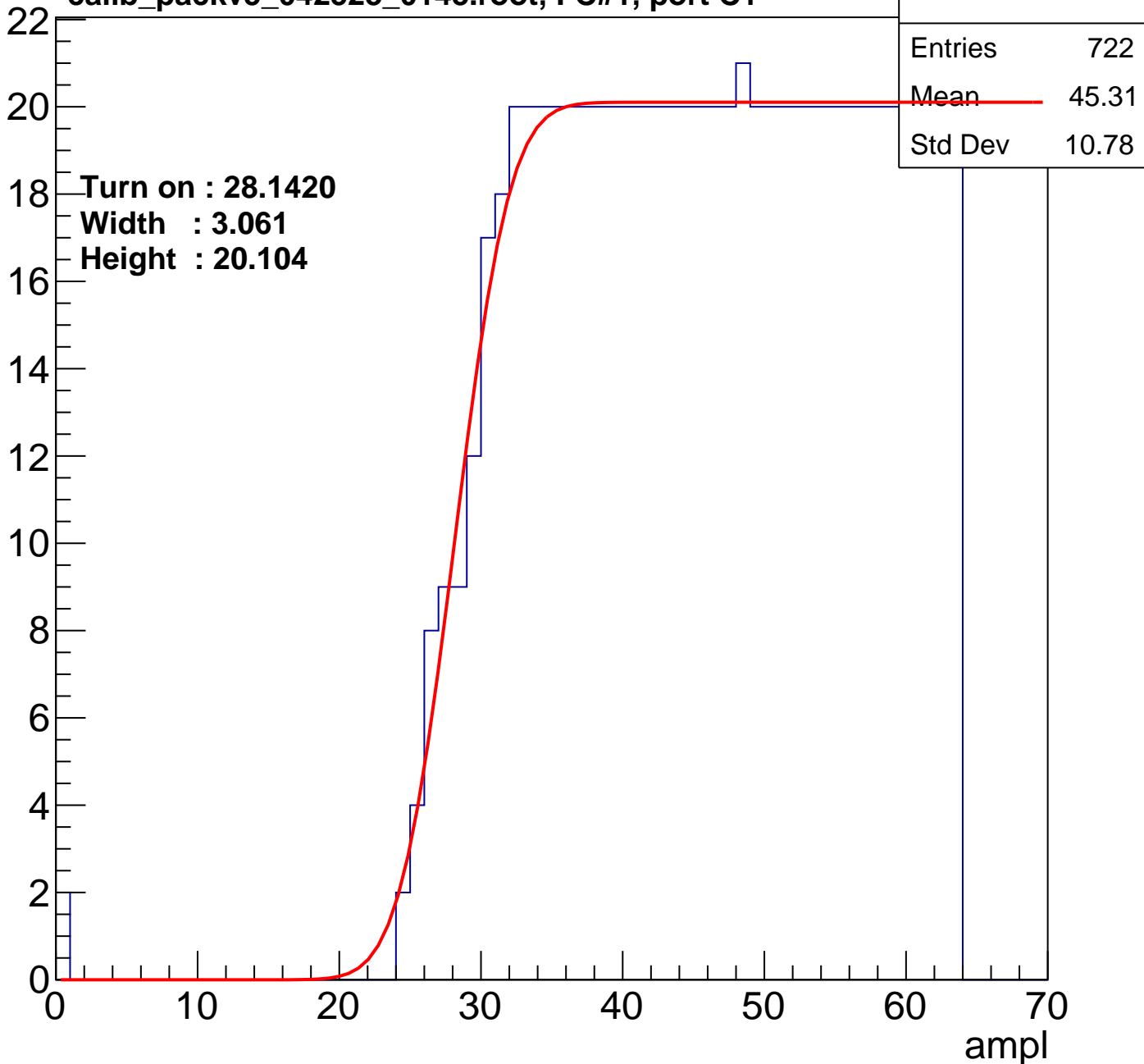
ampl



B0L101S, U19-ch51

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch52

calib_packv5_042523_0143.root, FC#1, port C1

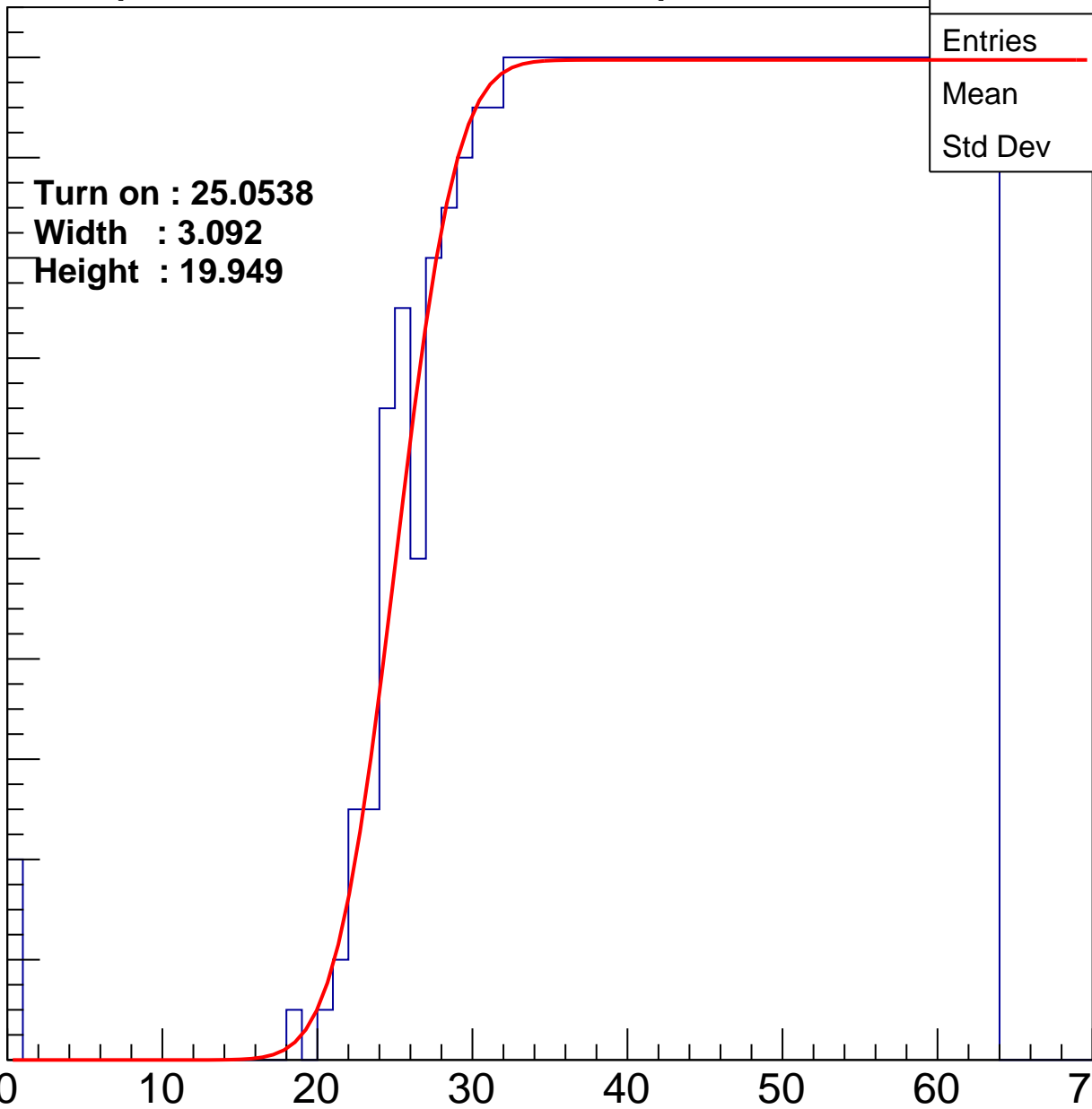
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0538
Width : 3.092
Height : 19.949

Entries	785
Mean	43.62
Std Dev	11.88

ampl



B0L101S, U19-ch53

calib_packv5_042523_0143.root, FC#1, port C1

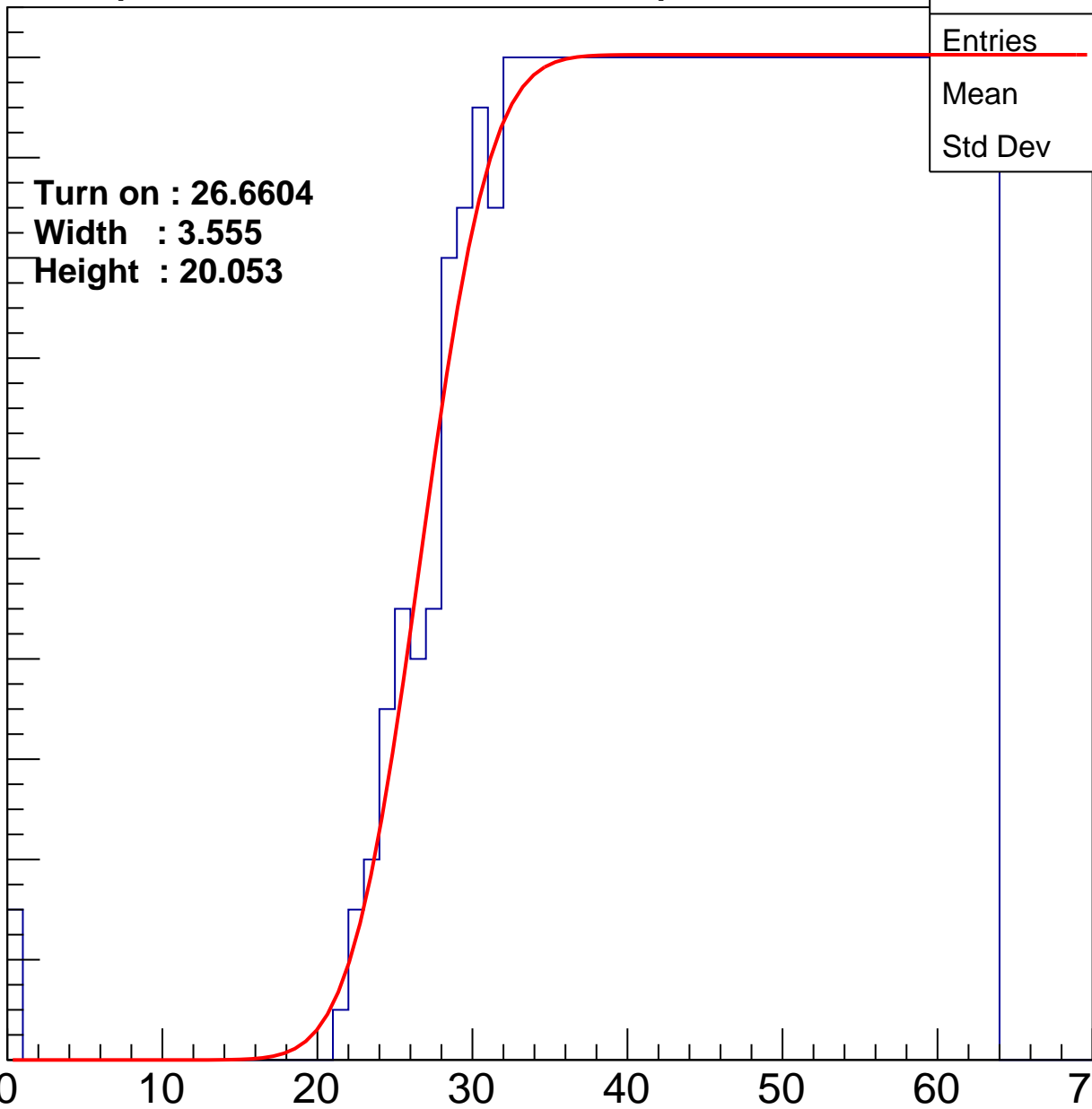
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6604
Width : 3.555
Height : 20.053

Entries	753
Mean	44.44
Std Dev	11.38

ampl



B0L101S, U19-ch54

calib_packv5_042523_0143.root, FC#1, port C1

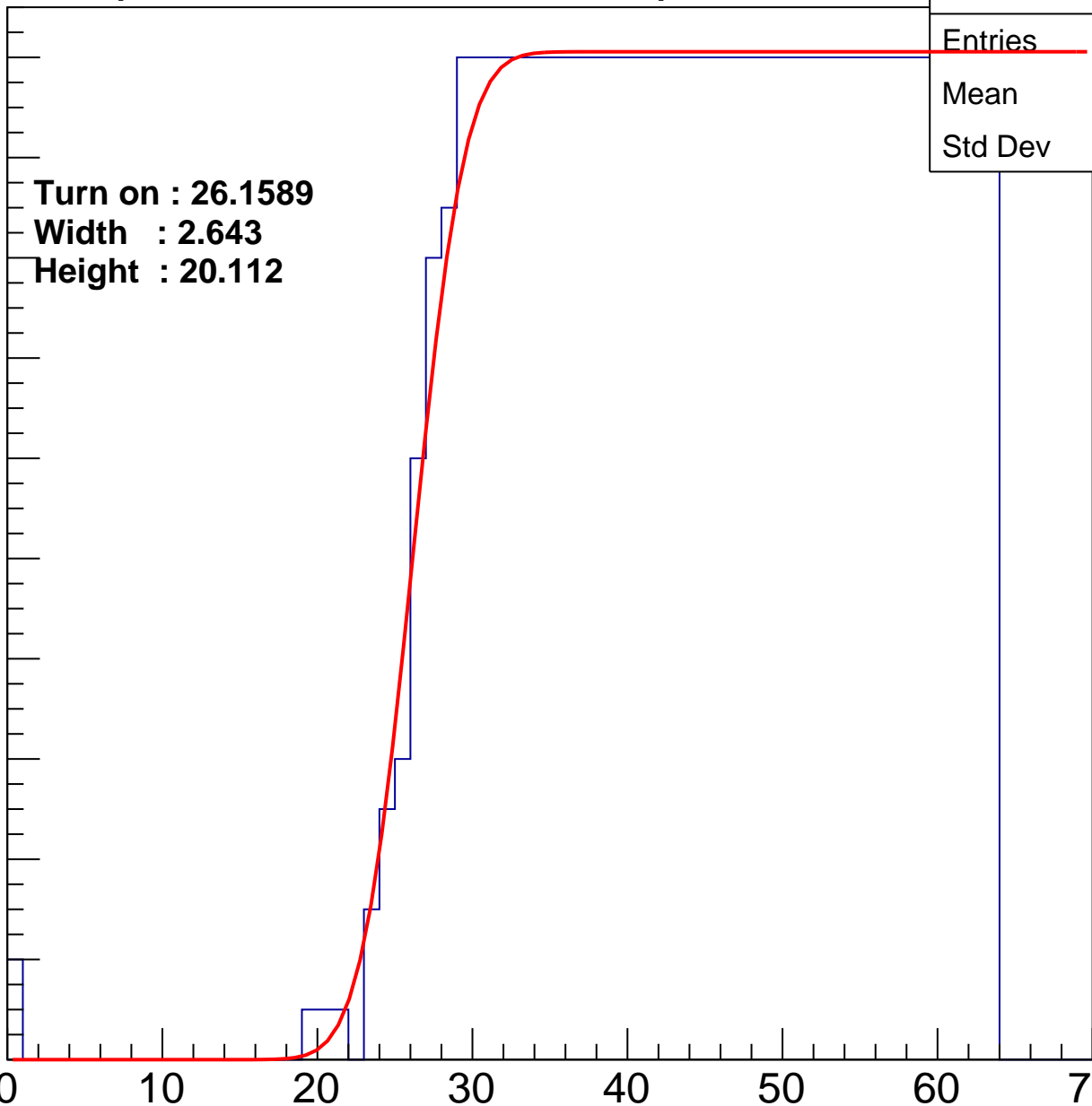
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1589
Width : 2.643
Height : 20.112

Entries	764
Mean	44.27
Std Dev	11.33

ampl



B0L101S, U19-ch55

calib_packv5_042523_0143.root, FC#1, port C1

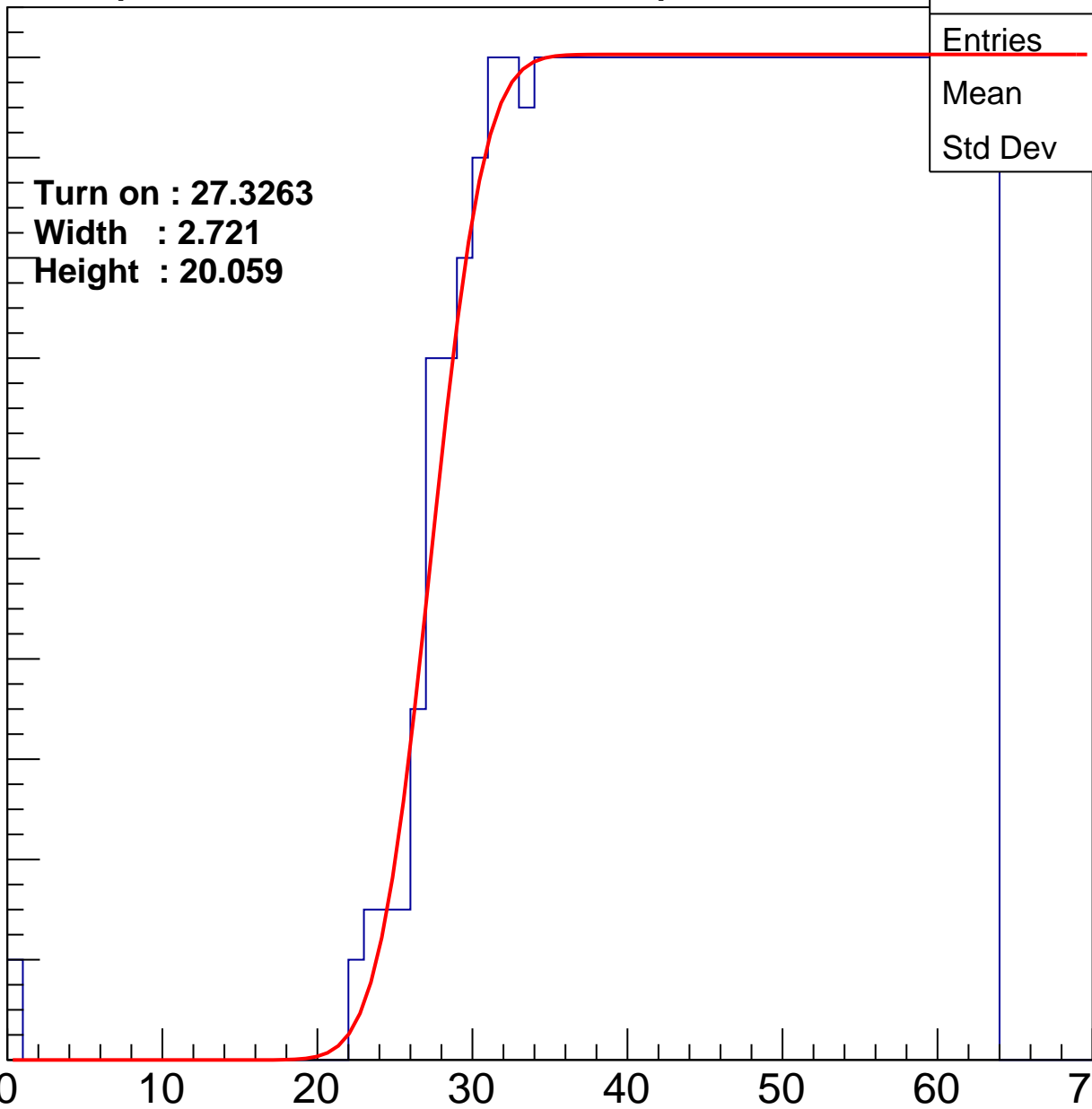
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3263
Width : 2.721
Height : 20.059

Entries	741
Mean	44.81
Std Dev	11.06

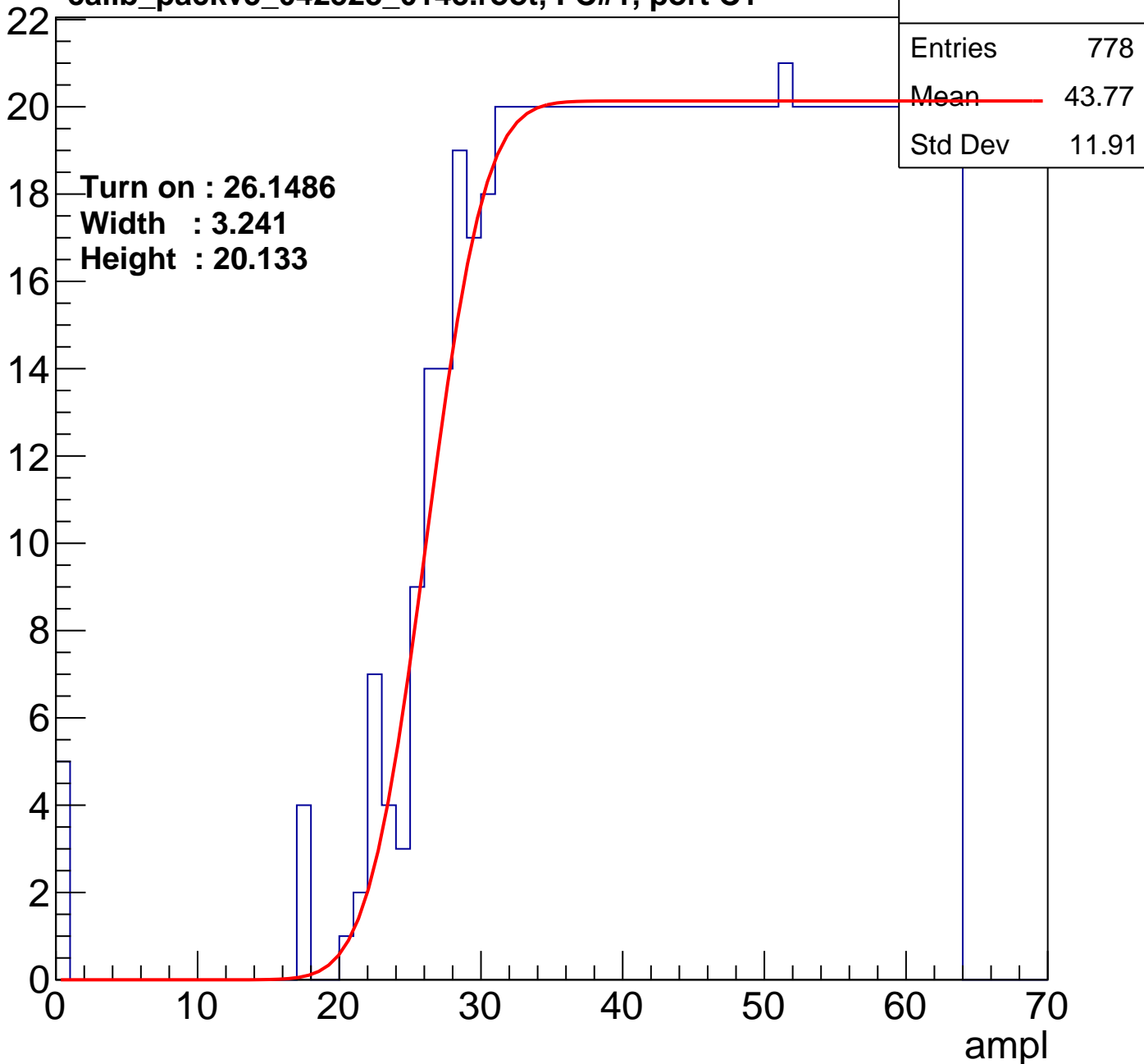
ampl



B0L101S, U19-ch56

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch57

calib_packv5_042523_0143.root, FC#1, port C1

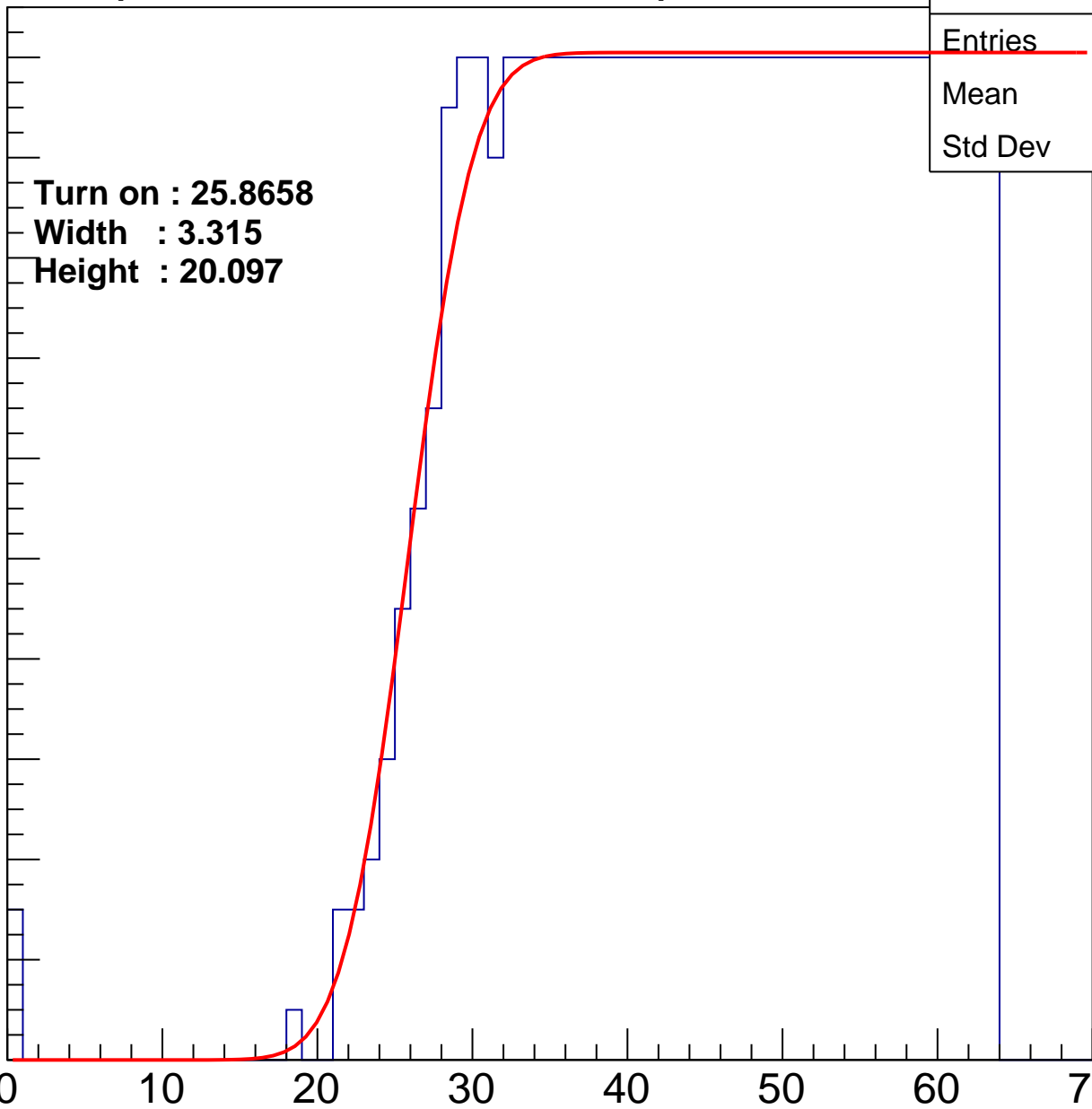
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8658
Width : 3.315
Height : 20.097

Entries	770
Mean	44.05
Std Dev	11.56

ampl



B0L101S, U19-ch58

calib_packv5_042523_0143.root, FC#1, port C1

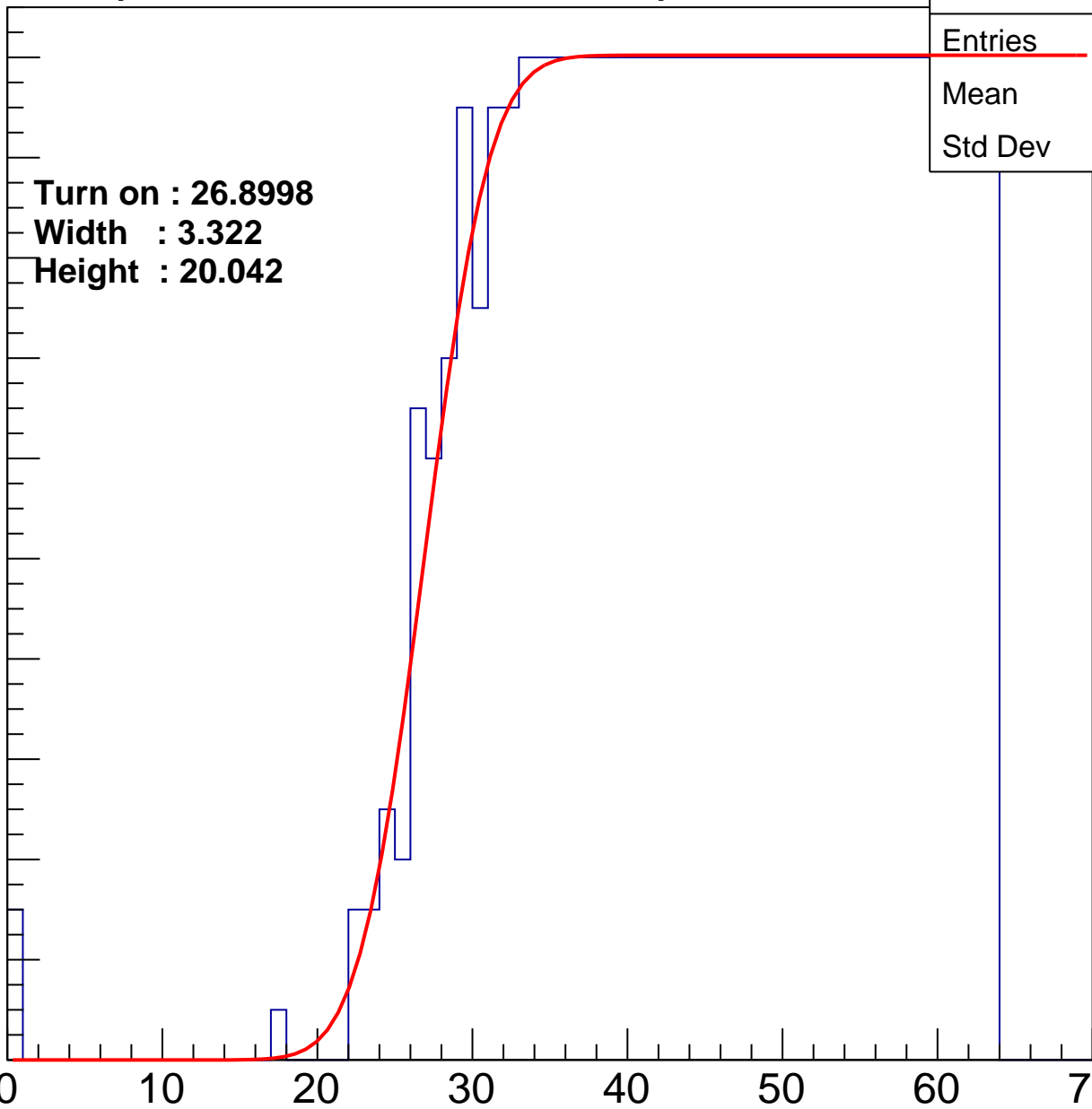
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8998
Width : 3.322
Height : 20.042

Entries	750
Mean	44.51
Std Dev	11.34

ampl



B0L101S, U19-ch59

calib_packv5_042523_0143.root, FC#1, port C1

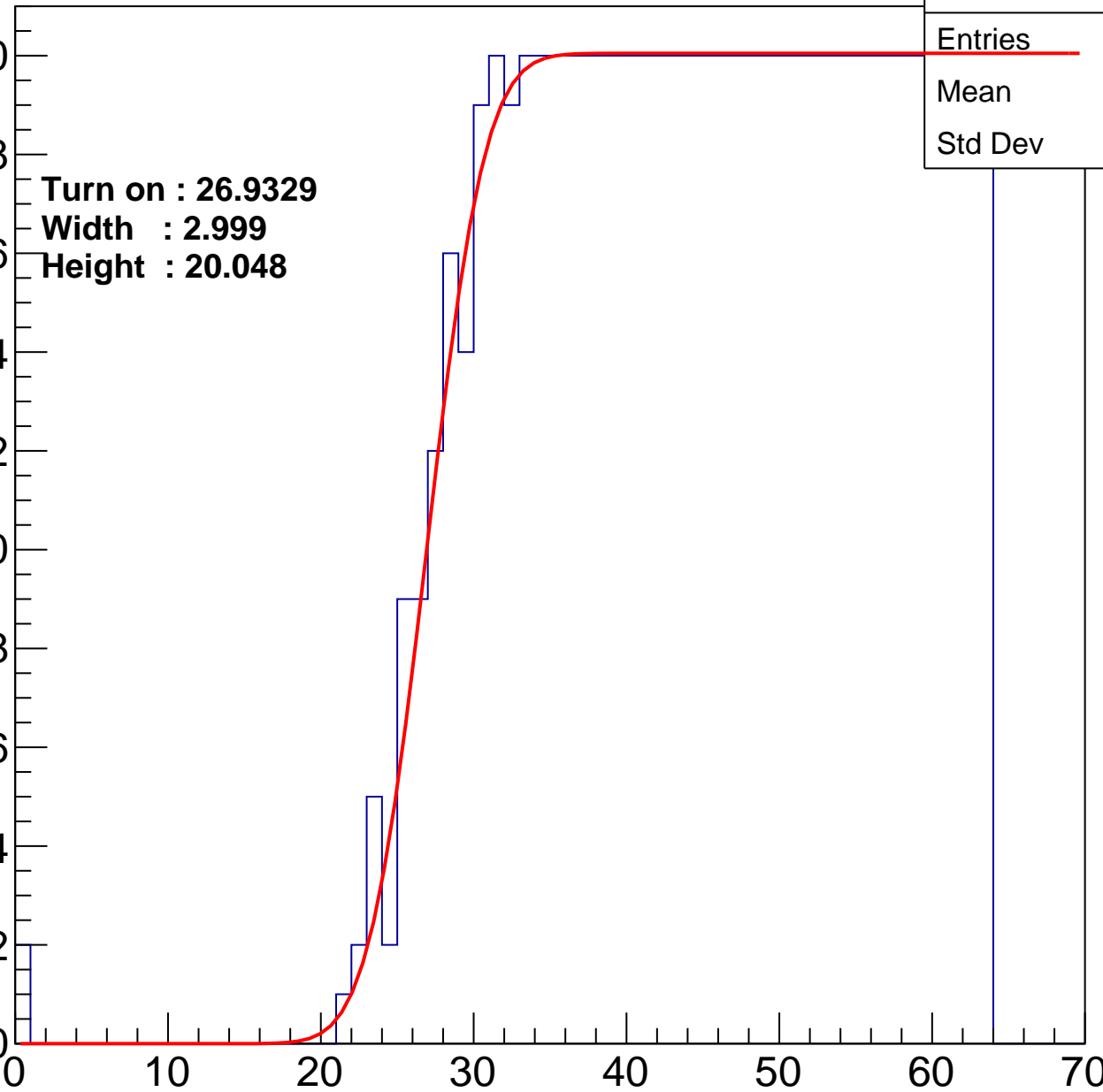
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9329
Width : 2.999
Height : 20.048

Entries	750
Mean	44.56
Std Dev	11.22

ampl



B0L101S, U19-ch60

calib_packv5_042523_0143.root, FC#1, port C1

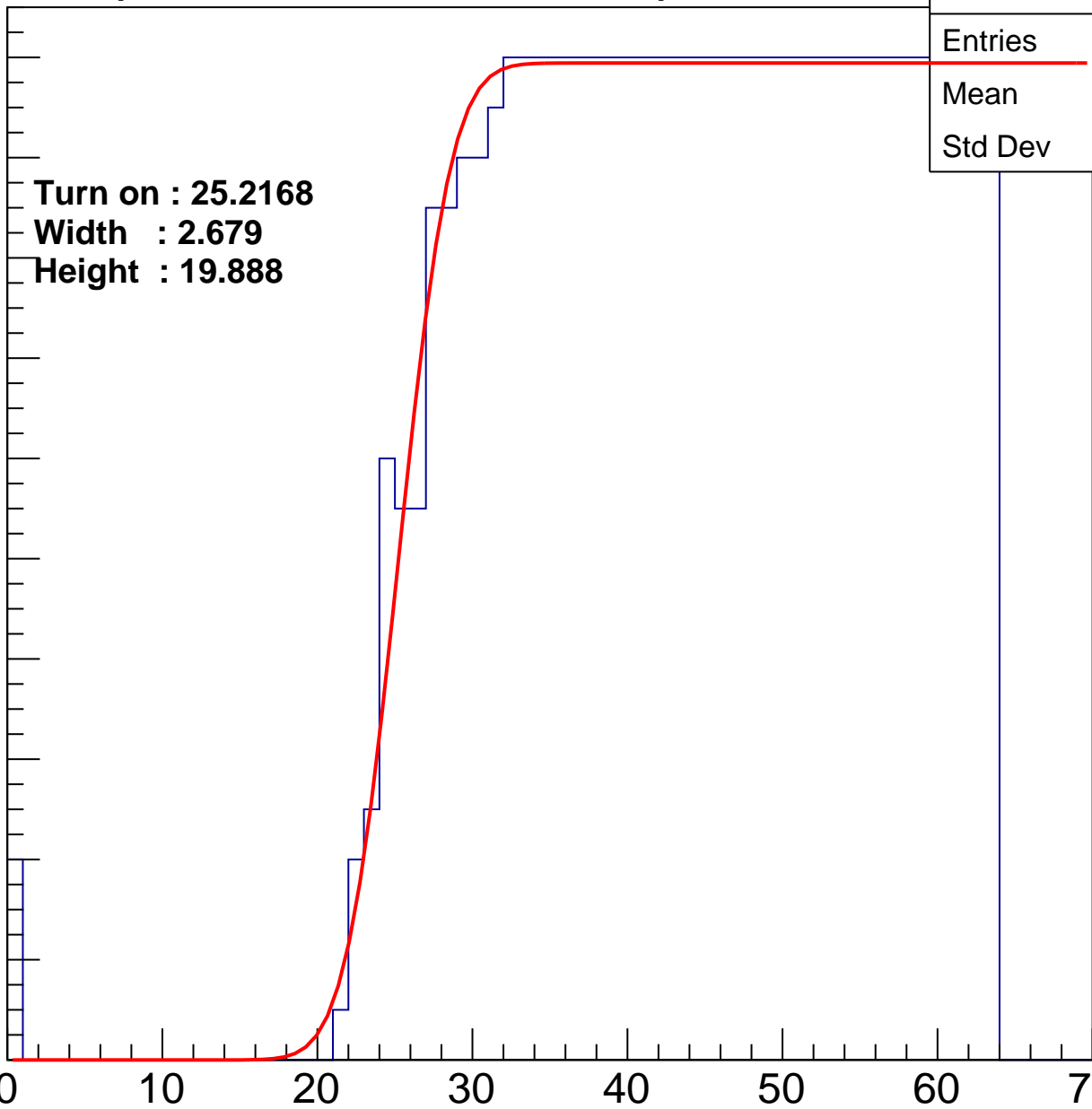
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2168
Width : 2.679
Height : 19.888

Entries	777
Mean	43.83
Std Dev	11.74

ampl



B0L101S, U19-ch61

calib_packv5_042523_0143.root, FC#1, port C1

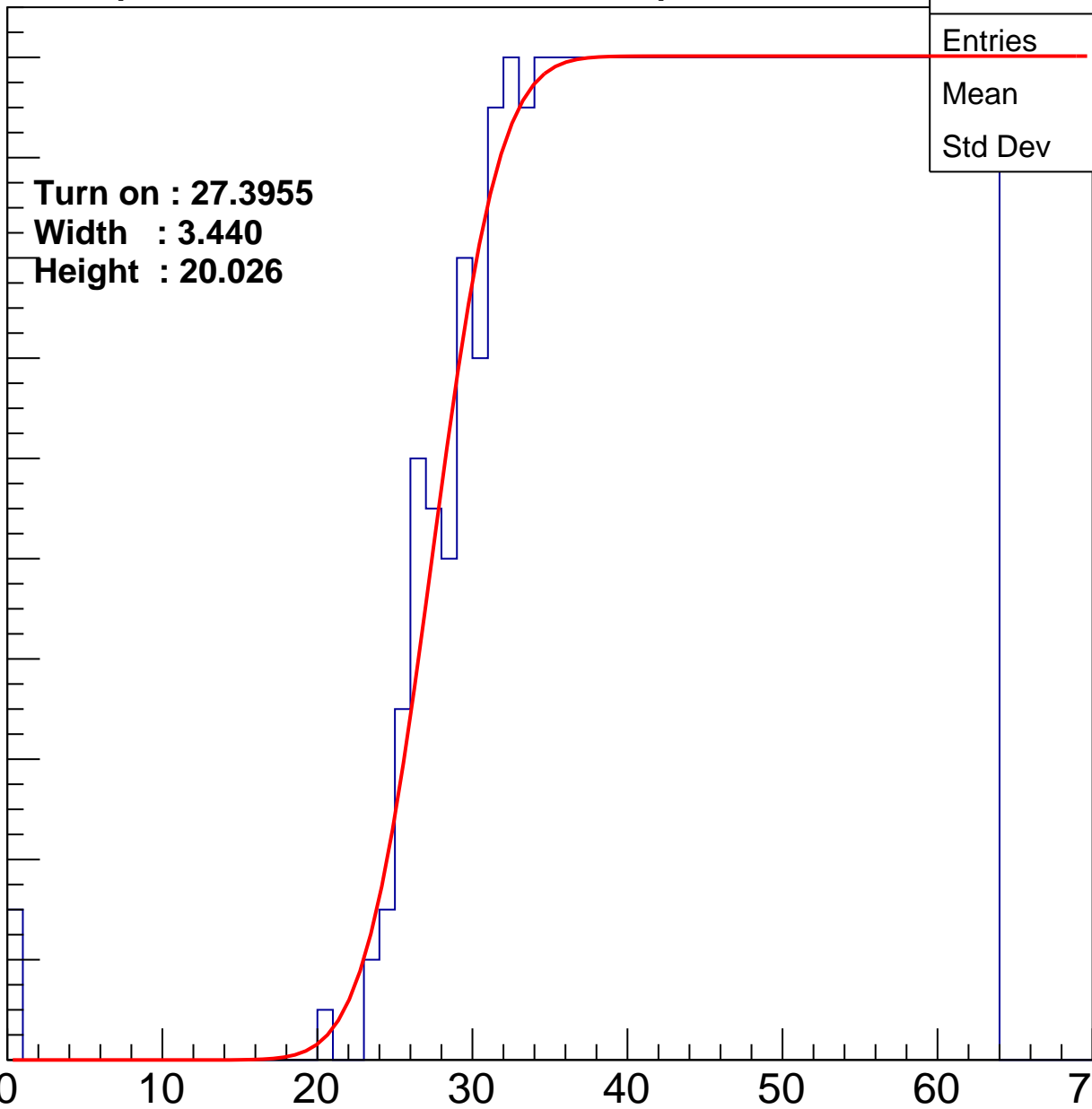
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3955
Width : 3.440
Height : 20.026

Entries	737
Mean	44.83
Std Dev	11.17

ampl



B0L101S, U19-ch62

calib_packv5_042523_0143.root, FC#1, port C1

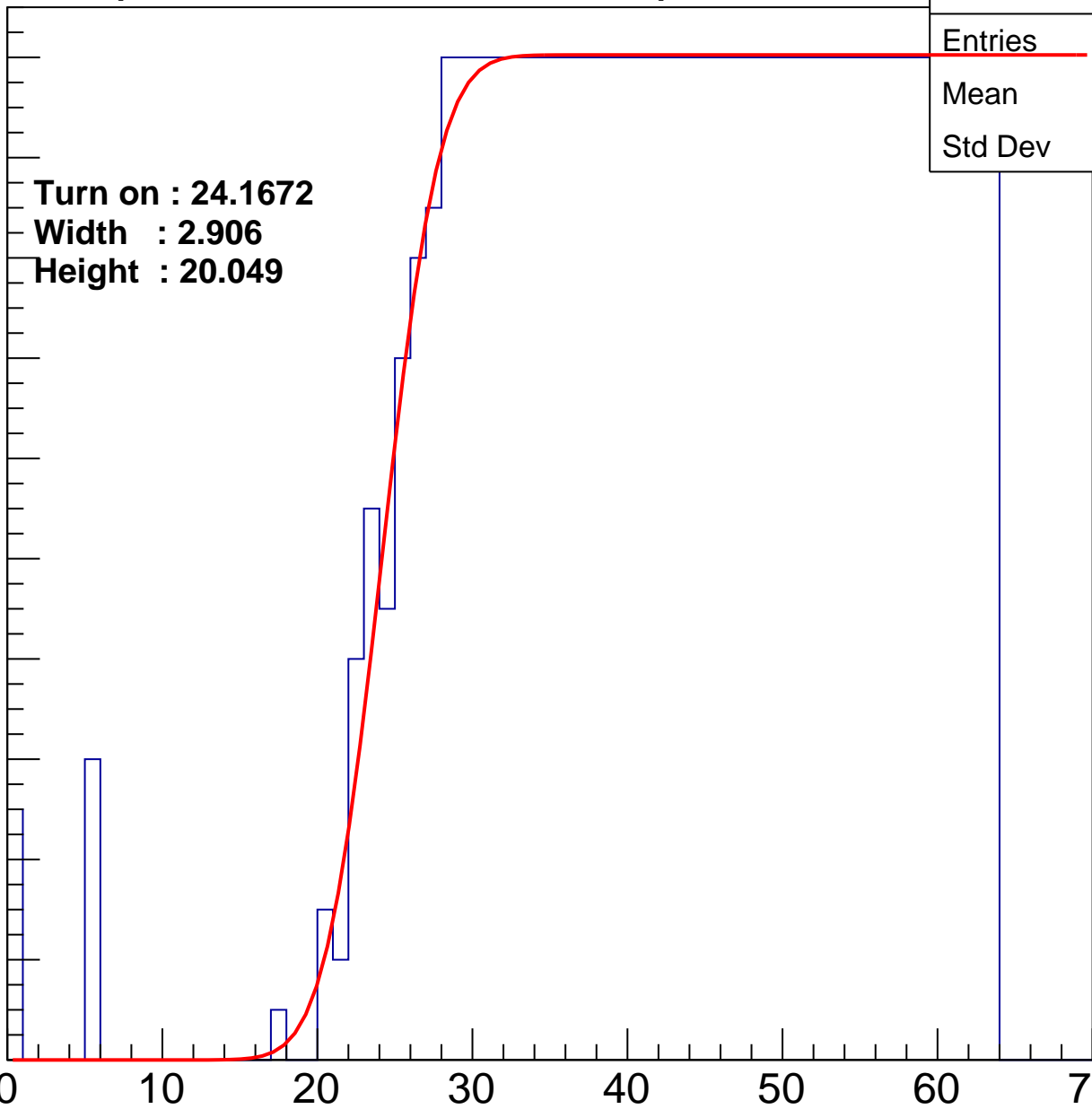
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.1672
Width : 2.906
Height : 20.049

Entries	812
Mean	42.83
Std Dev	12.56

ampl



B0L101S, U19-ch63

calib_packv5_042523_0143.root, FC#1, port C1

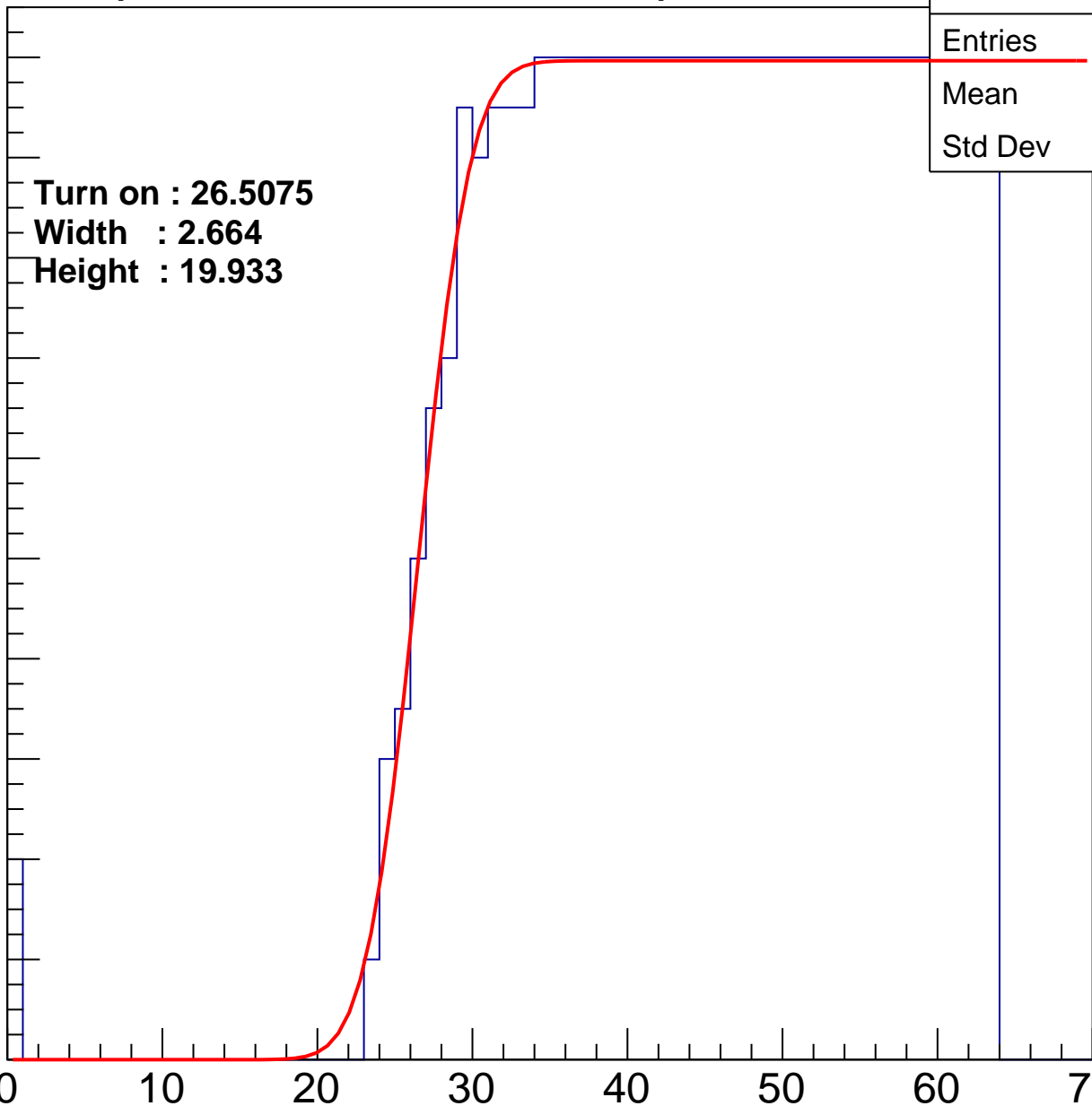
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5075
Width : 2.664
Height : 19.933

Entries	750
Mean	44.51
Std Dev	11.38

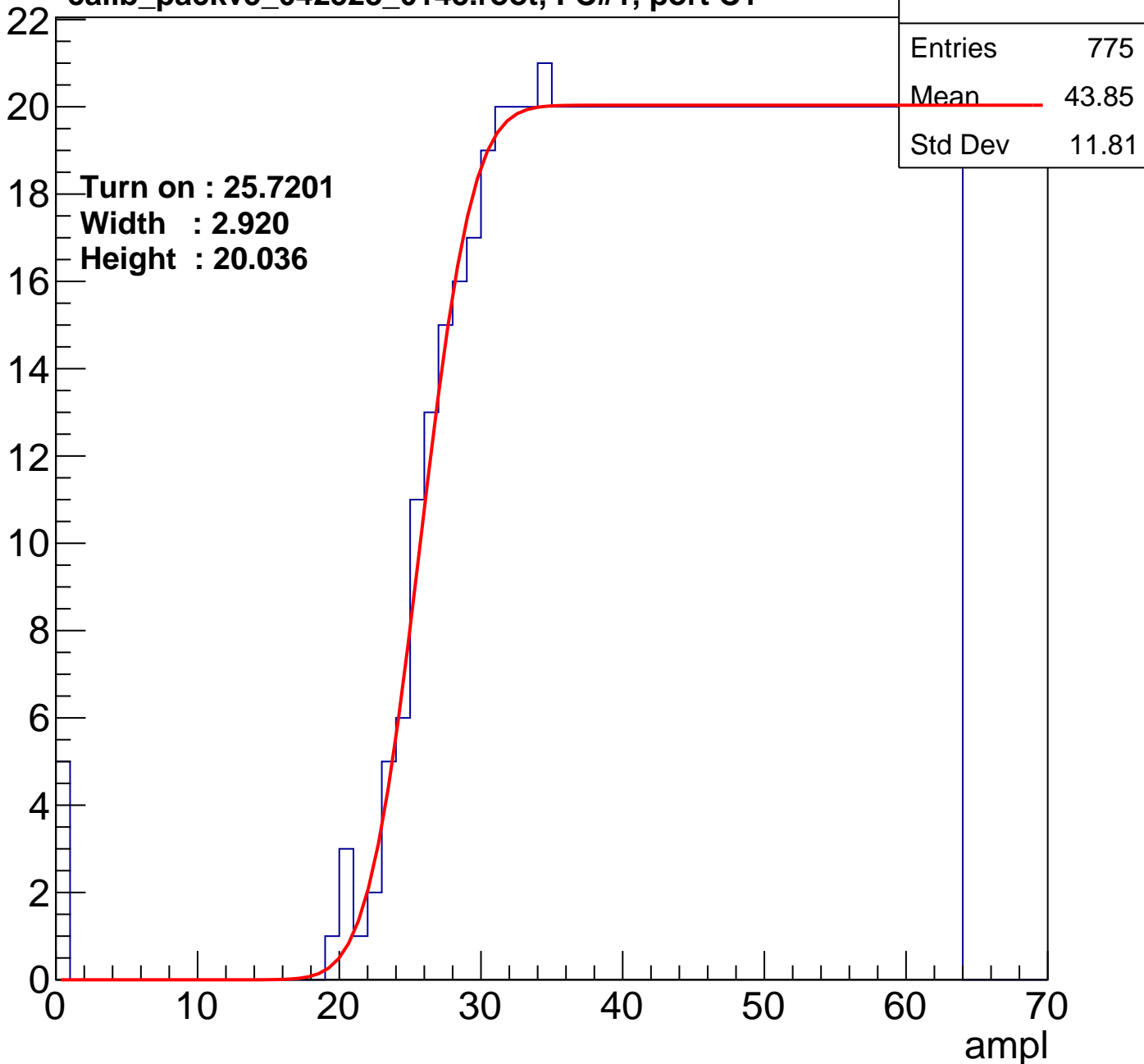
ampl



B0L101S, U19-ch64

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch65

calib_packv5_042523_0143.root, FC#1, port C1

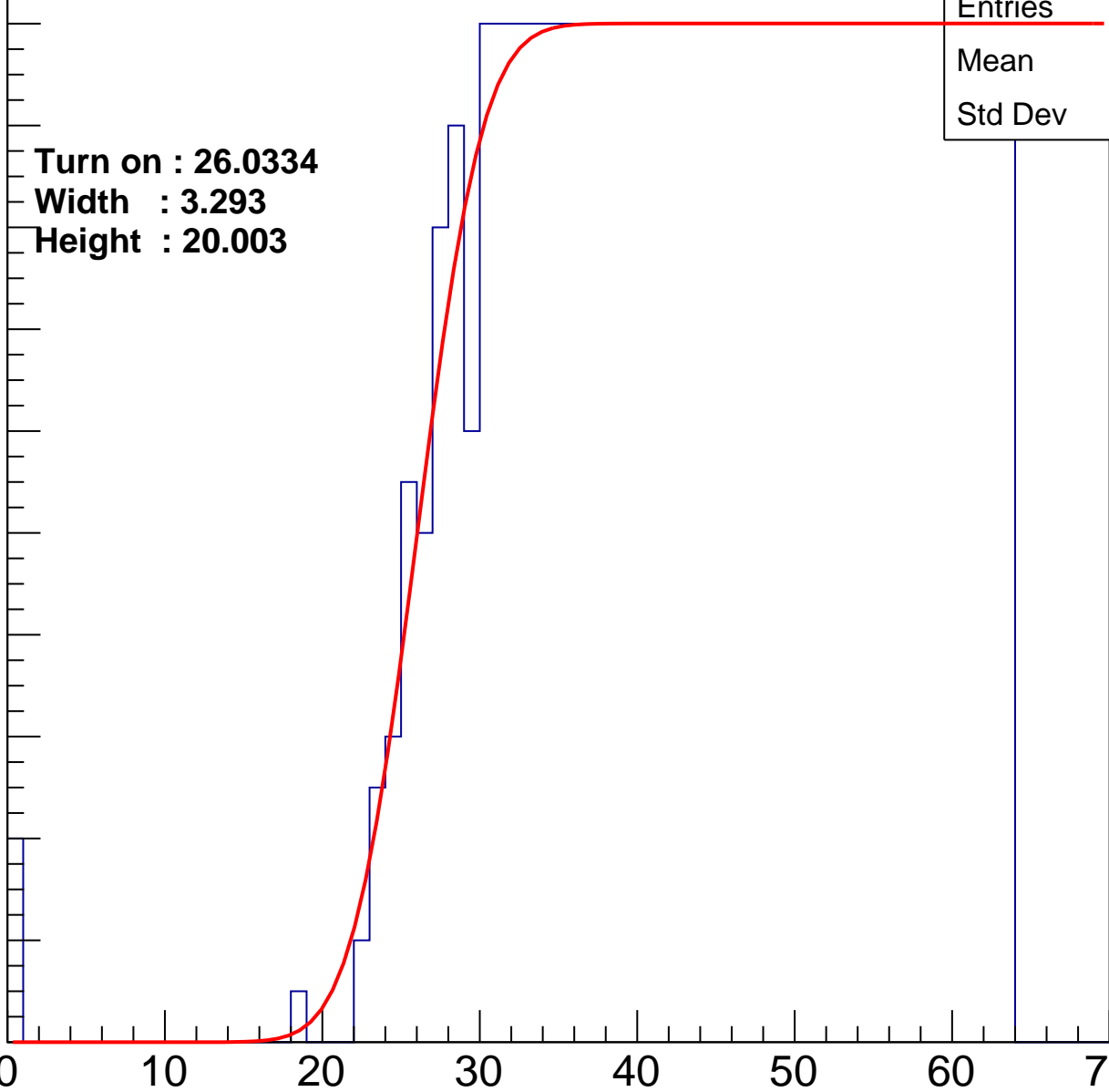
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0334
Width : 3.293
Height : 20.003

Entries	765
Mean	44.13
Std Dev	11.59

ampl



B0L101S, U19-ch66

calib_packv5_042523_0143.root, FC#1, port C1

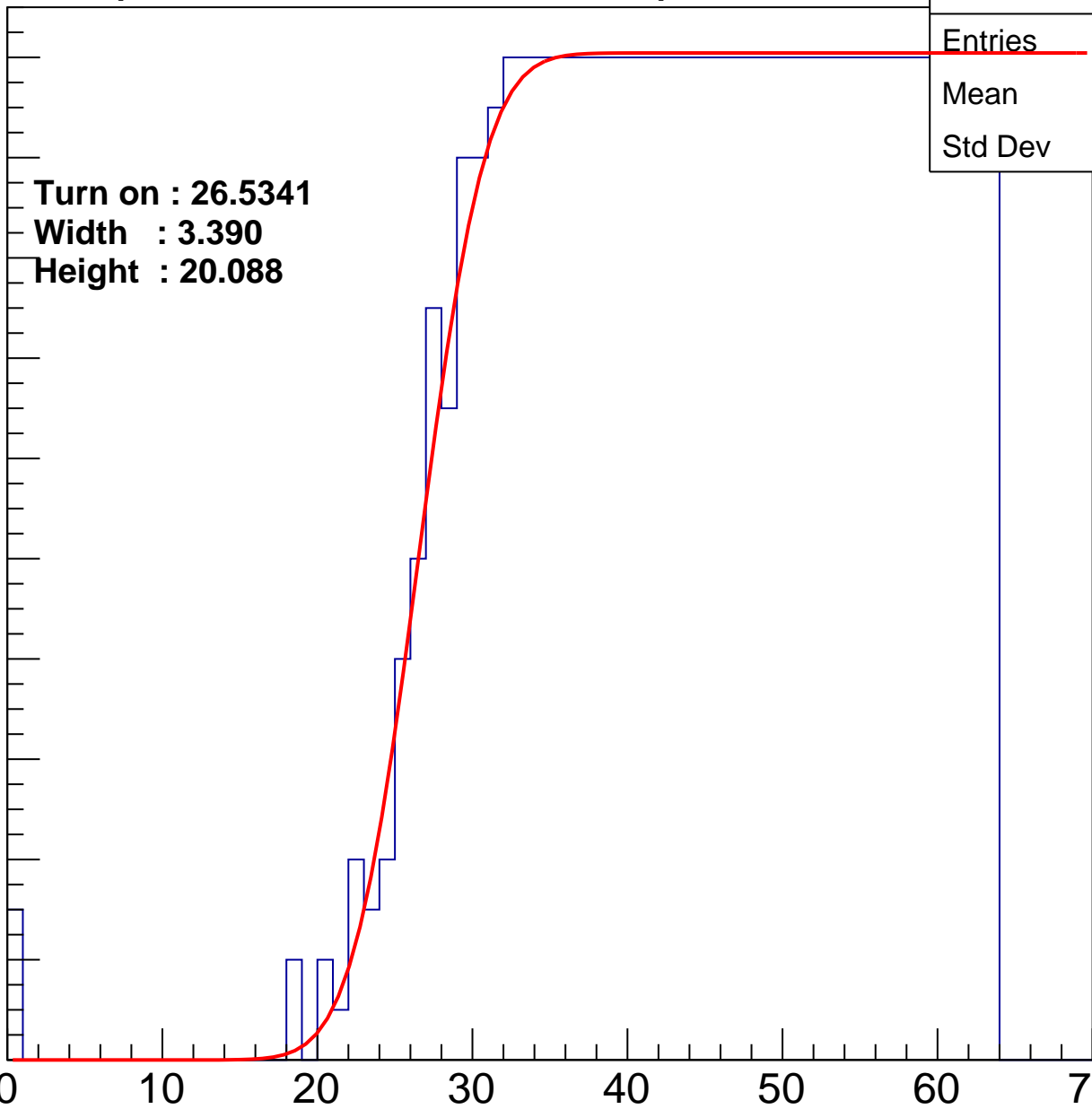
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5341
Width : 3.390
Height : 20.088

Entries	760
Mean	44.25
Std Dev	11.51

ampl



B0L101S, U19-ch67

calib_packv5_042523_0143.root, FC#1, port C1

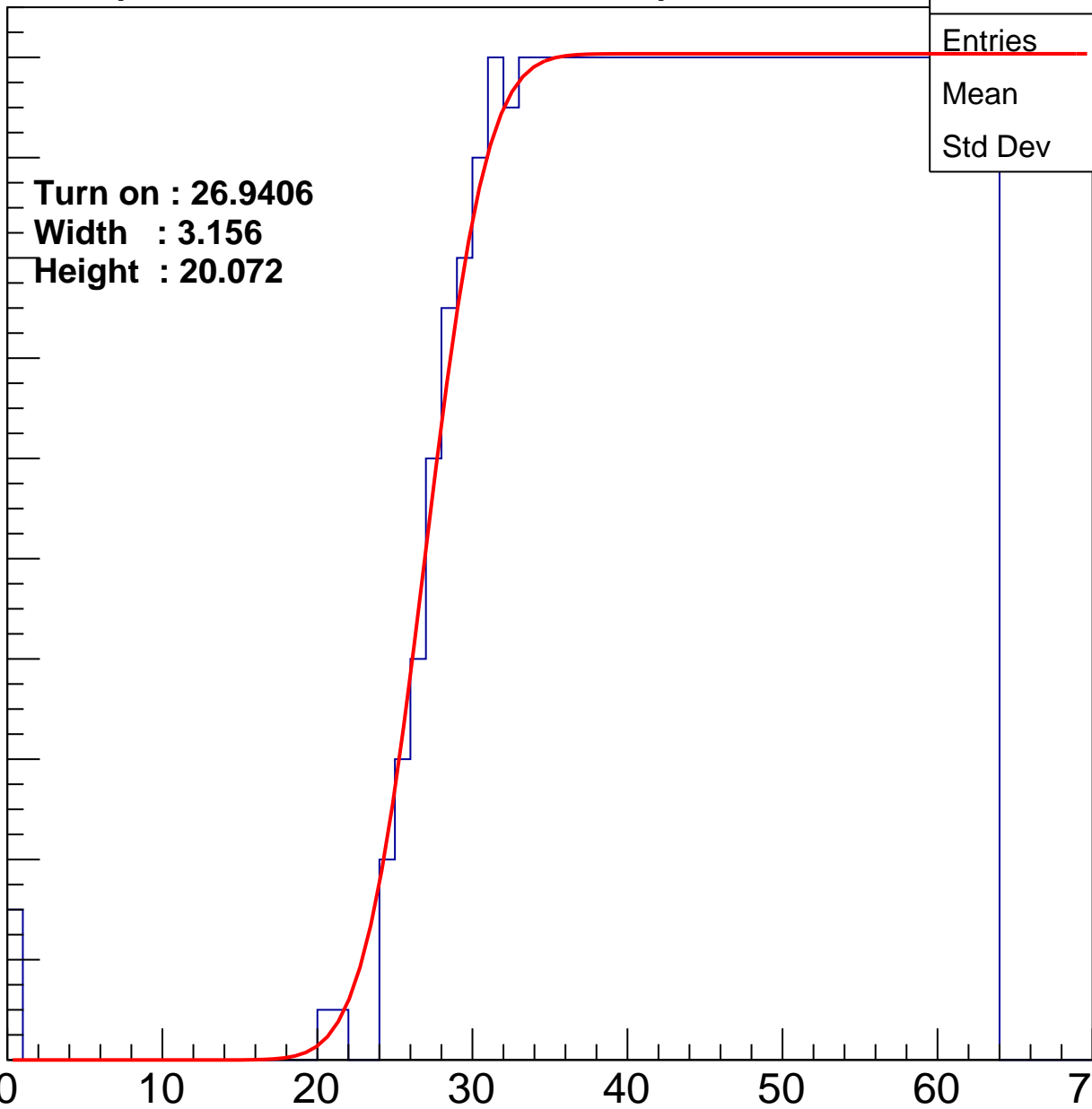
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9406
Width : 3.156
Height : 20.072

Entries	743
Mean	44.73
Std Dev	11.18

ampl



B0L101S, U19-ch68

calib_packv5_042523_0143.root, FC#1, port C1

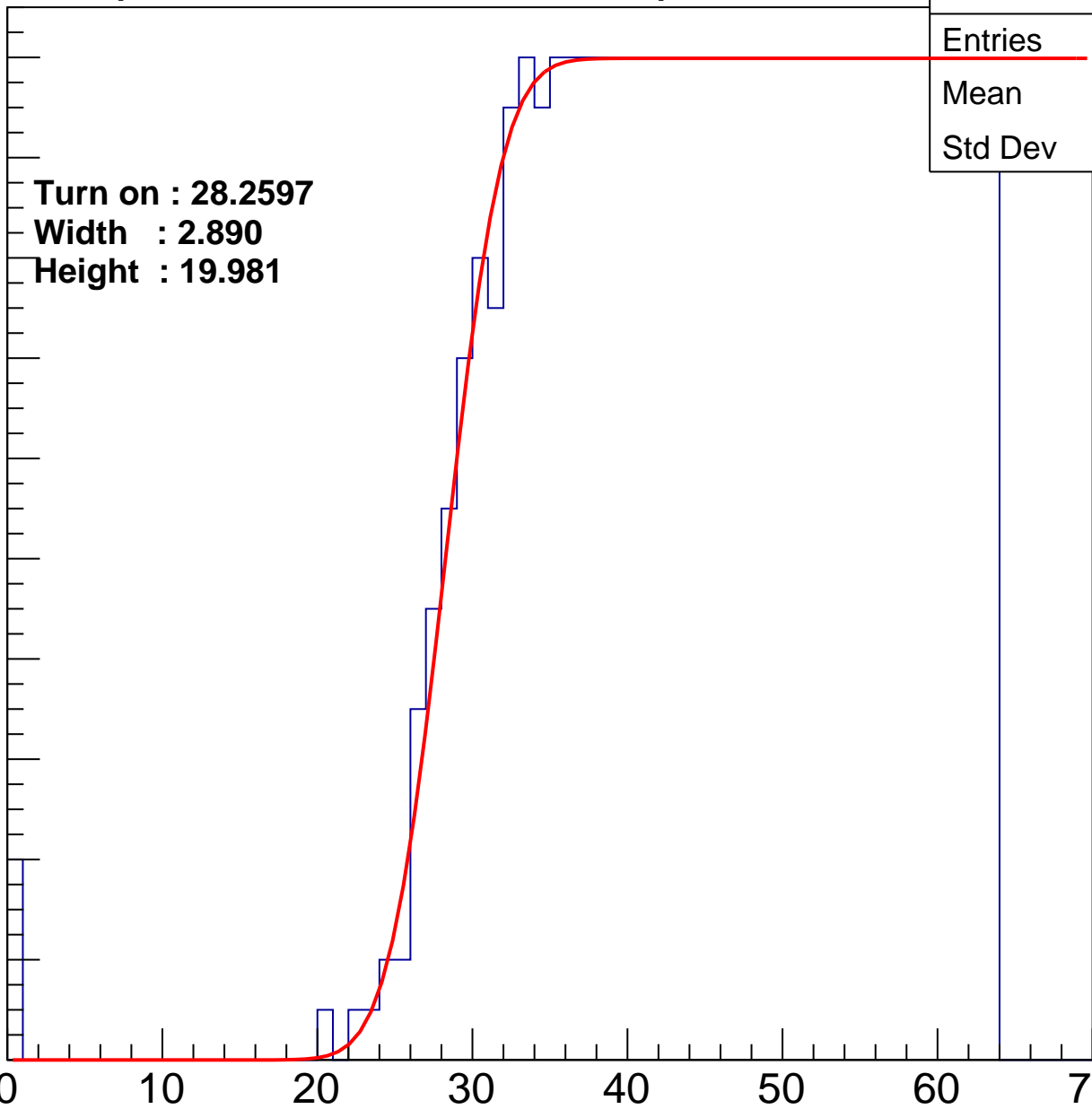
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.2597
Width : 2.890
Height : 19.981

Entries	721
Mean	45.19
Std Dev	11.07

ampl



B0L101S, U19-ch69

calib_packv5_042523_0143.root, FC#1, port C1

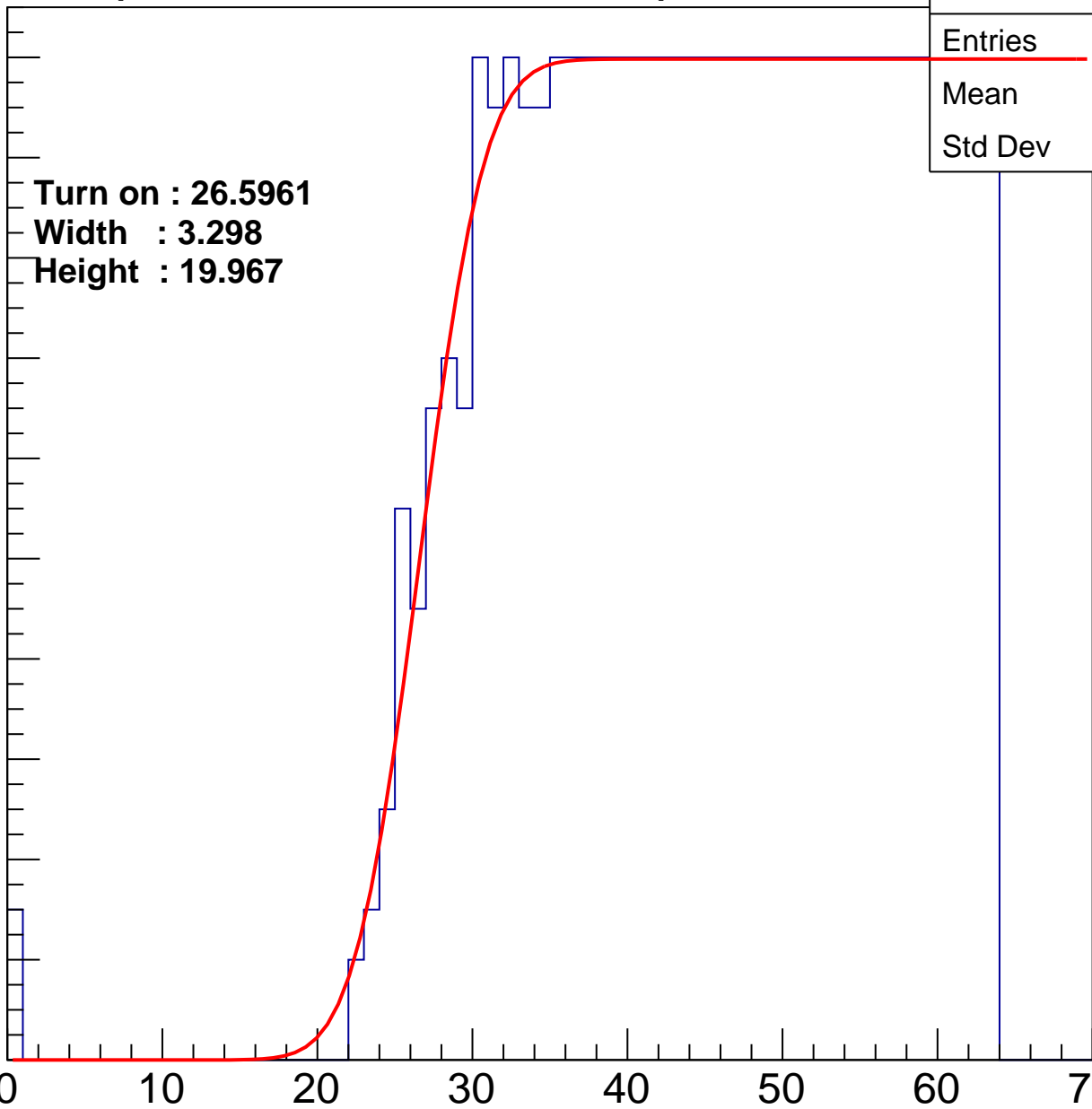
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5961
Width : 3.298
Height : 19.967

Entries	750
Mean	44.51
Std Dev	11.34

ampl



B0L101S, U19-ch70

calib_packv5_042523_0143.root, FC#1, port C1

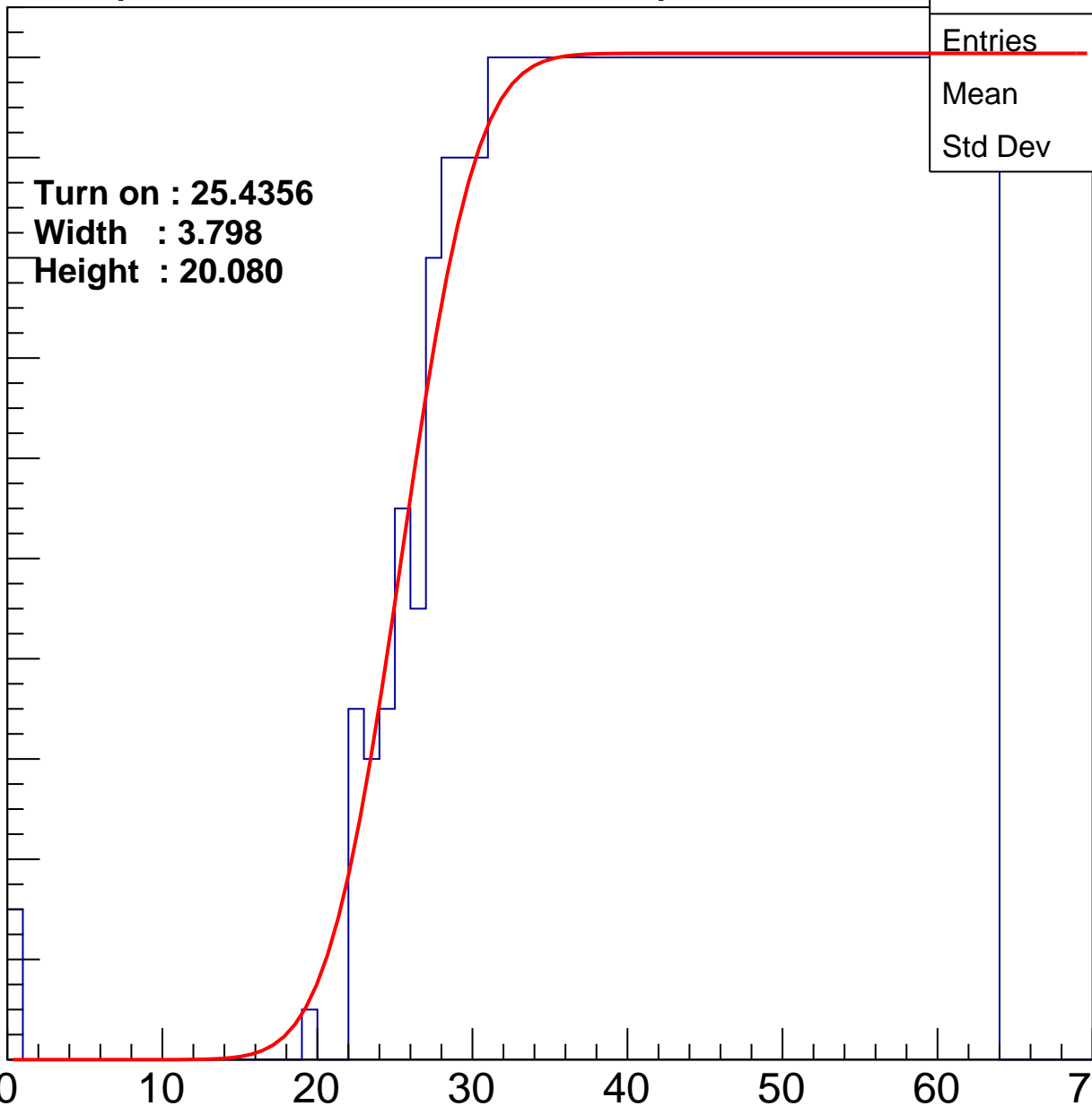
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4356
Width : 3.798
Height : 20.080

Entries	774
Mean	43.94
Std Dev	11.63

ampl



B0L101S, U19-ch71

calib_packv5_042523_0143.root, FC#1, port C1

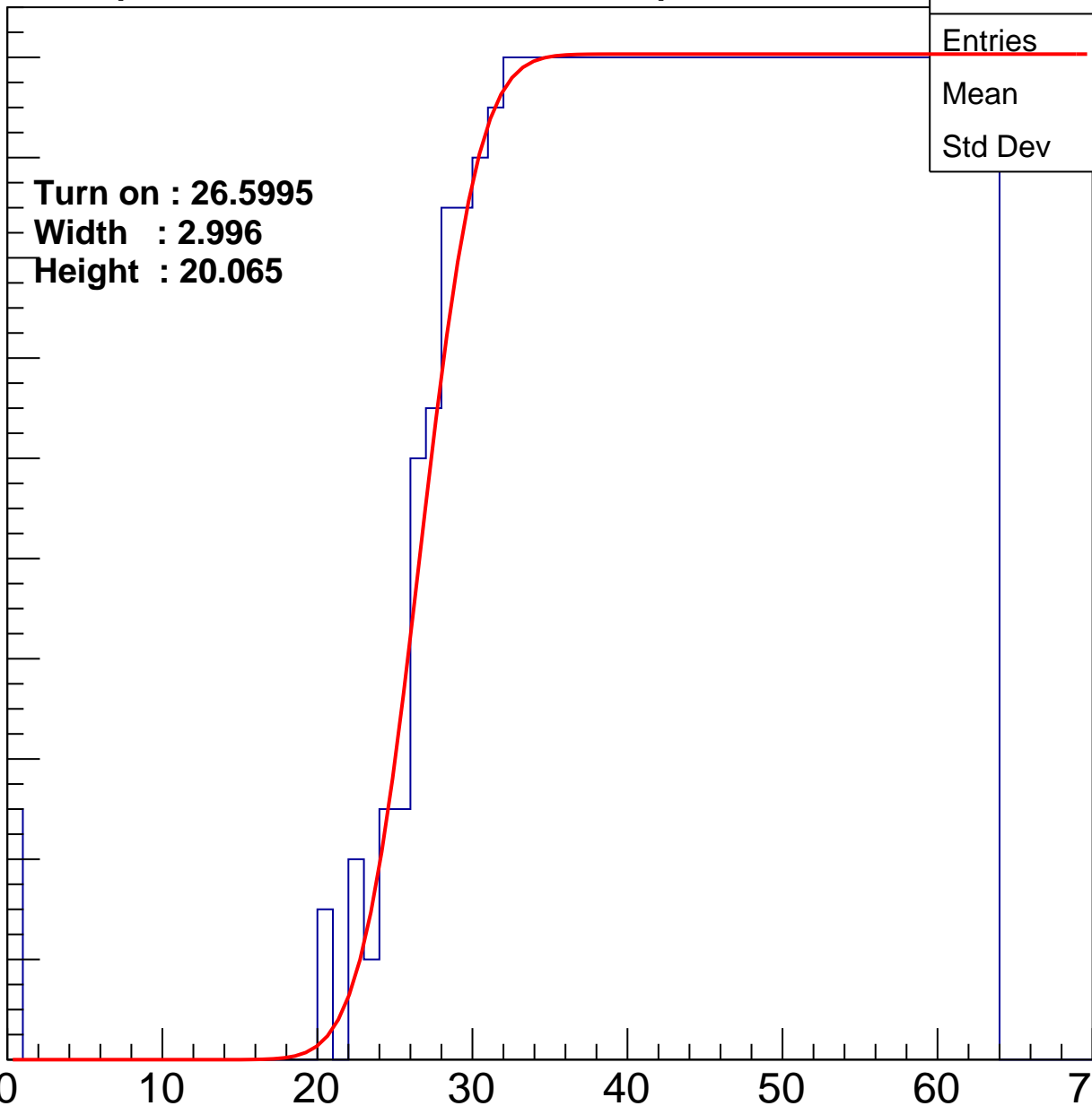
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5995
Width : 2.996
Height : 20.065

Entries	760
Mean	44.21
Std Dev	11.64

ampl



B0L101S, U19-ch72

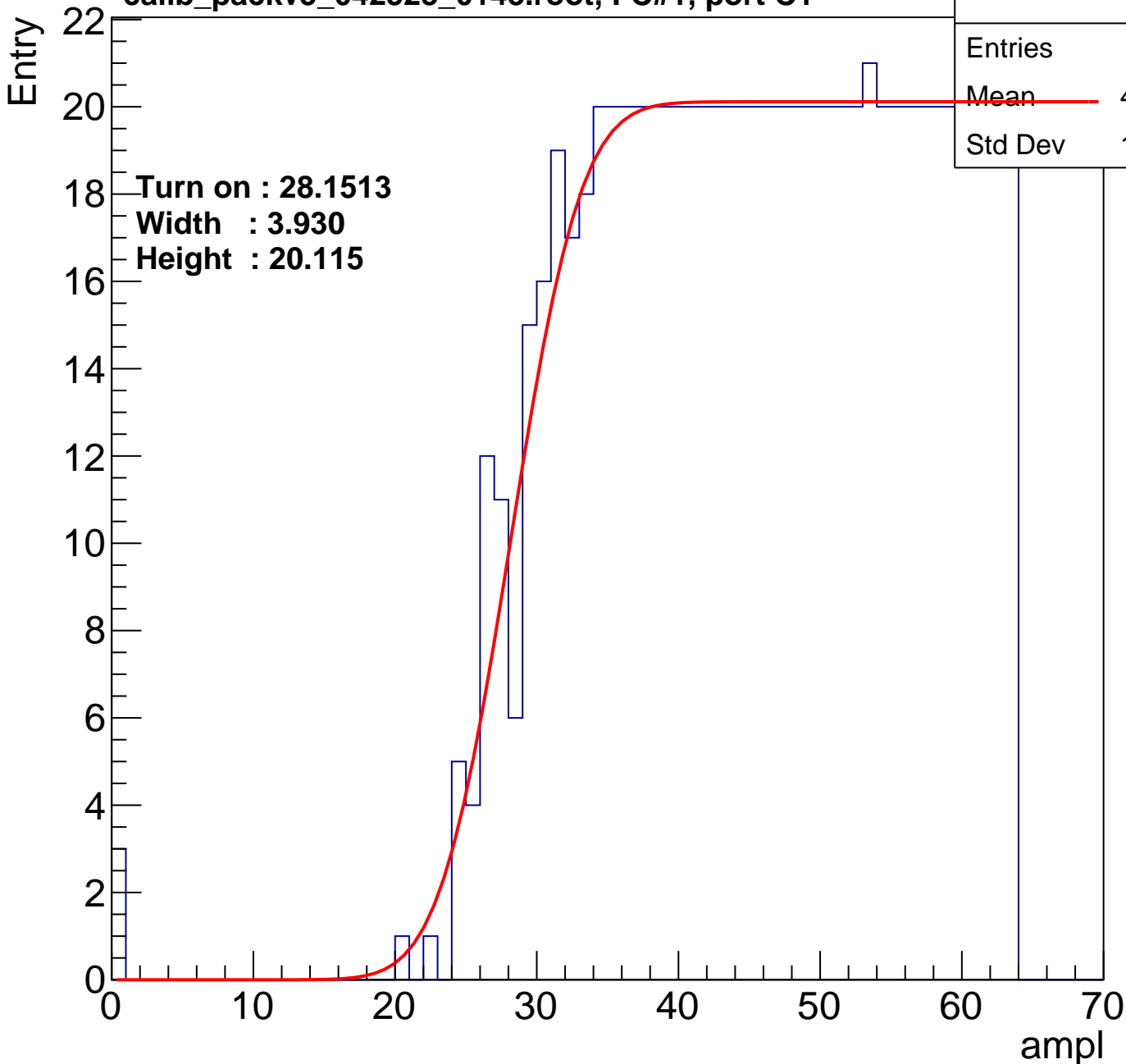
calib_packv5_042523_0143.root, FC#1, port C1

Entries	729
Mean	45.04
Std Dev	11.09

Turn on : 28.1513

Width : 3.930

Height : 20.115



B0L101S, U19-ch73

calib_packv5_042523_0143.root, FC#1, port C1

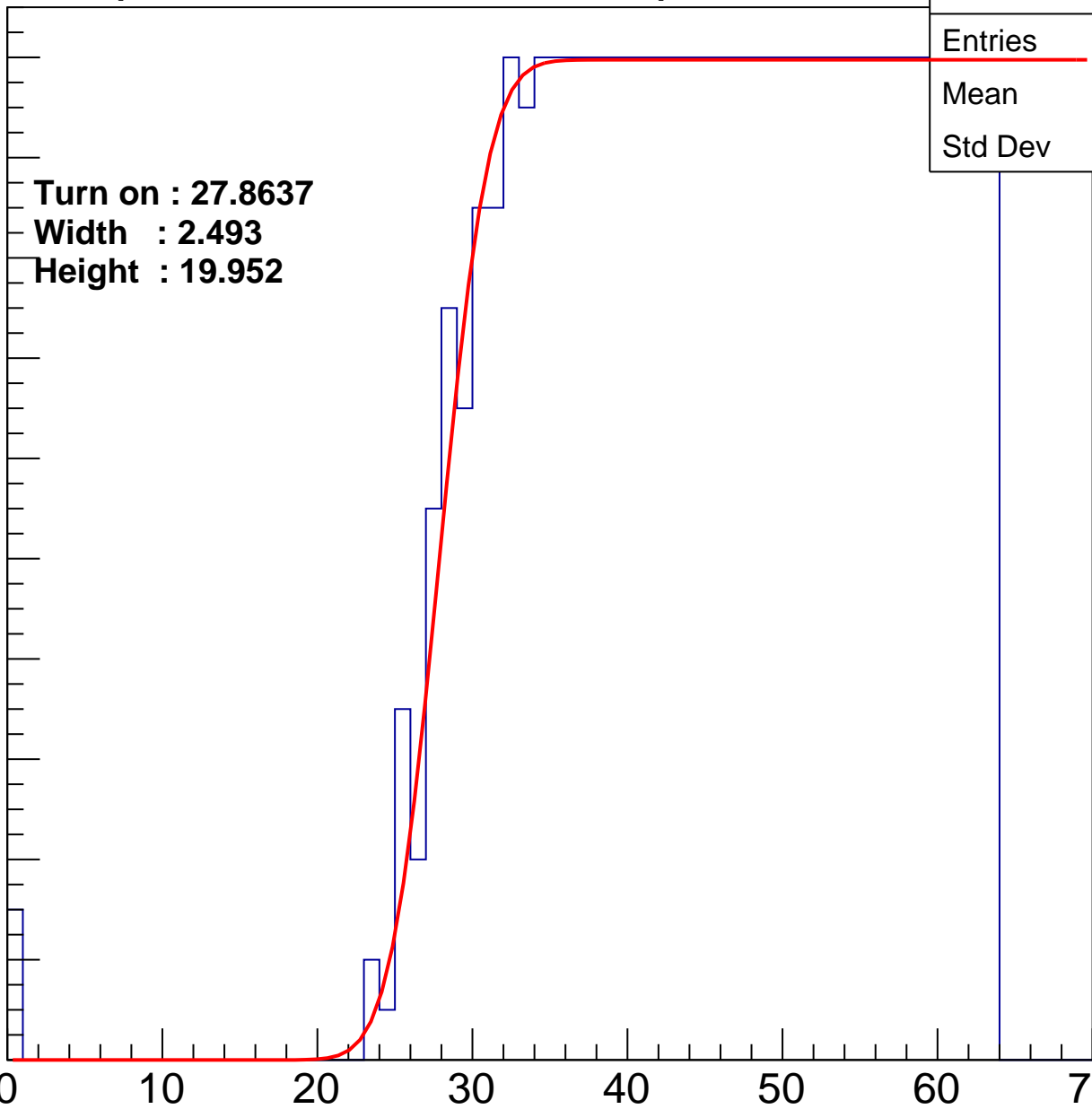
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8637
Width : 2.493
Height : 19.952

Entries	729
Mean	45.06
Std Dev	11.02

ampl

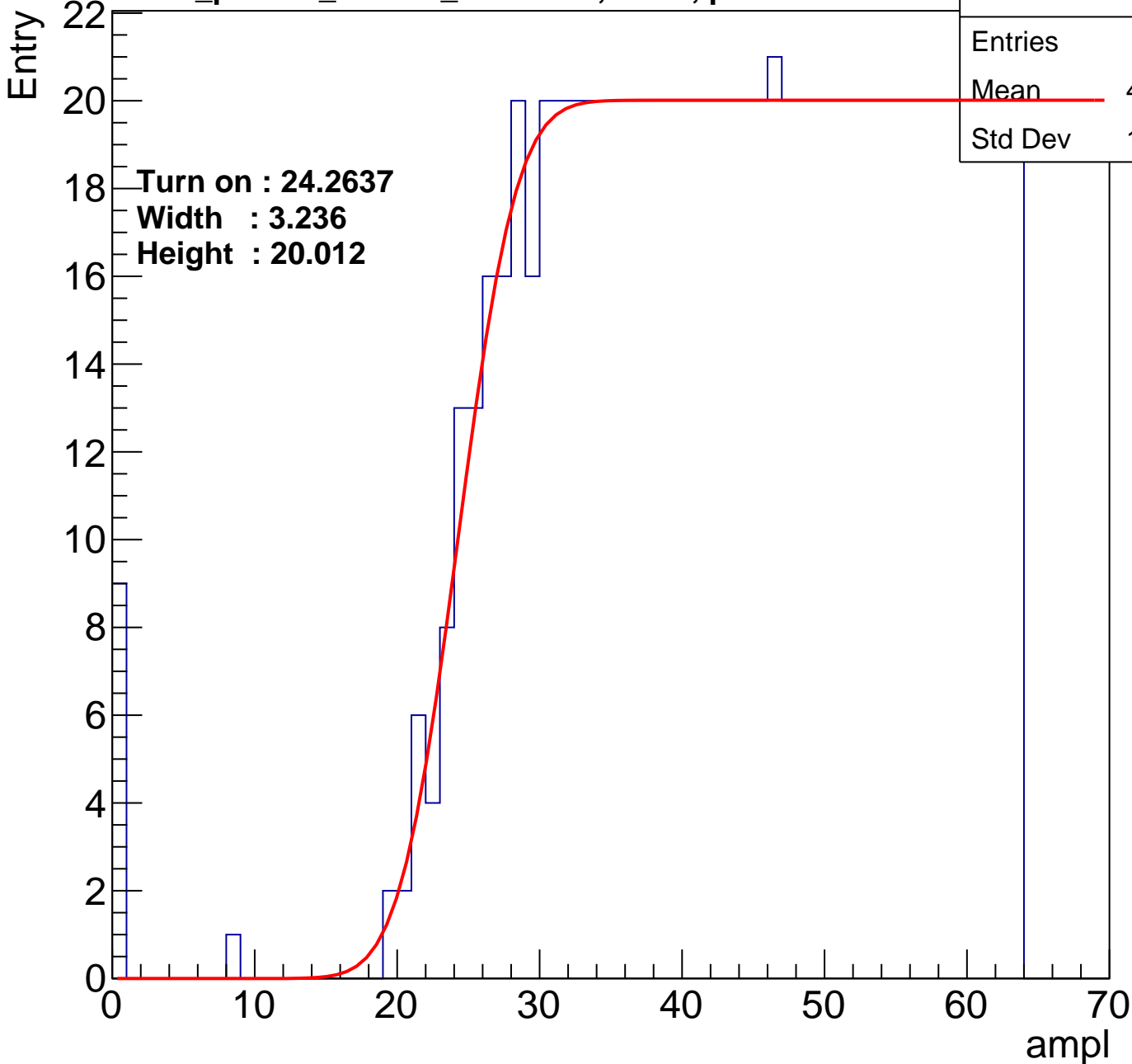


B0L101S, U19-ch74

calib_packv5_042523_0143.root, FC#1, port C1

Entries	807
Mean	42.95
Std Dev	12.54

Turn on : 24.2637
Width : 3.236
Height : 20.012



B0L101S, U19-ch75

calib_packv5_042523_0143.root, FC#1, port C1

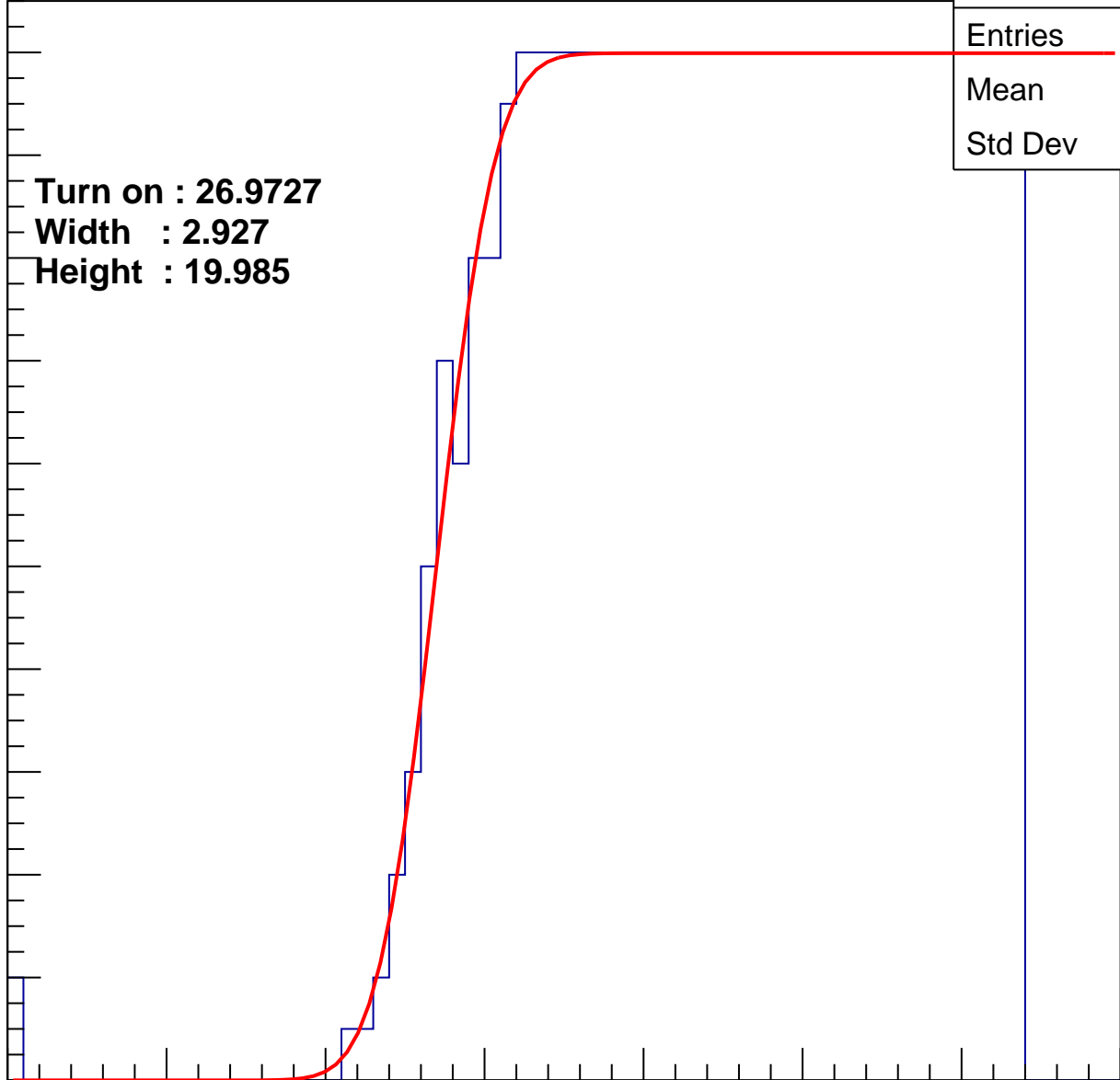
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9727
Width : 2.927
Height : 19.985

Entries	743
Mean	44.74
Std Dev	11.12

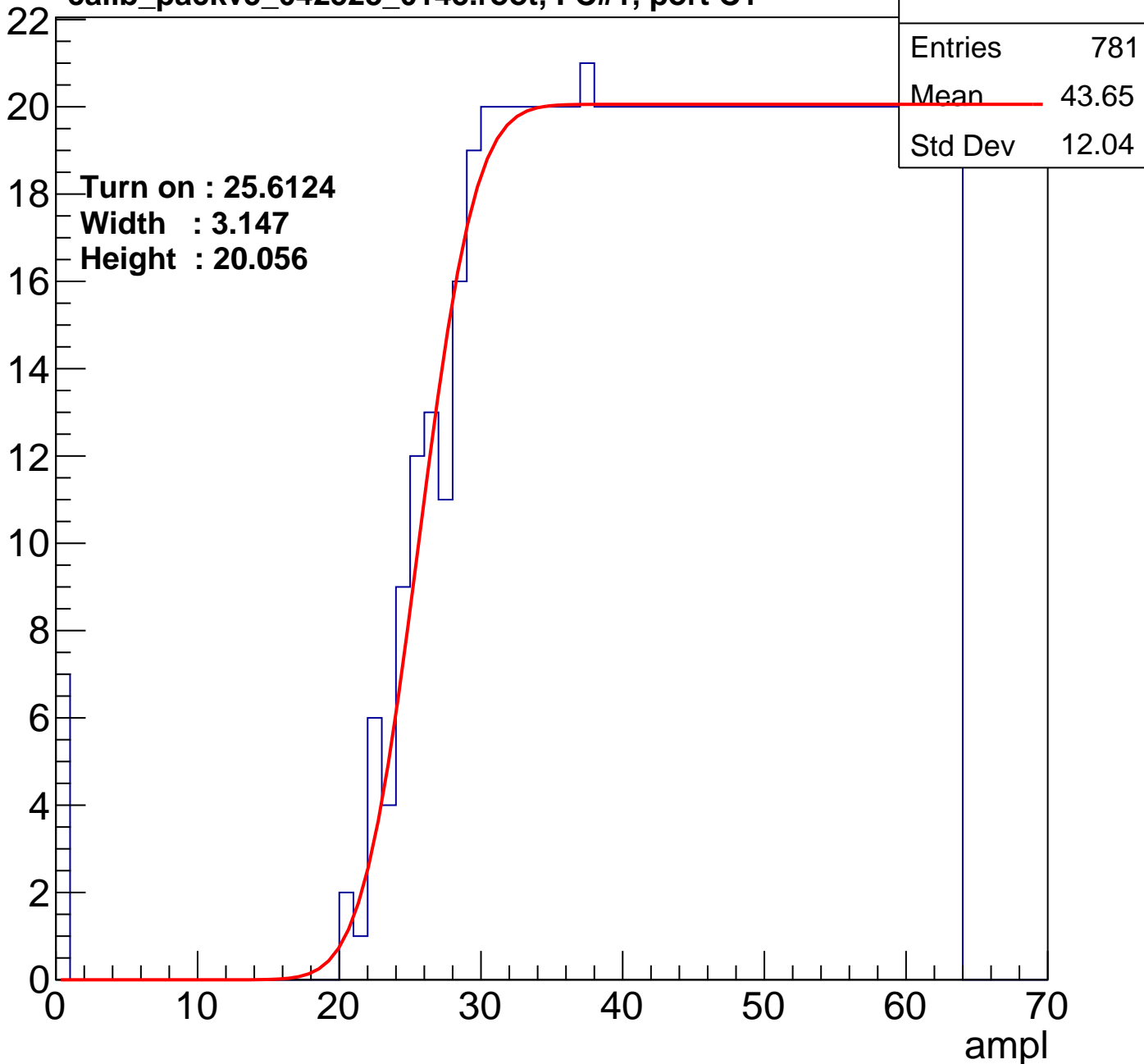
ampl



B0L101S, U19-ch76

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch77

calib_packv5_042523_0143.root, FC#1, port C1

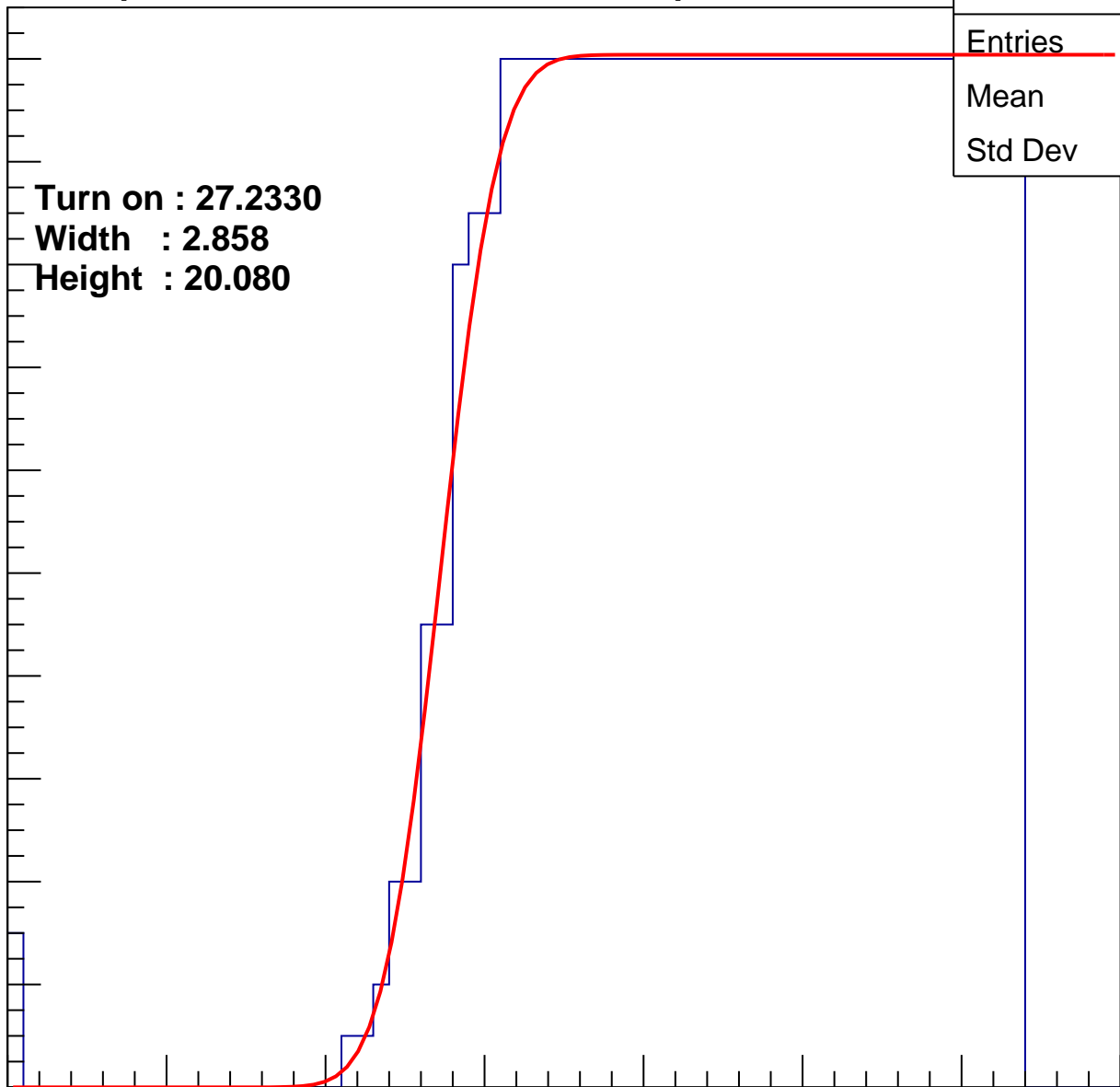
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2330
Width : 2.858
Height : 20.080

Entries	743
Mean	44.73
Std Dev	11.18

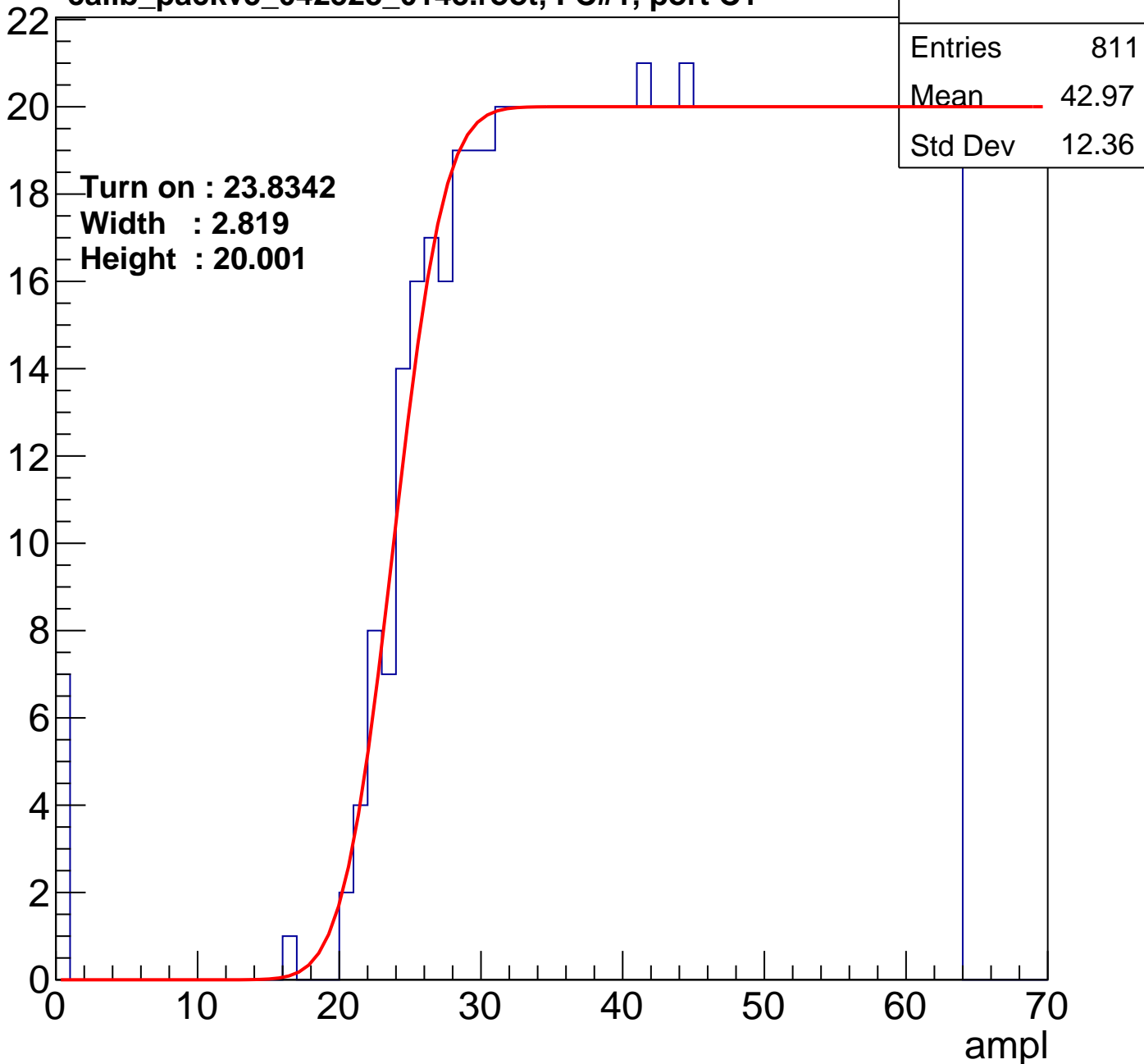
ampl



B0L101S, U19-ch78

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch79

calib_packv5_042523_0143.root, FC#1, port C1

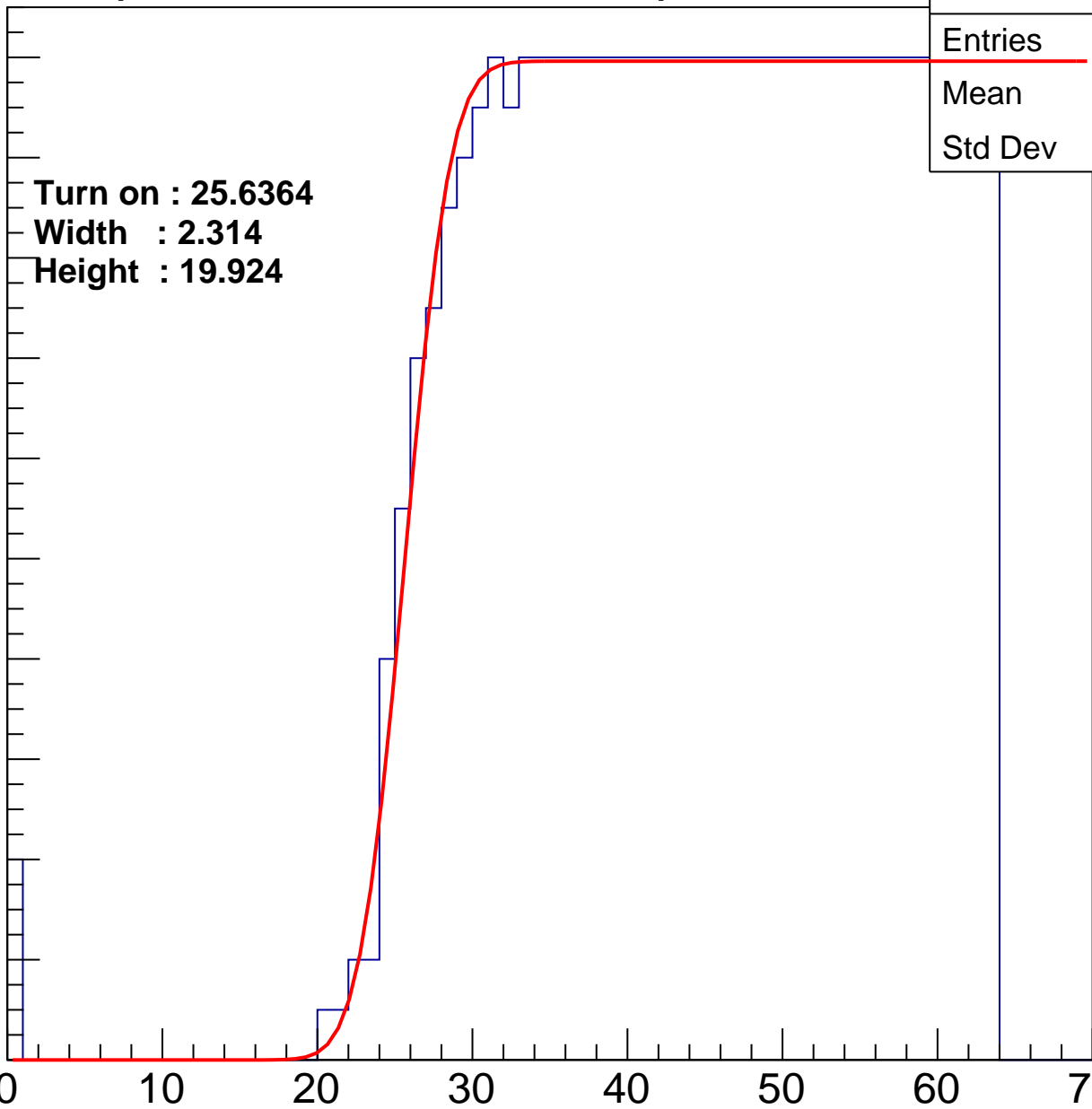
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6364
Width : 2.314
Height : 19.924

Entries	771
Mean	44
Std Dev	11.64

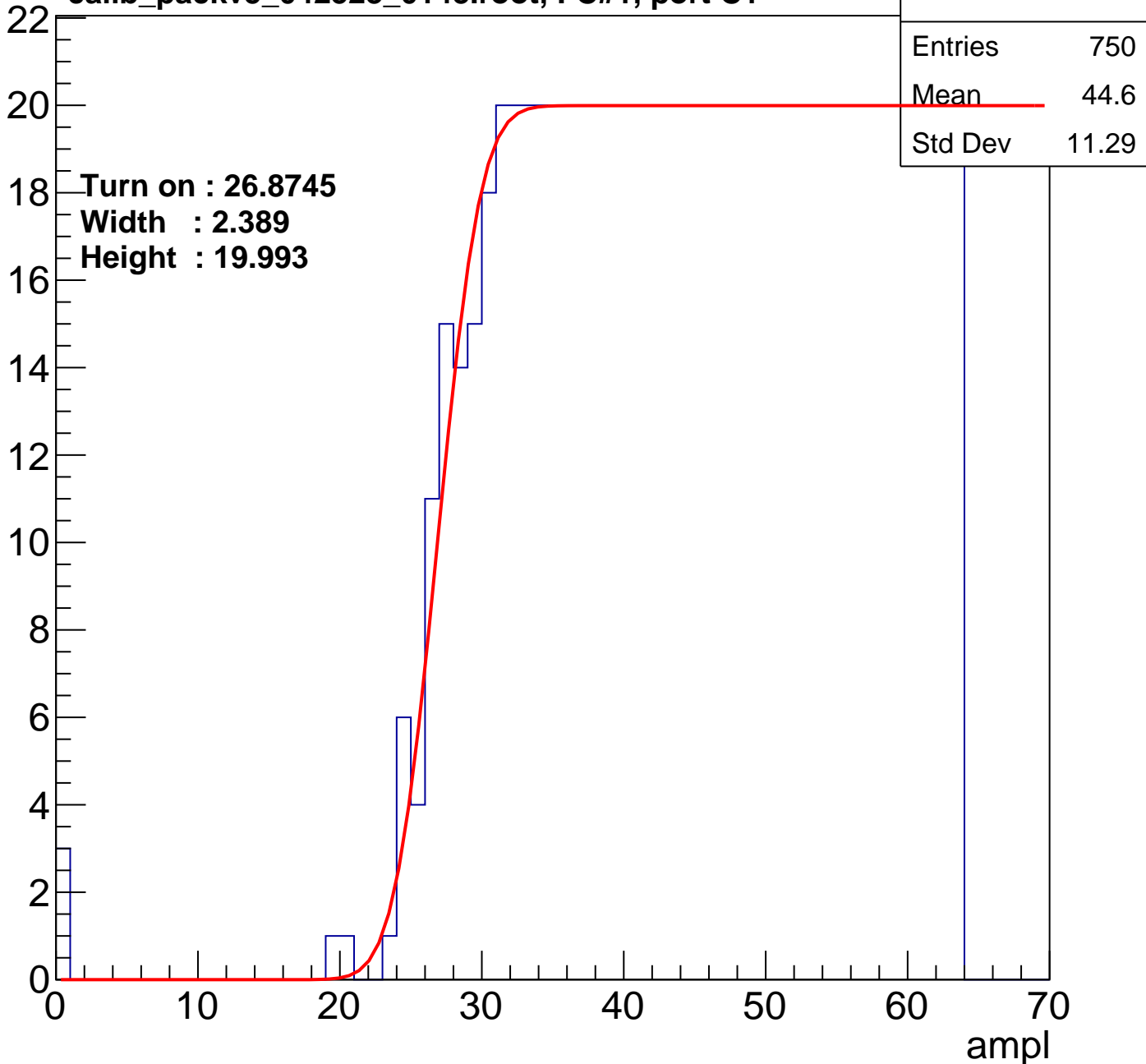
ampl



B0L101S, U19-ch80

calib_packv5_042523_0143.root, FC#1, port C1

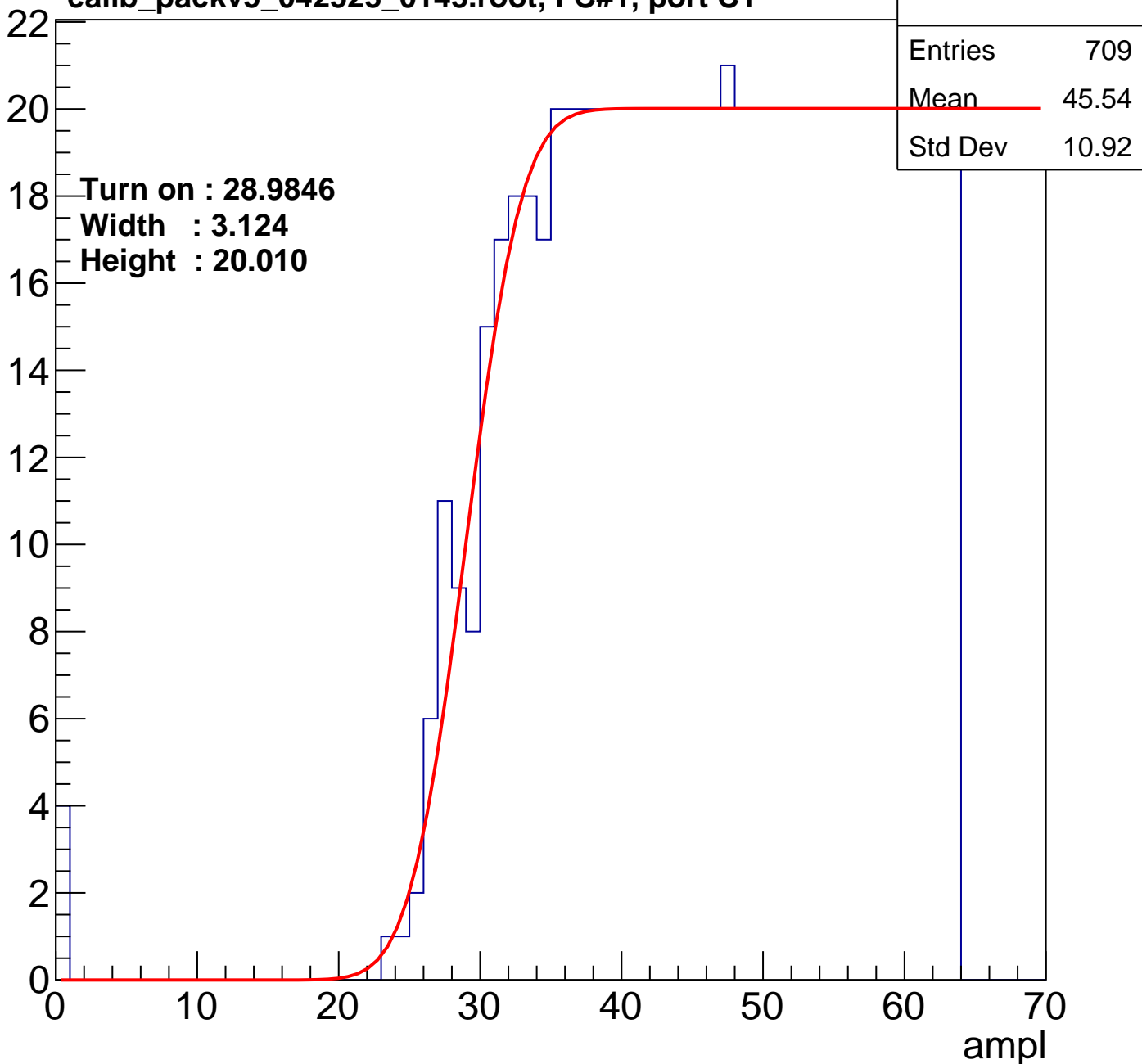
Entry



B0L101S, U19-ch81

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch82

calib_packv5_042523_0143.root, FC#1, port C1

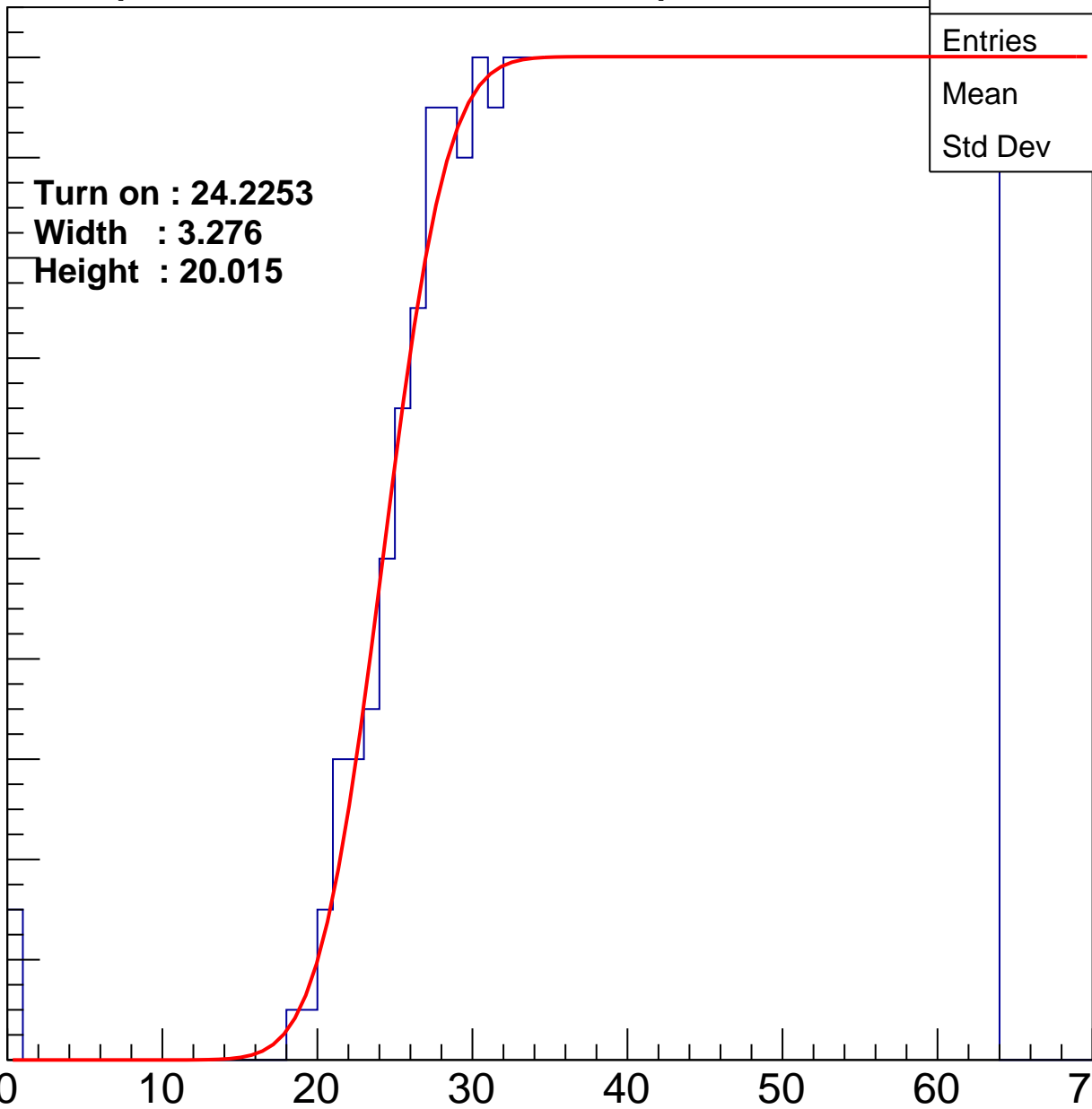
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.2253
Width : 3.276
Height : 20.015

Entries	800
Mean	43.28
Std Dev	11.99

ampl



B0L101S, U19-ch83

calib_packv5_042523_0143.root, FC#1, port C1

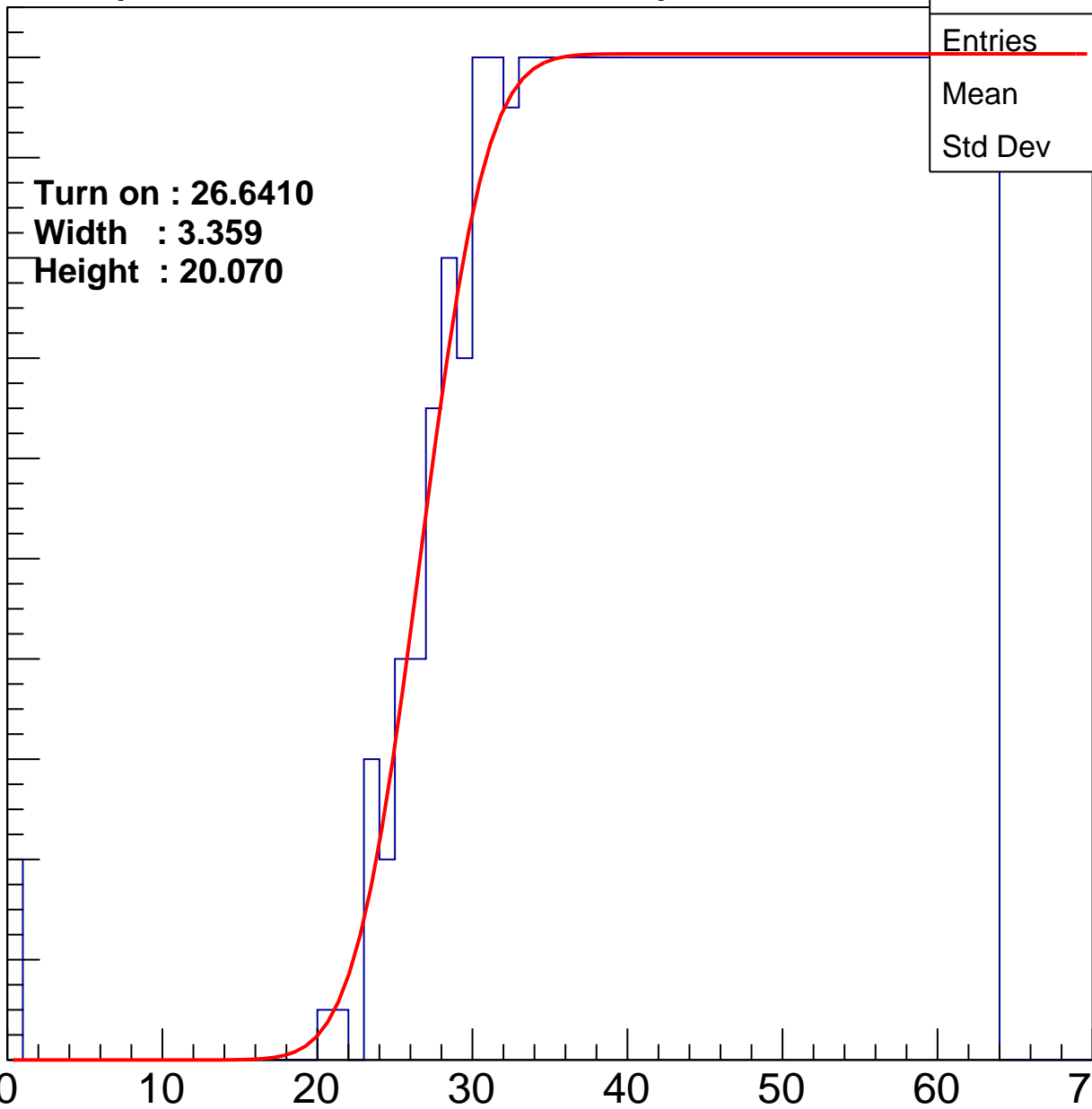
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6410
Width : 3.359
Height : 20.070

Entries	754
Mean	44.4
Std Dev	11.46

ampl



B0L101S, U19-ch84

calib_packv5_042523_0143.root, FC#1, port C1

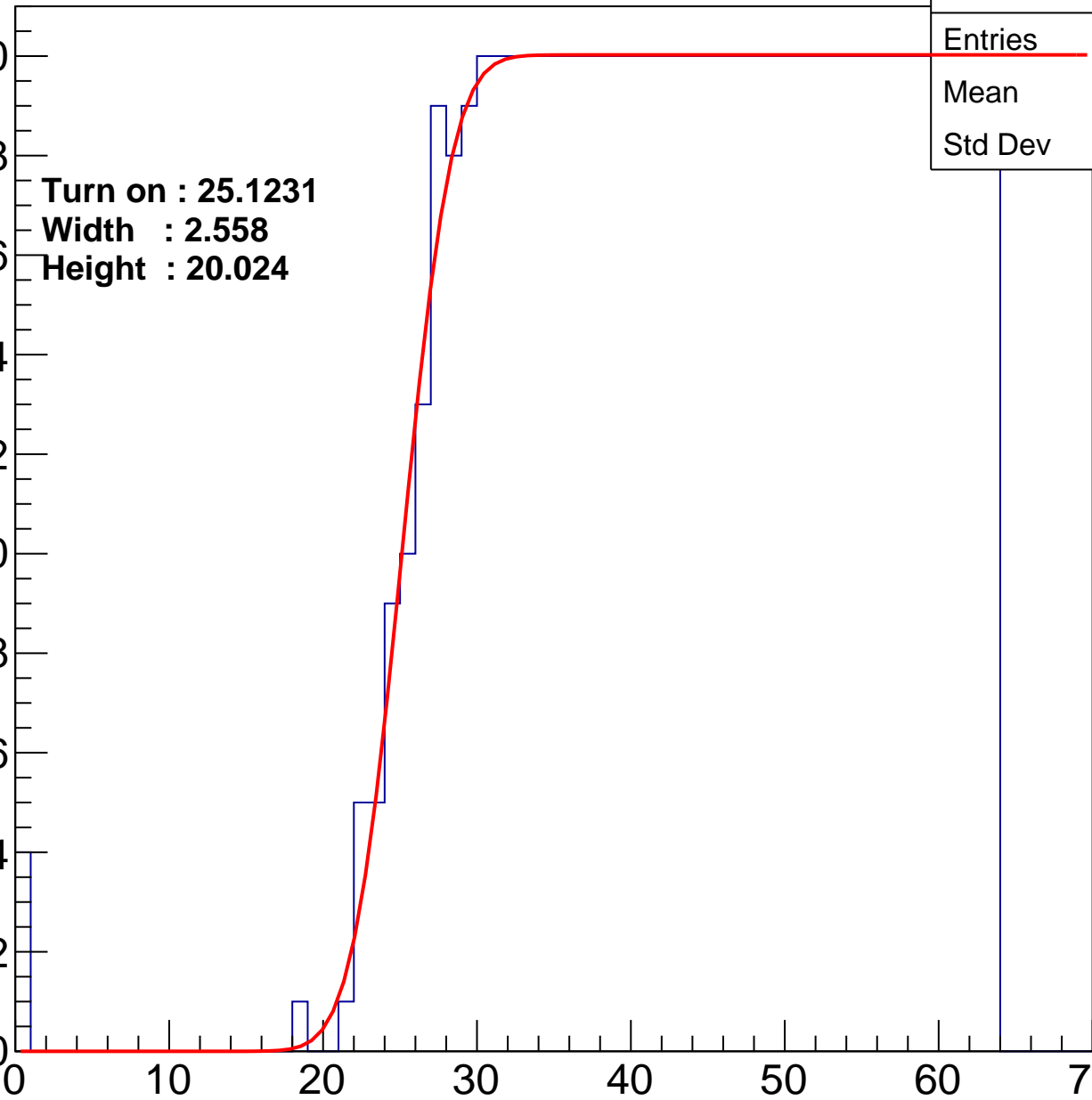
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1231
Width : 2.558
Height : 20.024

Entries	784
Mean	43.69
Std Dev	11.79

ampl



B0L101S, U19-ch85

calib_packv5_042523_0143.root, FC#1, port C1

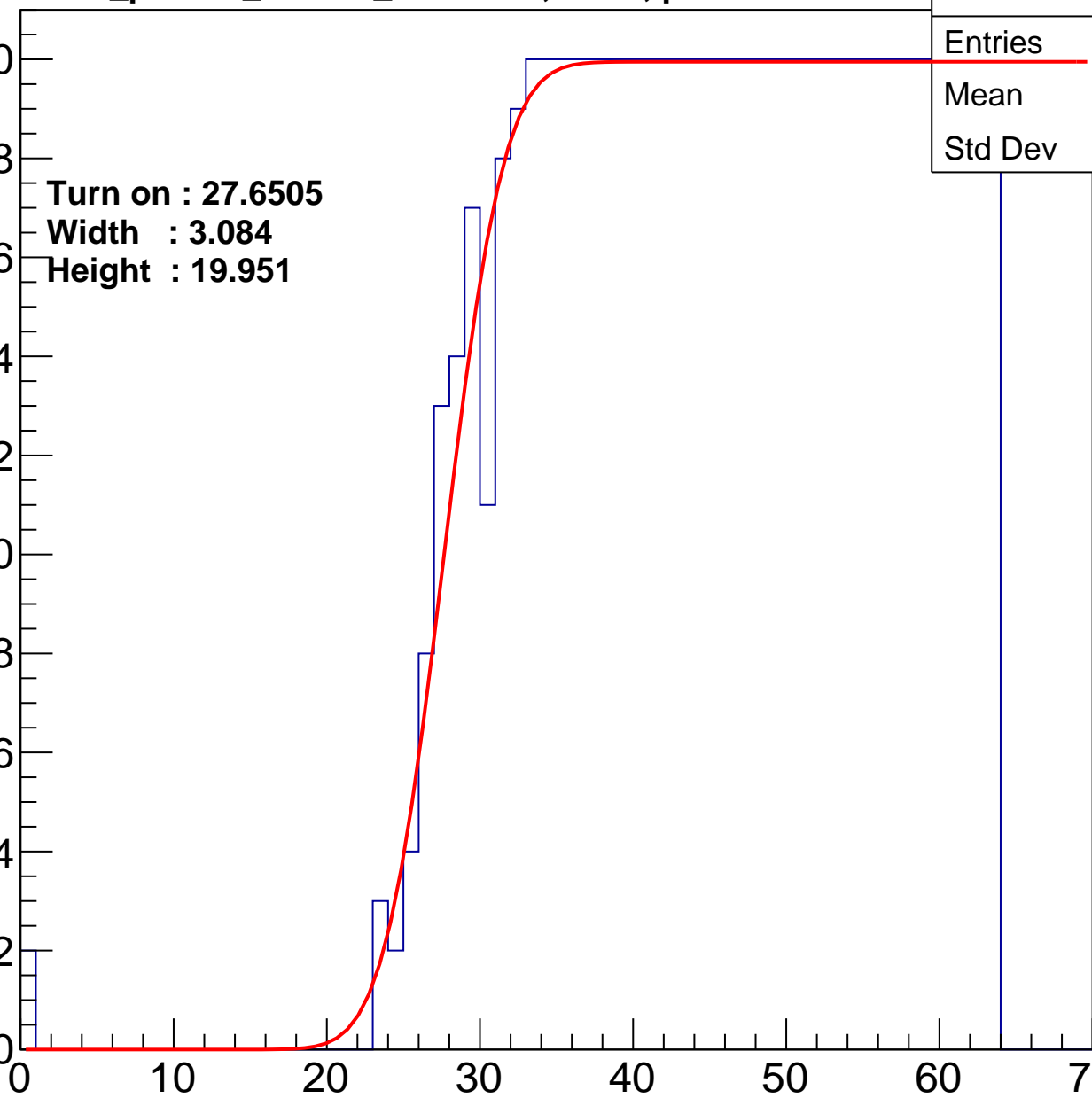
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6505
Width : 3.084
Height : 19.951

Entries	731
Mean	45.03
Std Dev	10.96

ampl



B0L101S, U19-ch86

calib_packv5_042523_0143.root, FC#1, port C1

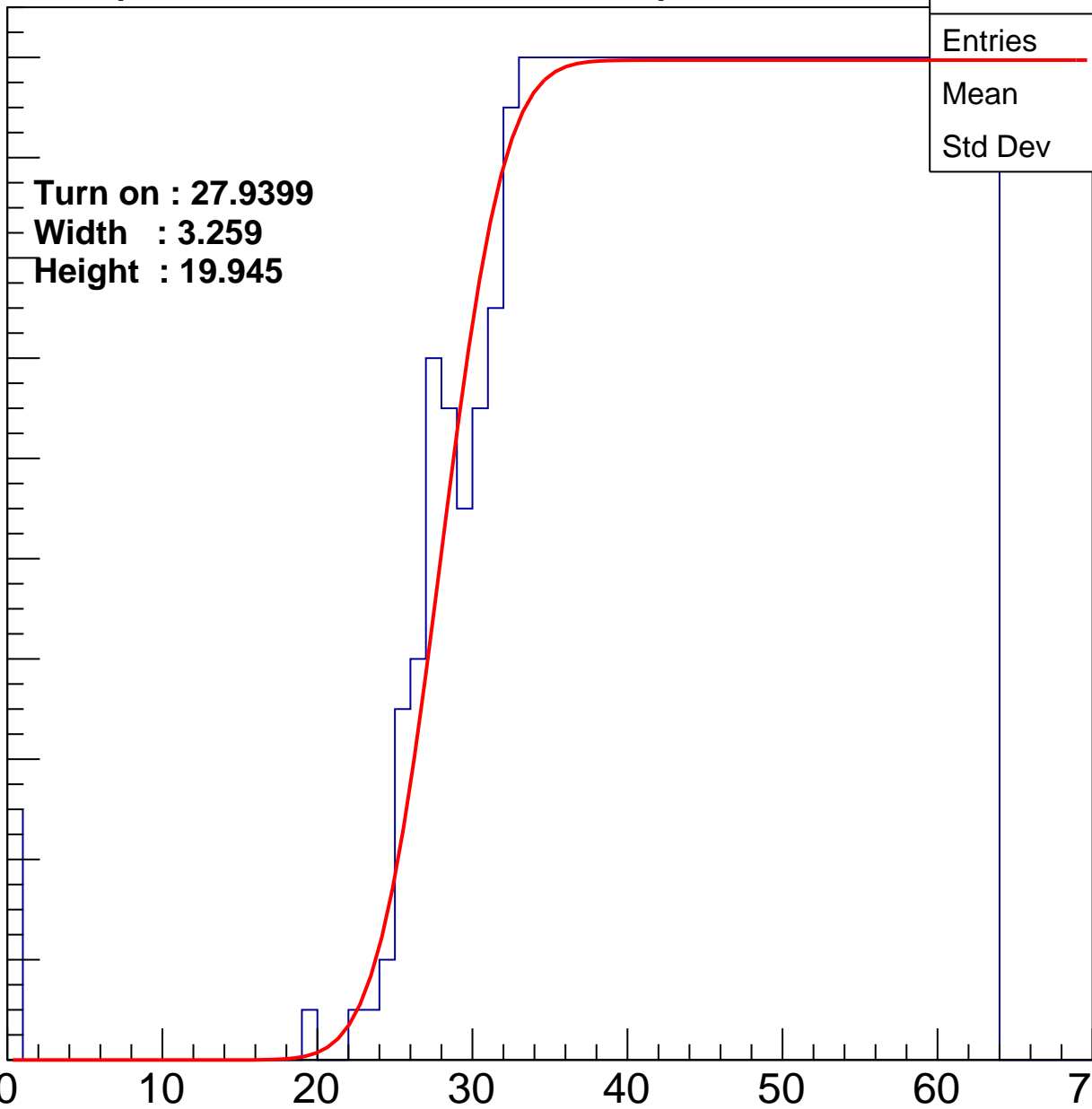
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9399
Width : 3.259
Height : 19.945

Entries	730
Mean	44.9
Std Dev	11.33

ampl



B0L101S, U19-ch87

calib_packv5_042523_0143.root, FC#1, port C1

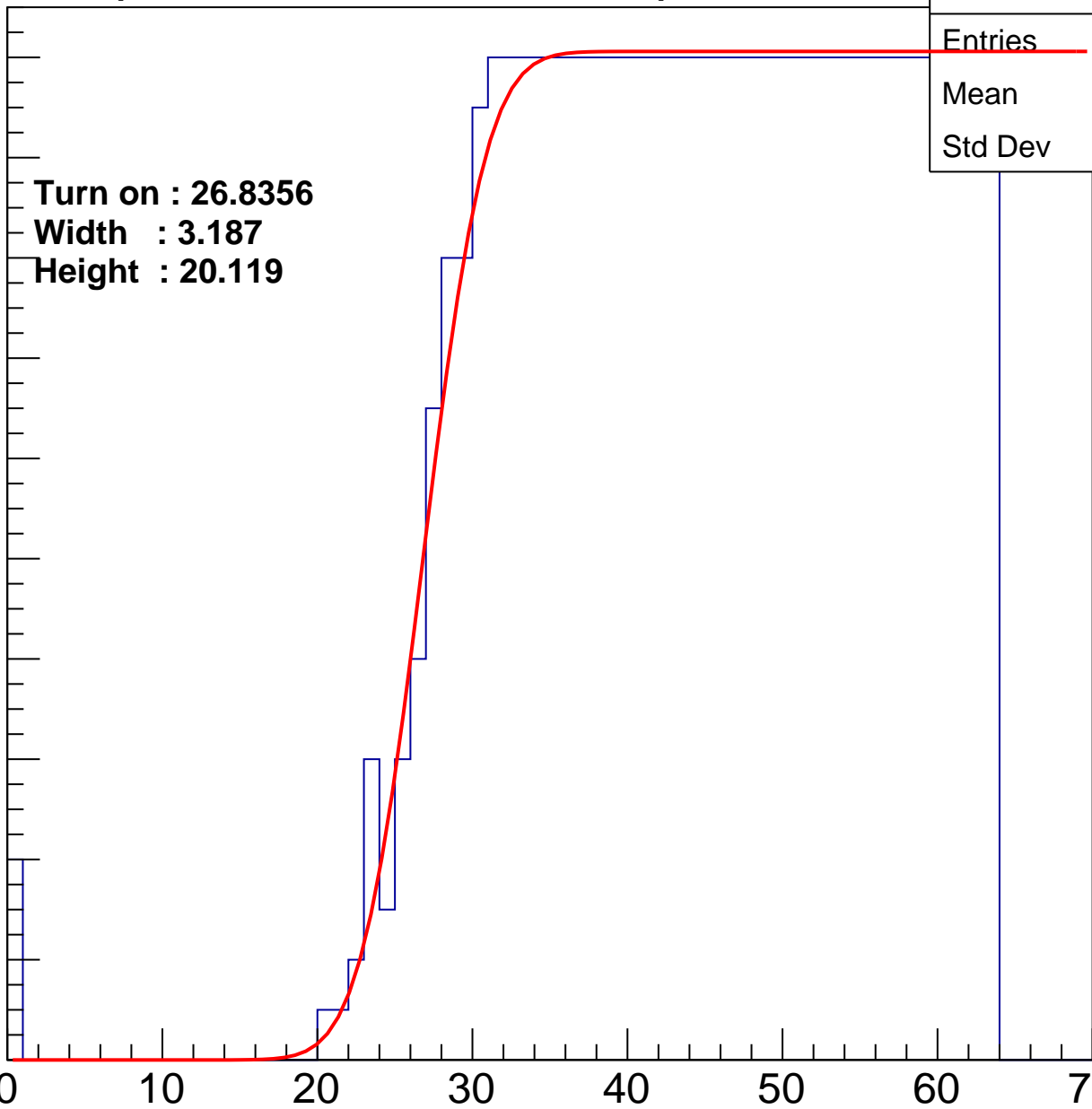
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8356
Width : 3.187
Height : 20.119

Entries	755
Mean	44.38
Std Dev	11.47

ampl



B0L101S, U19-ch88

calib_packv5_042523_0143.root, FC#1, port C1

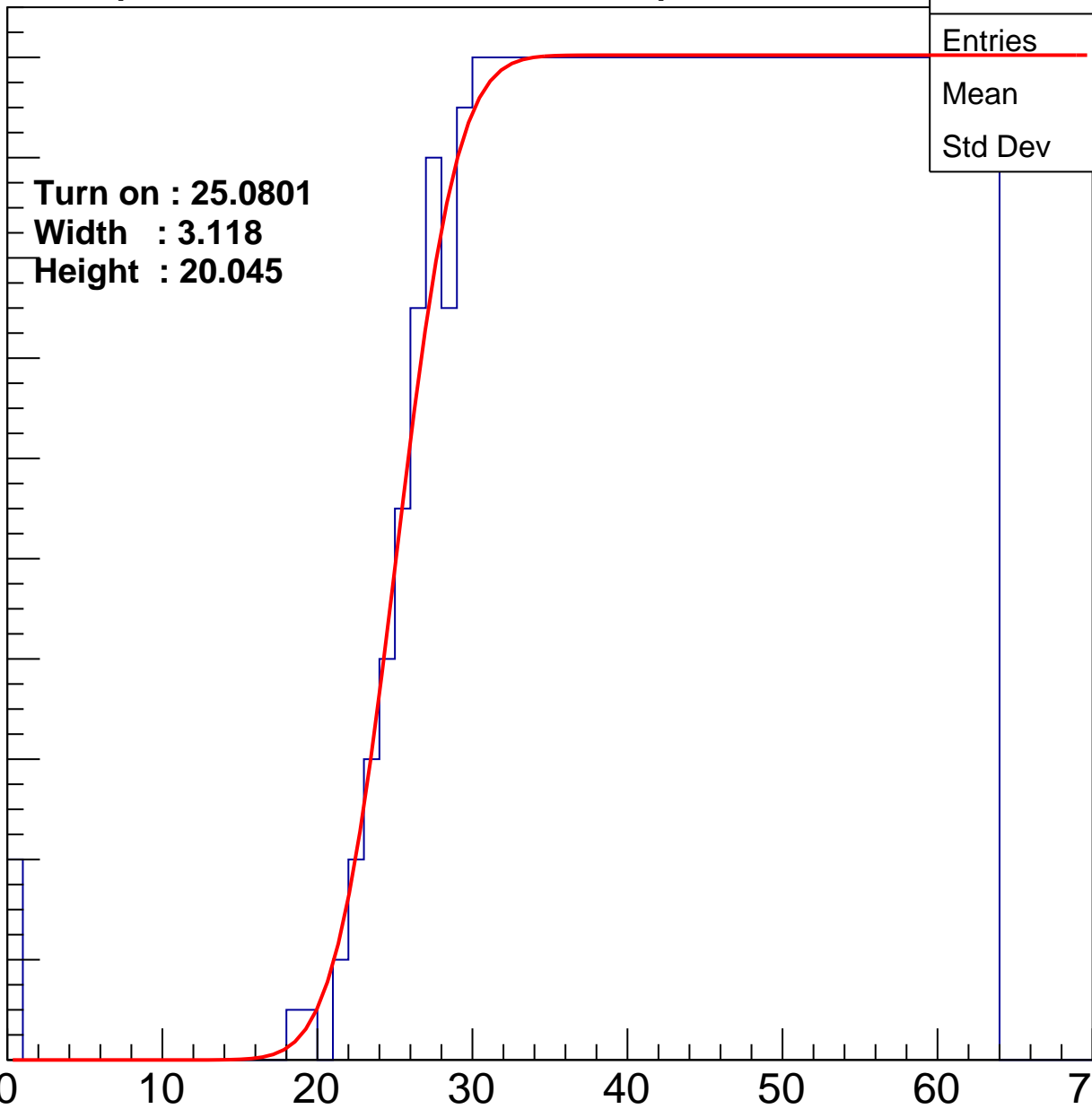
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0801
Width : 3.118
Height : 20.045

Entries	784
Mean	43.67
Std Dev	11.83

ampl



B0L101S, U19-ch89

calib_packv5_042523_0143.root, FC#1, port C1

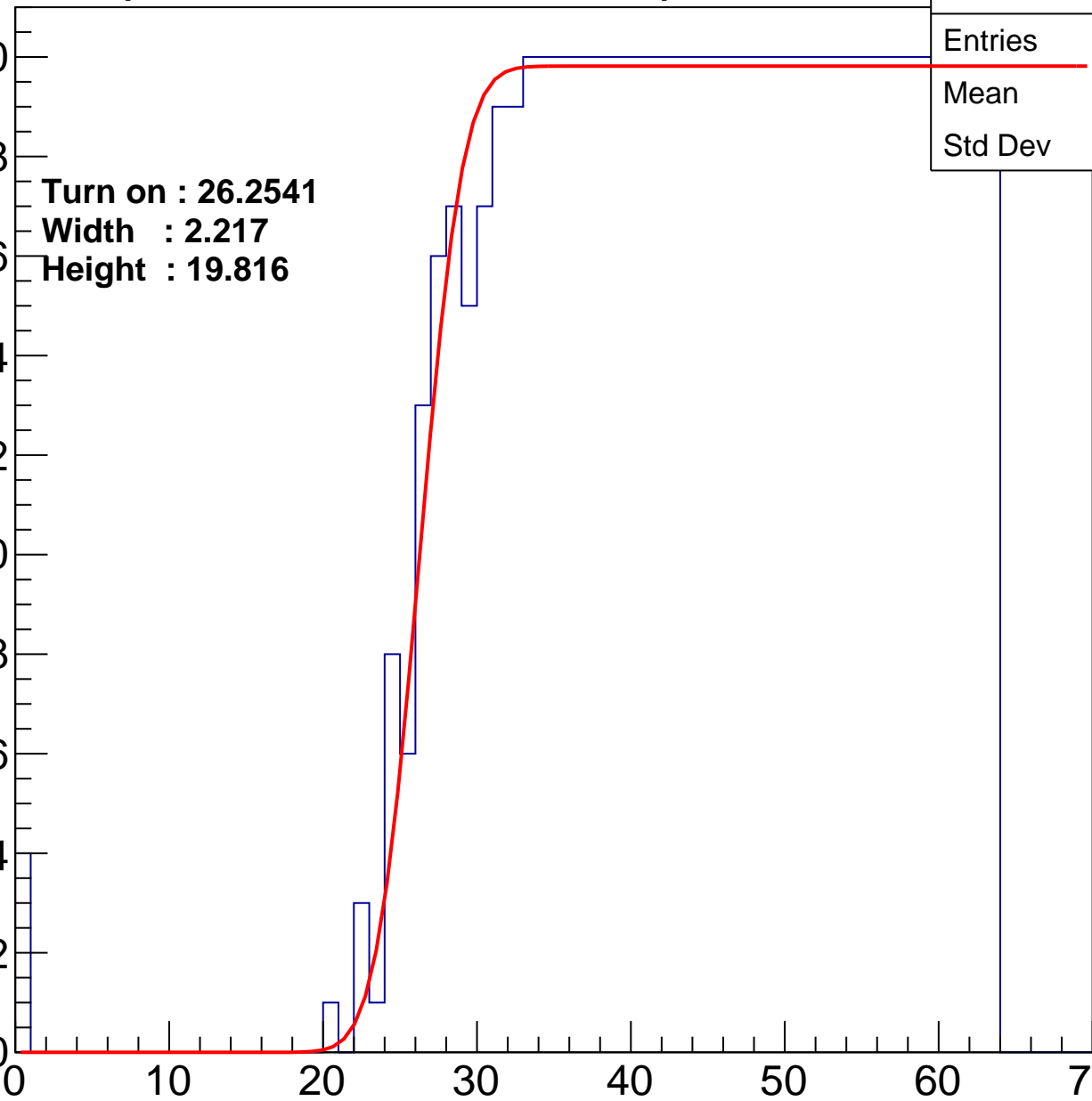
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2541
Width : 2.217
Height : 19.816

Entries	759
Mean	44.27
Std Dev	11.53

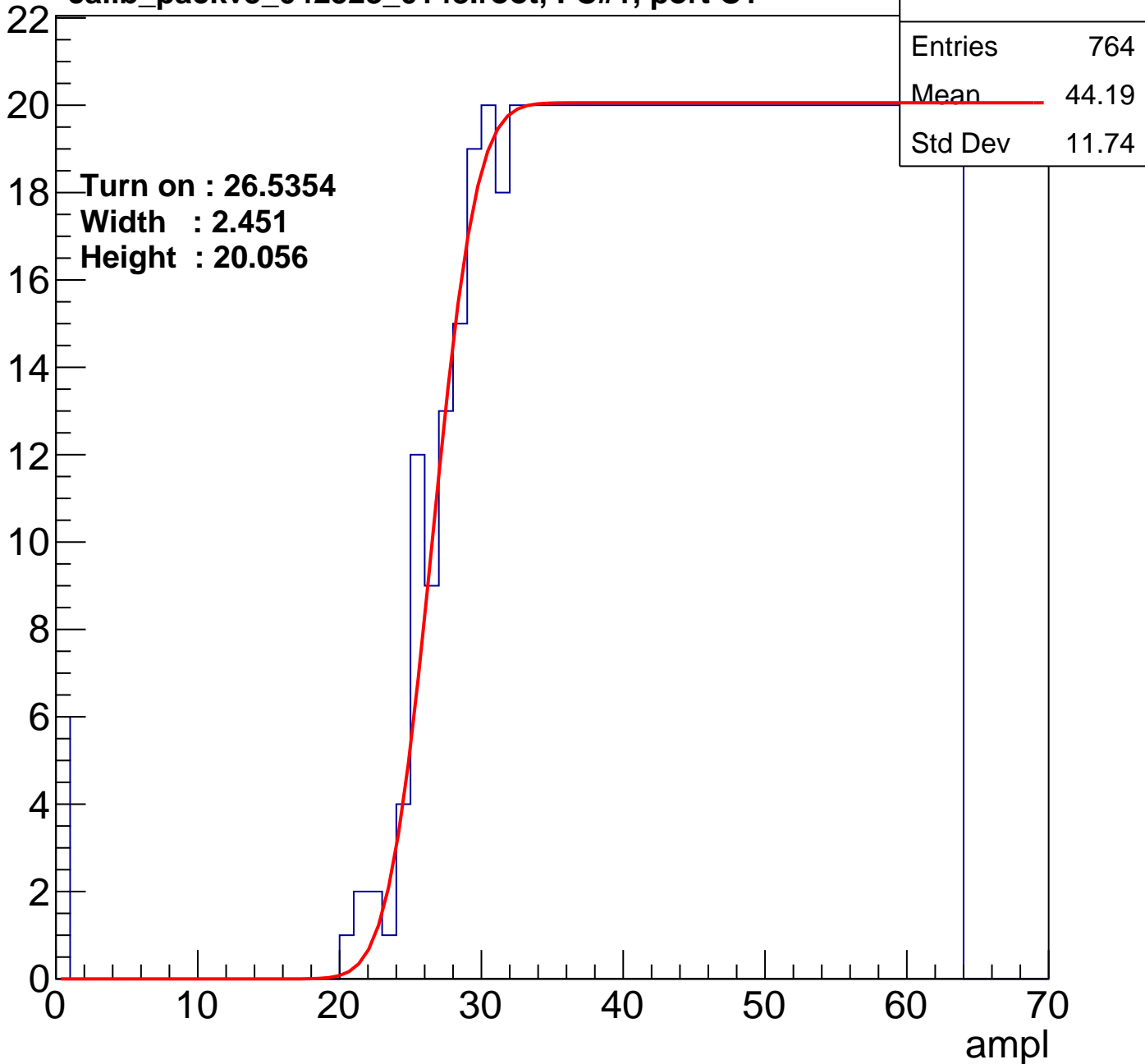
ampl



B0L101S, U19-ch90

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch91

calib_packv5_042523_0143.root, FC#1, port C1

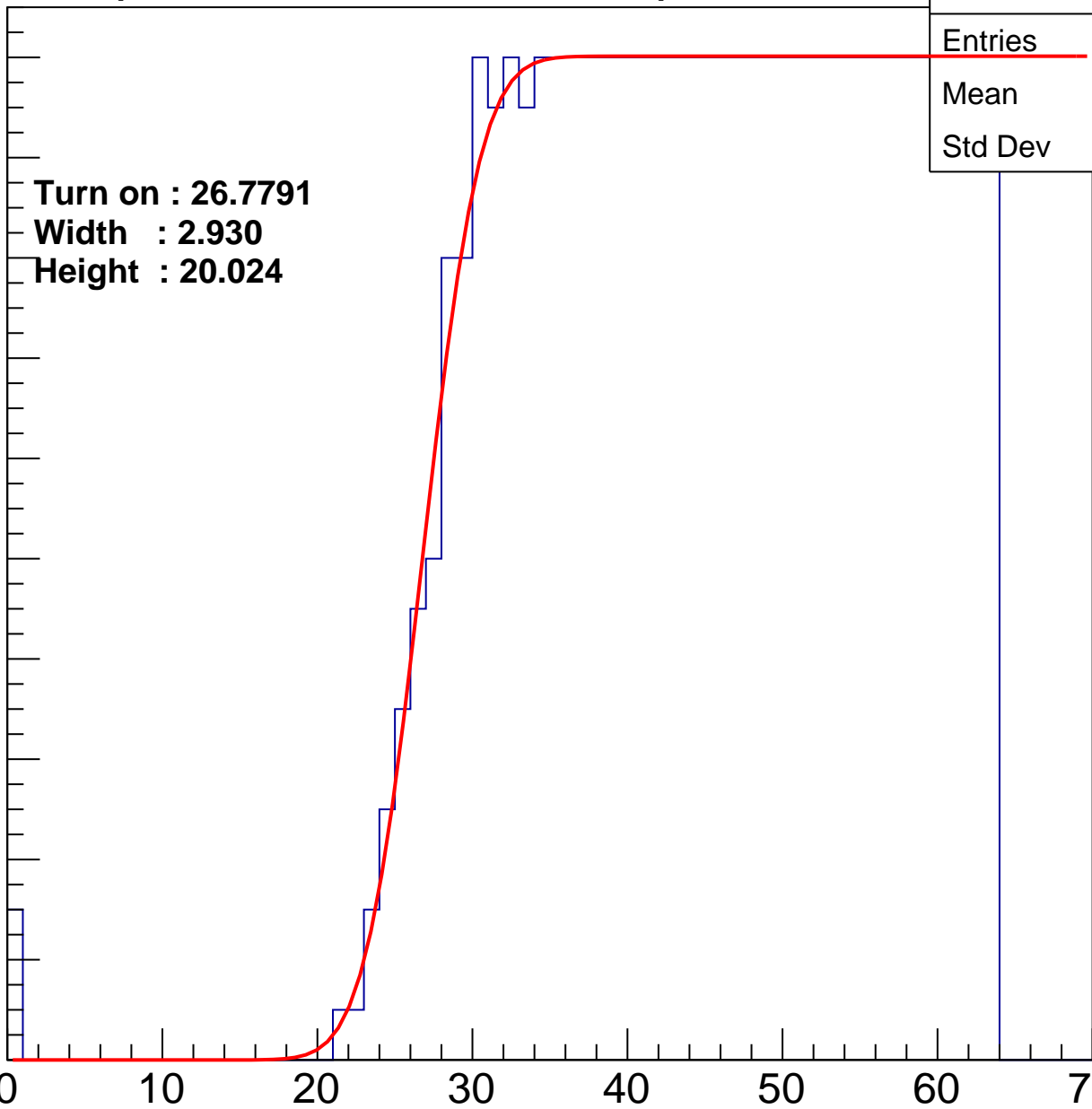
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7791
Width : 2.930
Height : 20.024

Entries	749
Mean	44.56
Std Dev	11.28

ampl



B0L101S, U19-ch92

calib_packv5_042523_0143.root, FC#1, port C1

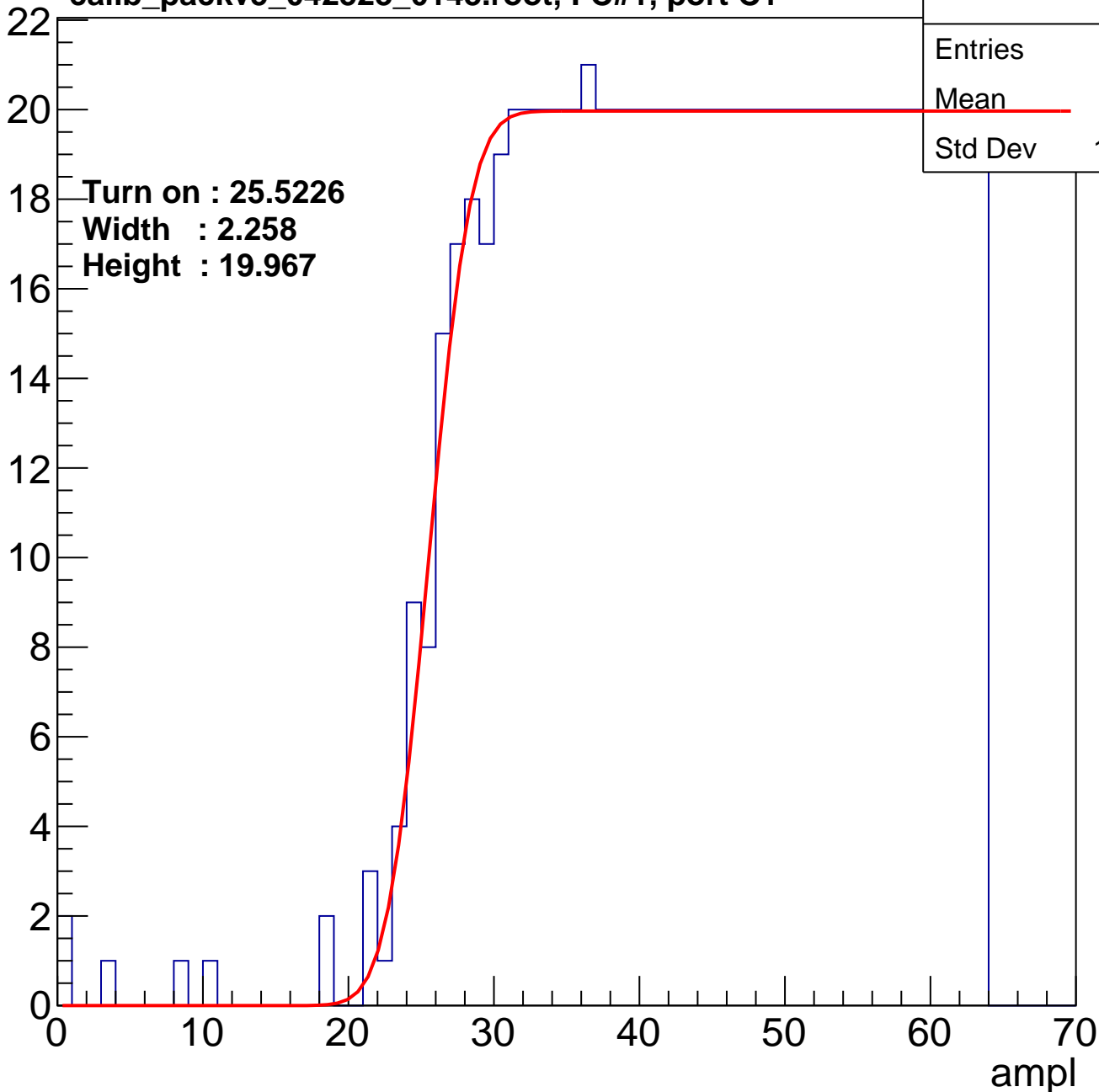
Entries	779
Mean	43.8
Std Dev	11.74

Turn on : 25.5226

Width : 2.258

Height : 19.967

Entry



B0L101S, U19-ch93

calib_packv5_042523_0143.root, FC#1, port C1

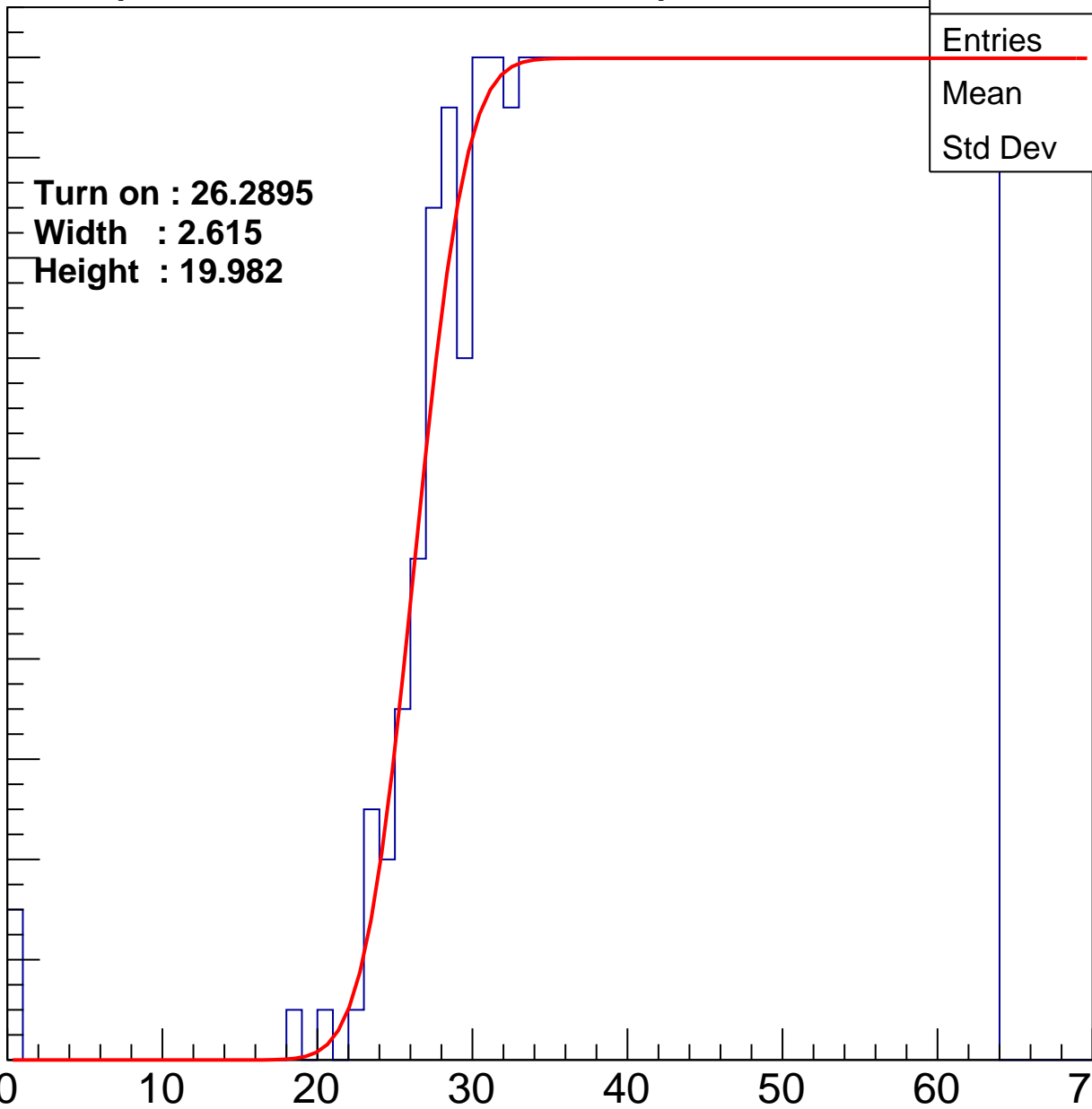
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2895
Width : 2.615
Height : 19.982

Entries	761
Mean	44.27
Std Dev	11.44

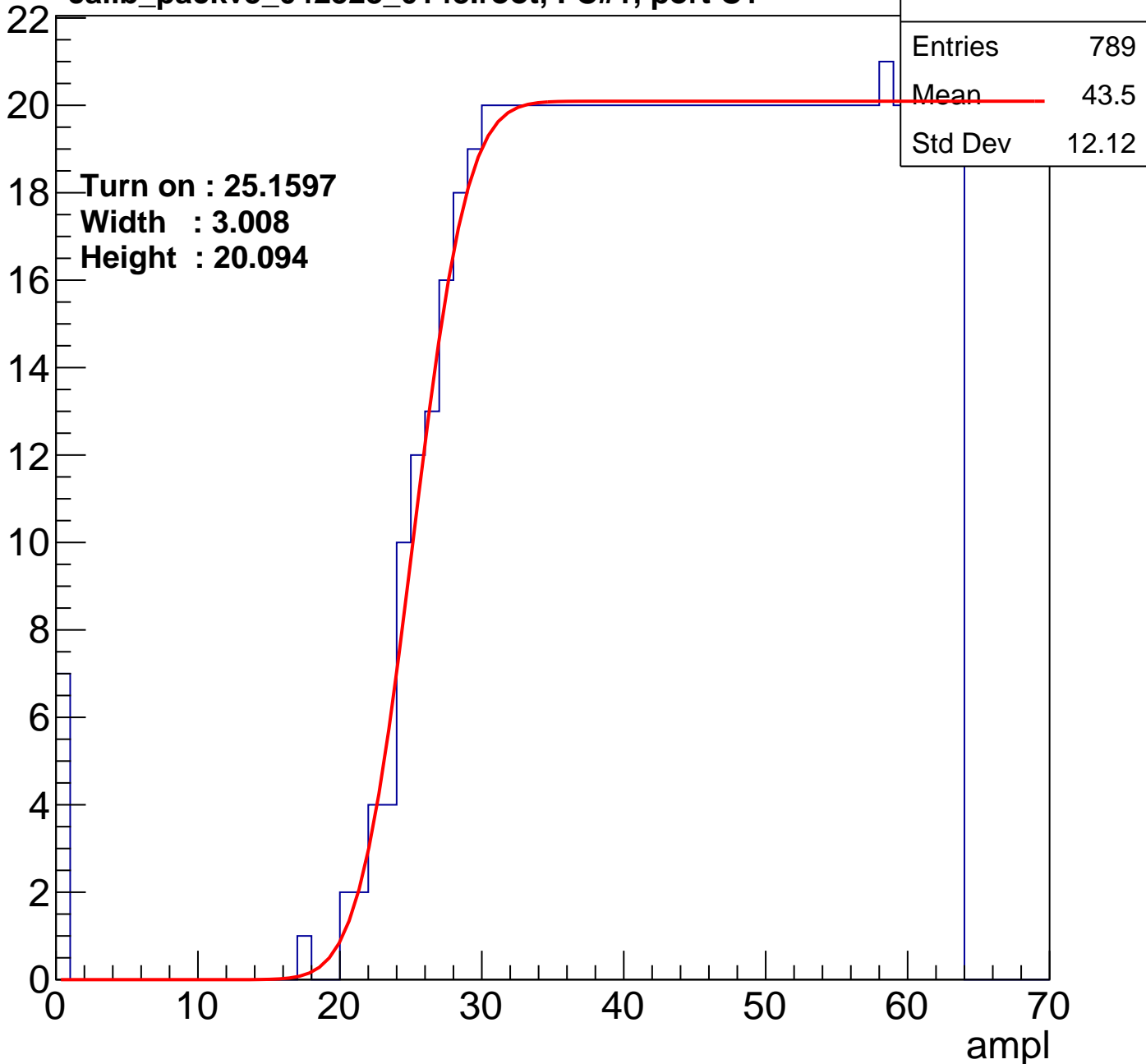
ampl



B0L101S, U19-ch94

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch95

calib_packv5_042523_0143.root, FC#1, port C1

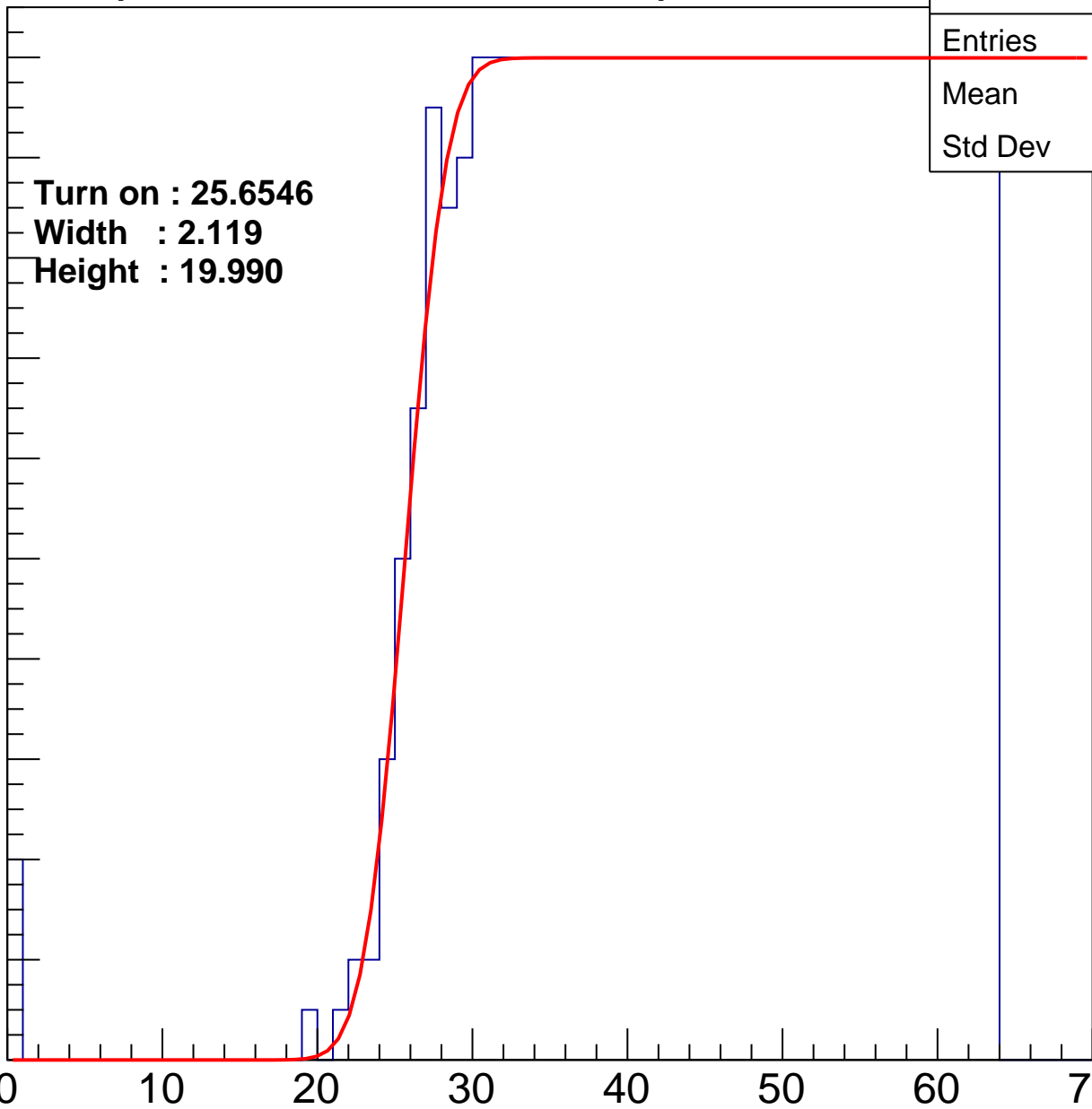
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6546
Width : 2.119
Height : 19.990

Entries	773
Mean	43.98
Std Dev	11.63

ampl



B0L101S, U19-ch96

calib_packv5_042523_0143.root, FC#1, port C1

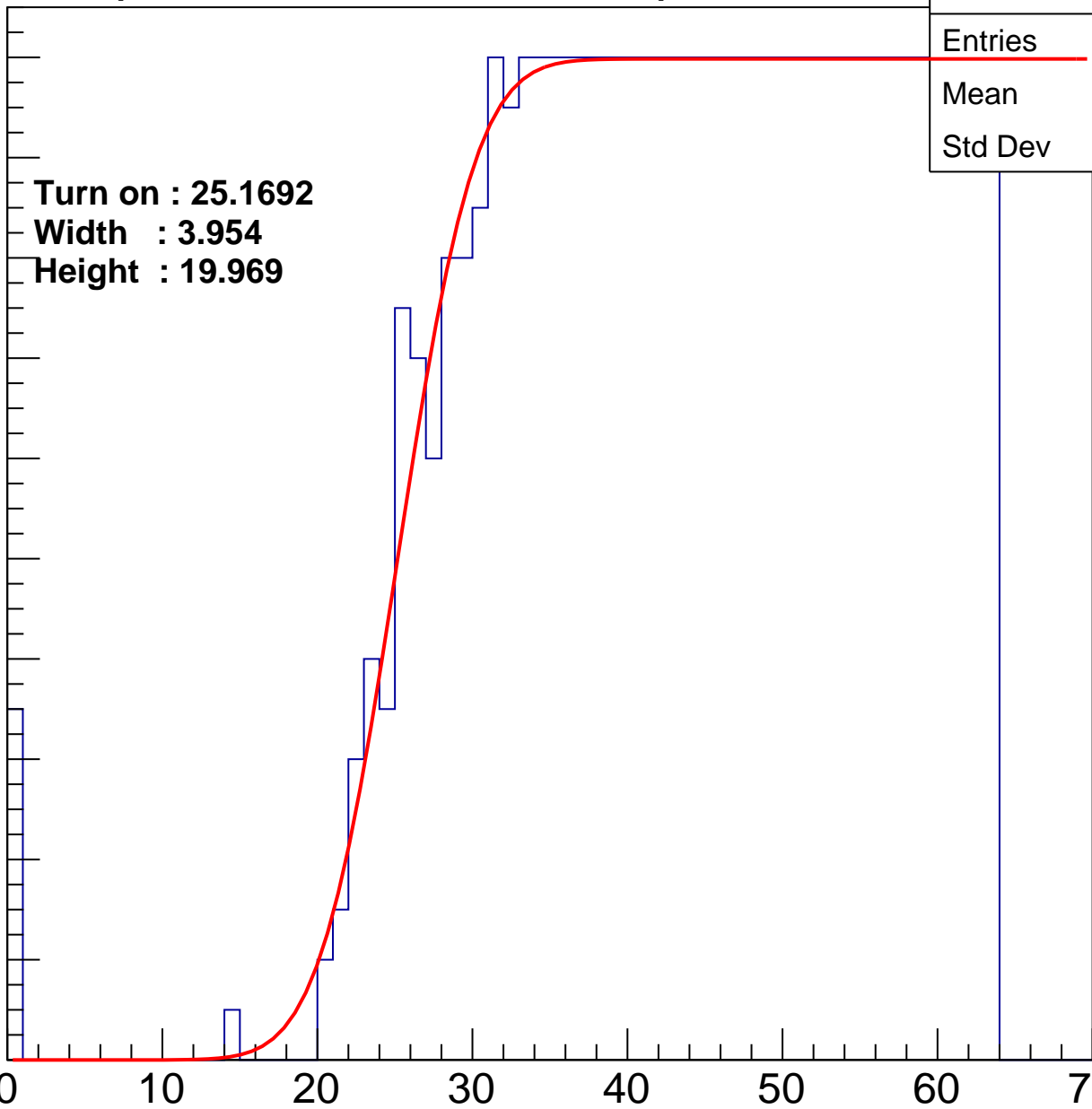
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1692
Width : 3.954
Height : 19.969

Entries	783
Mean	43.52
Std Dev	12.19

ampl



B0L101S, U19-ch97

calib_packv5_042523_0143.root, FC#1, port C1

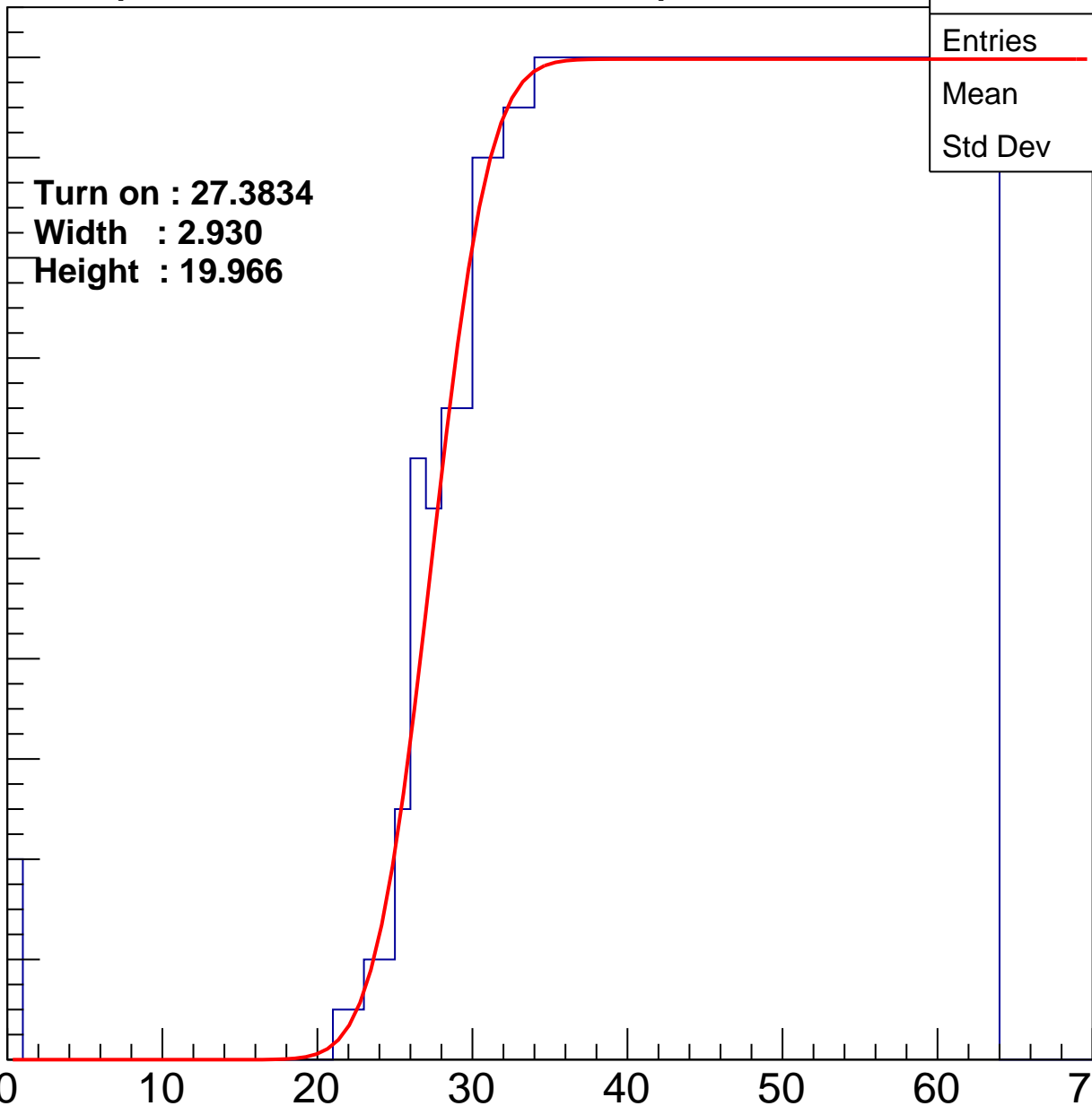
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.3834
Width : 2.930
Height : 19.966

Entries	738
Mean	44.78
Std Dev	11.28

ampl



B0L101S, U19-ch98

calib_packv5_042523_0143.root, FC#1, port C1

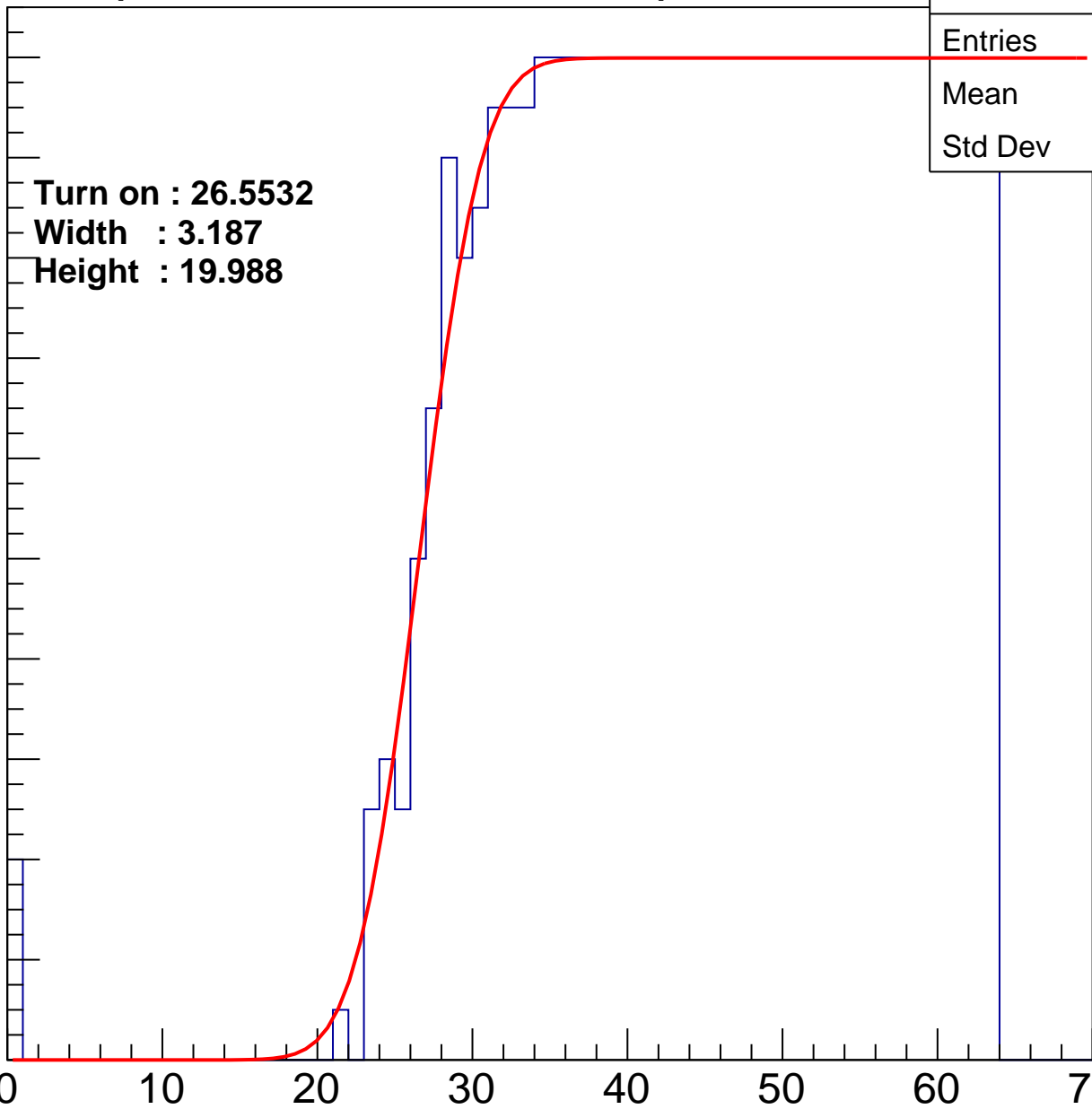
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5532
Width : 3.187
Height : 19.988

Entries	752
Mean	44.44
Std Dev	11.44

ampl



B0L101S, U19-ch99

calib_packv5_042523_0143.root, FC#1, port C1

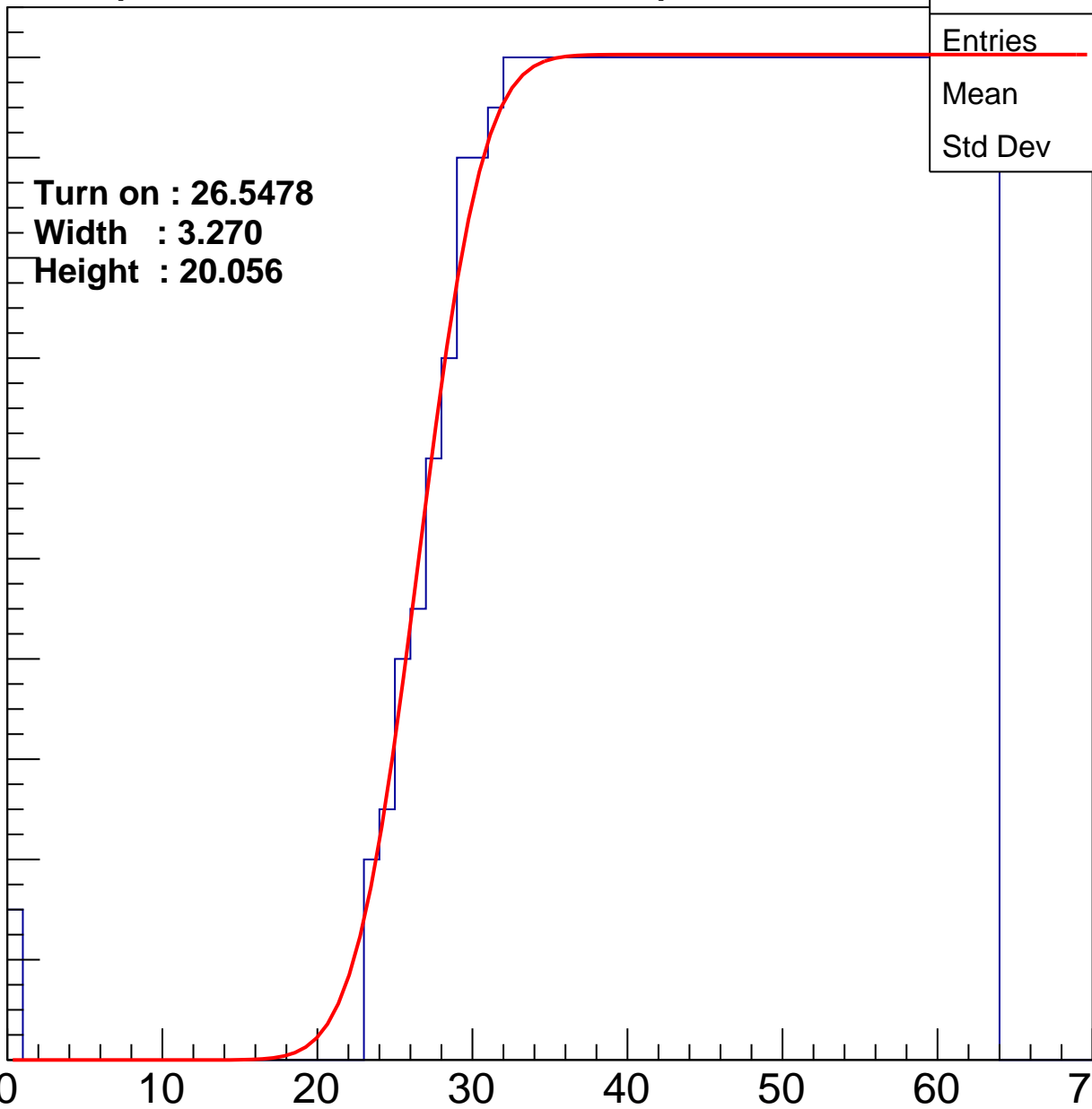
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5478
Width : 3.270
Height : 20.056

Entries	750
Mean	44.55
Std Dev	11.28

ampl



B0L101S, U19-ch100

calib_packv5_042523_0143.root, FC#1, port C1

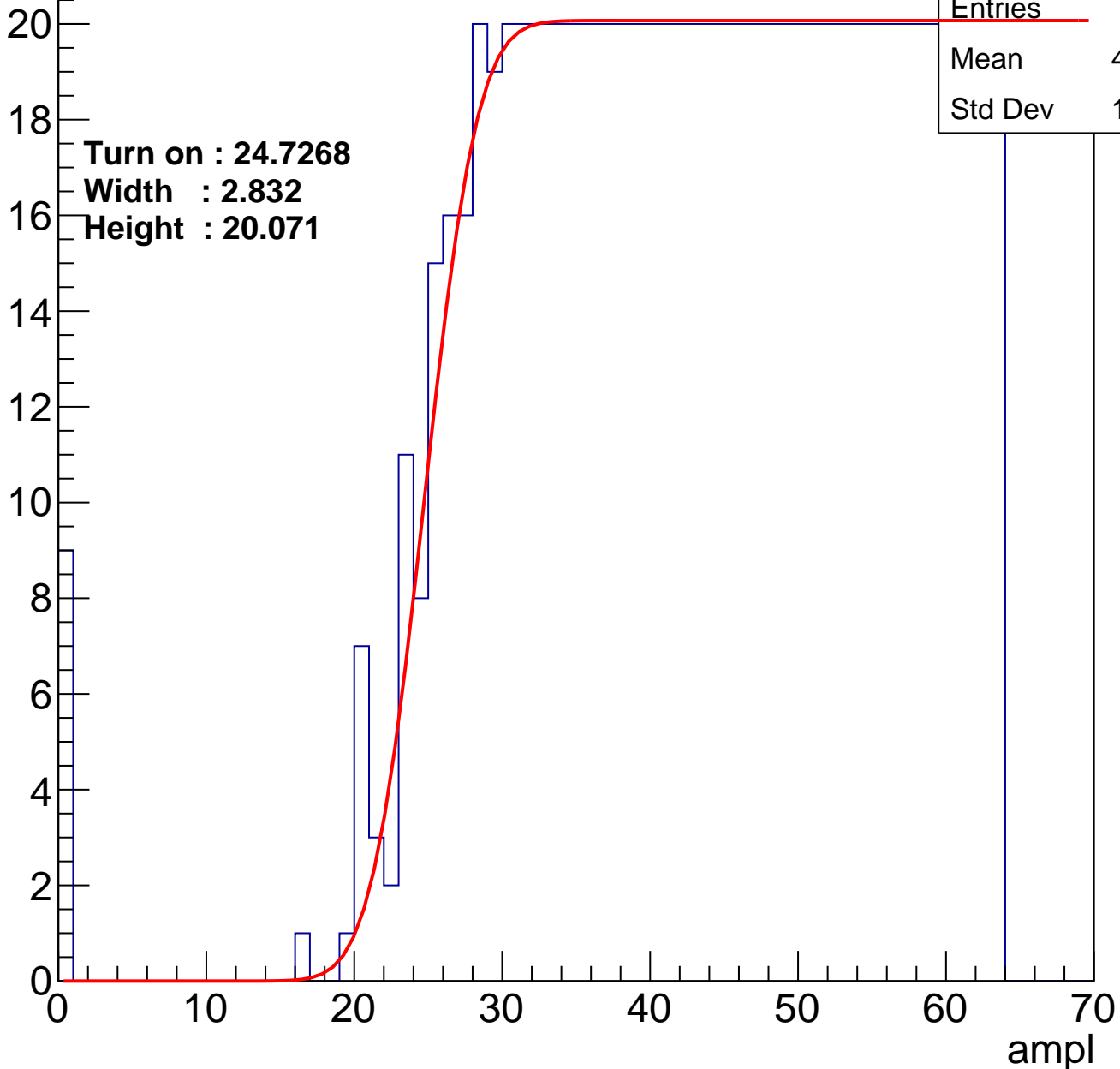
Entries	808
Mean	42.92
Std Dev	12.53

Turn on : 24.7268

Width : 2.832

Height : 20.071

Entry



B0L101S, U19-ch101

calib_packv5_042523_0143.root, FC#1, port C1

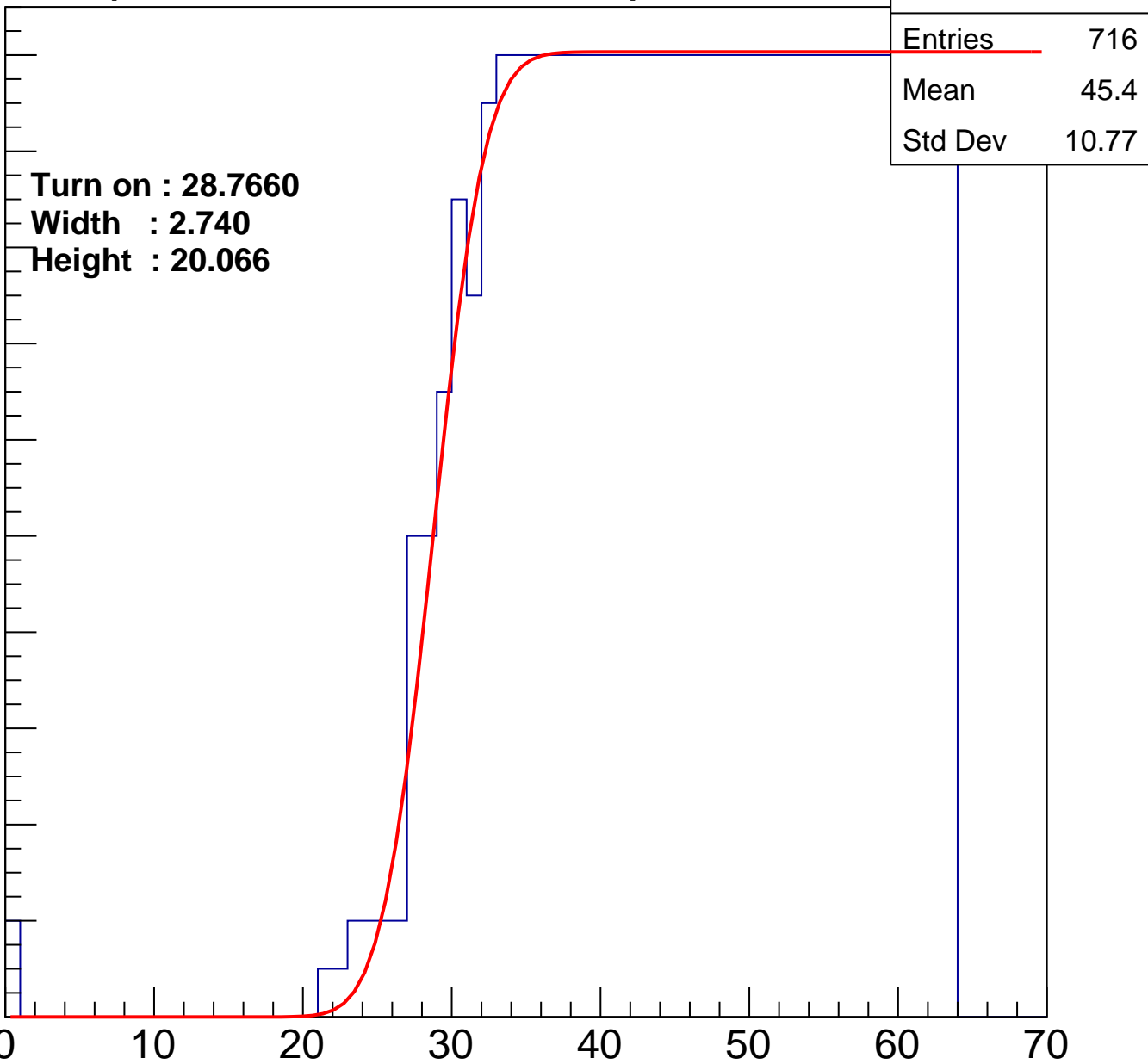
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7660
Width : 2.740
Height : 20.066

Entries	716
Mean	45.4
Std Dev	10.77

ampl



B0L101S, U19-ch102

calib_packv5_042523_0143.root, FC#1, port C1

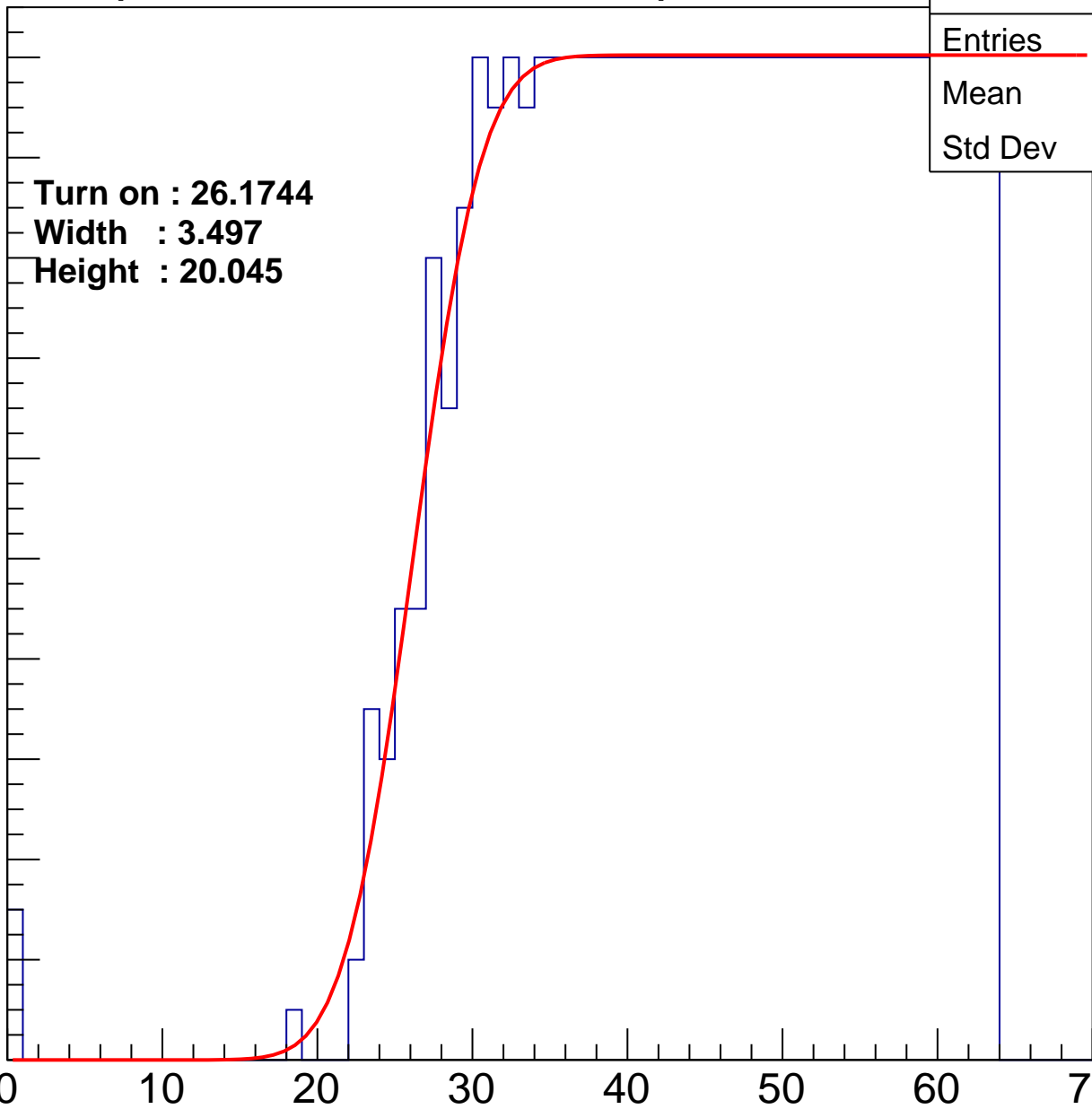
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1744
Width : 3.497
Height : 20.045

Entries	761
Mean	44.25
Std Dev	11.48

ampl



B0L101S, U19-ch103

calib_packv5_042523_0143.root, FC#1, port C1

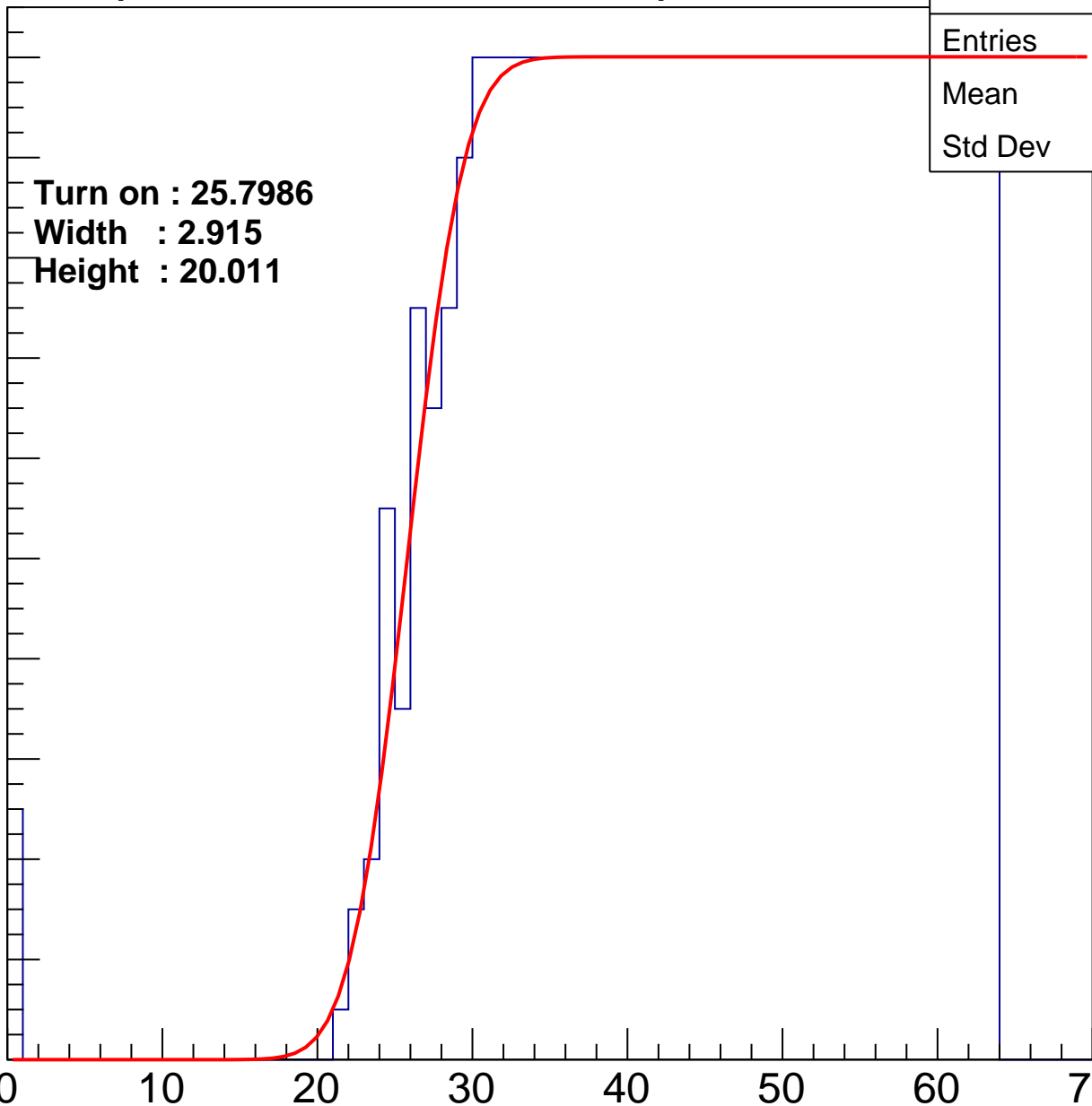
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.7986
Width : 2.915
Height : 20.011

Entries	772
Mean	43.94
Std Dev	11.75

ampl

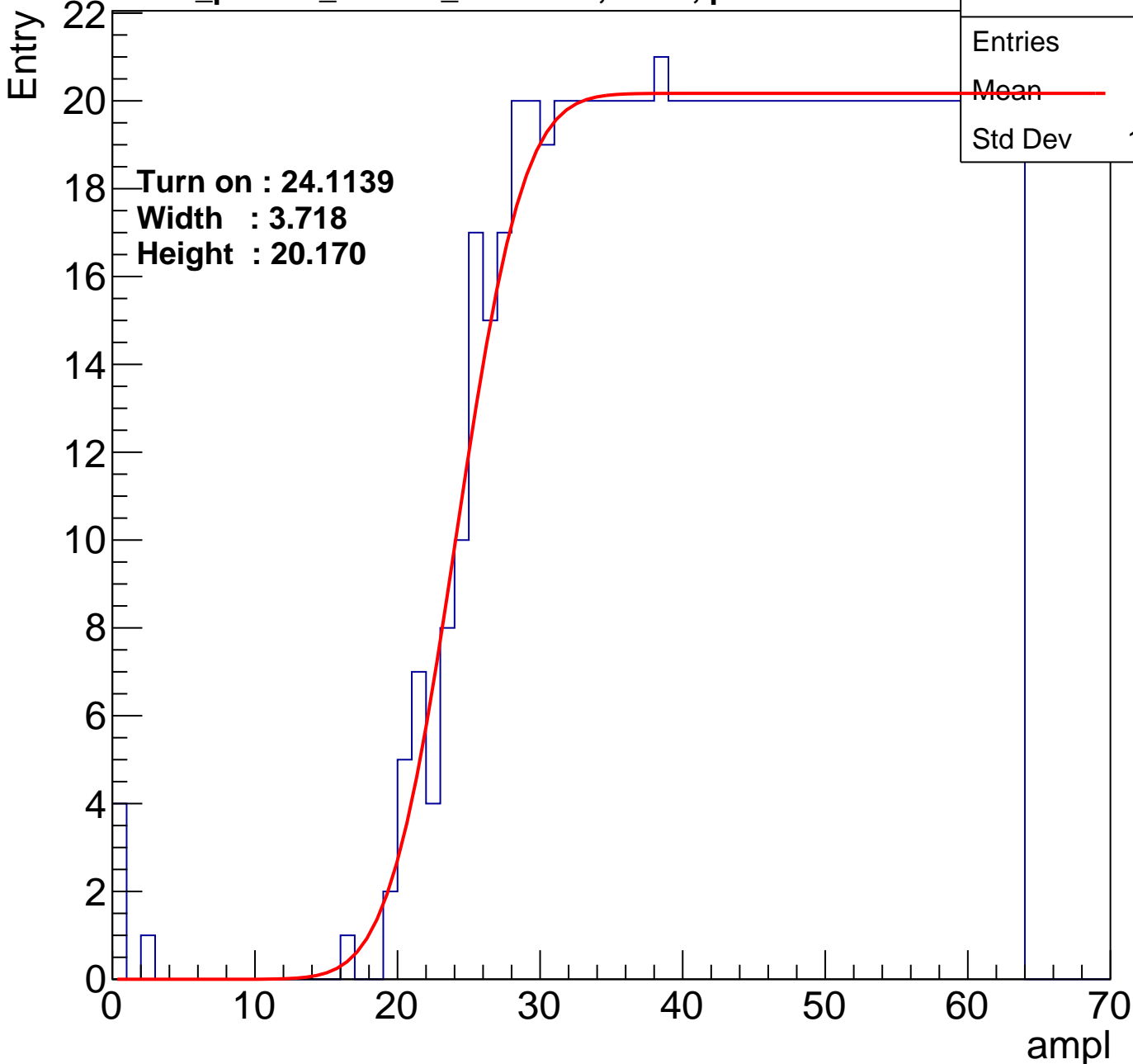


B0L101S, U19-ch104

calib_packv5_042523_0143.root, FC#1, port C1

Entries	812
Mean	43
Std Dev	12.26

Turn on : 24.1139
Width : 3.718
Height : 20.170



B0L101S, U19-ch105

calib_packv5_042523_0143.root, FC#1, port C1

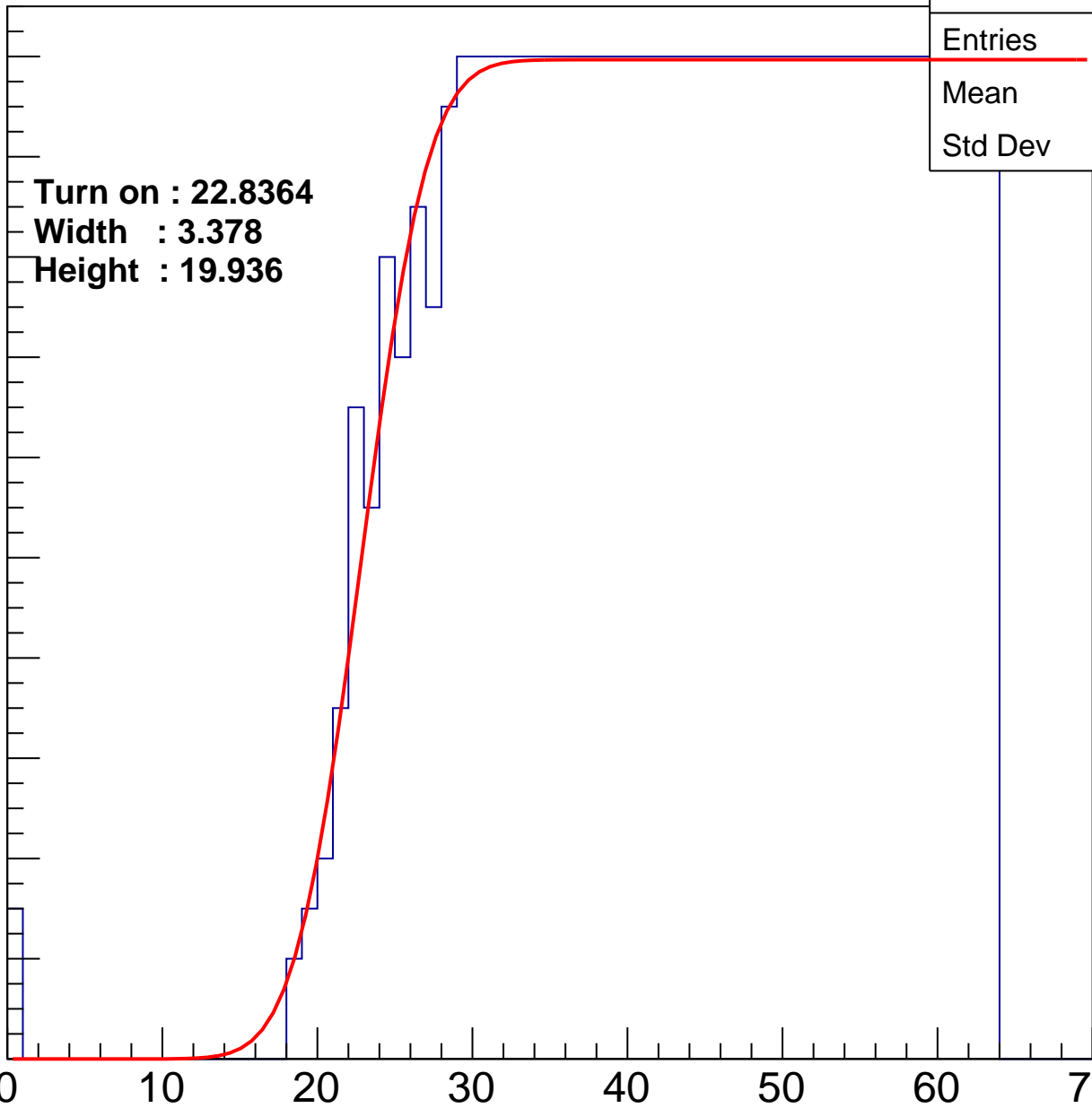
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 22.8364
Width : 3.378
Height : 19.936

Entries	824
Mean	42.68
Std Dev	12.32

ampl



B0L101S, U19-ch106

calib_packv5_042523_0143.root, FC#1, port C1

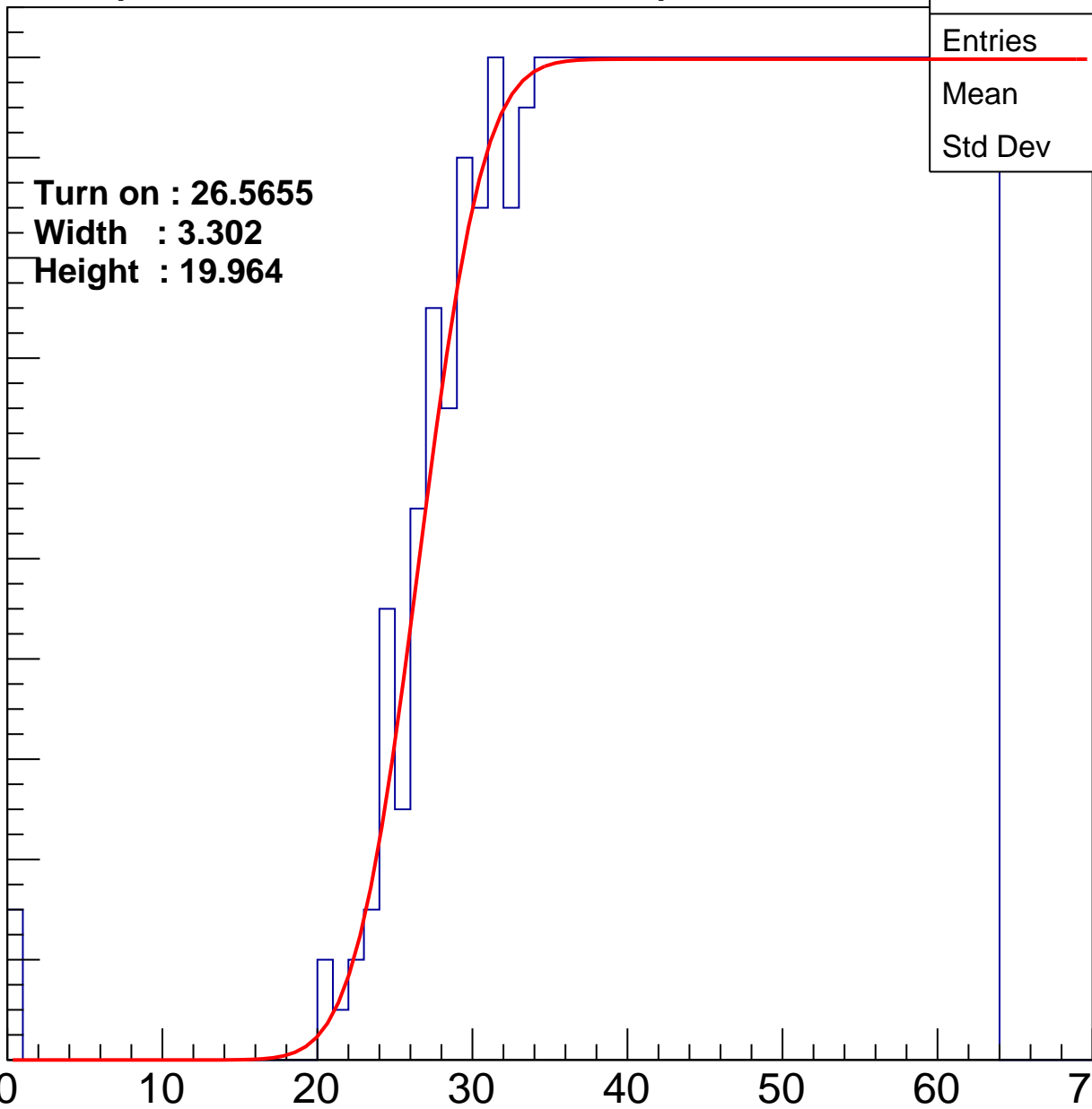
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.5655
Width : 3.302
Height : 19.964

Entries	755
Mean	44.36
Std Dev	11.45

ampl



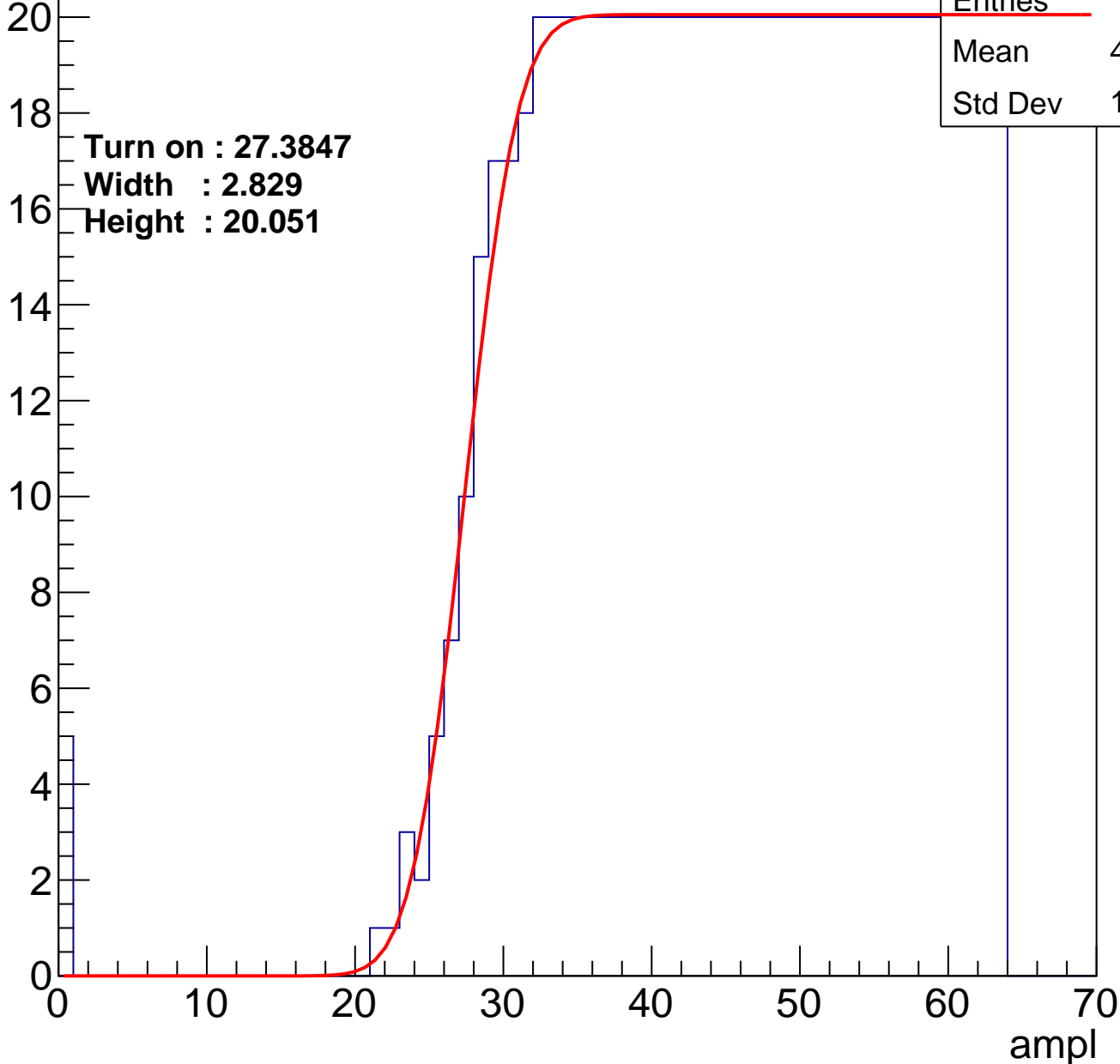
B0L101S, U19-ch107

calib_packv5_042523_0143.root, FC#1, port C1

Entries	741
Mean	44.69
Std Dev	11.37

Turn on : 27.3847
Width : 2.829
Height : 20.051

Entry



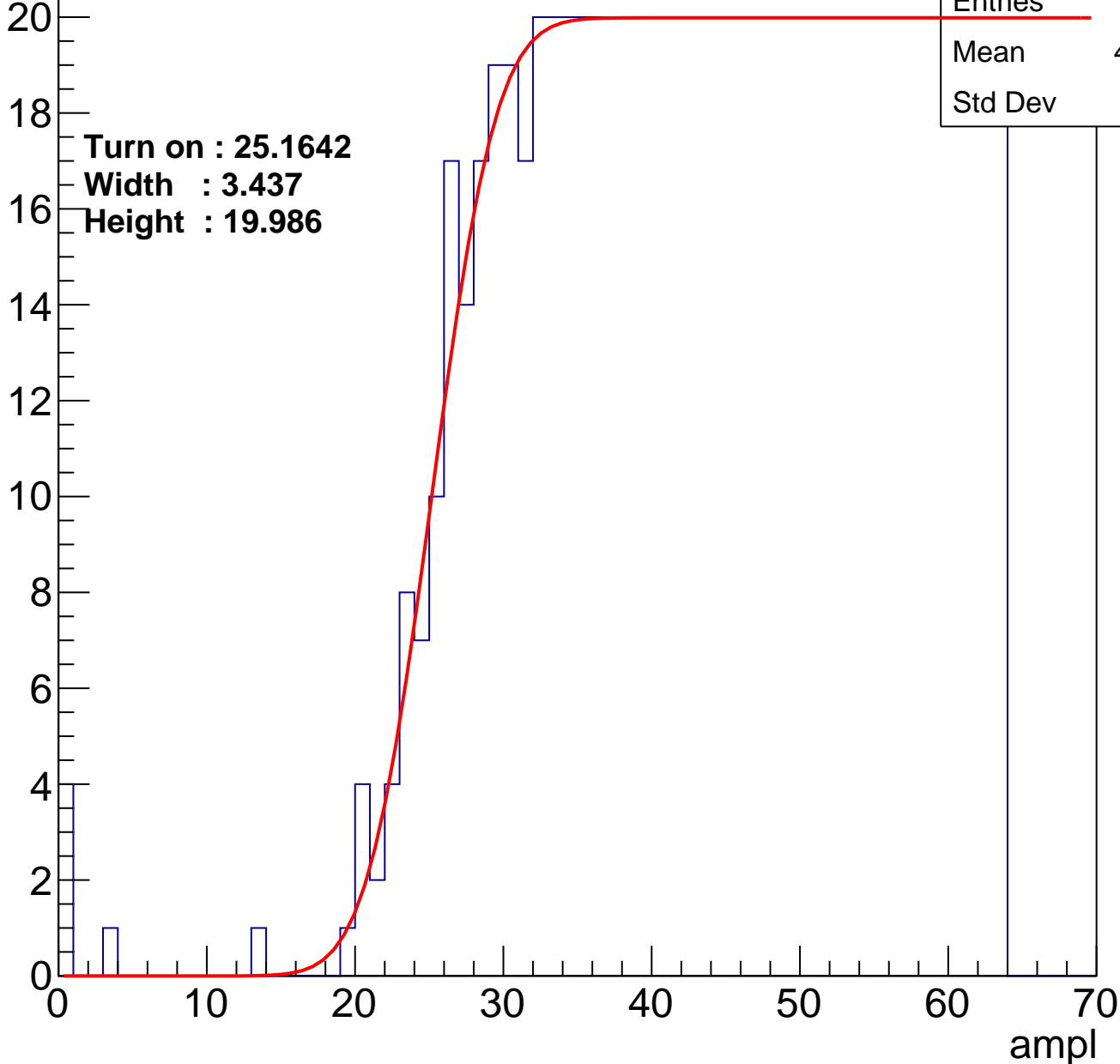
B0L101S, U19-ch108

calib_packv5_042523_0143.root, FC#1, port C1

Entries	785
Mean	43.56
Std Dev	12.01

Turn on : 25.1642
Width : 3.437
Height : 19.986

Entry



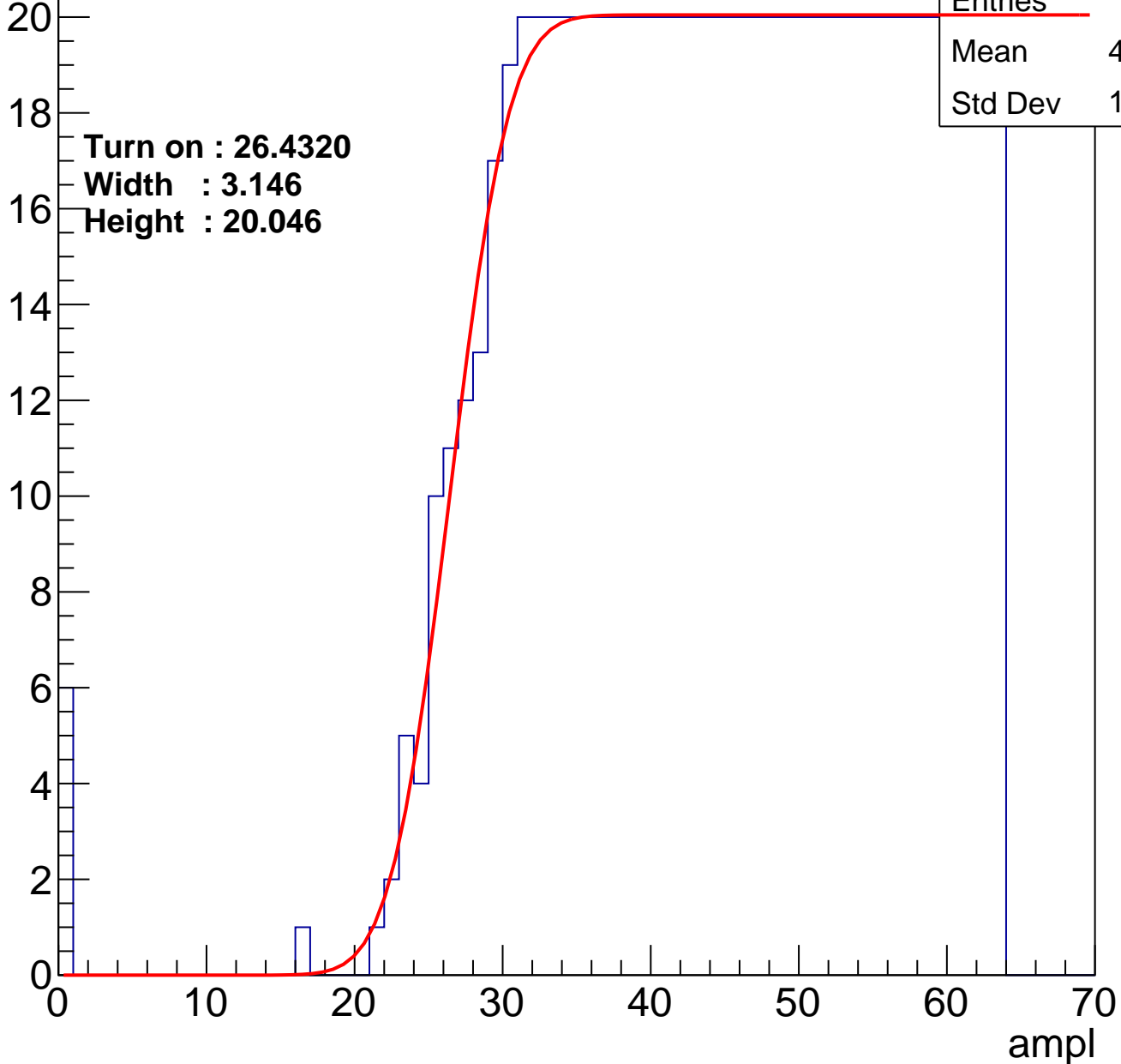
B0L101S, U19-ch109

calib_packv5_042523_0143.root, FC#1, port C1

Entries	761
Mean	44.15
Std Dev	11.75

Turn on : 26.4320
Width : 3.146
Height : 20.046

Entry

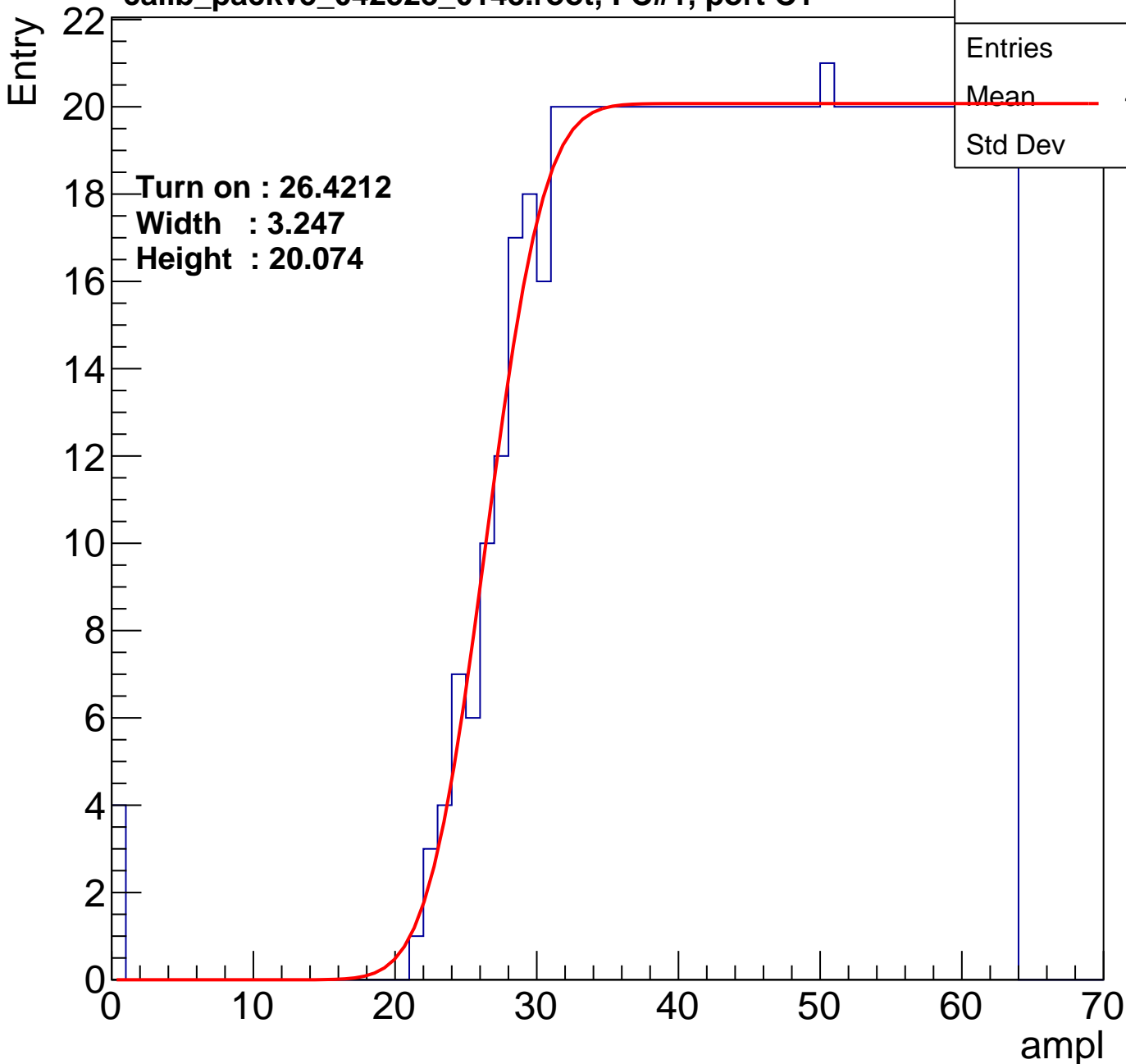


B0L101S, U19-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entries	759
Mean	44.31
Std Dev	11.5

Turn on : 26.4212
Width : 3.247
Height : 20.074



B0L101S, U19-ch111

calib_packv5_042523_0143.root, FC#1, port C1

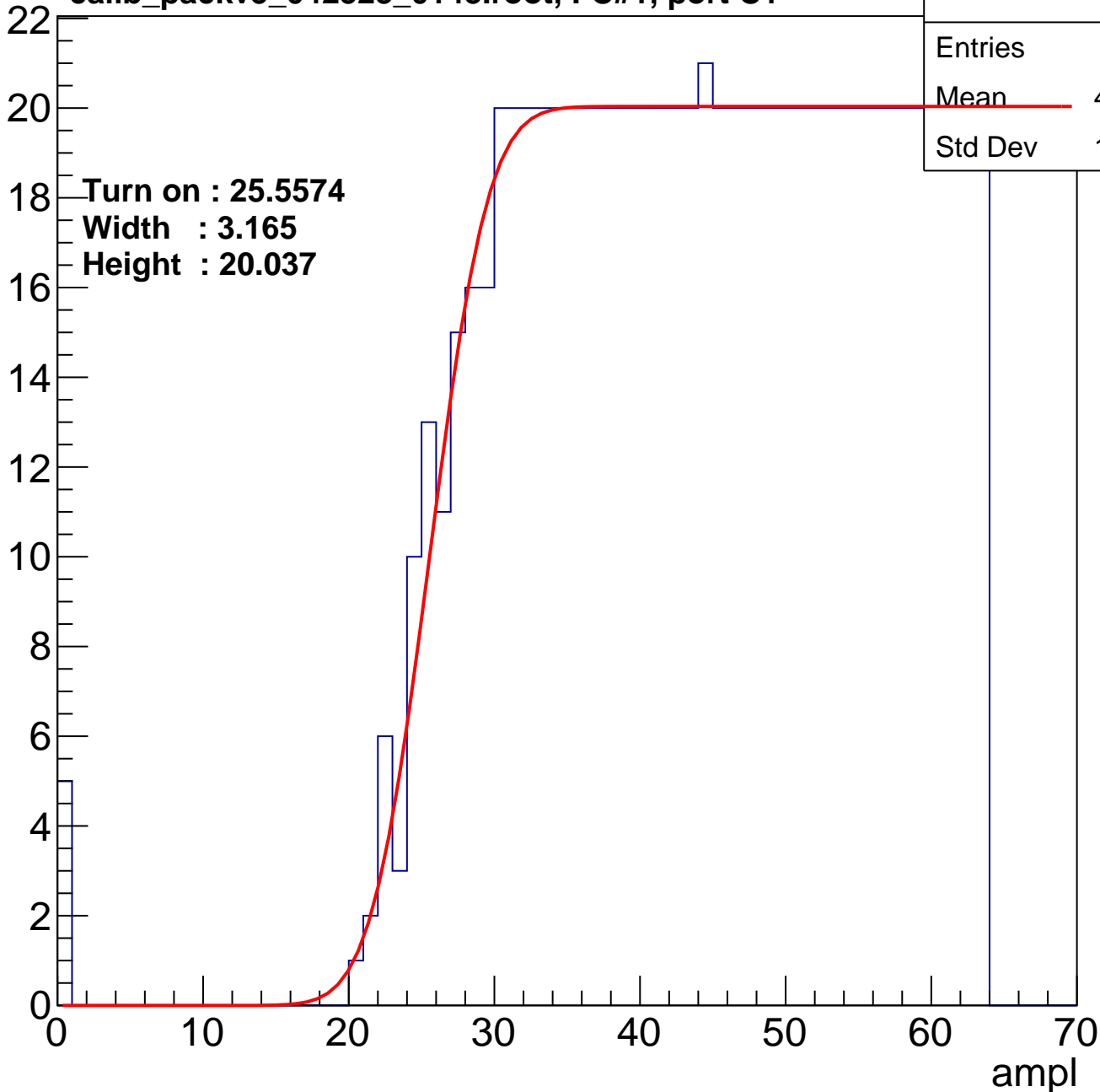
Entries	779
Mean	43.77
Std Dev	11.86

Turn on : 25.5574

Width : 3.165

Height : 20.037

Entry



B0L101S, U19-ch112

calib_packv5_042523_0143.root, FC#1, port C1

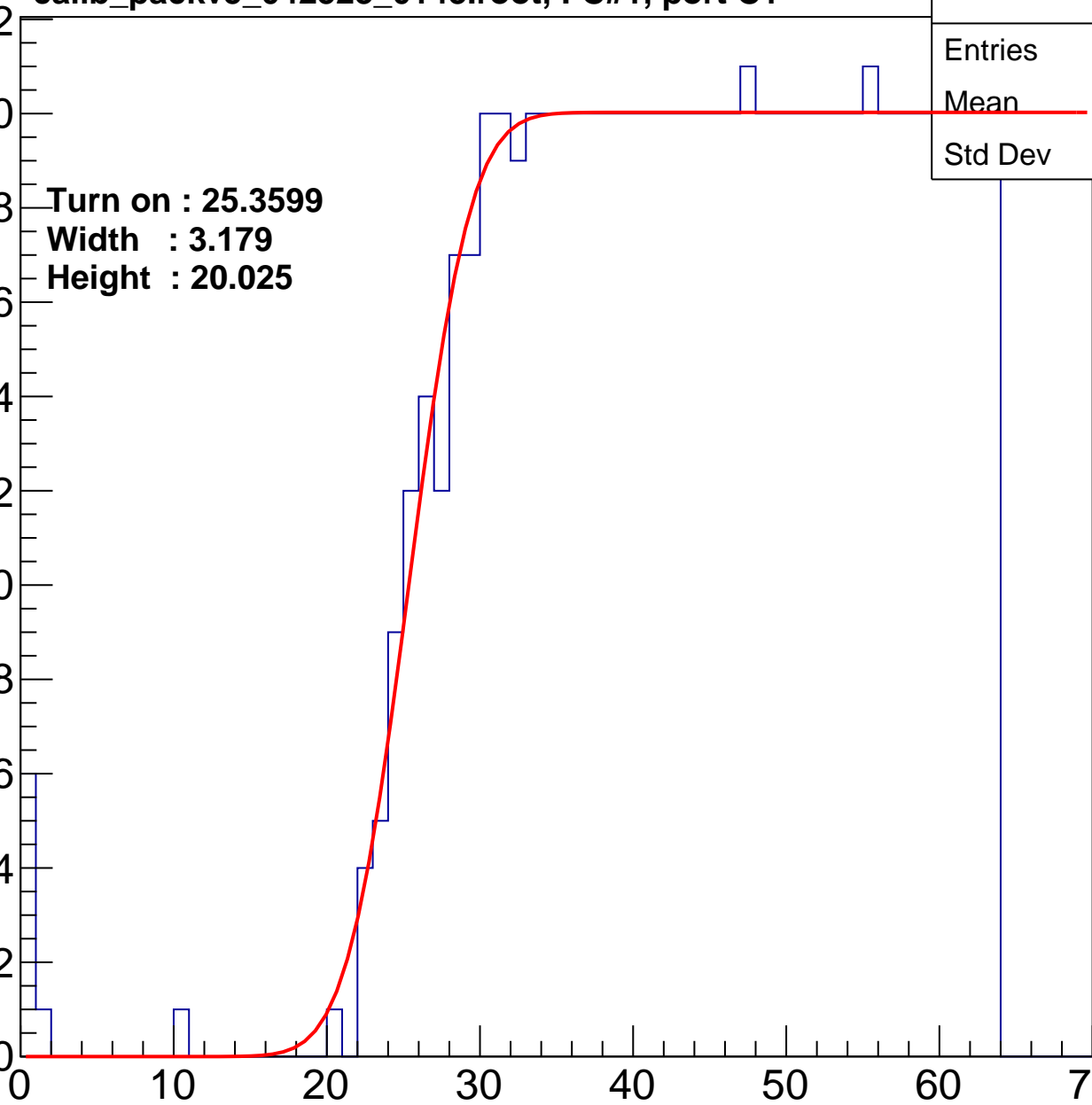
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3599
Width : 3.179
Height : 20.025

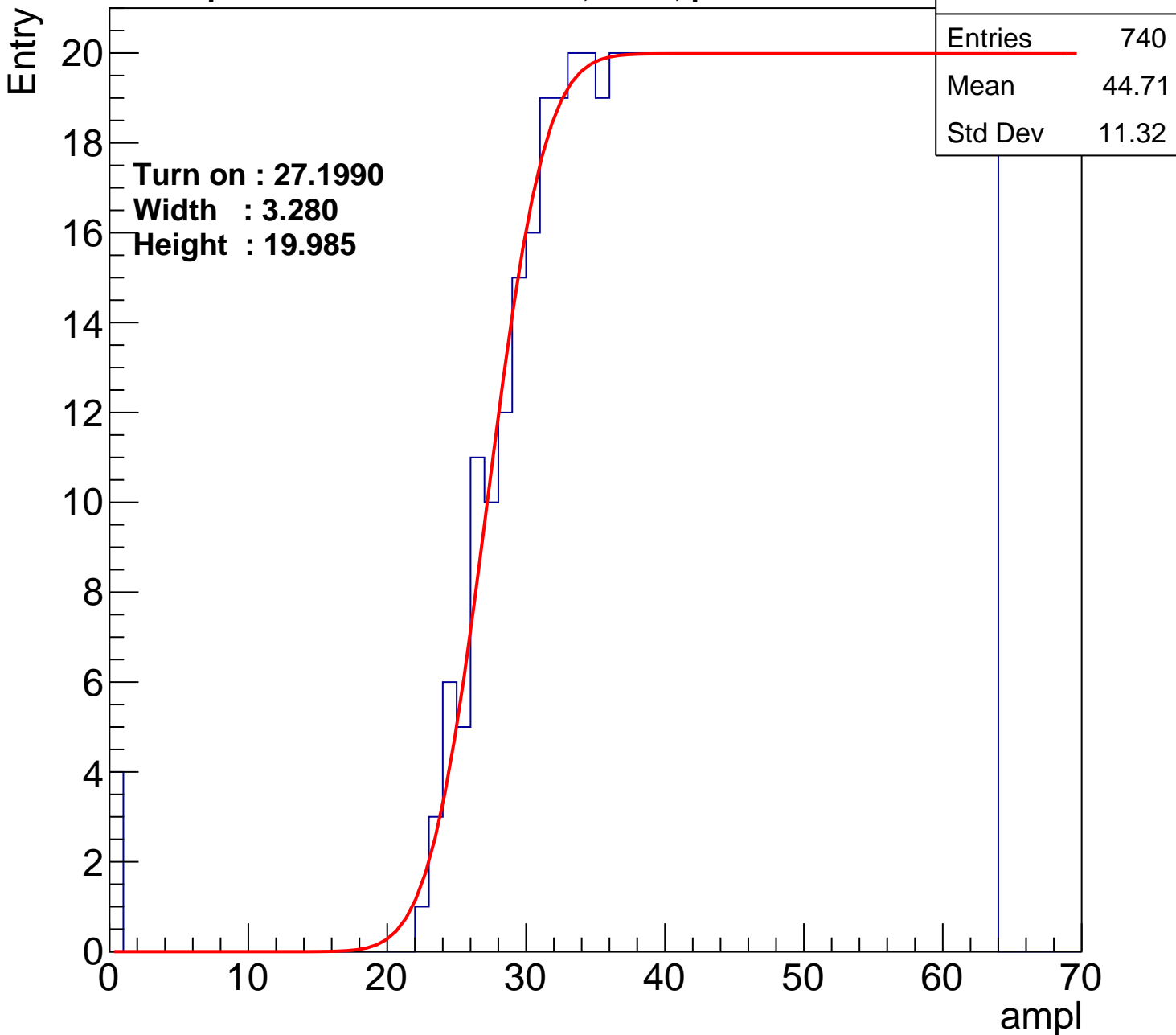
Entries	780
Mean	43.71
Std Dev	12.04

ampl



B0L101S, U19-ch113

calib_packv5_042523_0143.root, FC#1, port C1

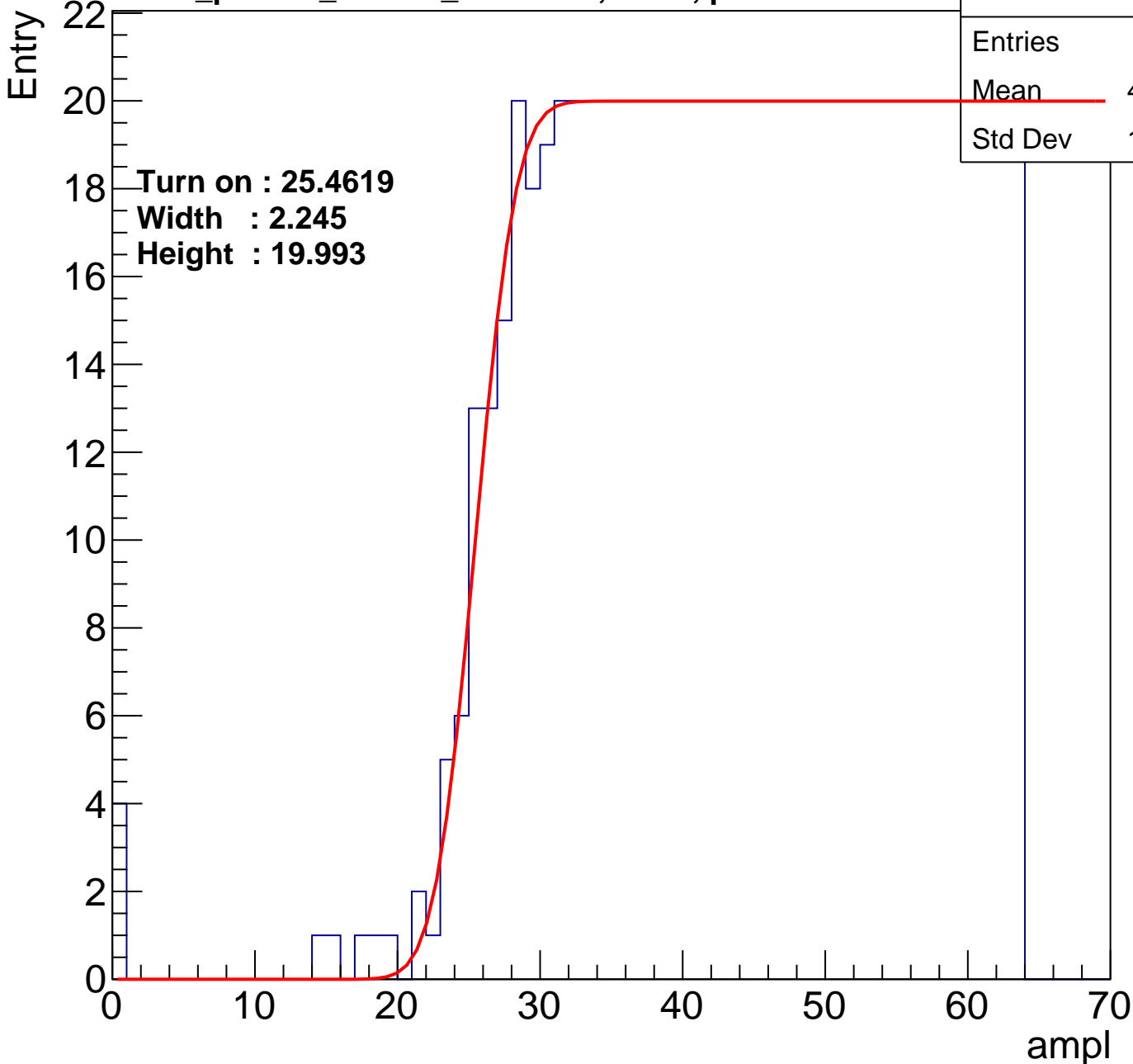


B0L101S, U19-ch114

calib_packv5_042523_0143.root, FC#1, port C1

Entries	782
Mean	43.74
Std Dev	11.85

Turn on : 25.4619
Width : 2.245
Height : 19.993



B0L101S, U19-ch115

calib_packv5_042523_0143.root, FC#1, port C1

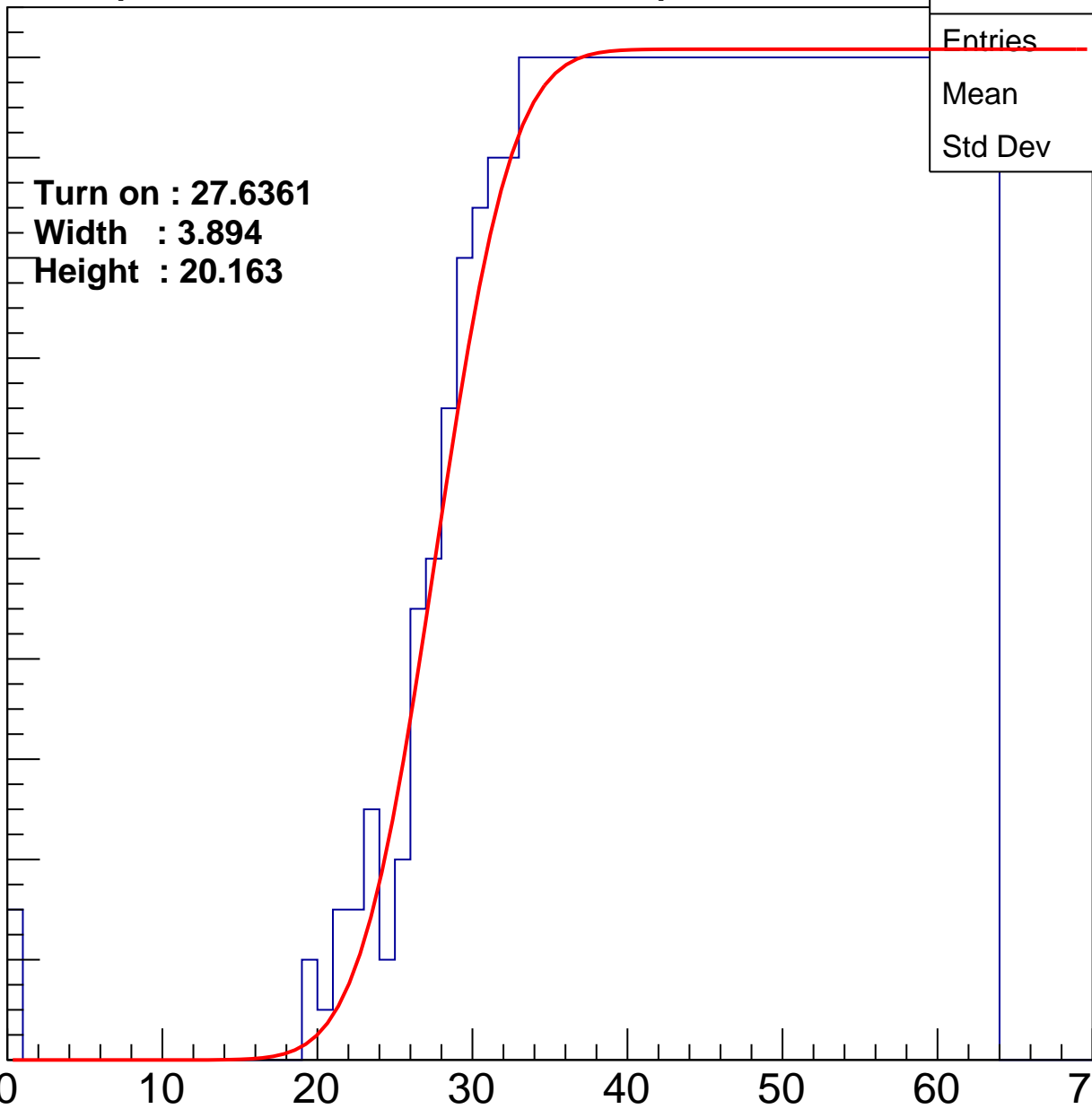
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6361
Width : 3.894
Height : 20.163

Entries	744
Mean	44.6
Std Dev	11.36

ampl



B0L101S, U19-ch116

calib_packv5_042523_0143.root, FC#1, port C1

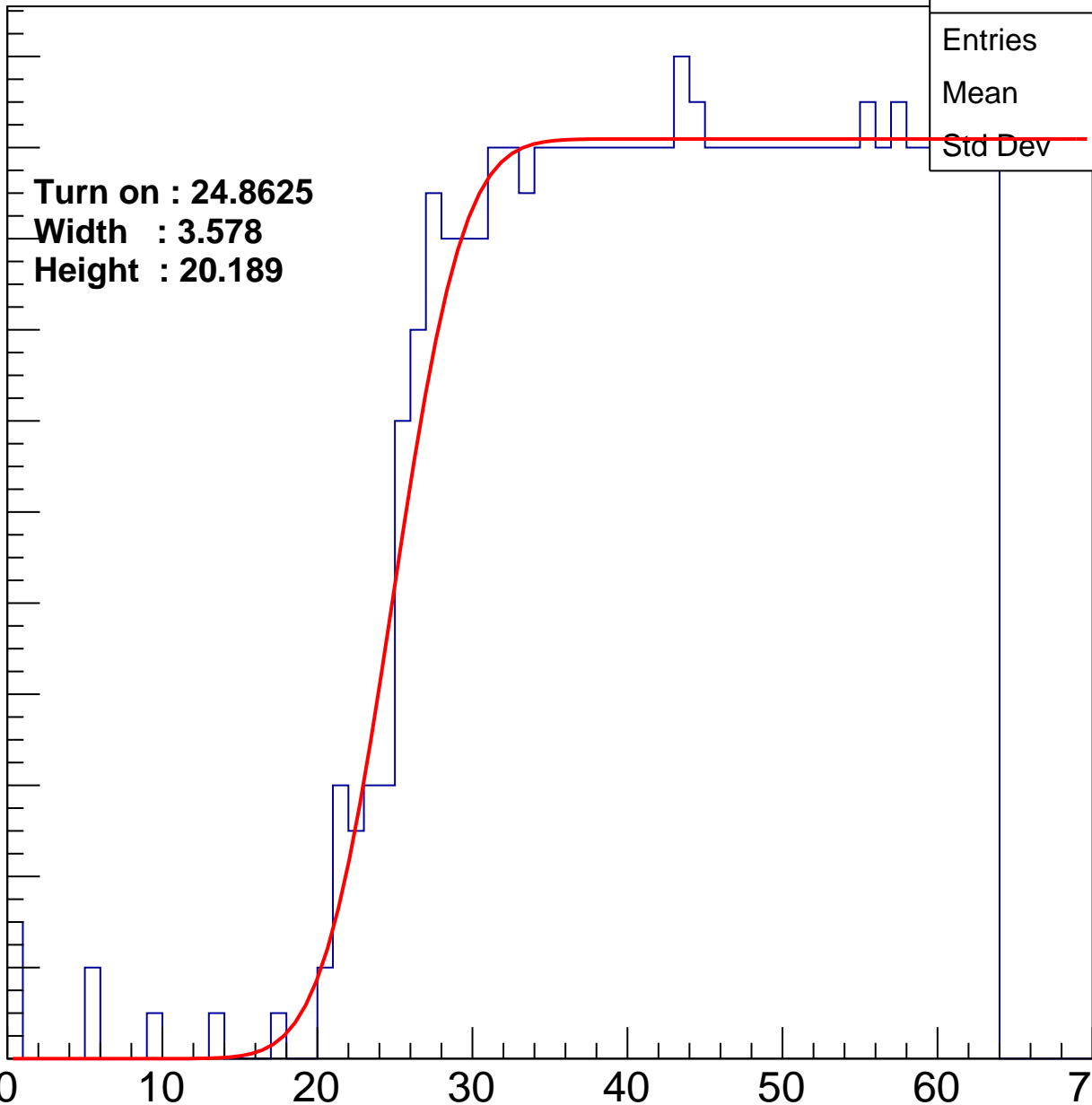
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8625
Width : 3.578
Height : 20.189

Entries	800
Mean	43.35
Std Dev	12.1

ampl



B0L101S, U19-ch117

calib_packv5_042523_0143.root, FC#1, port C1

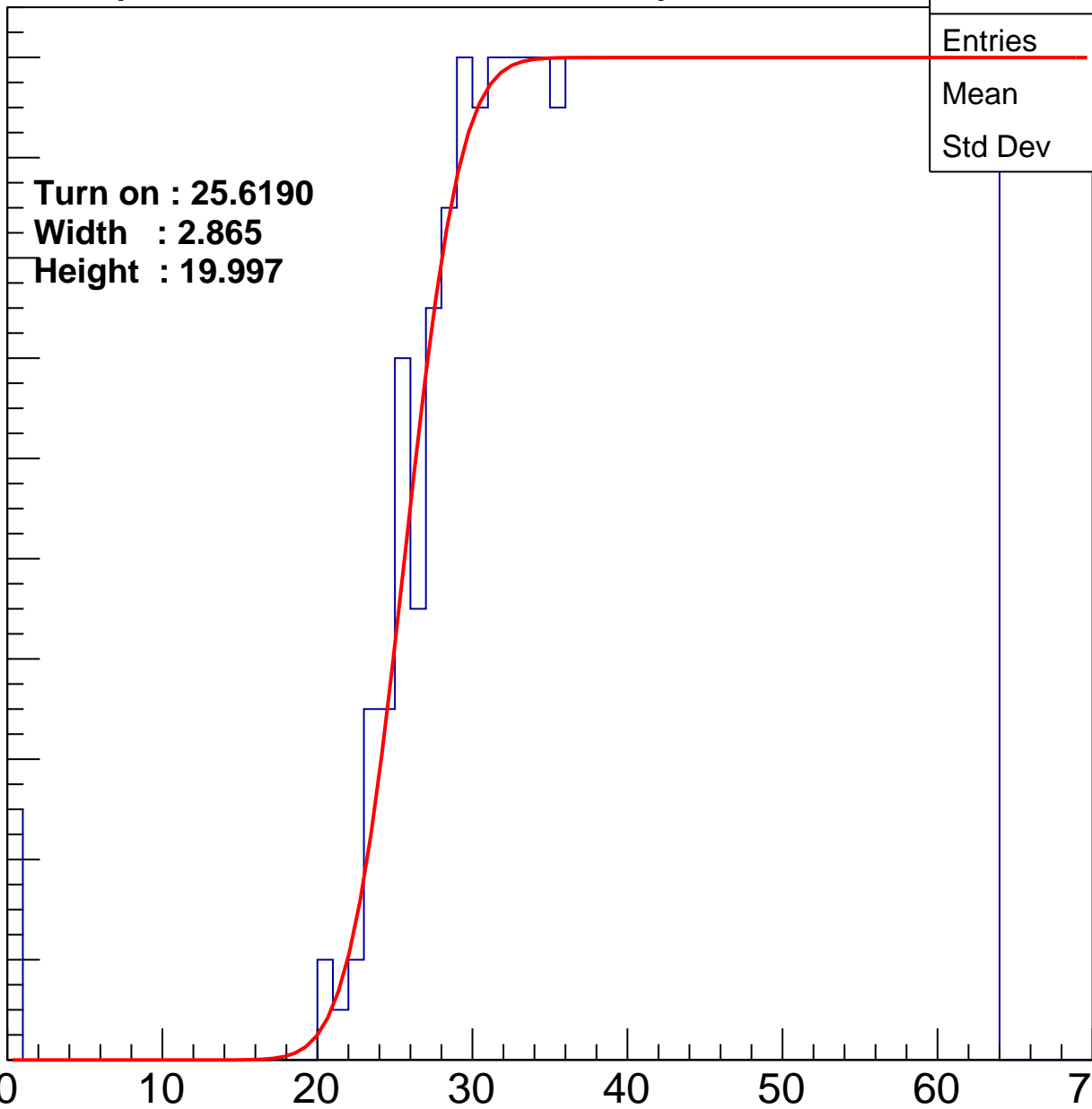
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6190
Width : 2.865
Height : 19.997

Entries	777
Mean	43.8
Std Dev	11.83

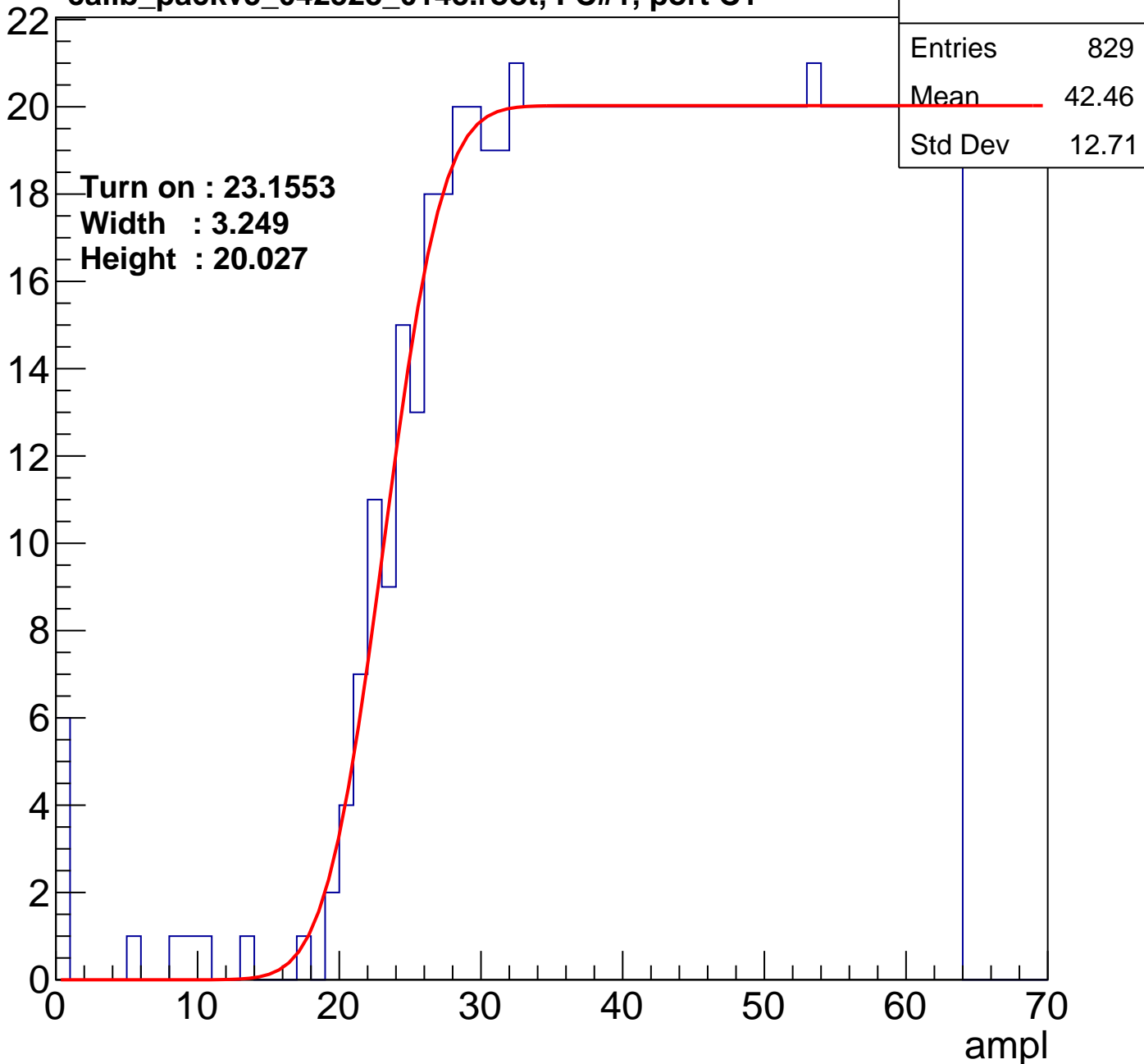
ampl



B0L101S, U19-ch118

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch119

calib_packv5_042523_0143.root, FC#1, port C1

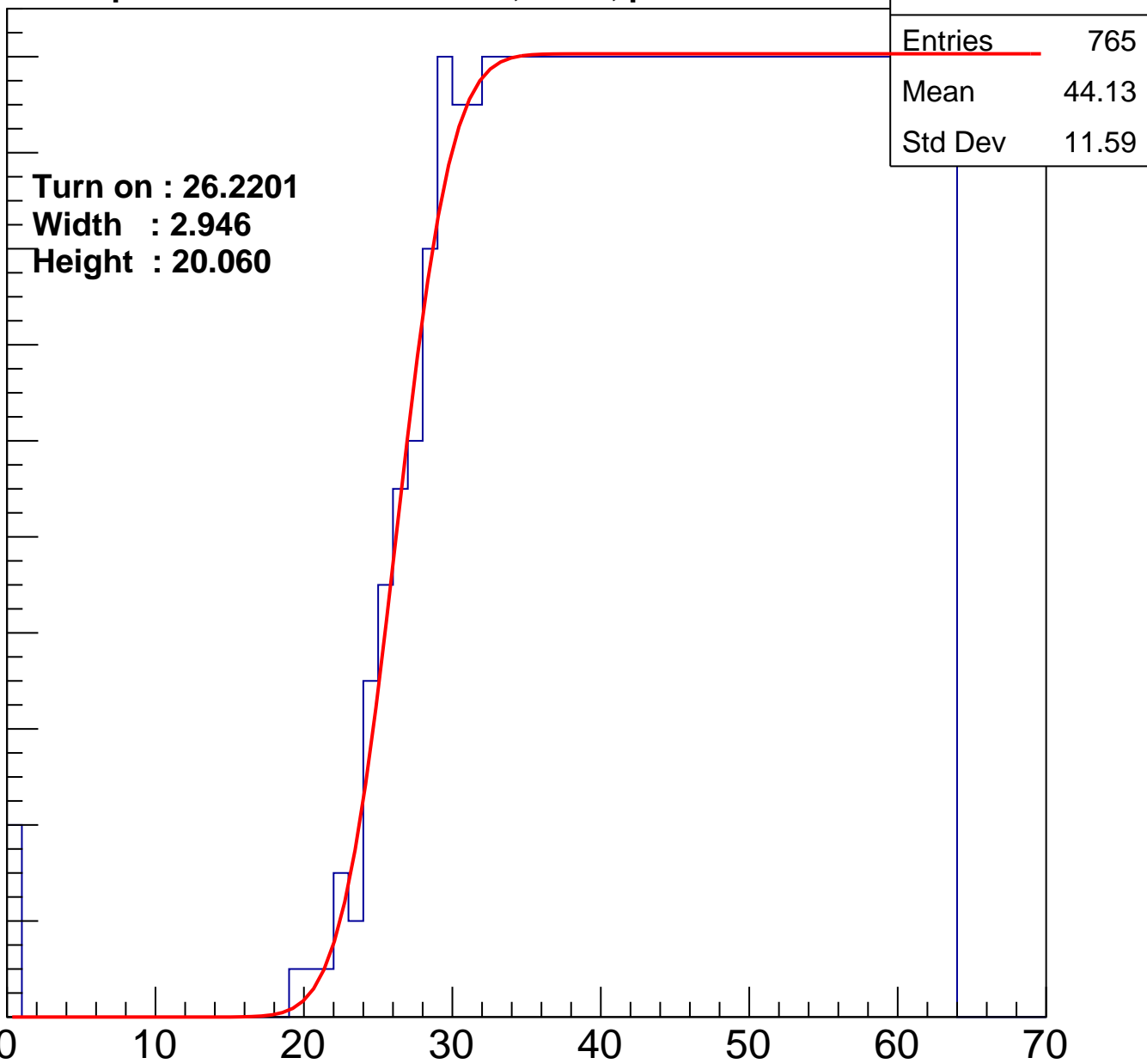
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2201
Width : 2.946
Height : 20.060

Entries	765
Mean	44.13
Std Dev	11.59

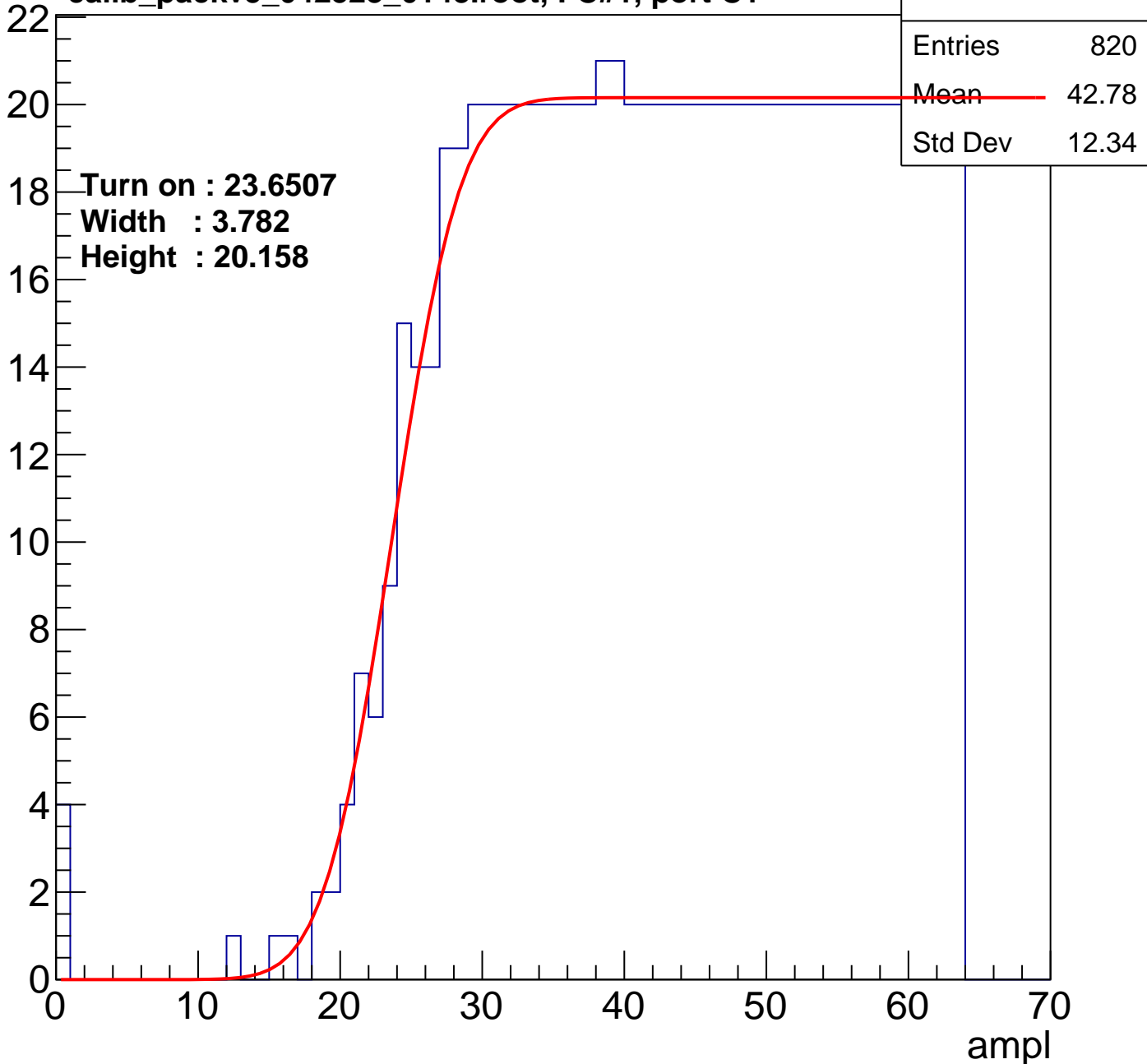
ampl



B0L101S, U19-ch120

calib_packv5_042523_0143.root, FC#1, port C1

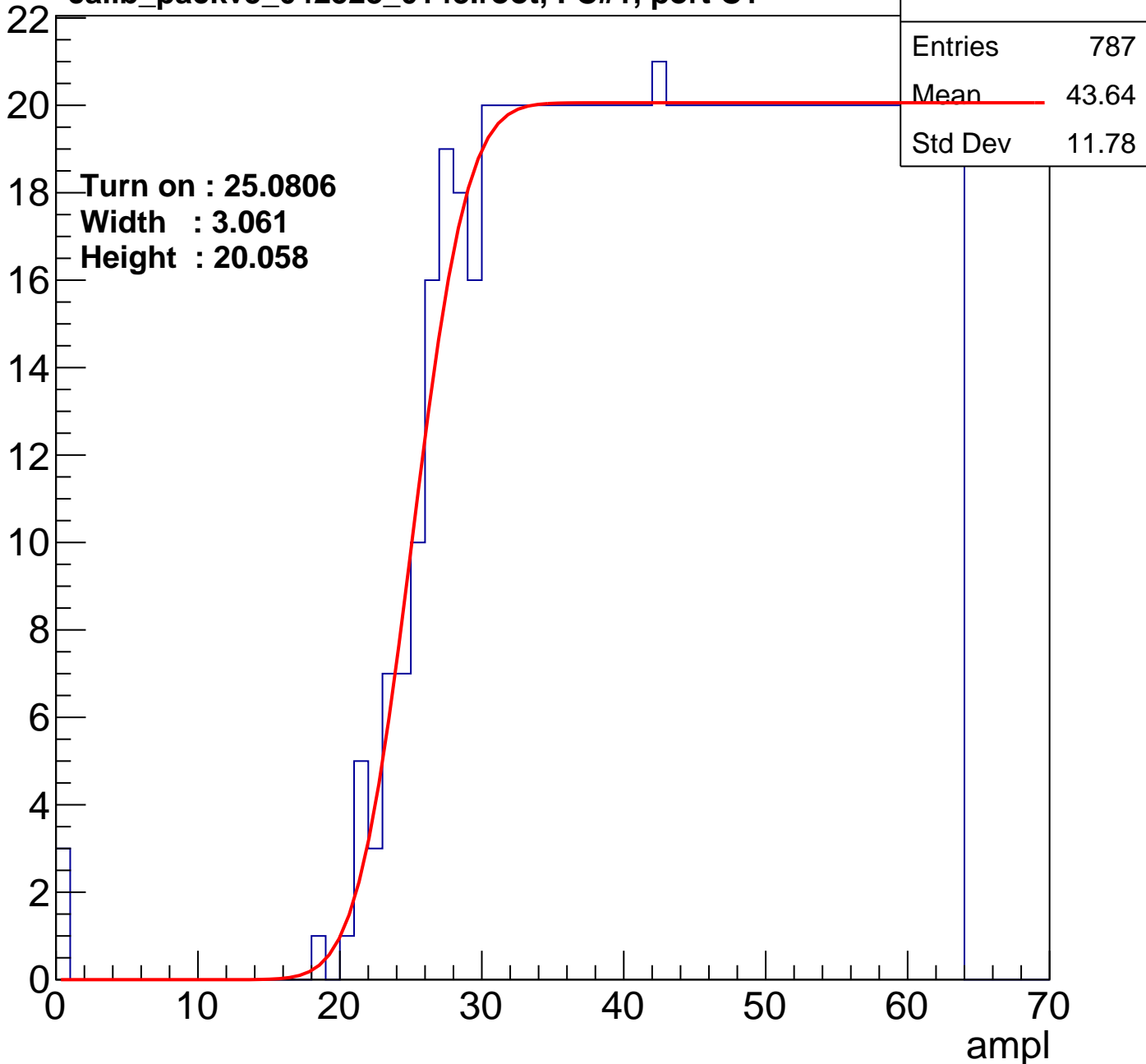
Entry



B0L101S, U19-ch121

calib_packv5_042523_0143.root, FC#1, port C1

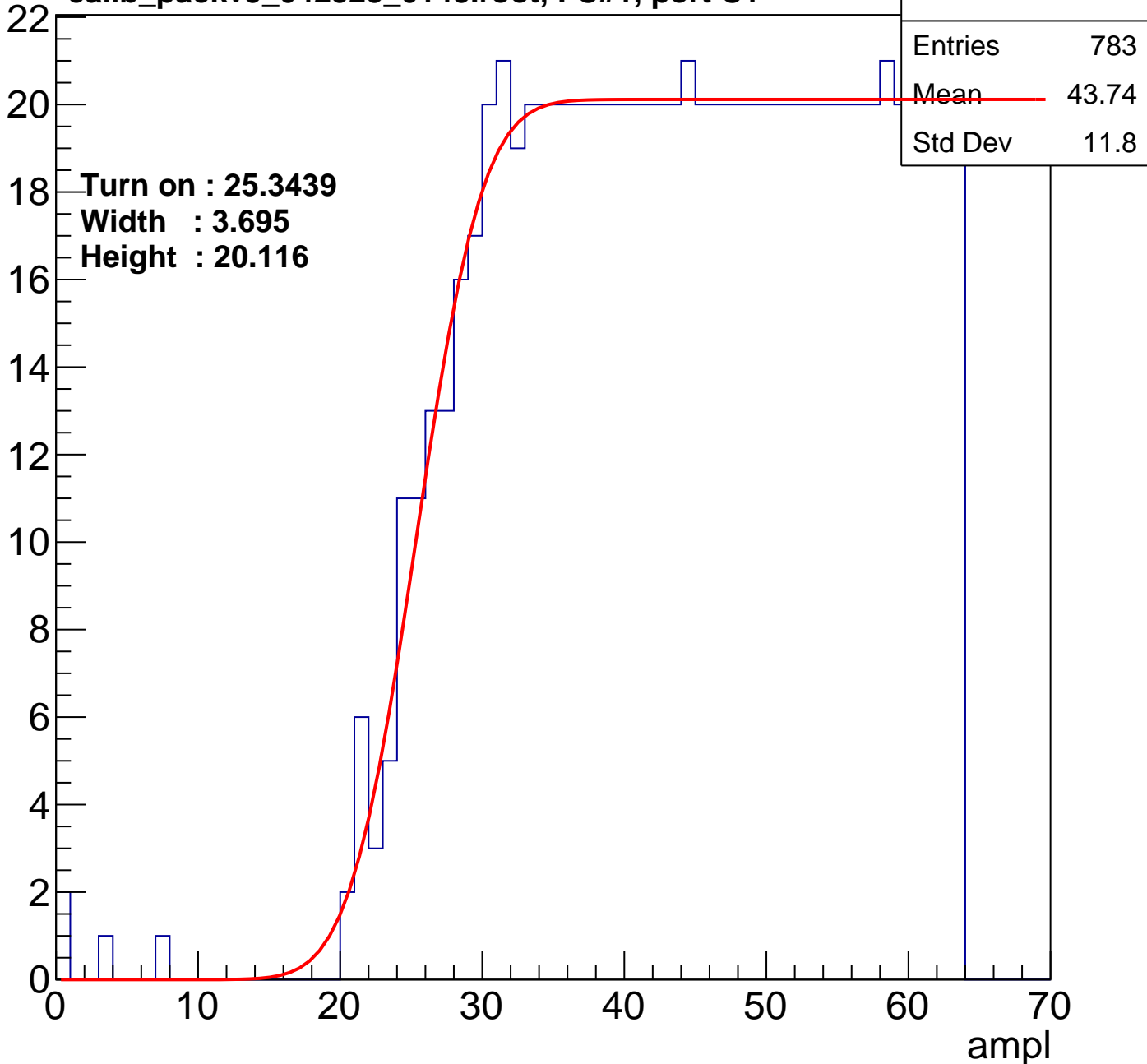
Entry



B0L101S, U19-ch122

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch123

calib_packv5_042523_0143.root, FC#1, port C1

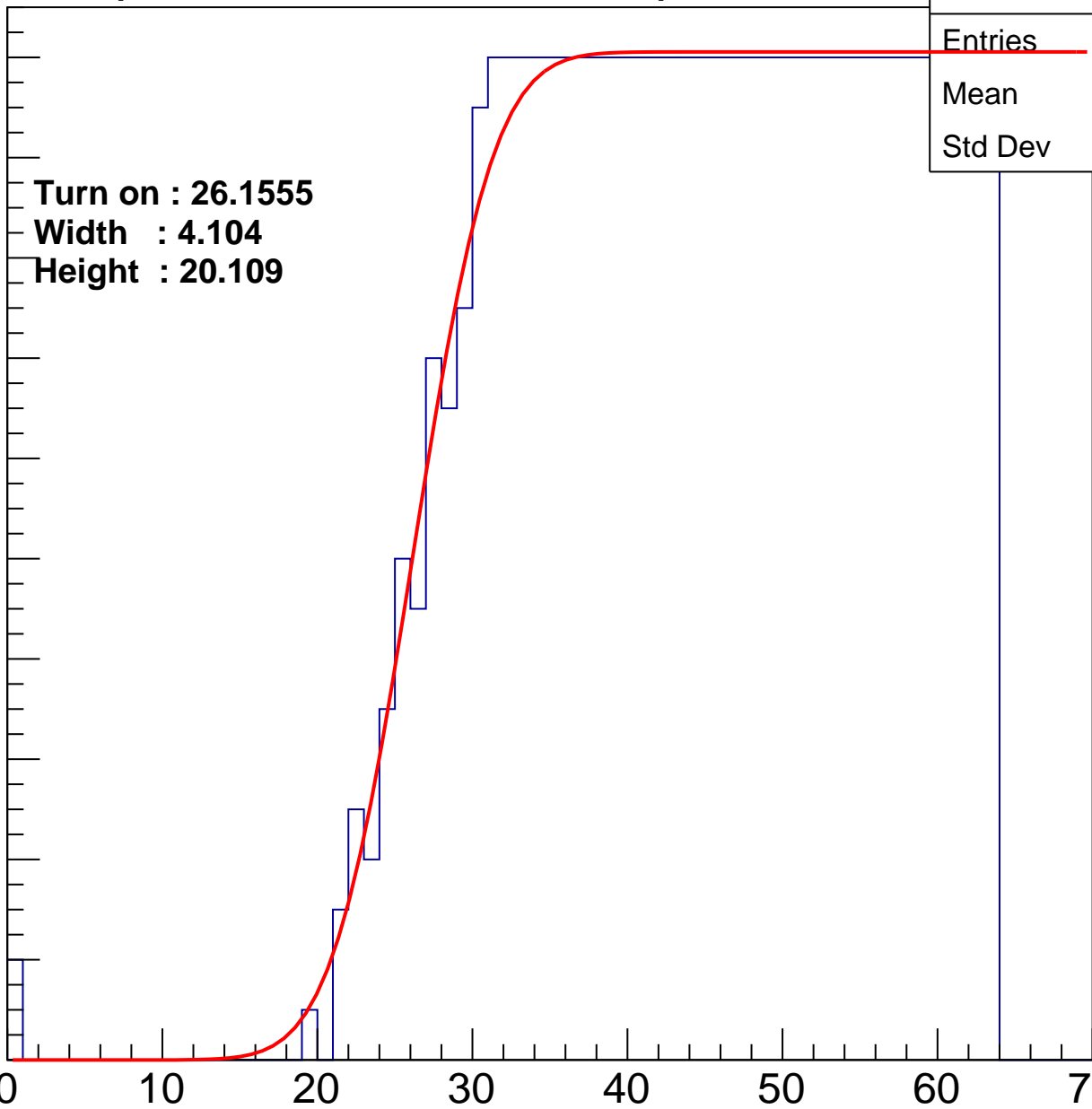
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1555
Width : 4.104
Height : 20.109

Entries	762
Mean	44.23
Std Dev	11.44

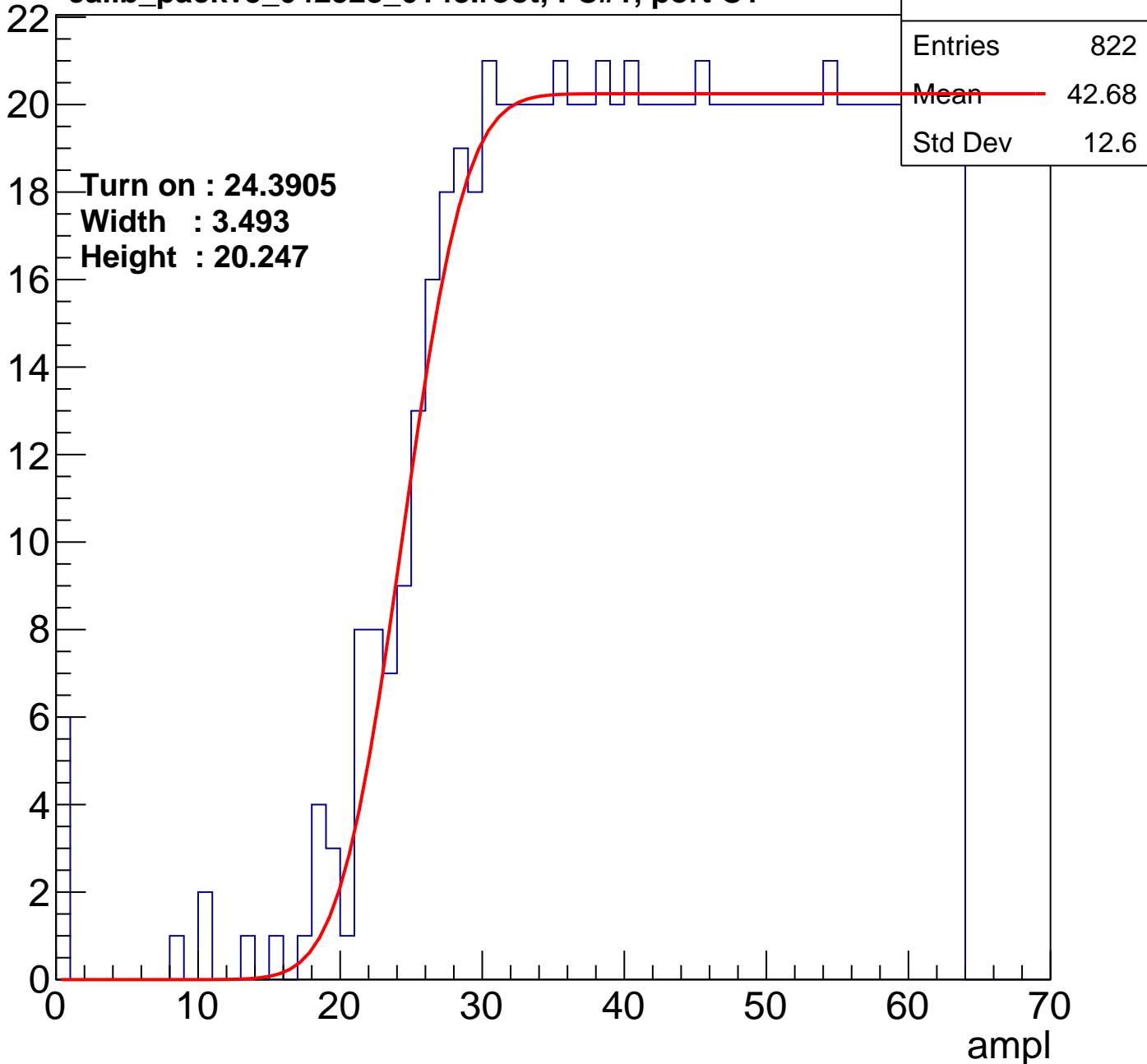
ampl



B0L101S, U19-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U19-ch125

calib_packv5_042523_0143.root, FC#1, port C1

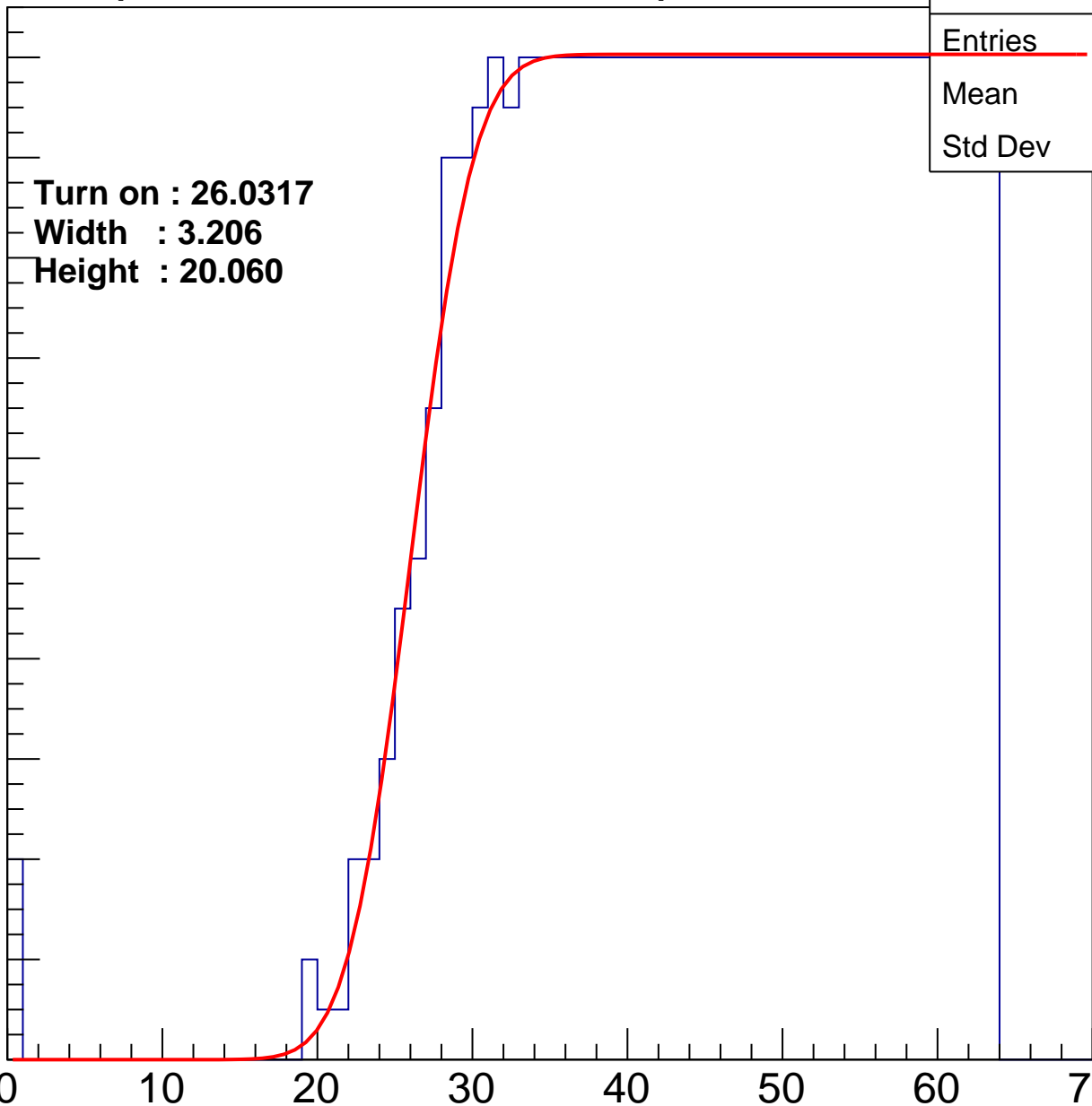
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0317
Width : 3.206
Height : 20.060

Entries	768
Mean	44.04
Std Dev	11.66

ampl



B0L101S, U19-ch126

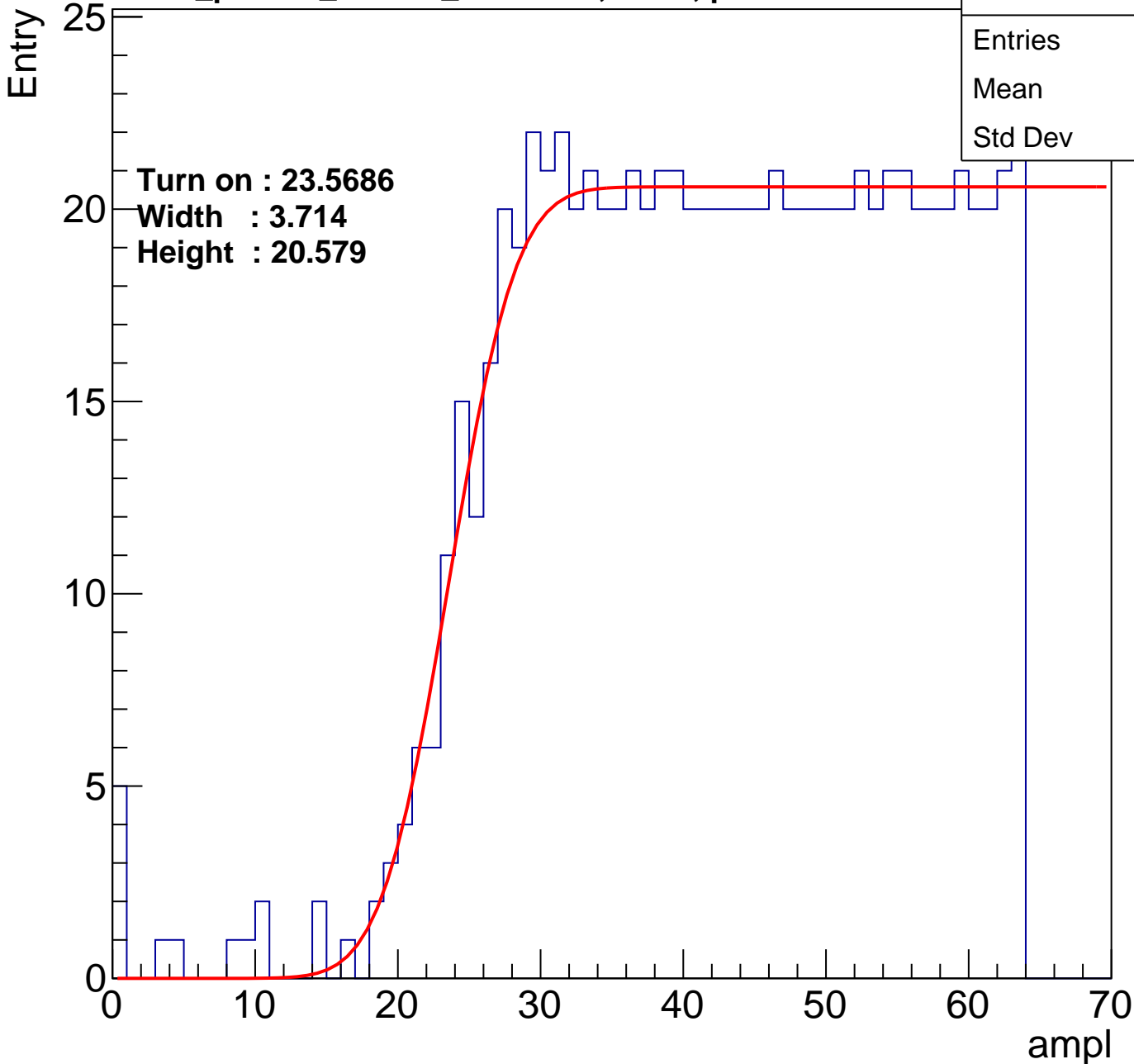
calib_packv5_042523_0143.root, FC#1, port C1

Entries	847
Mean	42.5
Std Dev	12.8

Turn on : 23.5686

Width : 3.714

Height : 20.579



B0L101S, U19-ch127

calib_packv5_042523_0143.root, FC#1, port C1

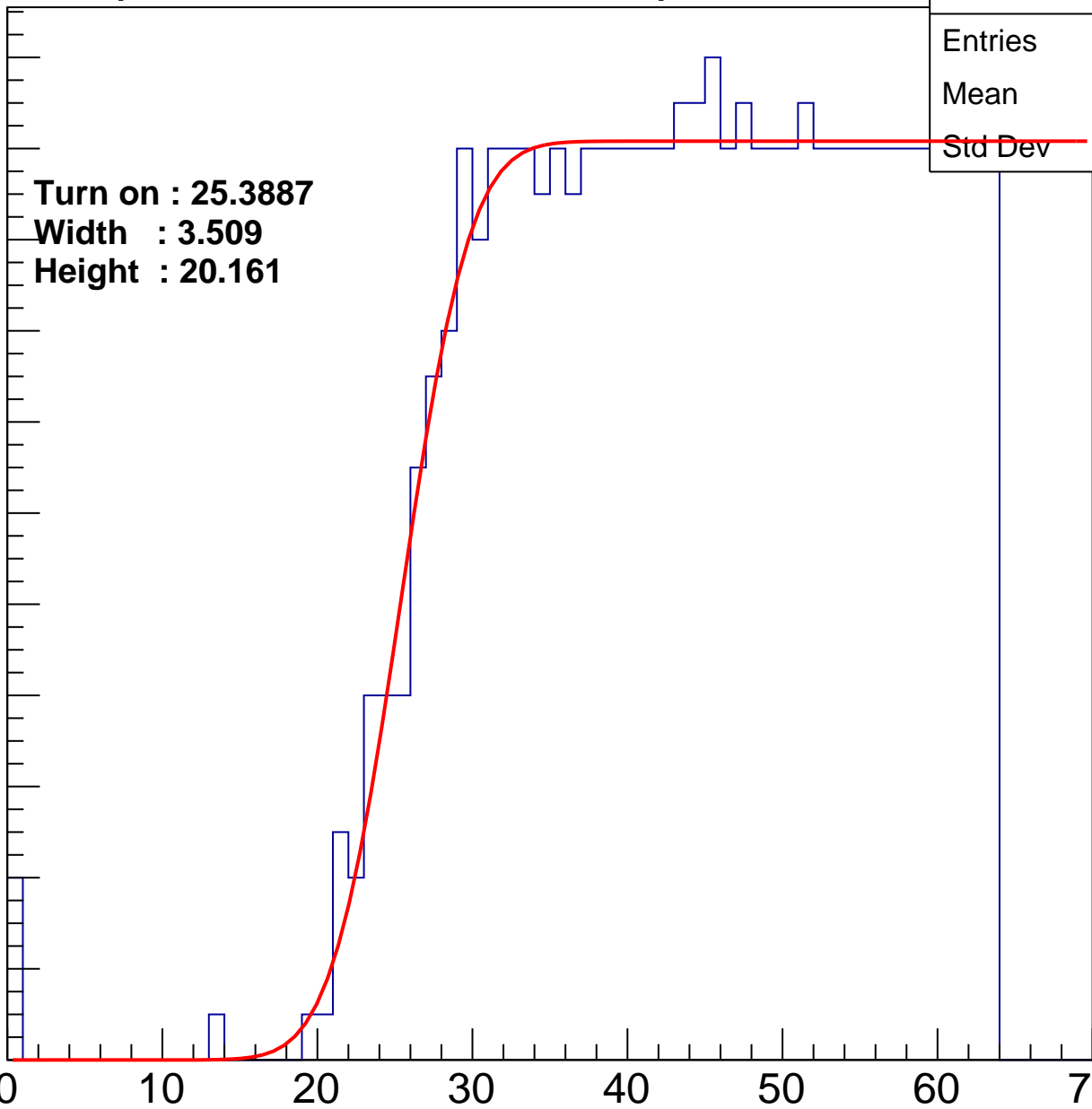
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3887
Width : 3.509
Height : 20.161

Entries	786
Mean	43.71
Std Dev	11.85

ampl



B0L101S, U19-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3887
Width : 3.509
Height : 20.161

Entries	786
Mean	43.71
Std Dev	11.85

ampl

