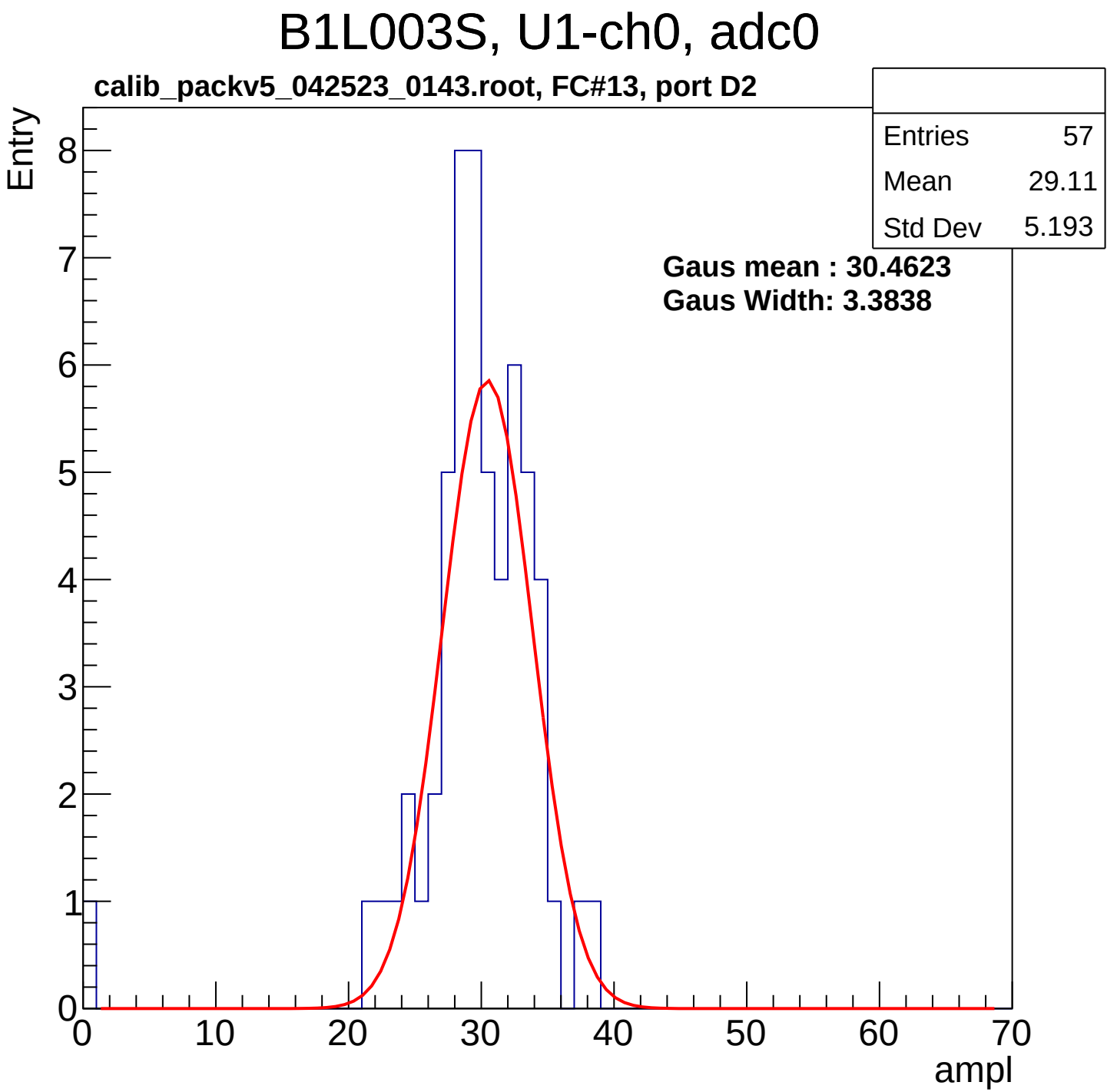
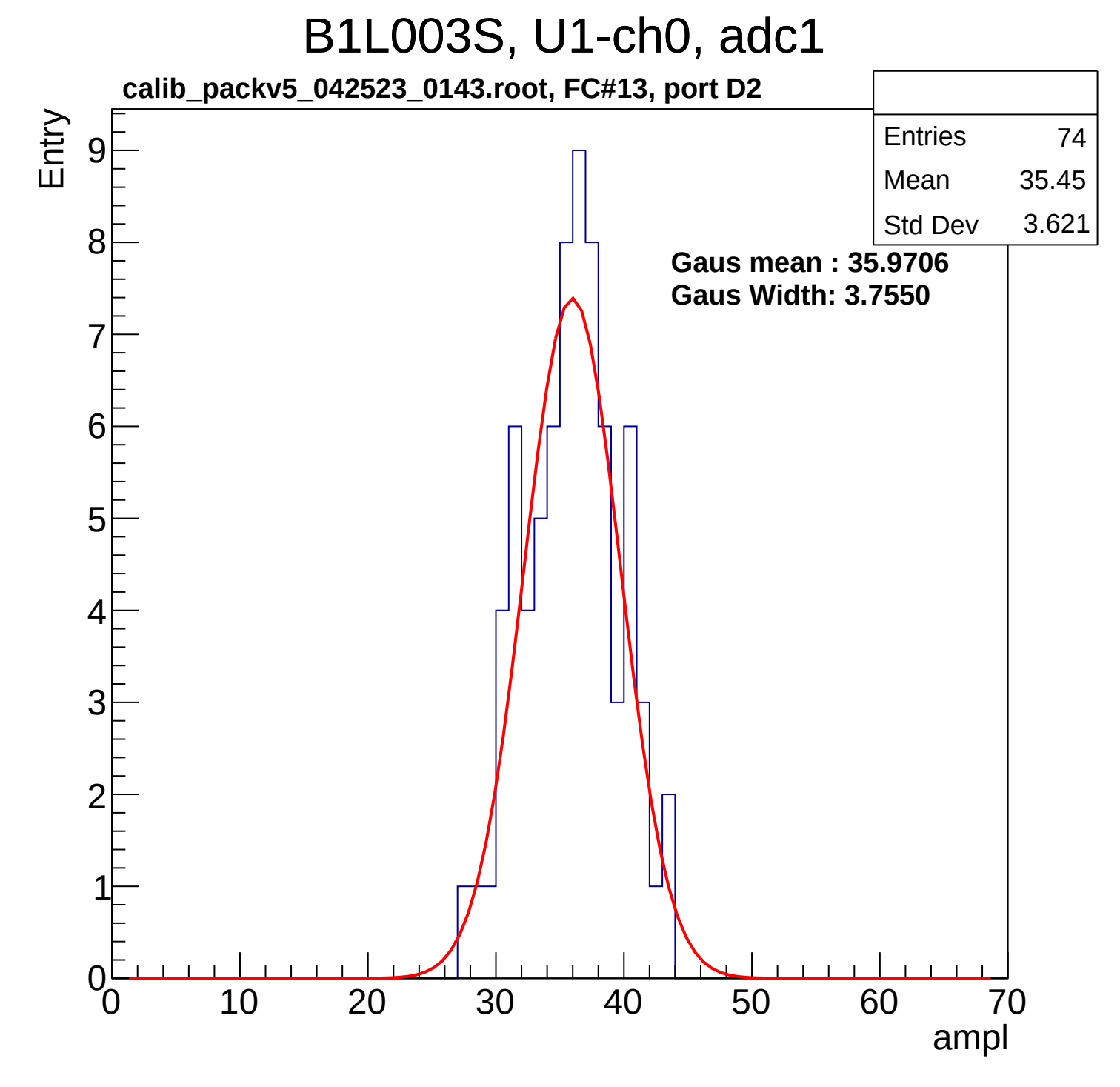

B1L003S, U1-ch0, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 29.11
Std Dev 5.193
Gaus mean : 30.4623
Gaus Width: 3.3838
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch0, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 35.45
Std Dev 3.621
Gaus mean : 35.9706
Gaus Width: 3.7550
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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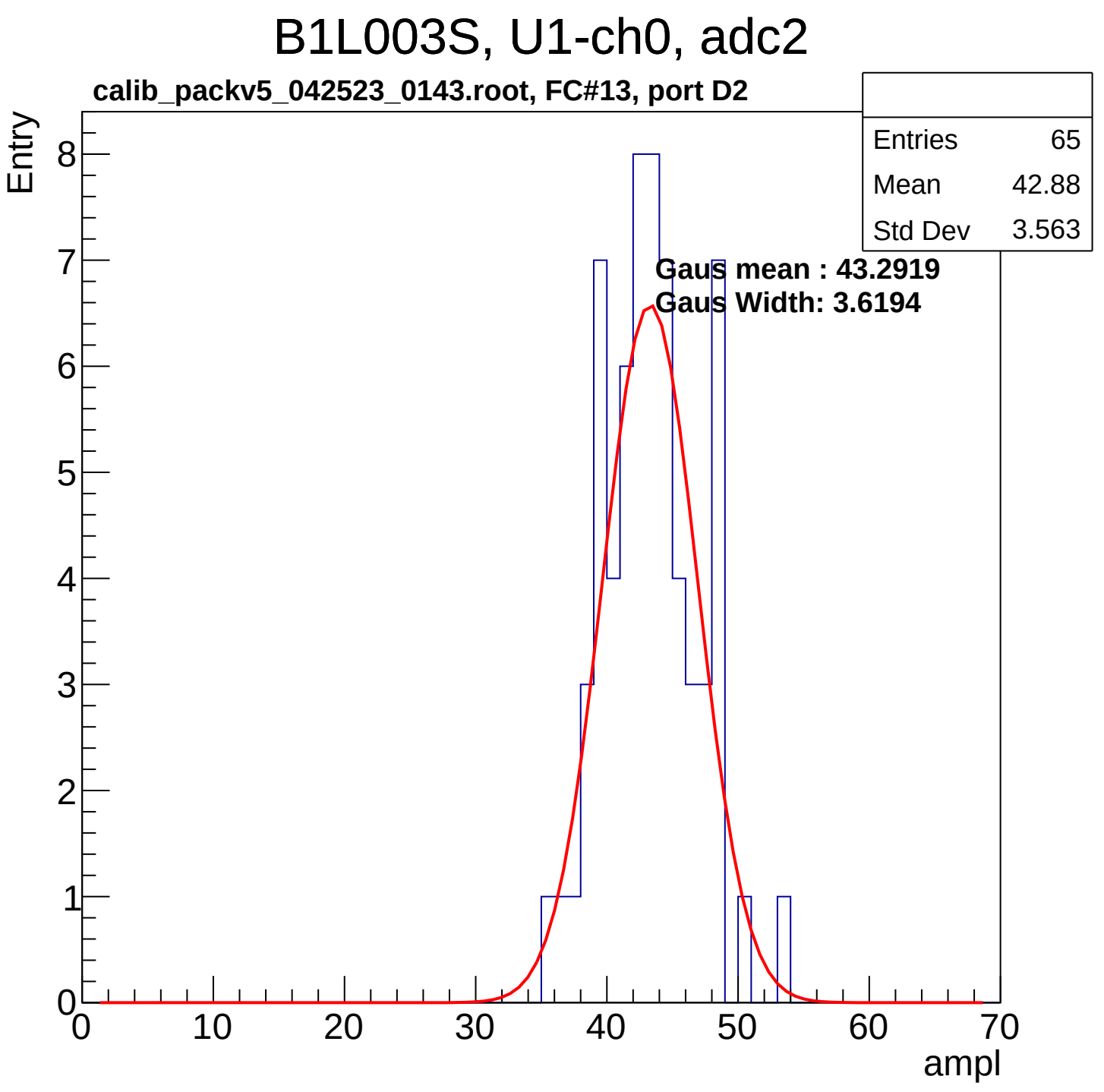
B1L003S, U1-ch0, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.88
Std Dev 3.563
Gaus mean : 43.2919
Gaus Width: 3.6194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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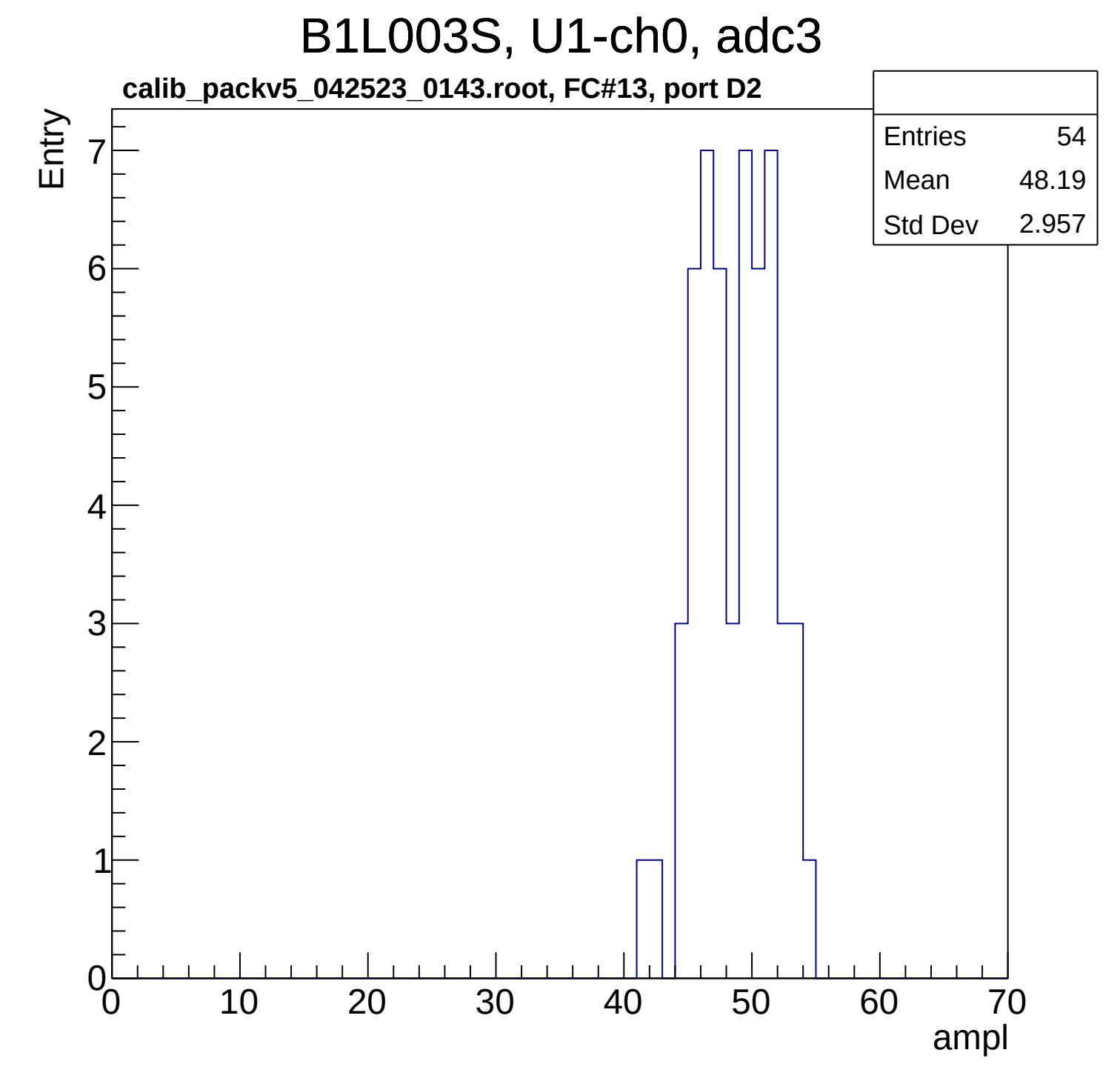
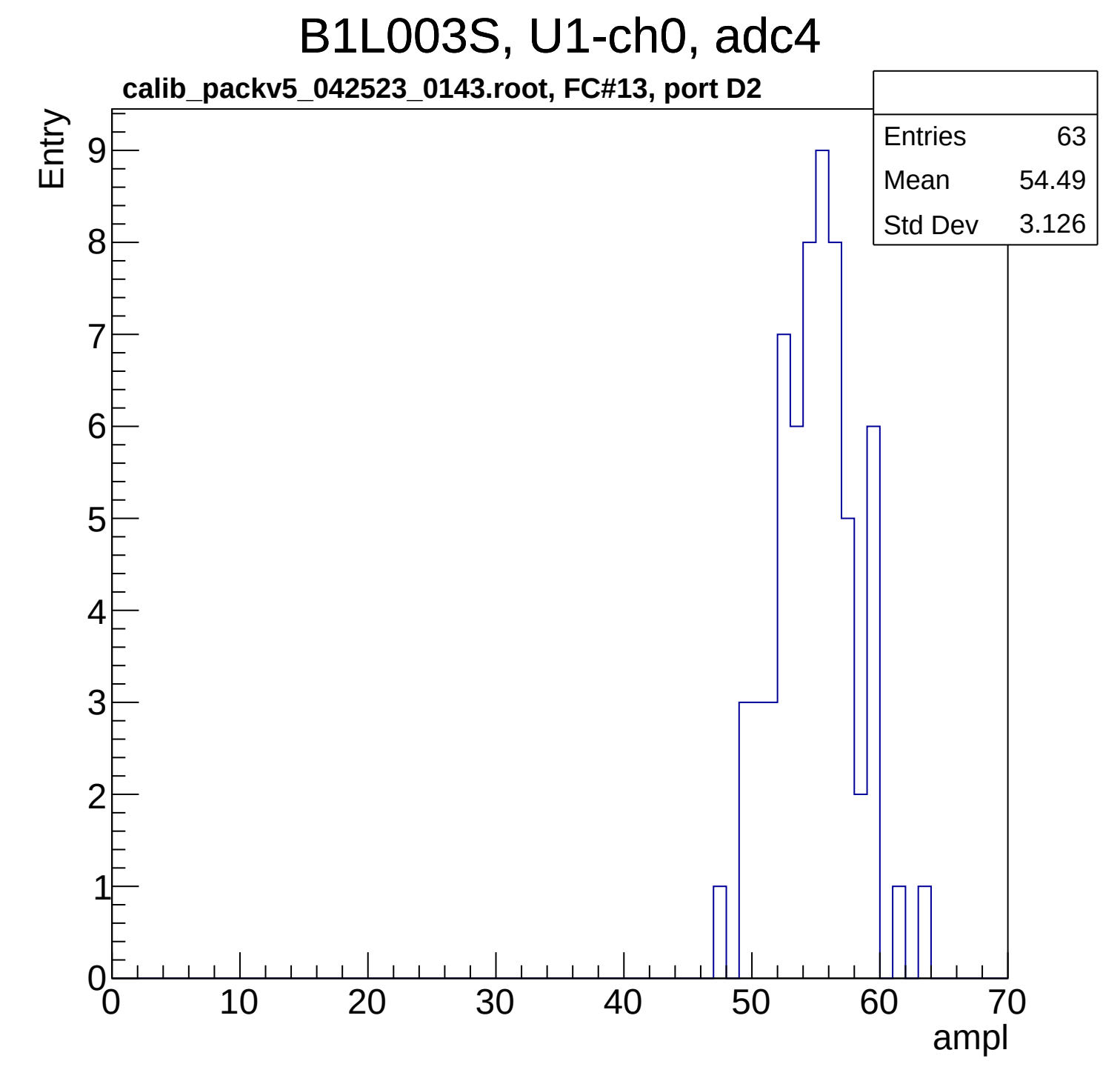
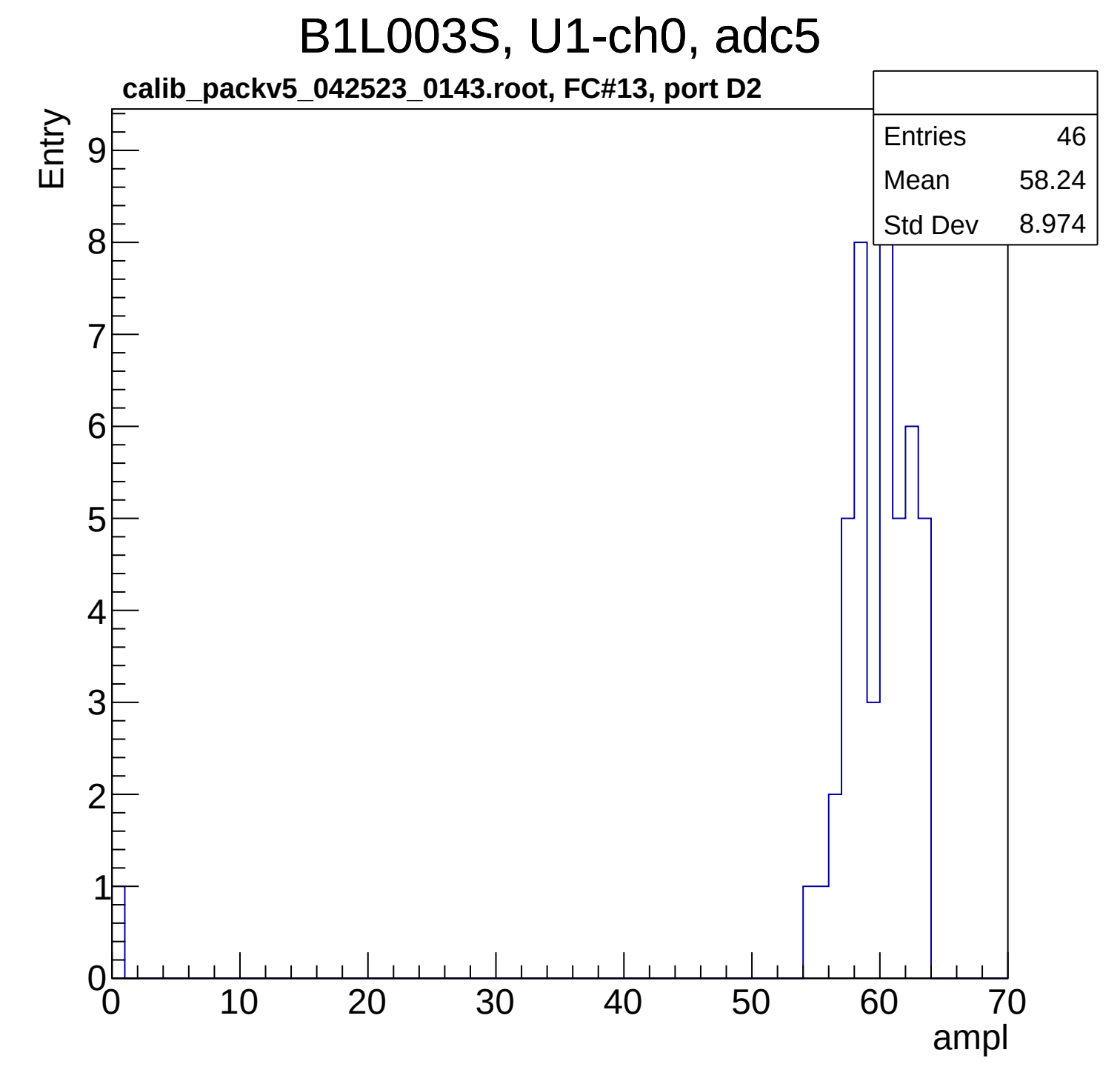

B1L003S, U1-ch0, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 48.19
Std Dev 2.957
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch0, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.49
Std Dev 3.126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch0, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.24
Std Dev 8.974
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
50
60
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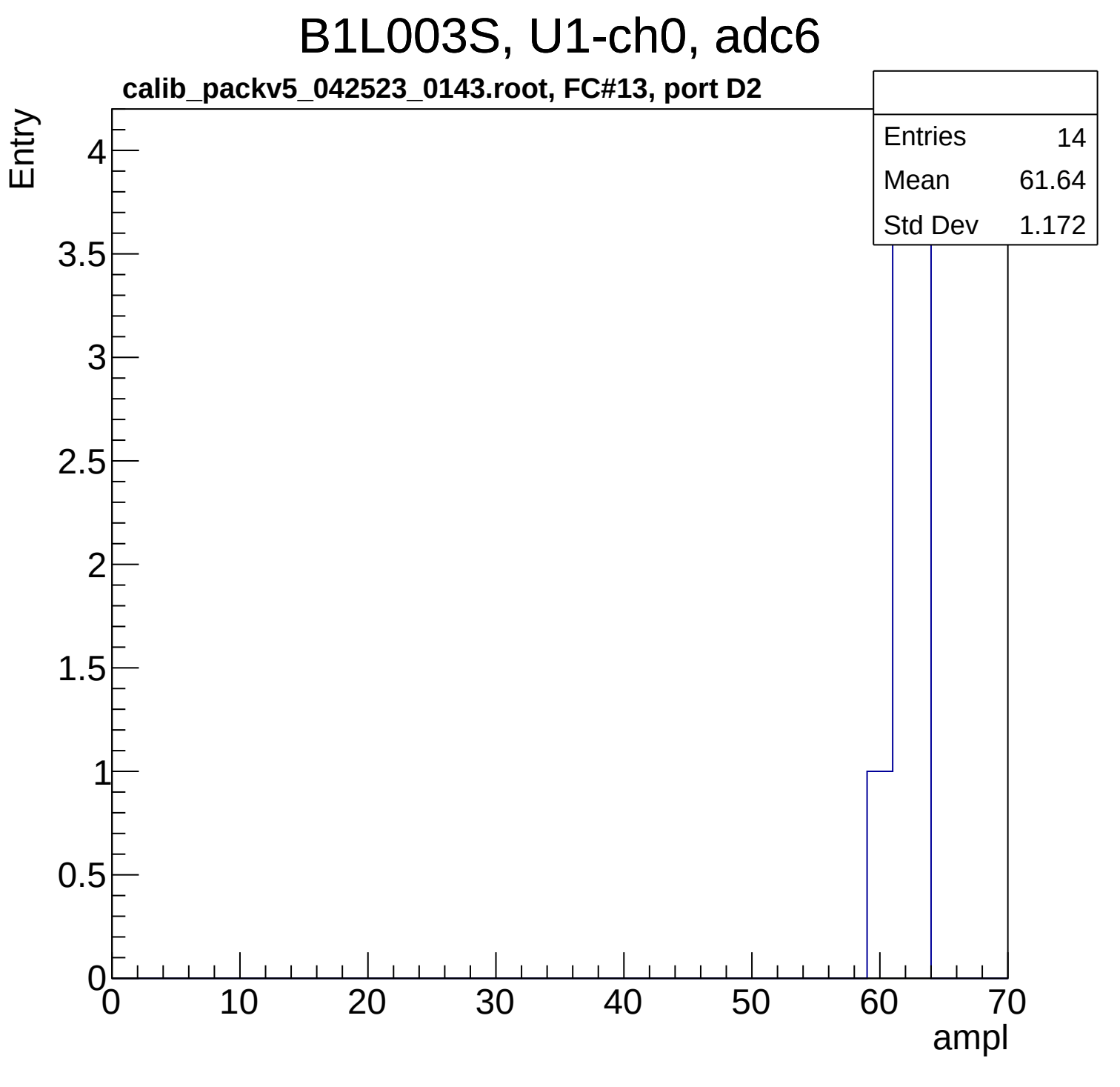
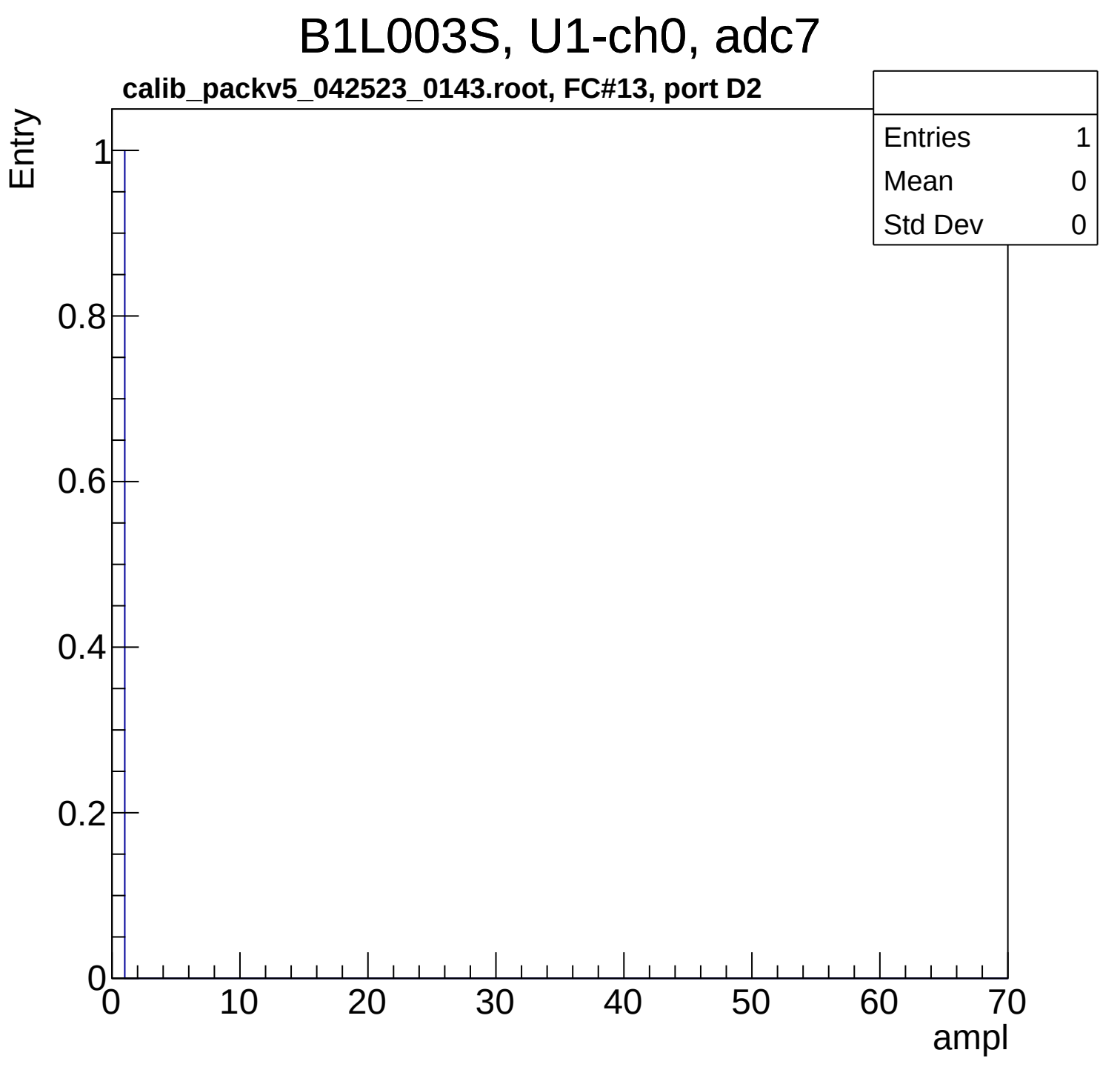
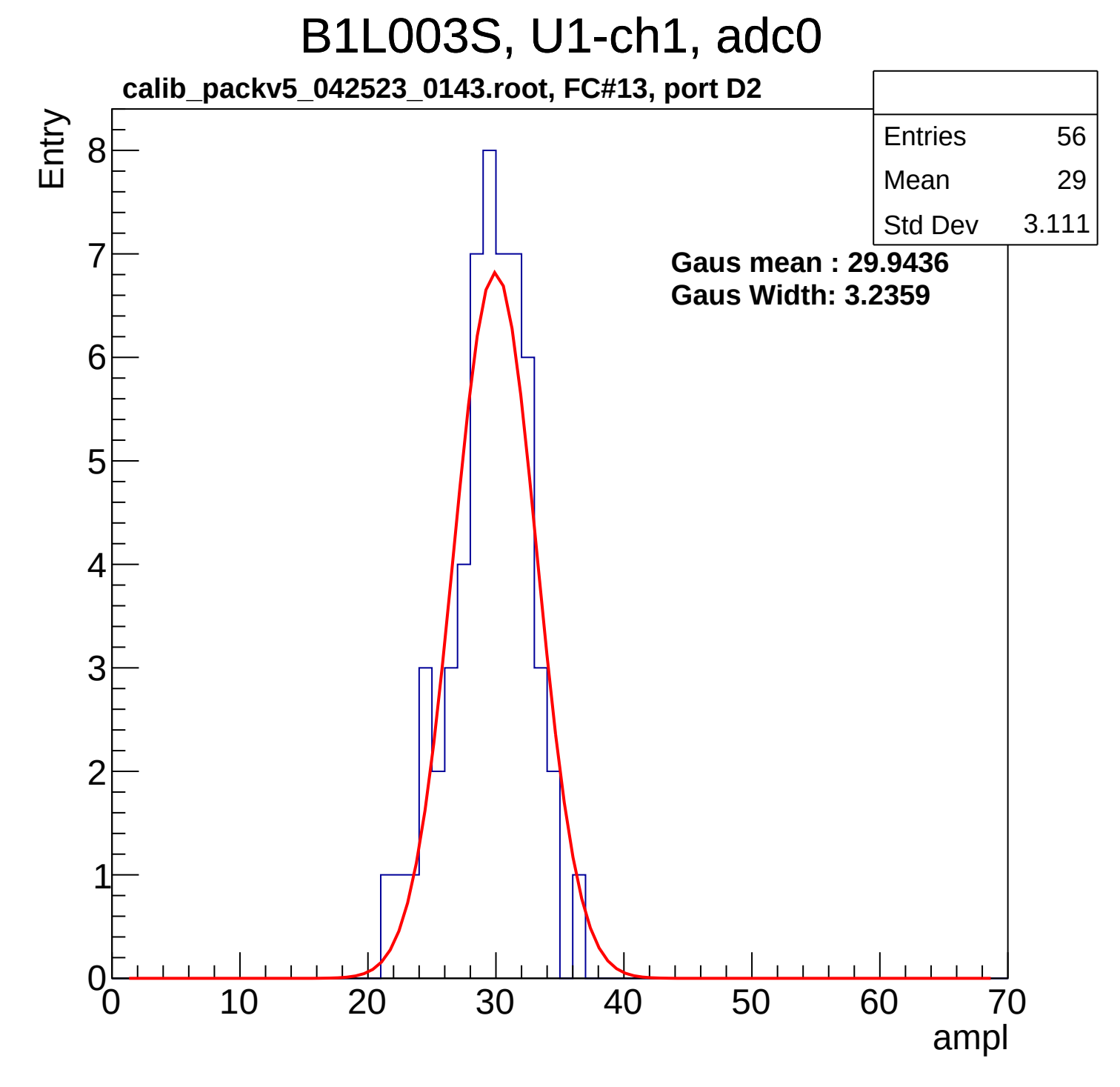
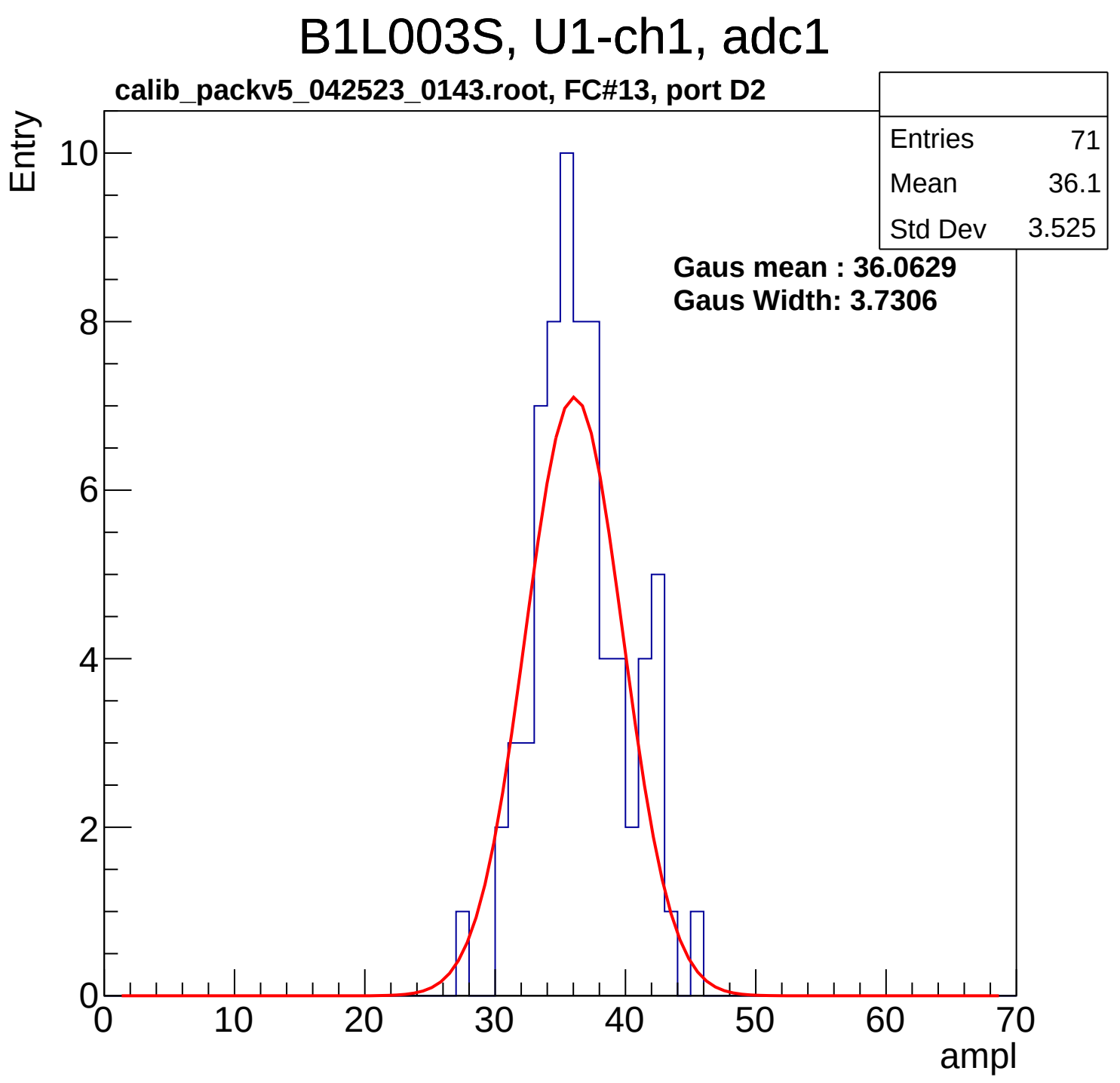
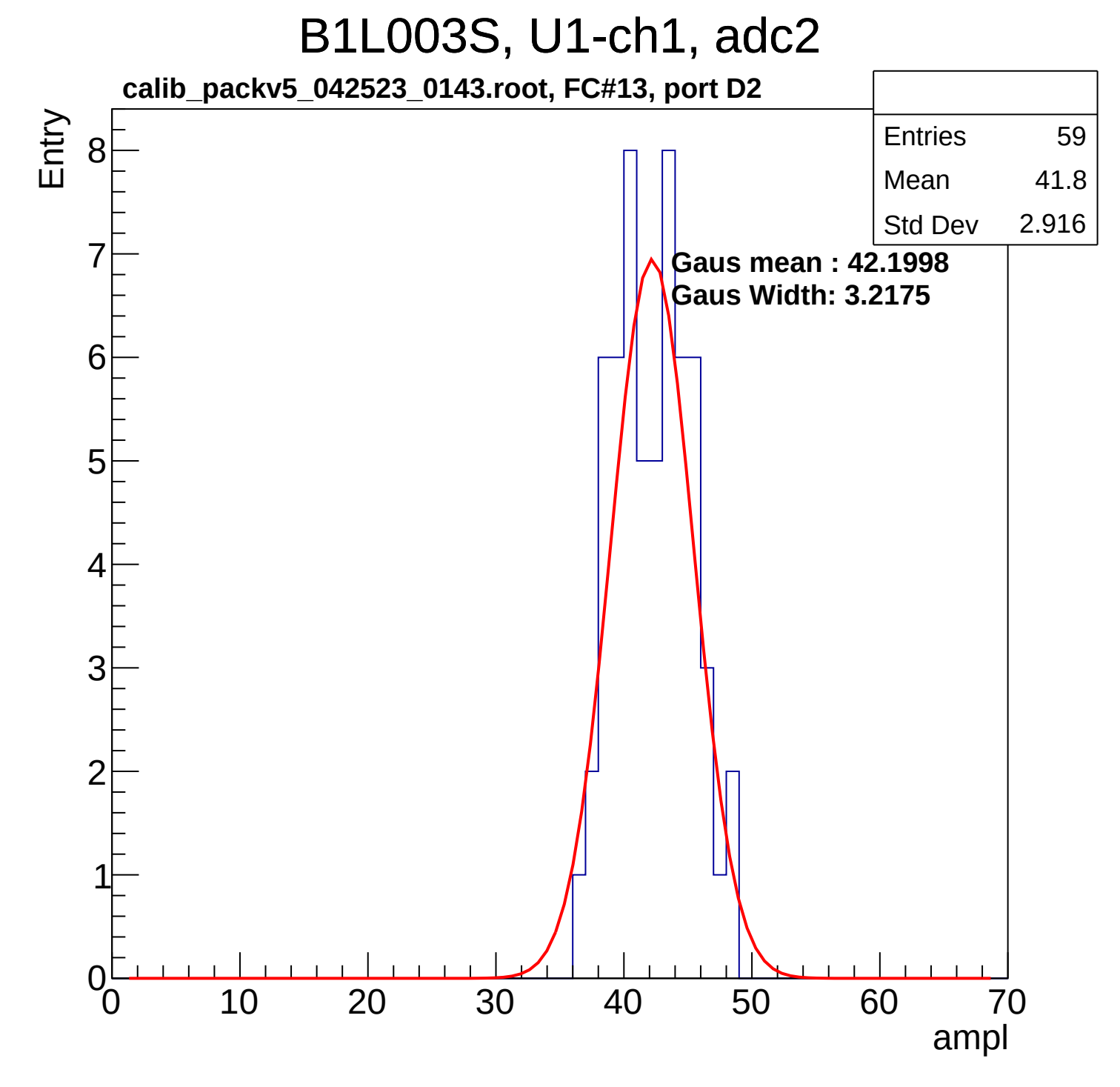

B1L003S, U1-ch0, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.64
Std Dev 1.172
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch0, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch1, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 29
Std Dev 3.111
Gaus mean : 29.9436
Gaus Width: 3.2359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch1, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.1
Std Dev 3.525
Gaus mean : 36.0629
Gaus Width: 3.7306
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch1, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 41.8
Std Dev 2.916
Gaus mean : 42.1998
Gaus Width: 3.2175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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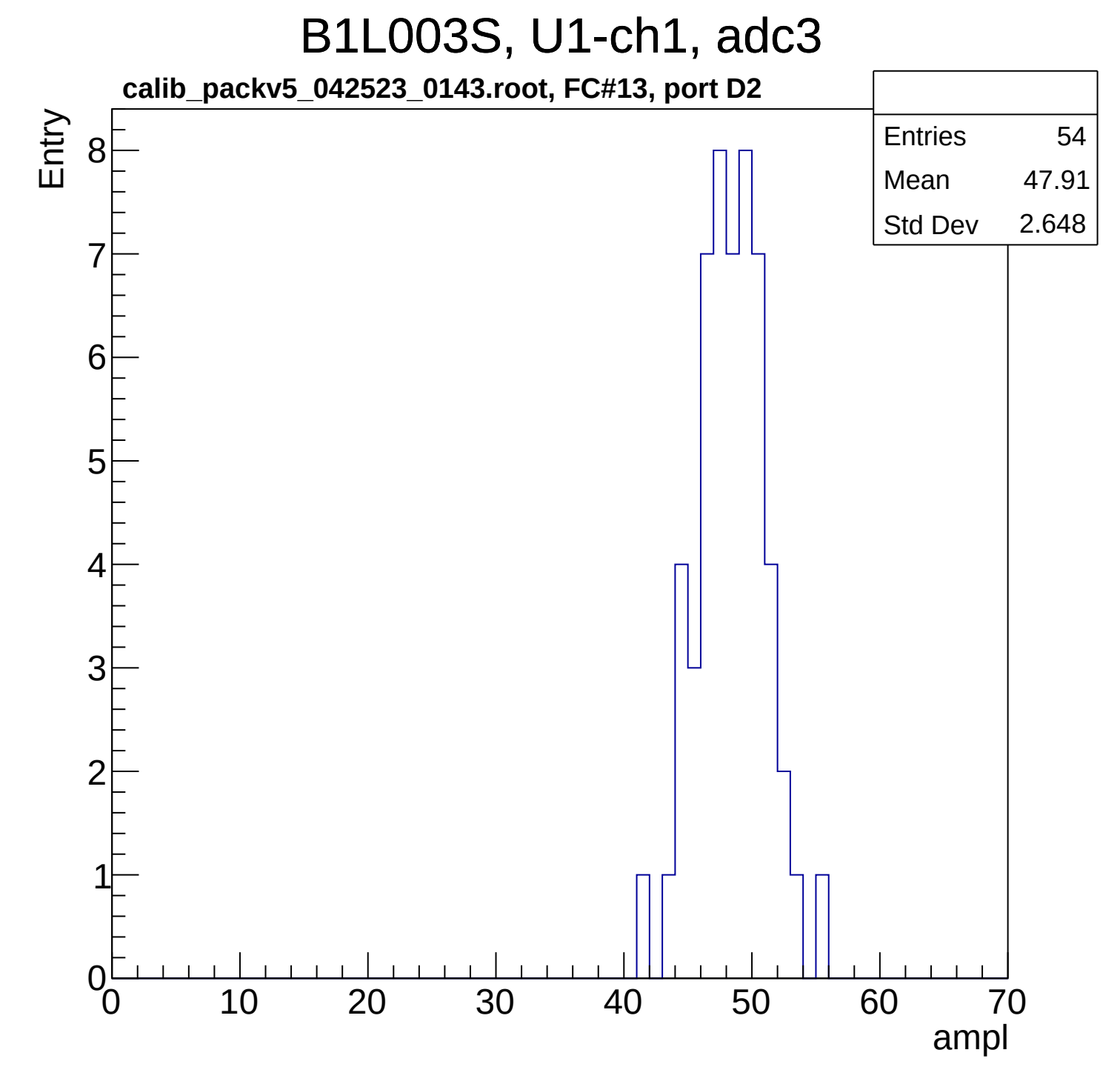
B1L003S, U1-ch1, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 47.91
Std Dev 2.648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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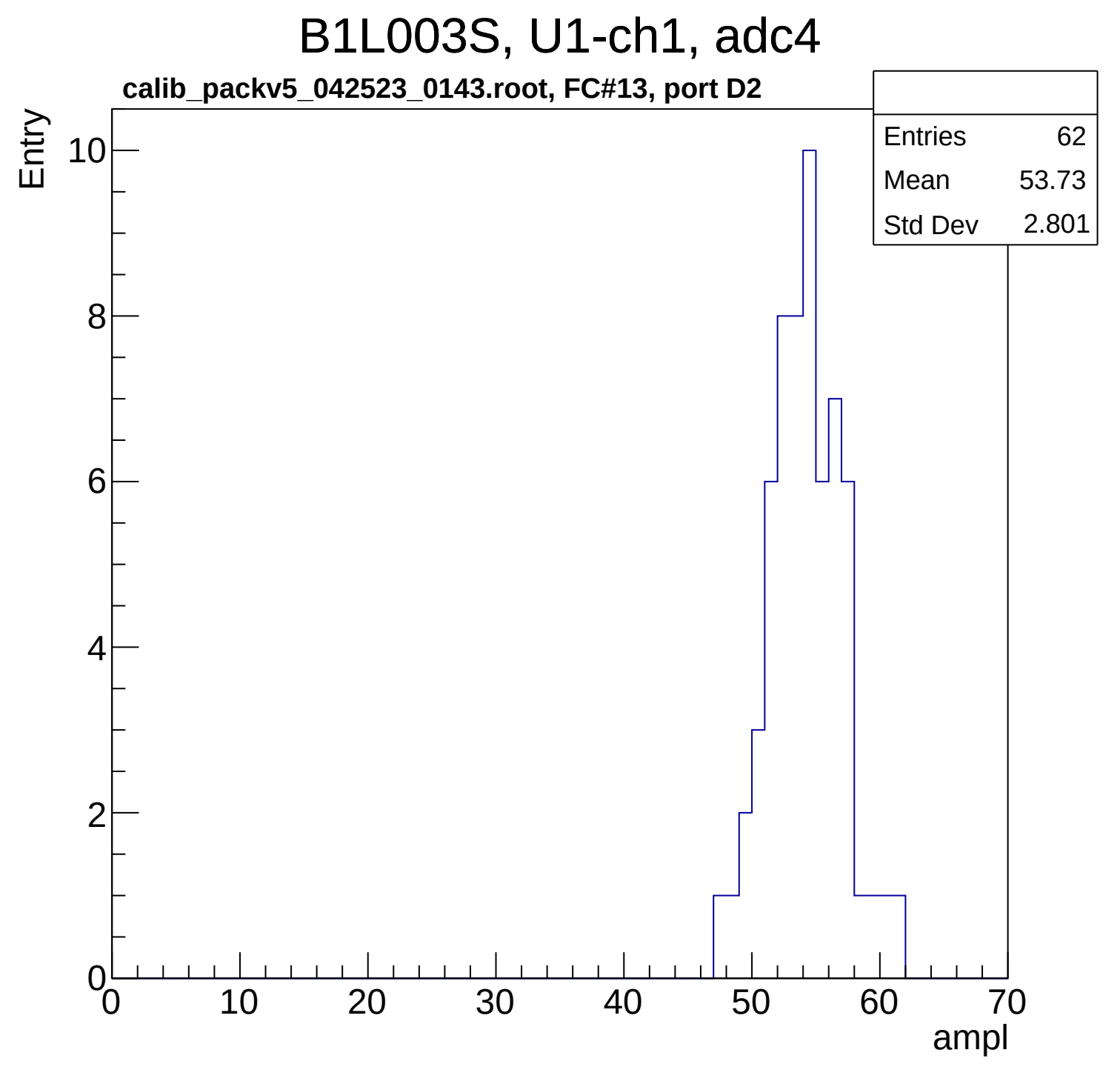
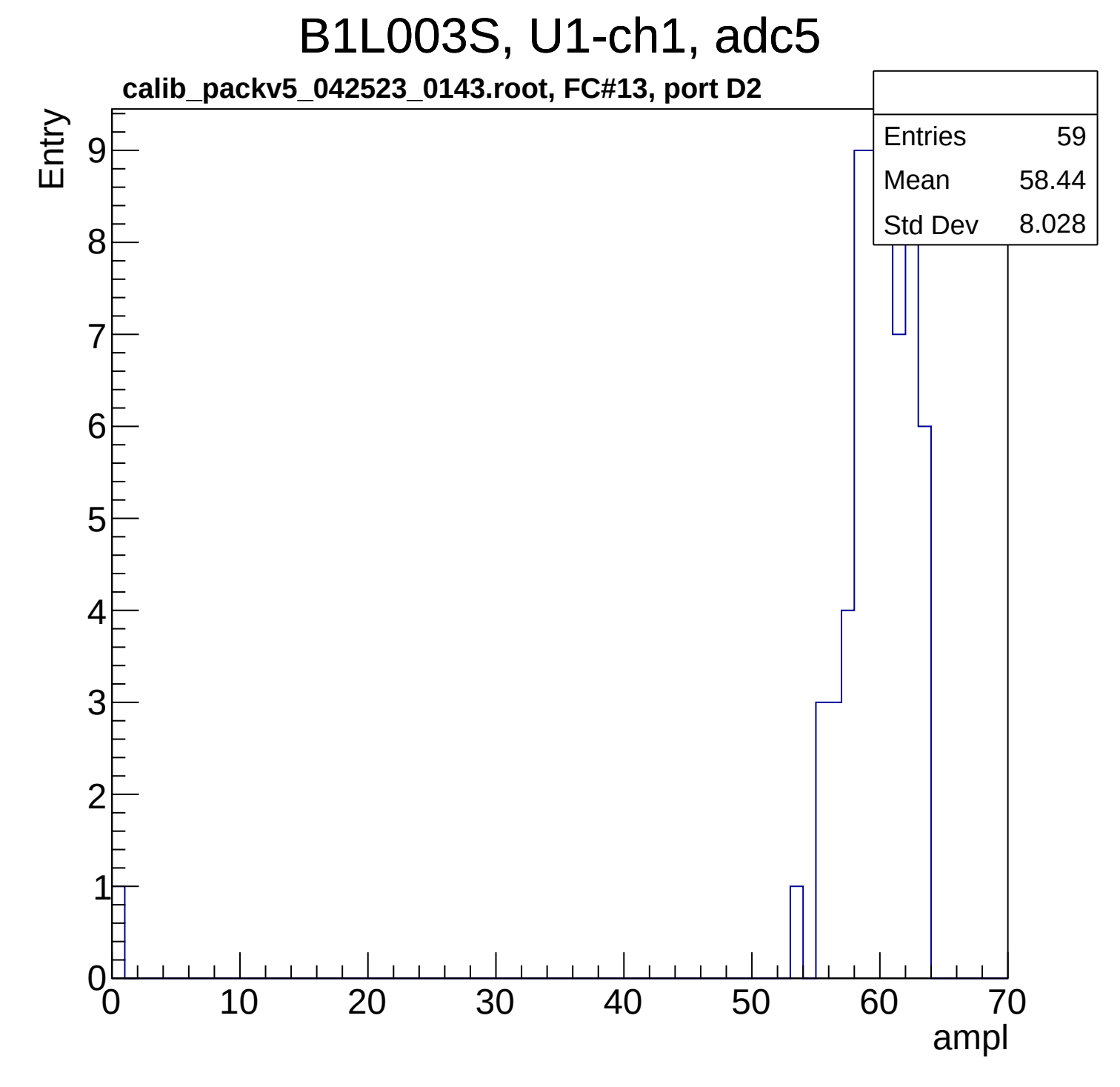

B1L003S, U1-ch1, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 53.73
Std Dev 2.801
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch1, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.44
Std Dev 8.028
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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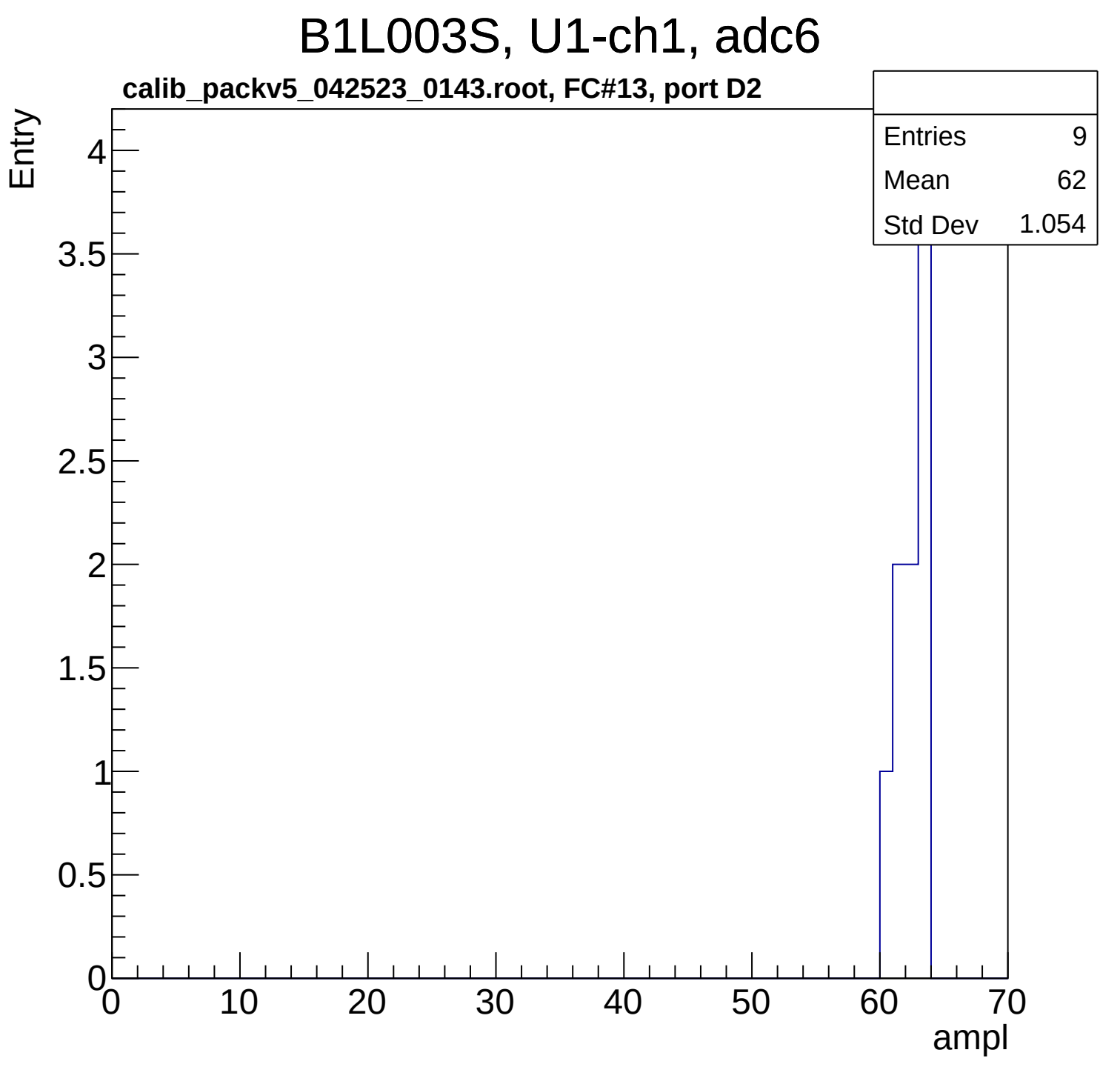
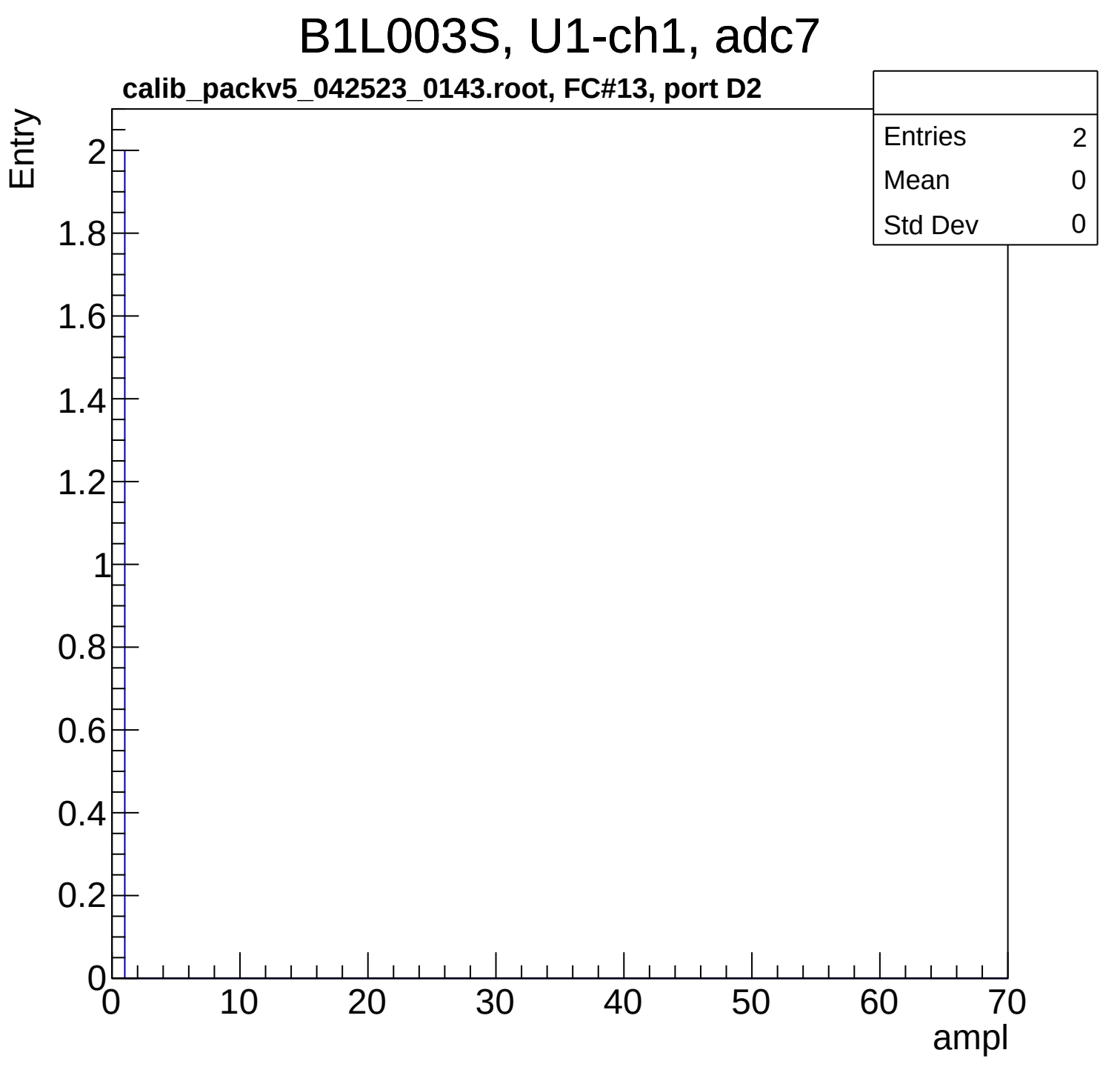
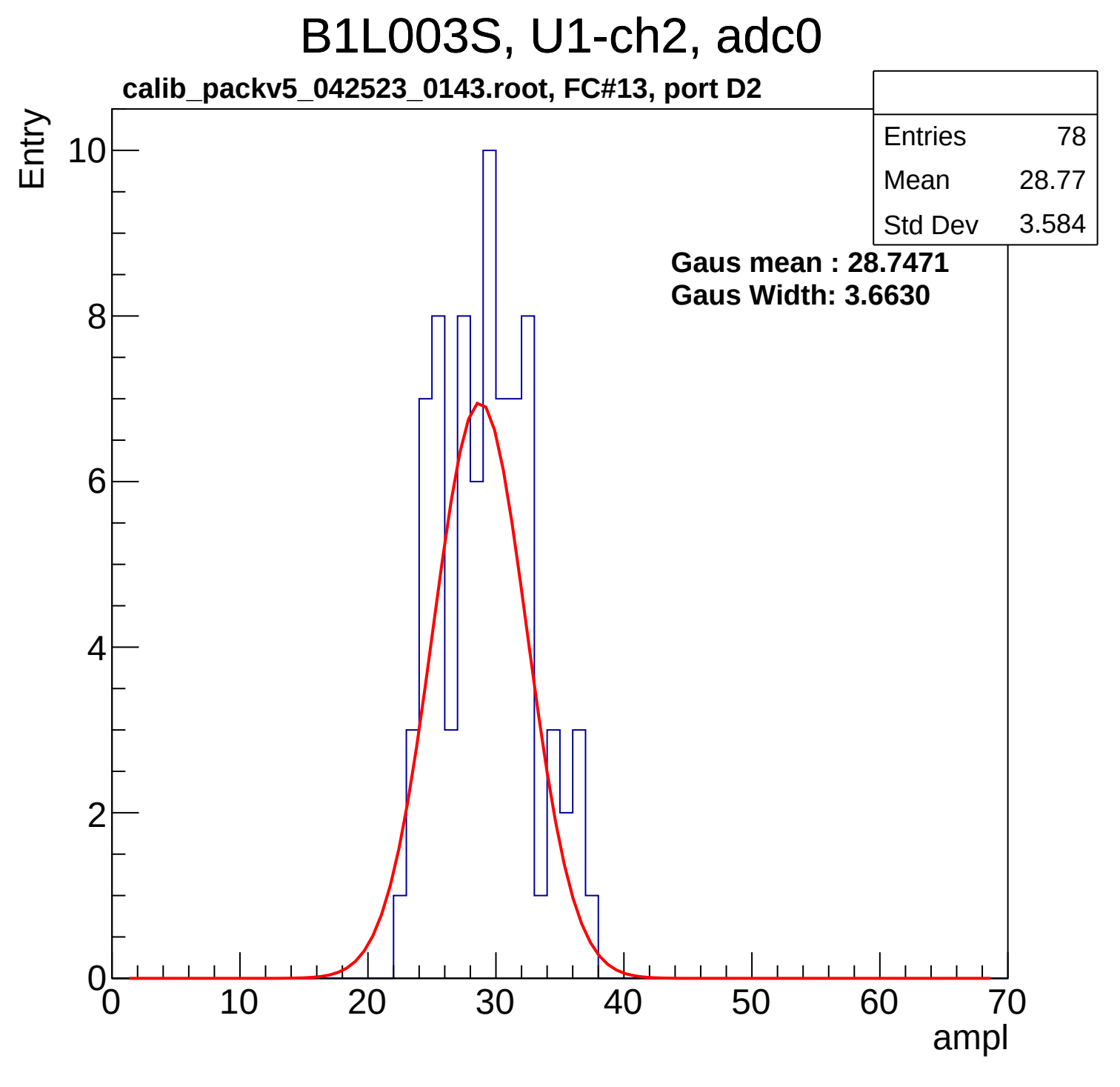
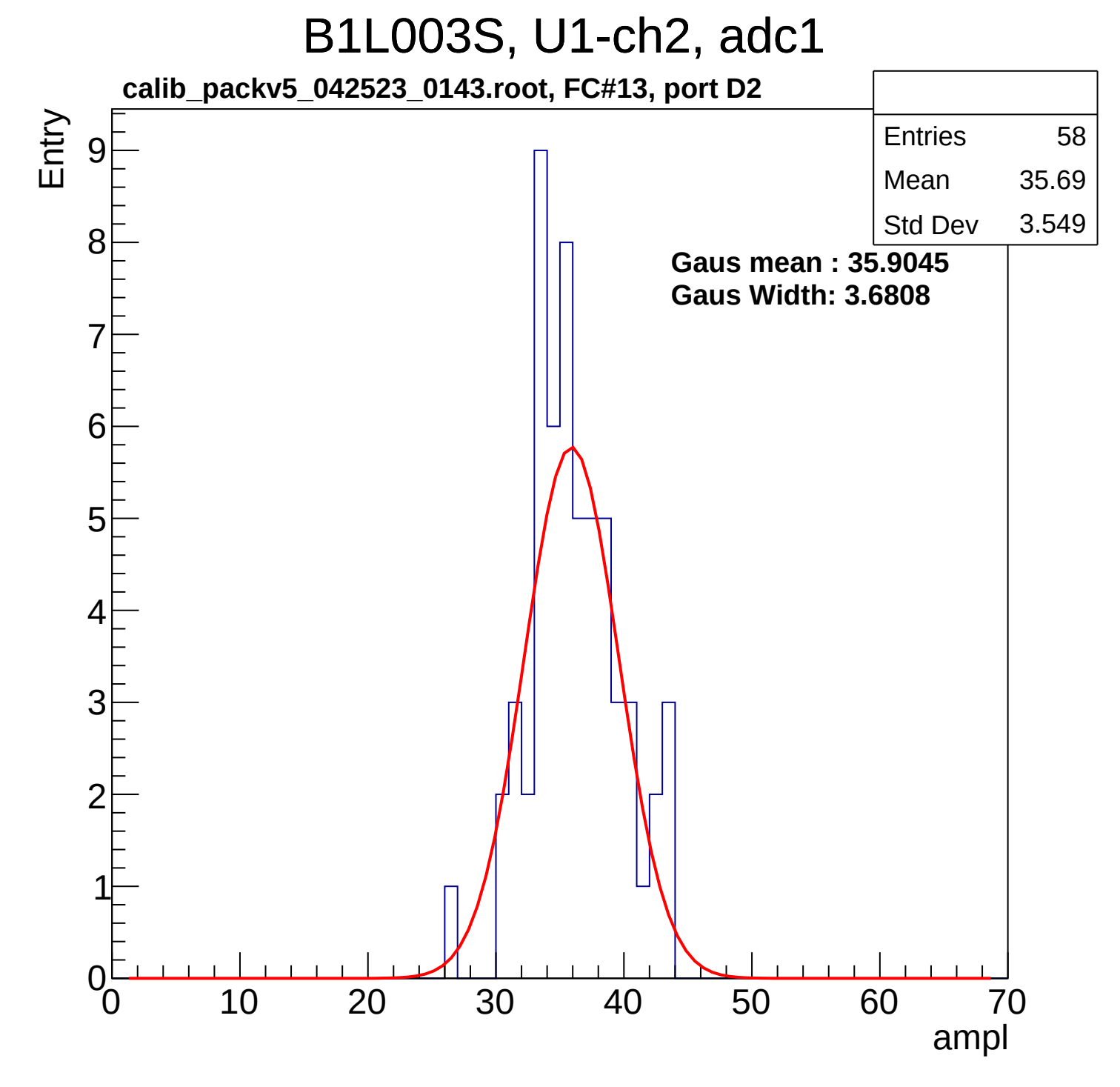
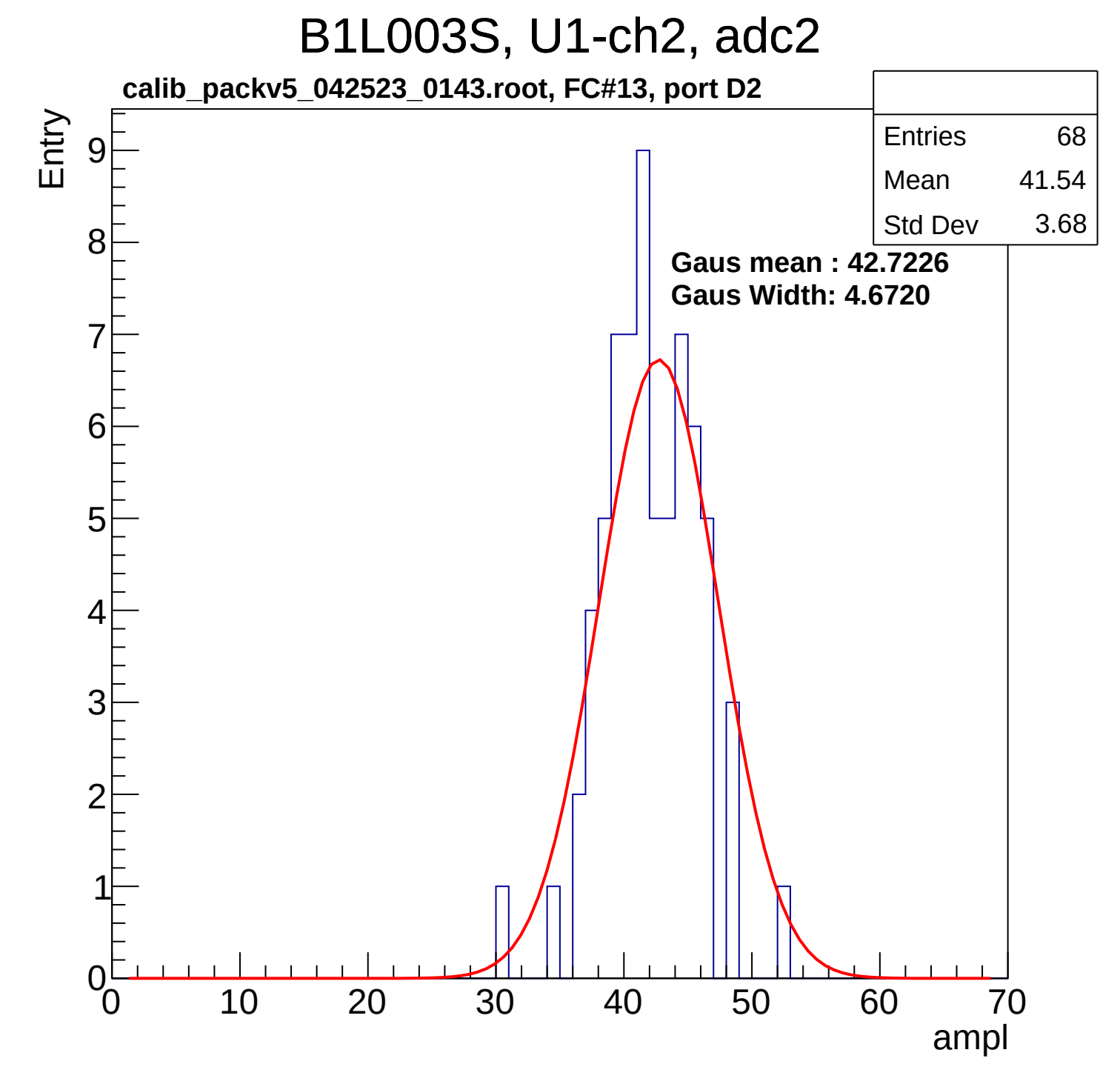

B1L003S, U1-ch1, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62
Std Dev 1.054
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch1, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch2, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 28.77
Std Dev 3.584
Gaus mean : 28.7471
Gaus Width: 3.6630
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch2, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.69
Std Dev 3.549
Gaus mean : 35.9045
Gaus Width: 3.6808
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch2, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 41.54
Std Dev 3.68
Gaus mean : 42.7226
Gaus Width: 4.6720
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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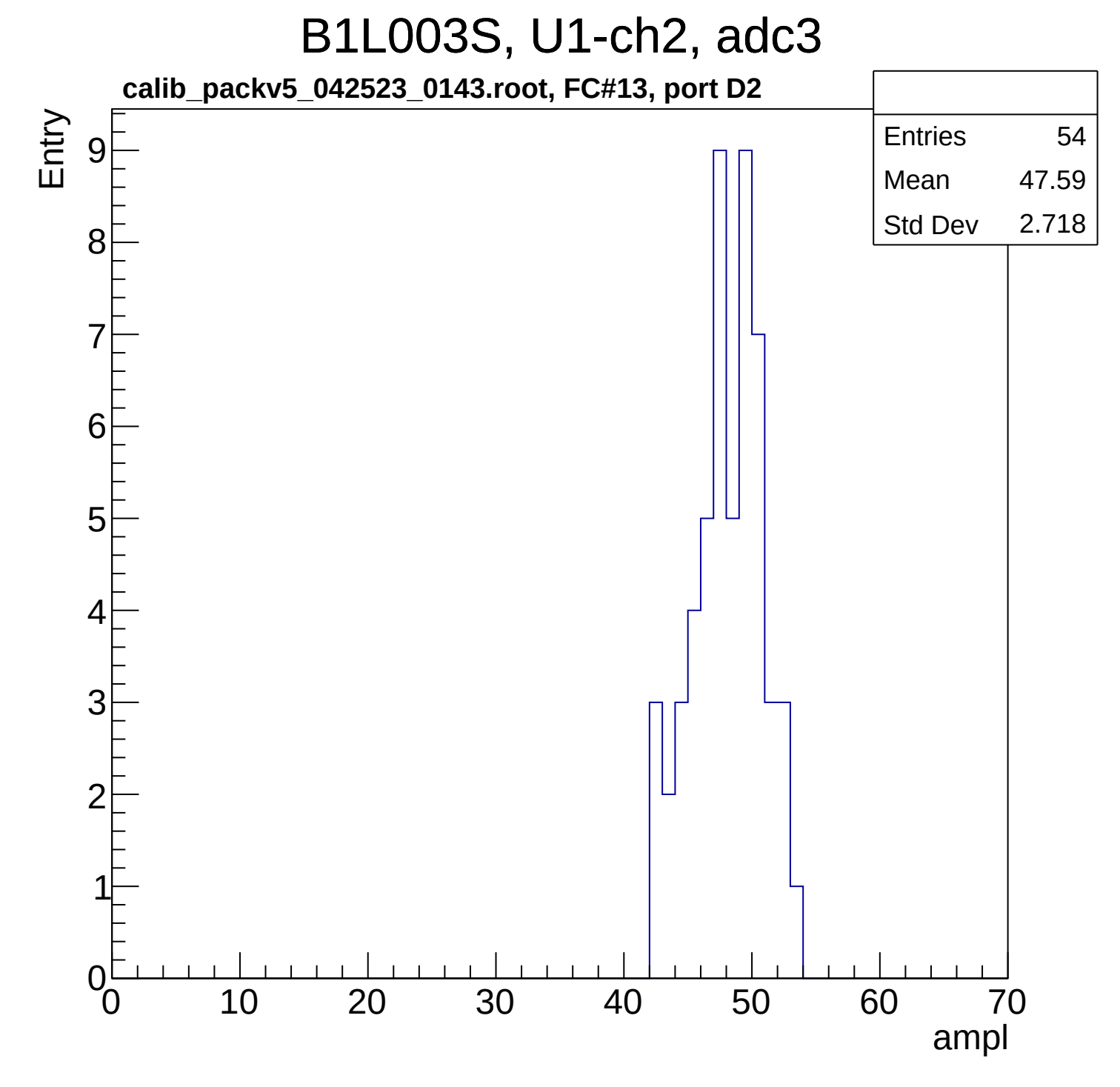
60
70


B1L003S, U1-ch2, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 47.59
Std Dev 2.718
Entry
ampl
0
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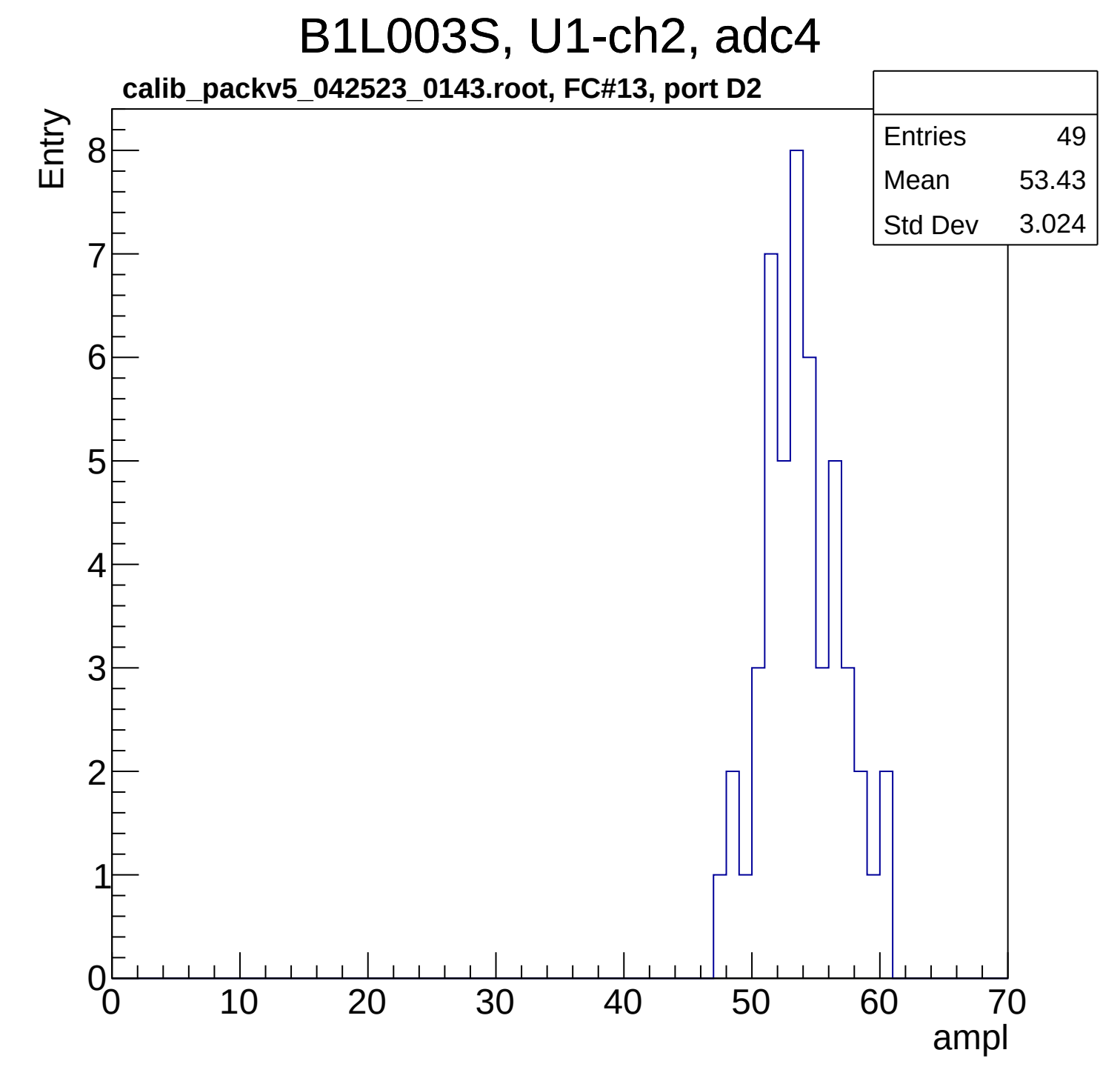
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch2, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.43
Std Dev 3.024
Entry
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2
3
4
5
6
7
8
0
10
20
30
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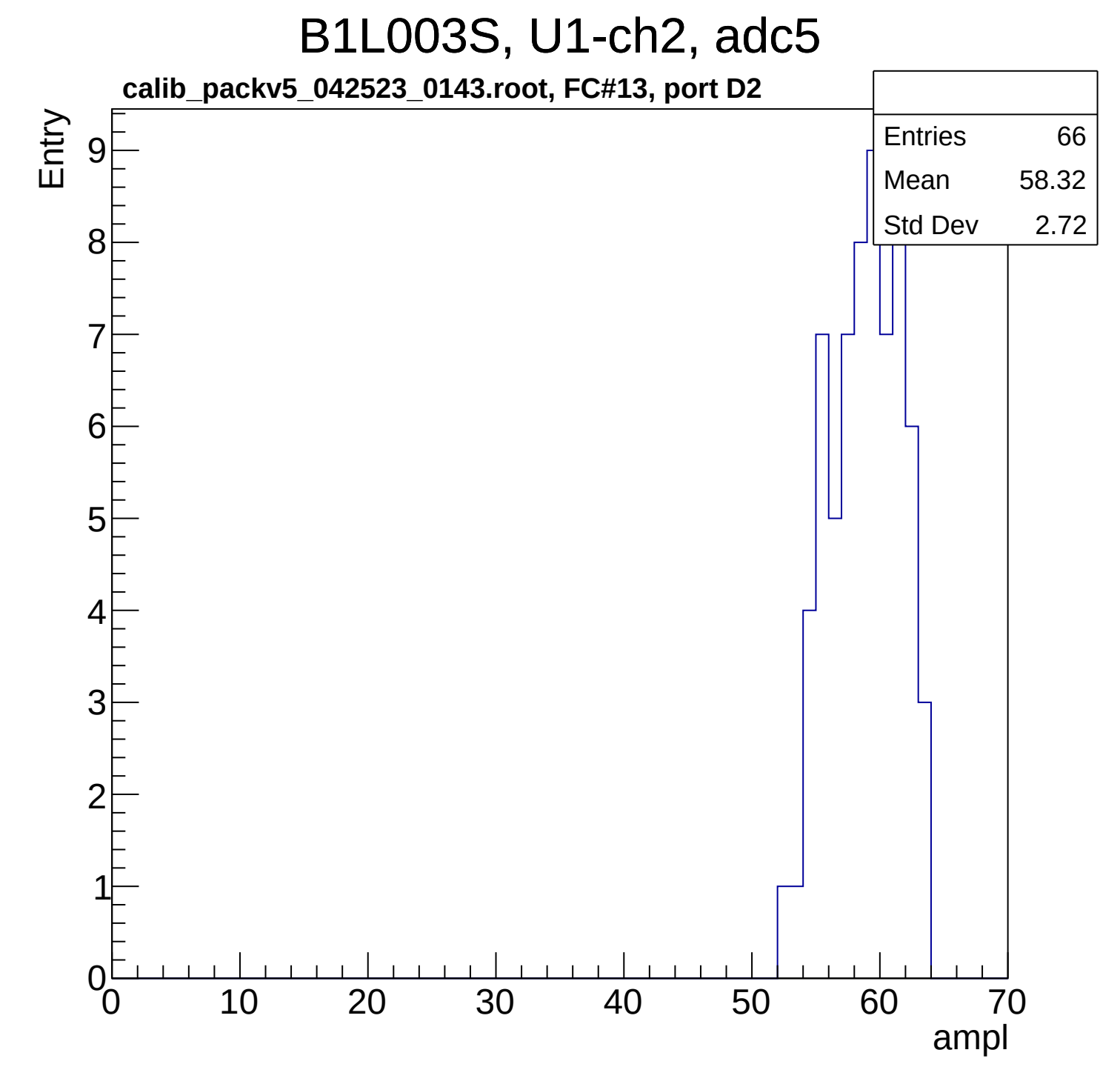
50
60
70
ampl


B1L003S, U1-ch2, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 58.32
Std Dev 2.72
Entry
ampl
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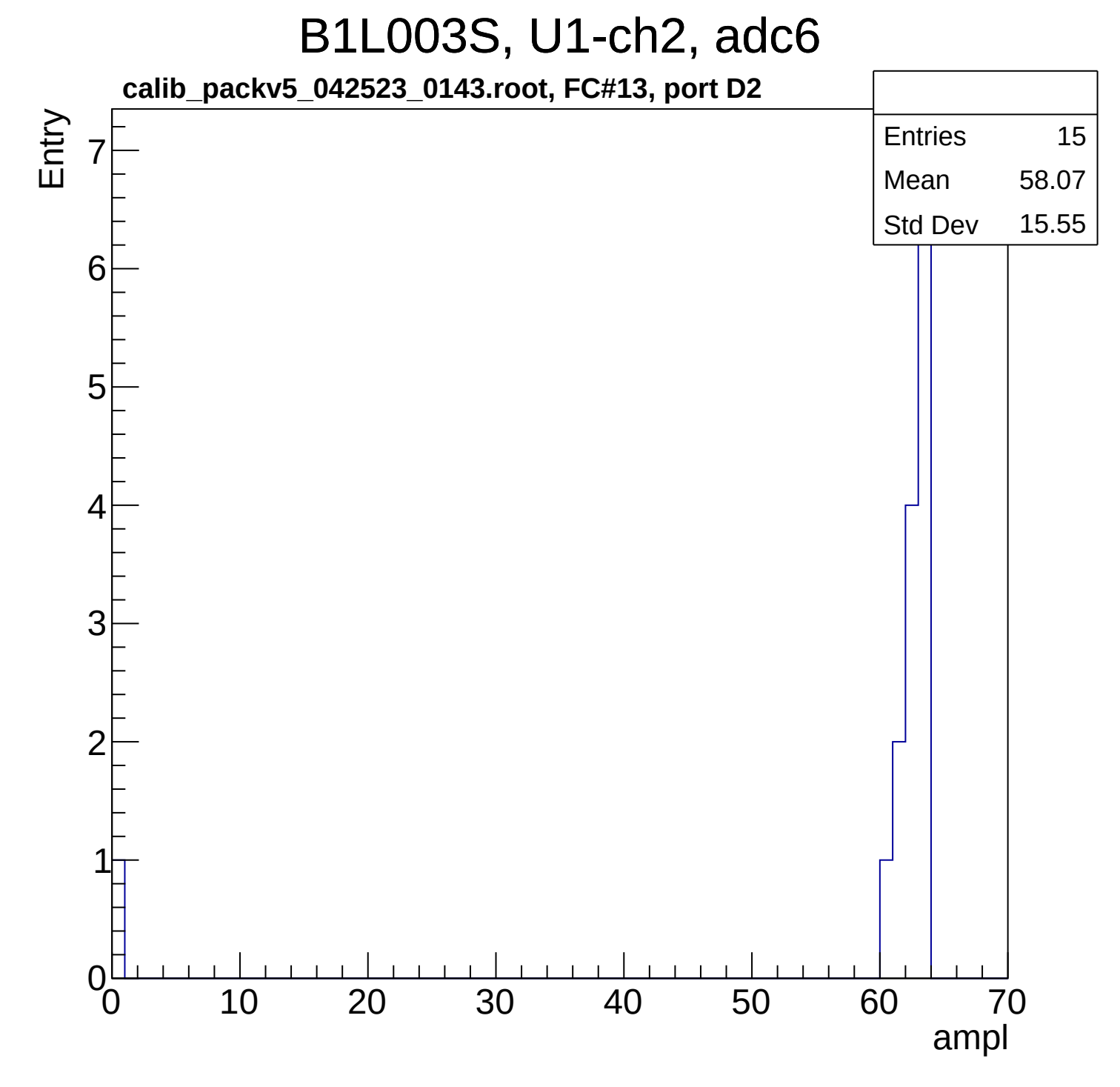
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch2, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 58.07
Std Dev 15.55
Entry
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1
2
3
4
5
6
7
0
10
20
30
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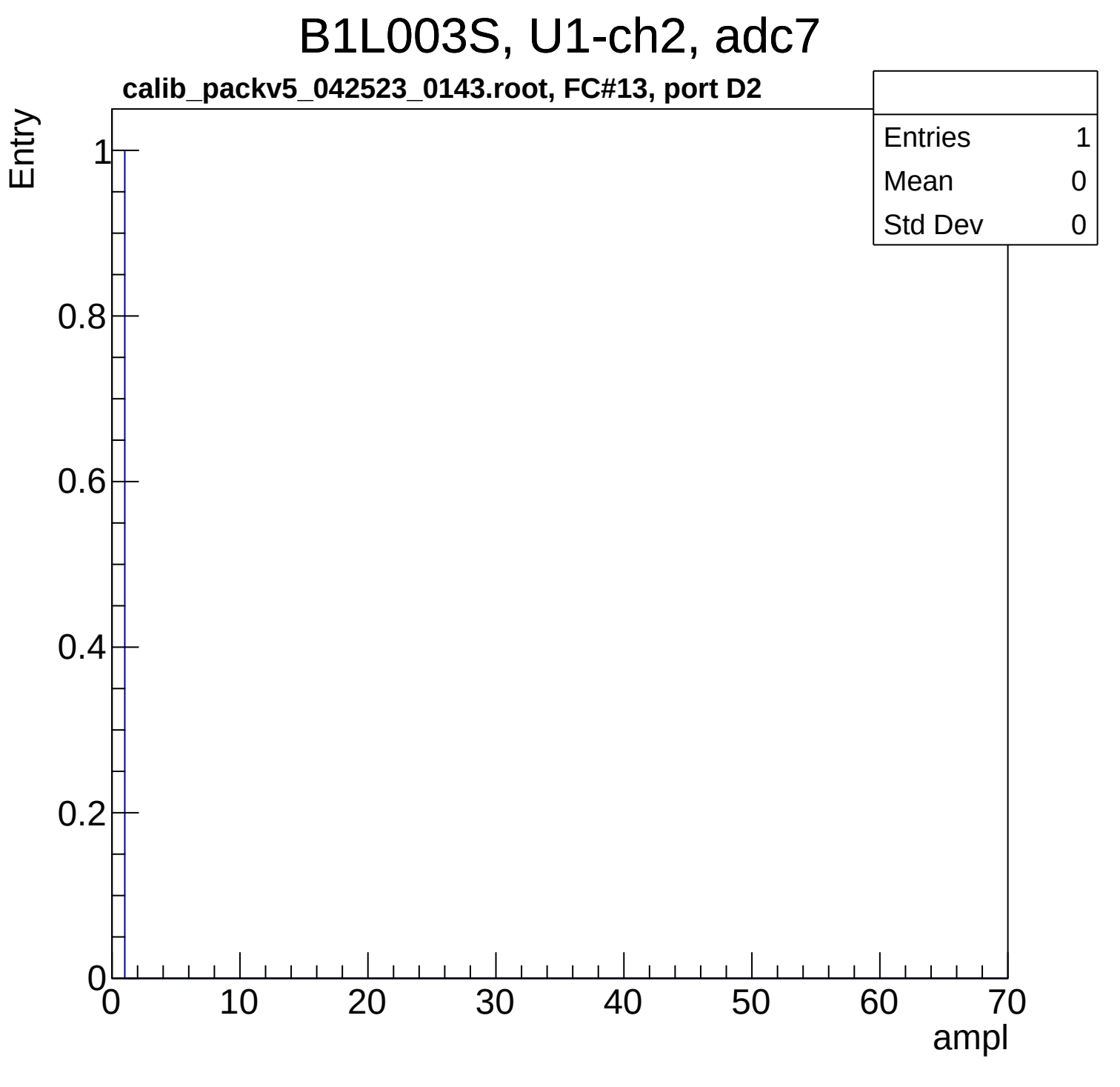
50
60
70
ampl


B1L003S, U1-ch2, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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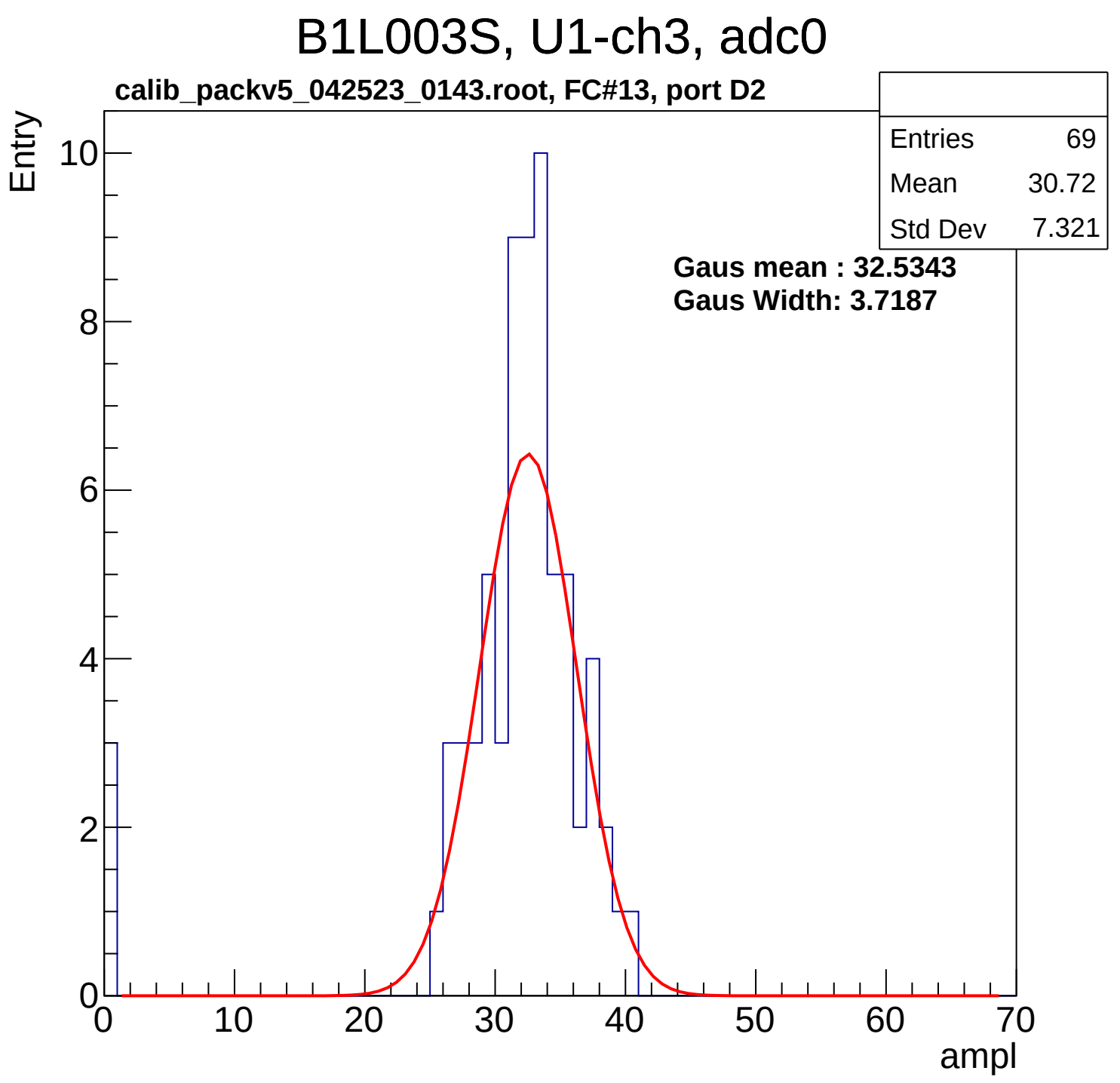
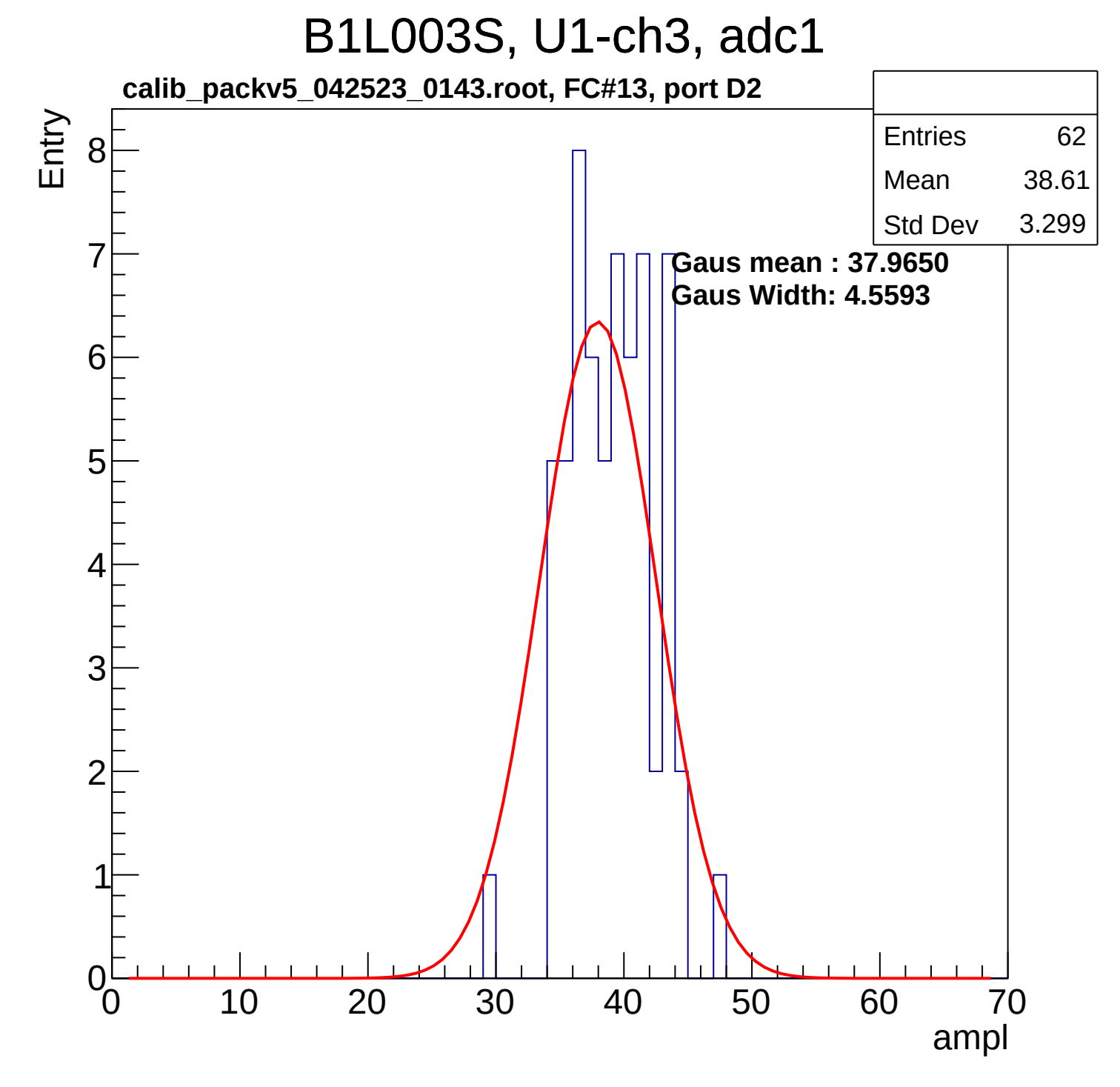
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch3, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 30.72
Std Dev 7.321
Gaus mean : 32.5343
Gaus Width: 3.7187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch3, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 38.61
Std Dev 3.299
Gaus mean : 37.9650
Gaus Width: 4.5593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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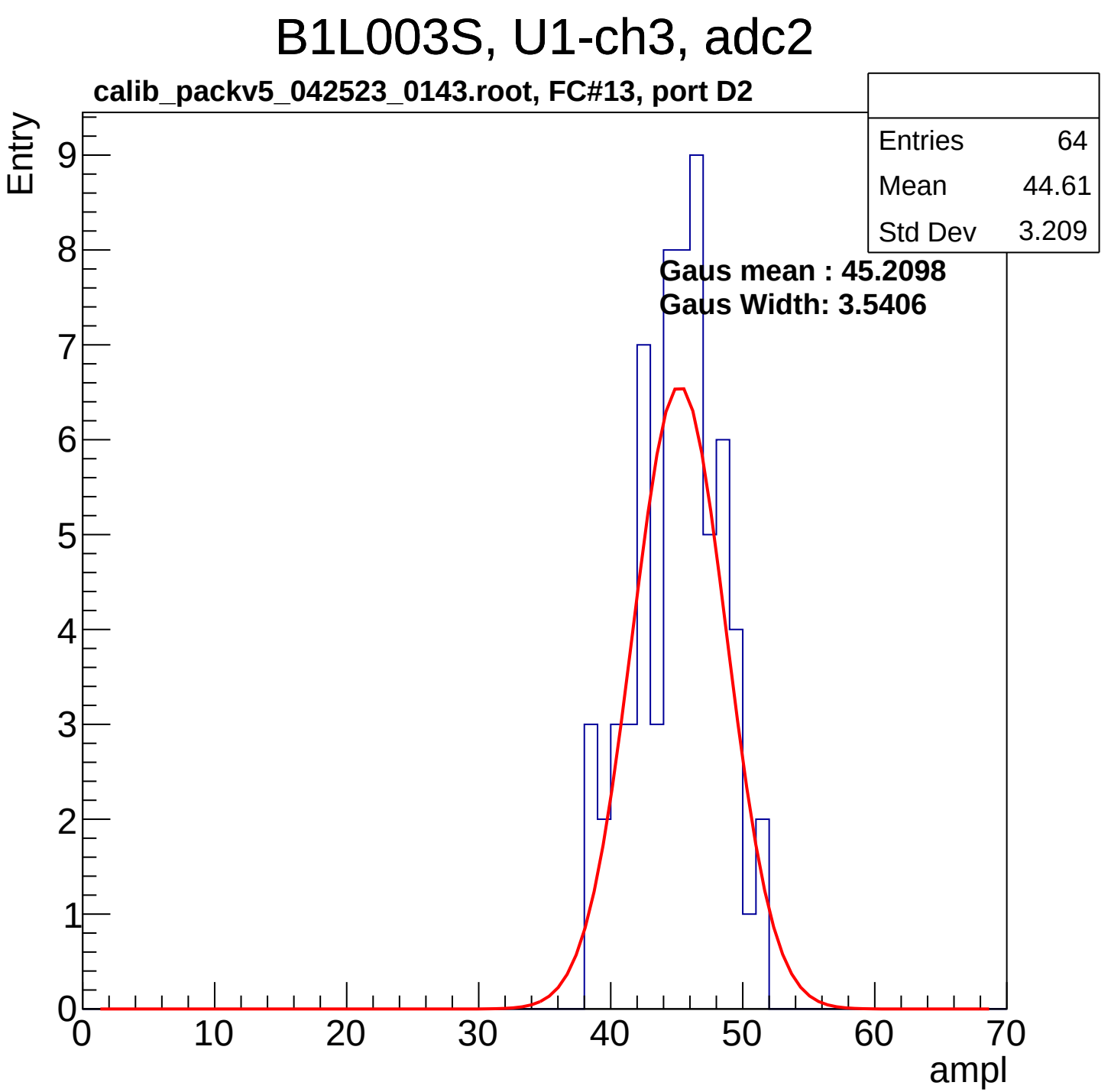
40
50
60
70


B1L003S, U1-ch3, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 44.61
Std Dev 3.209
Gaus mean : 45.2098
Gaus Width: 3.5406
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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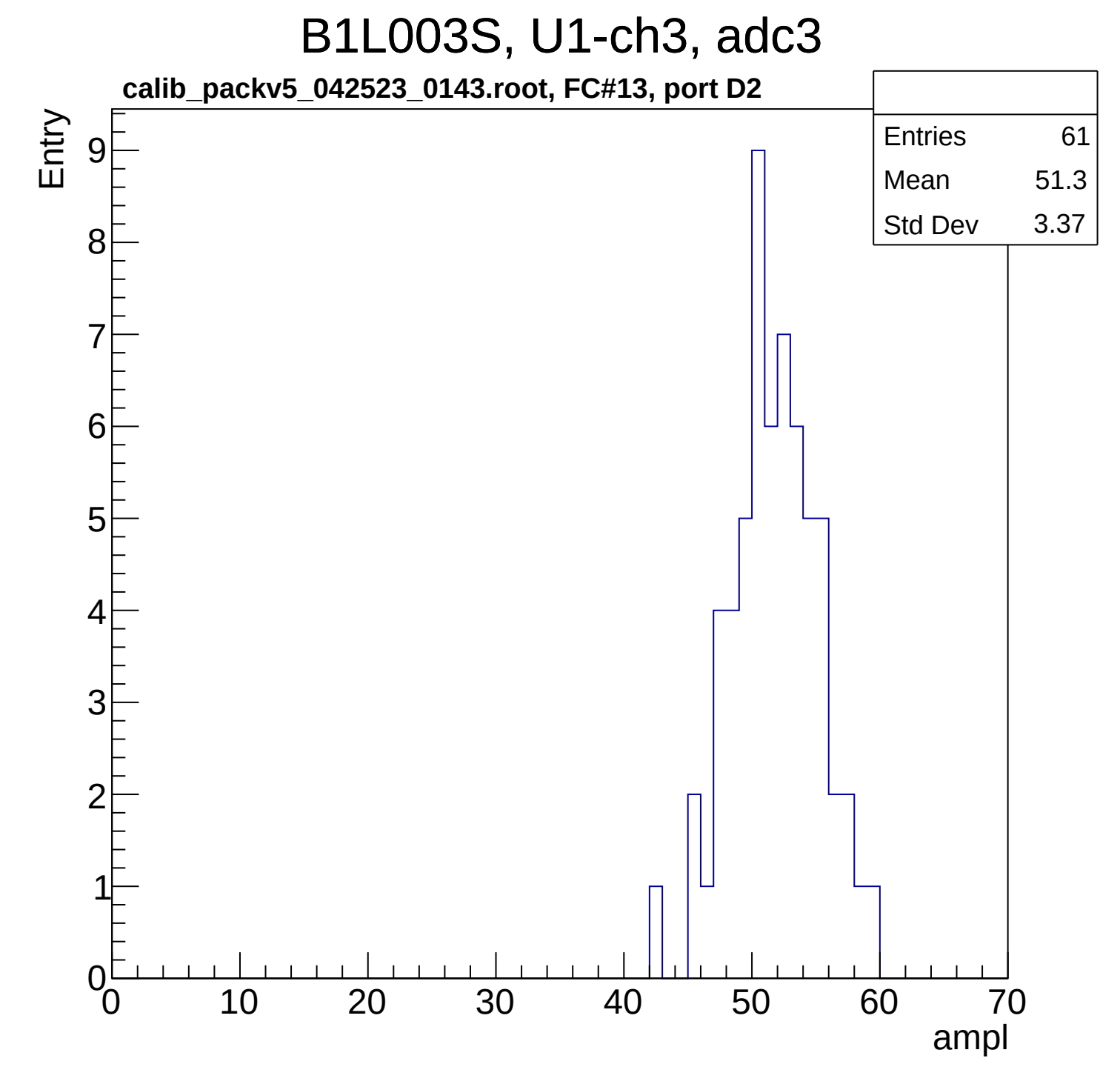
B1L003S, U1-ch3, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 51.3
Std Dev 3.37
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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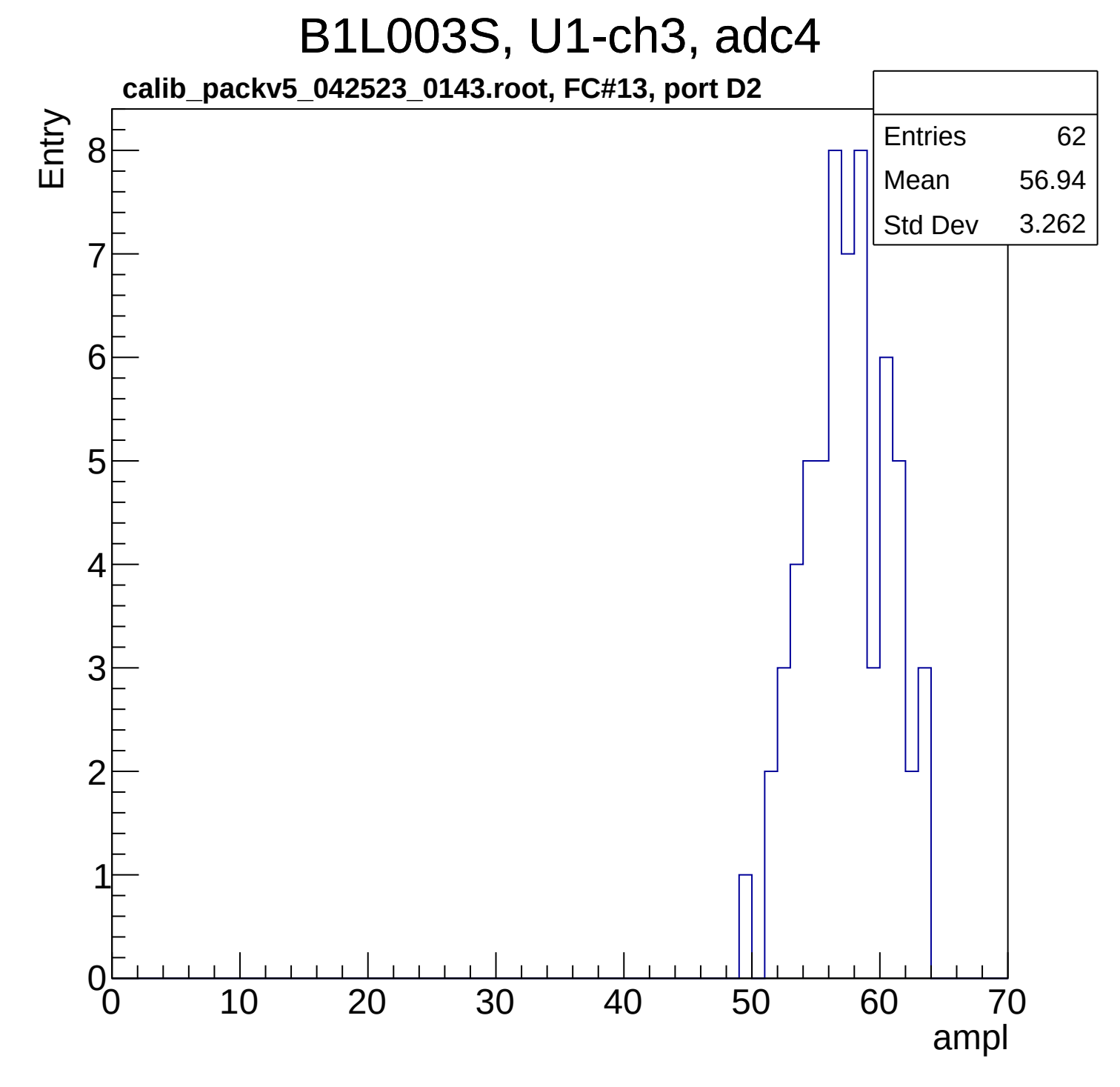
40
50
60
70


B1L003S, U1-ch3, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 56.94
Std Dev 3.262
Entry
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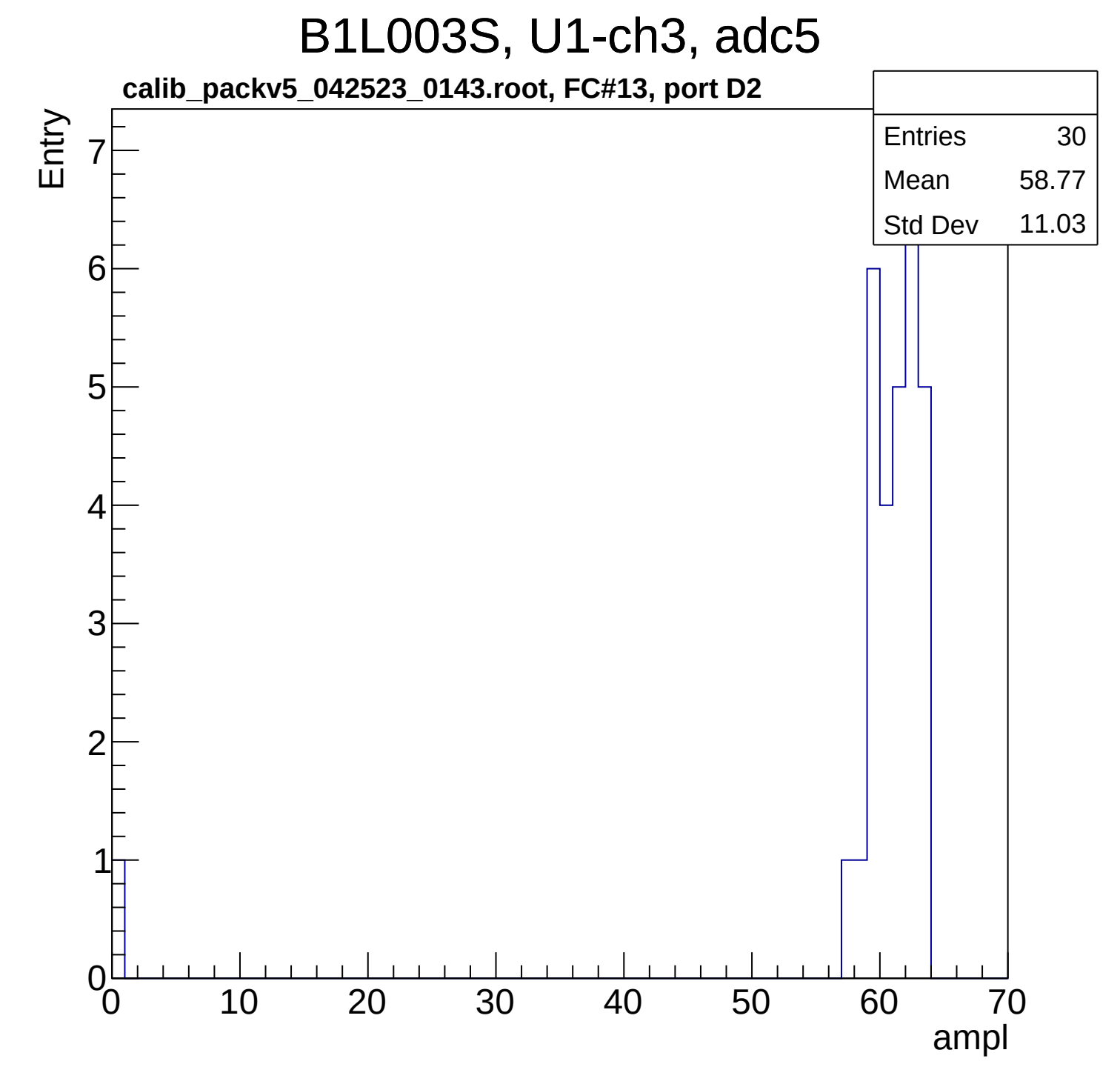
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch3, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 58.77
Std Dev 11.03
Entry
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2
3
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5
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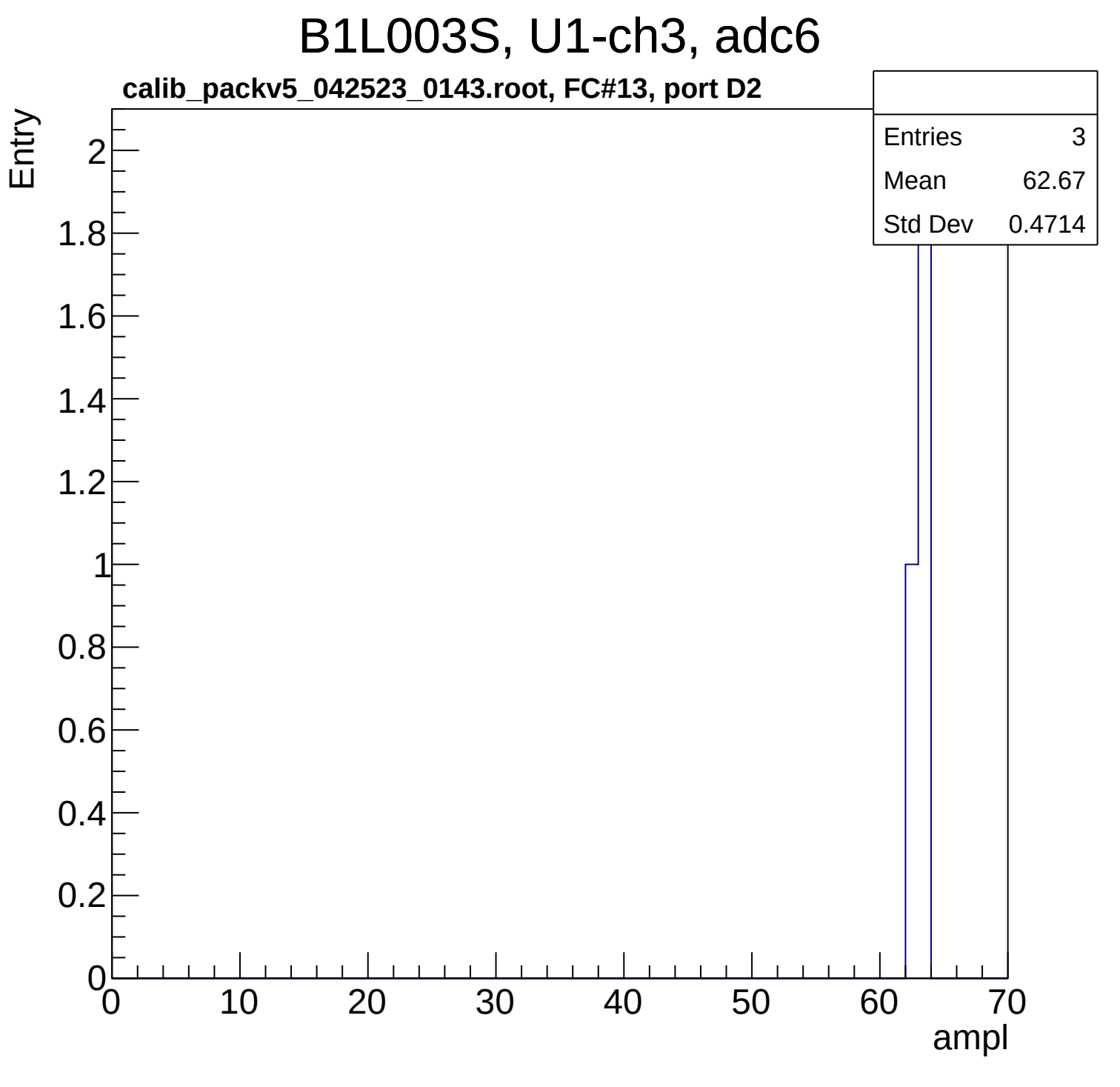
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch3, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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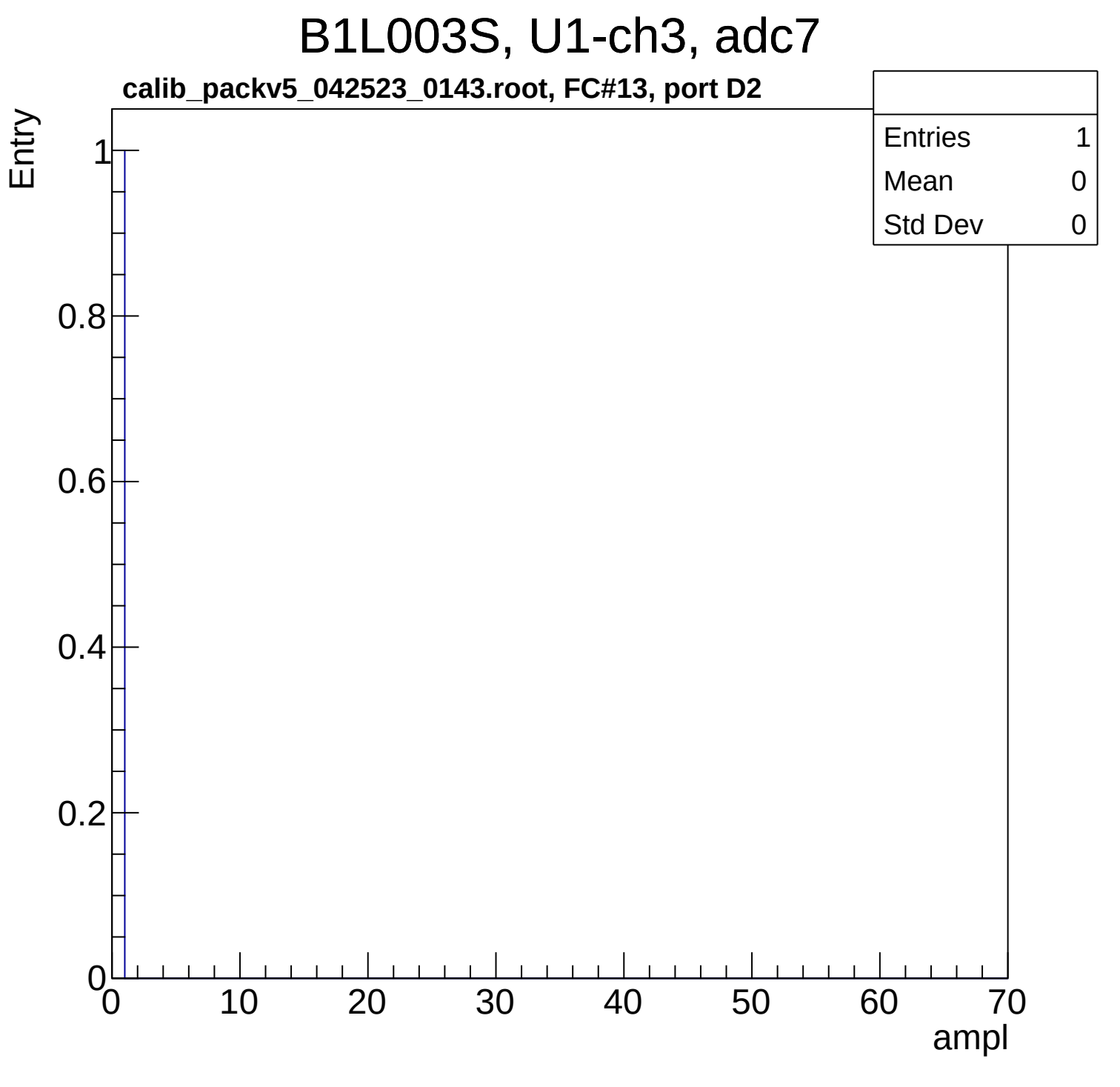
B1L003S, U1-ch3, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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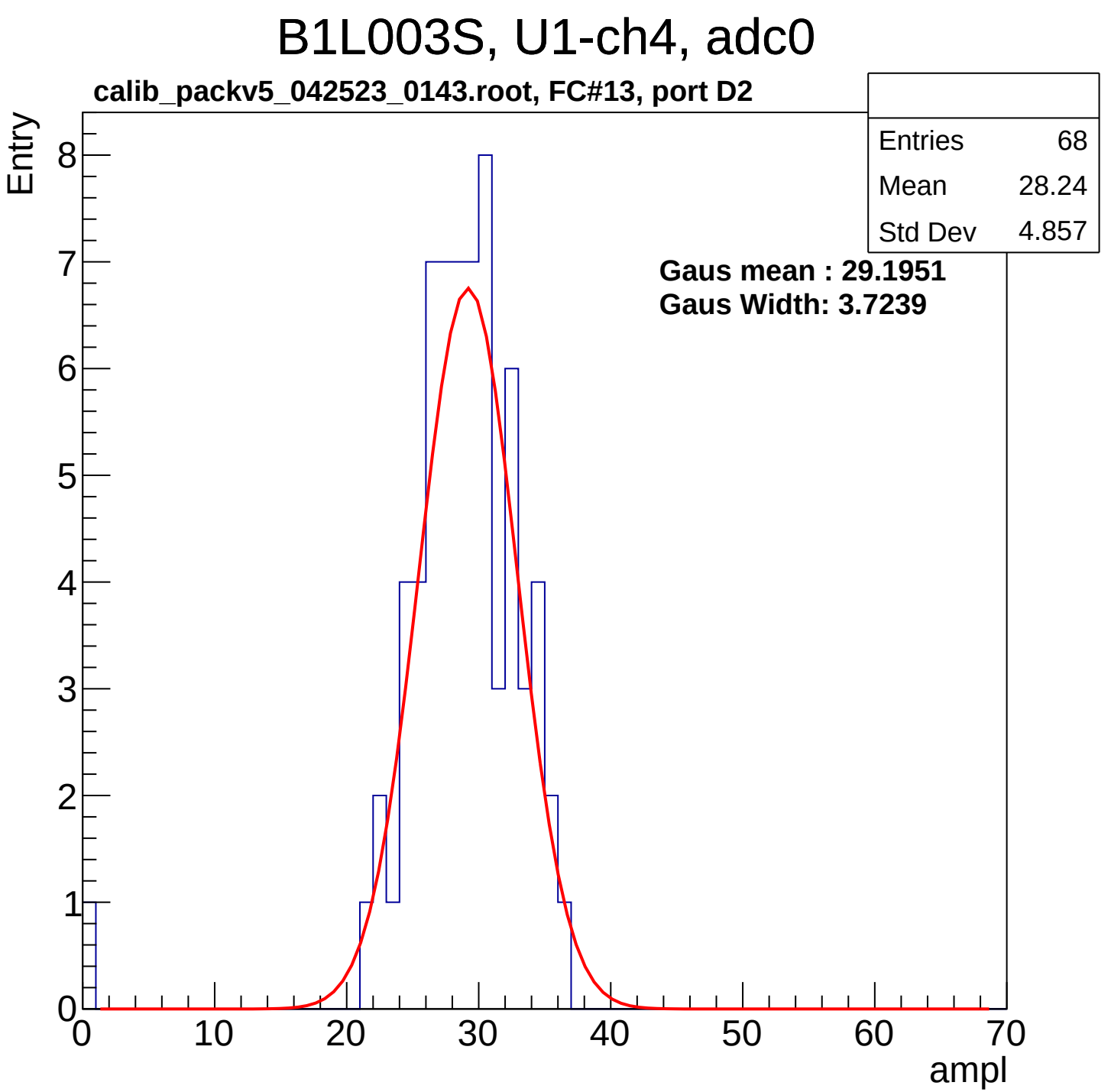
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch4, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 28.24
Std Dev 4.857
Gaus mean : 29.1951
Gaus Width: 3.7239
Entry
ampl
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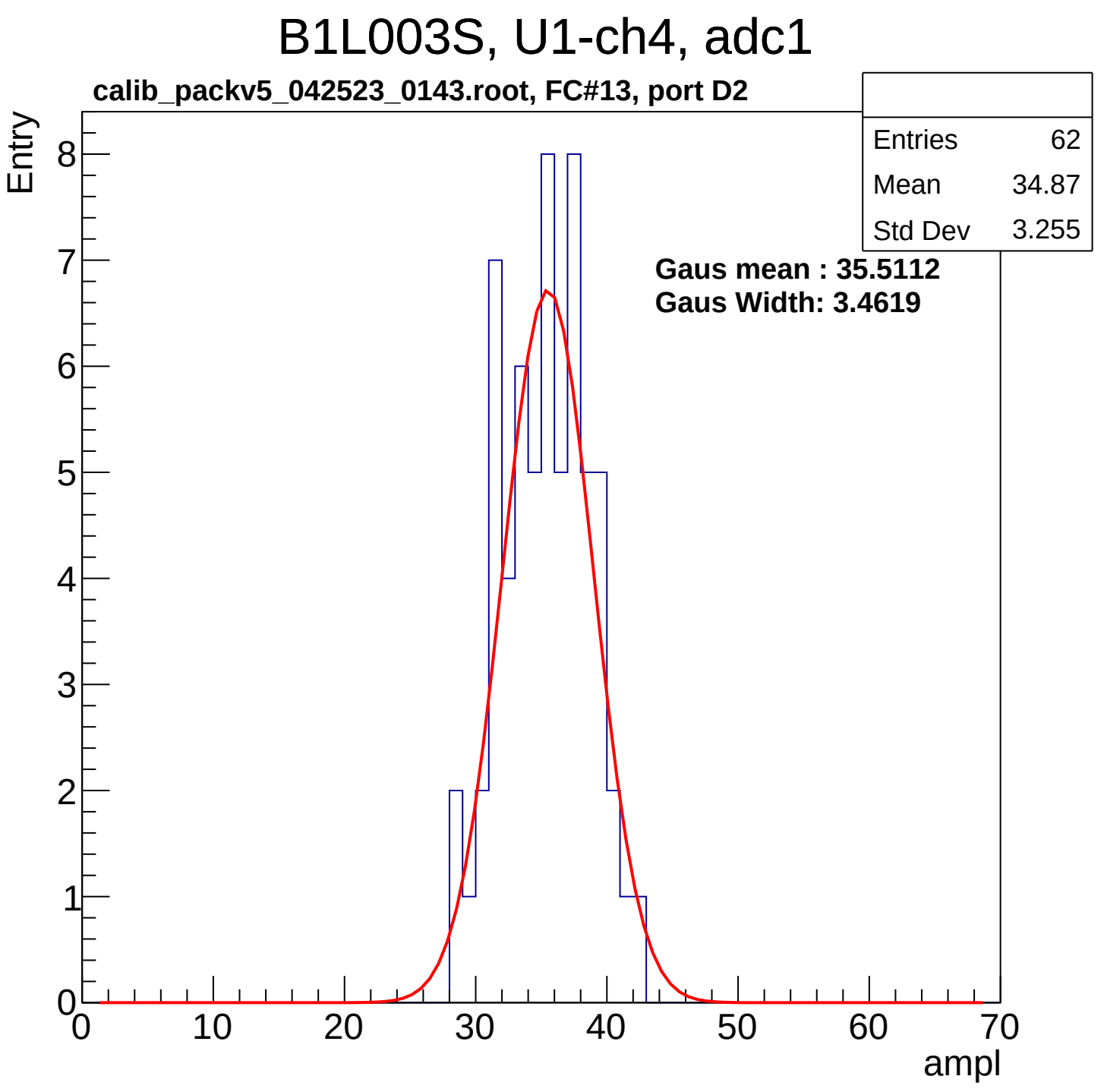
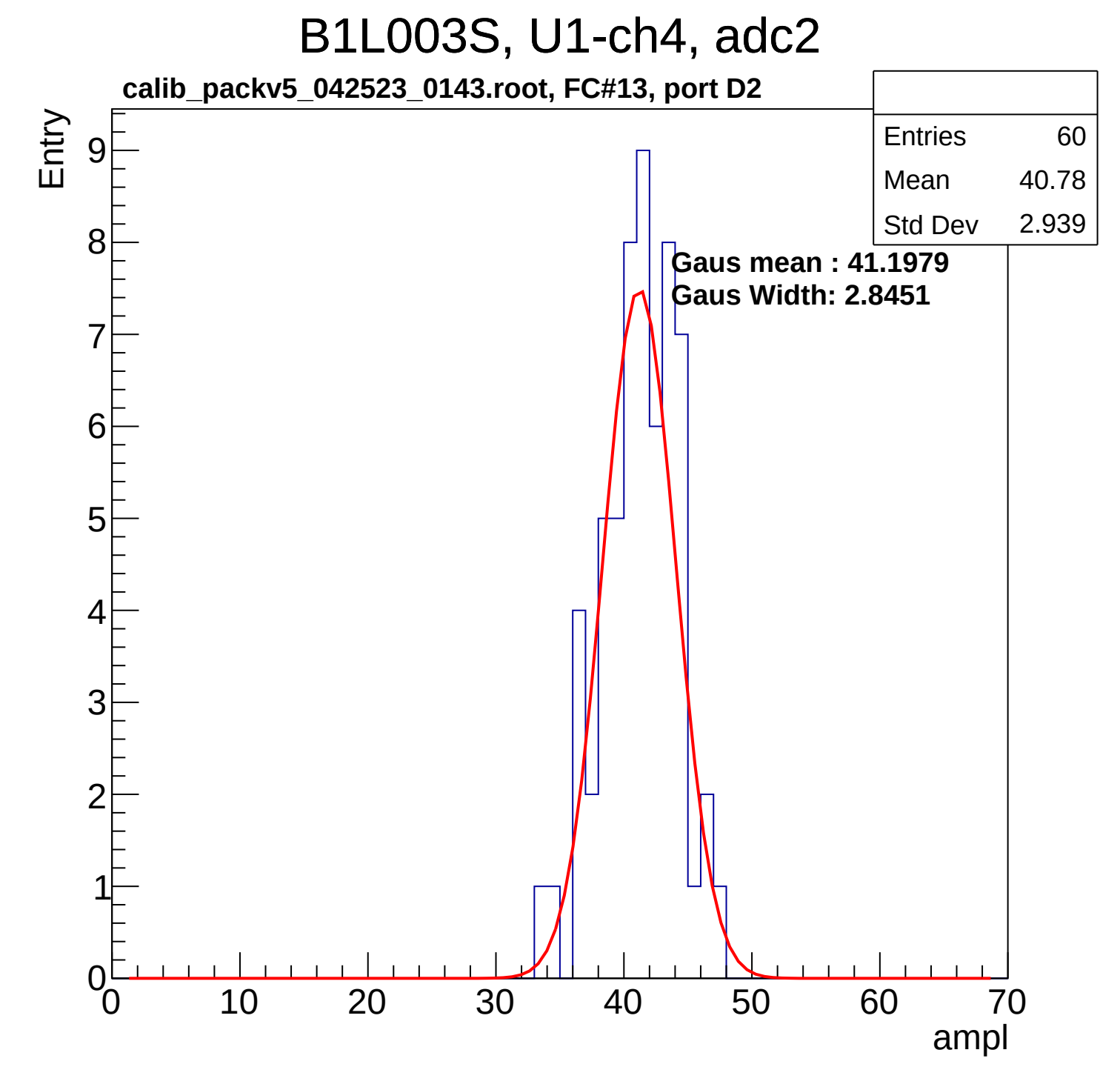
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch4, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 34.87
Std Dev 3.255
Gaus mean : 35.5112
Gaus Width: 3.4619
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch4, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 40.78
Std Dev 2.939
Gaus mean : 41.1979
Gaus Width: 2.8451
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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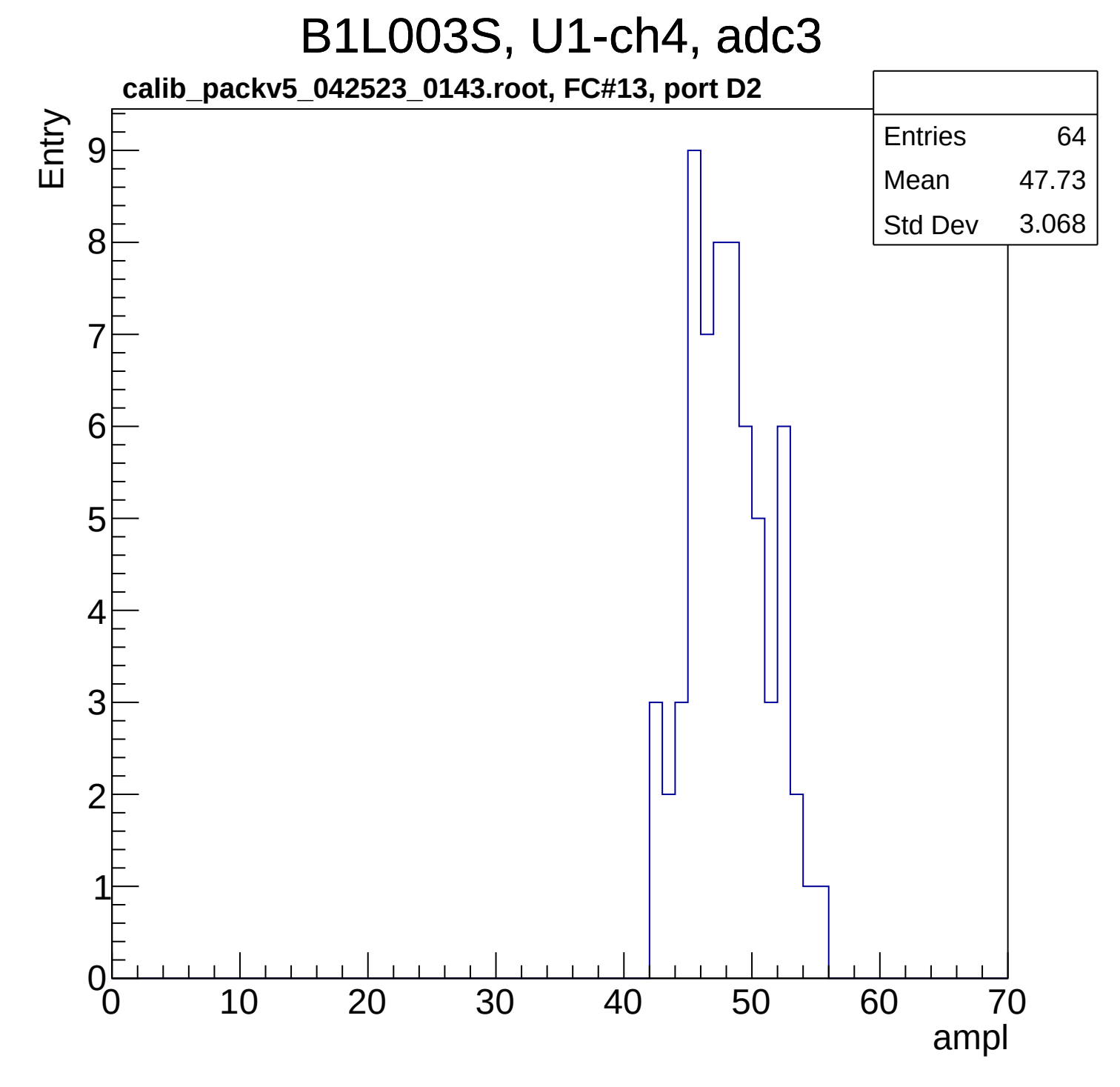
B1L003S, U1-ch4, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.73
Std Dev 3.068
Entry
ampl
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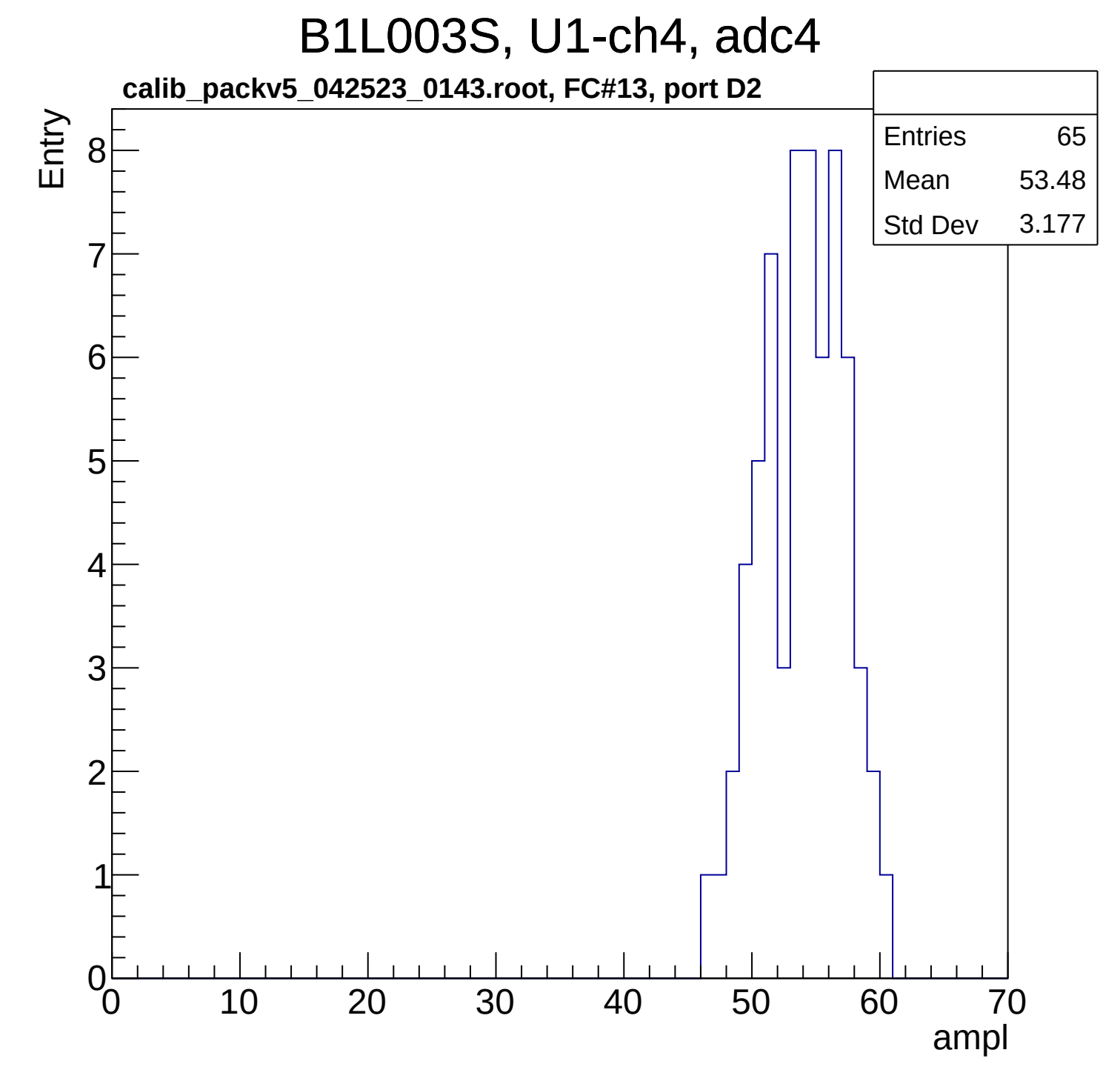
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch4, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.48
Std Dev 3.177
Entry
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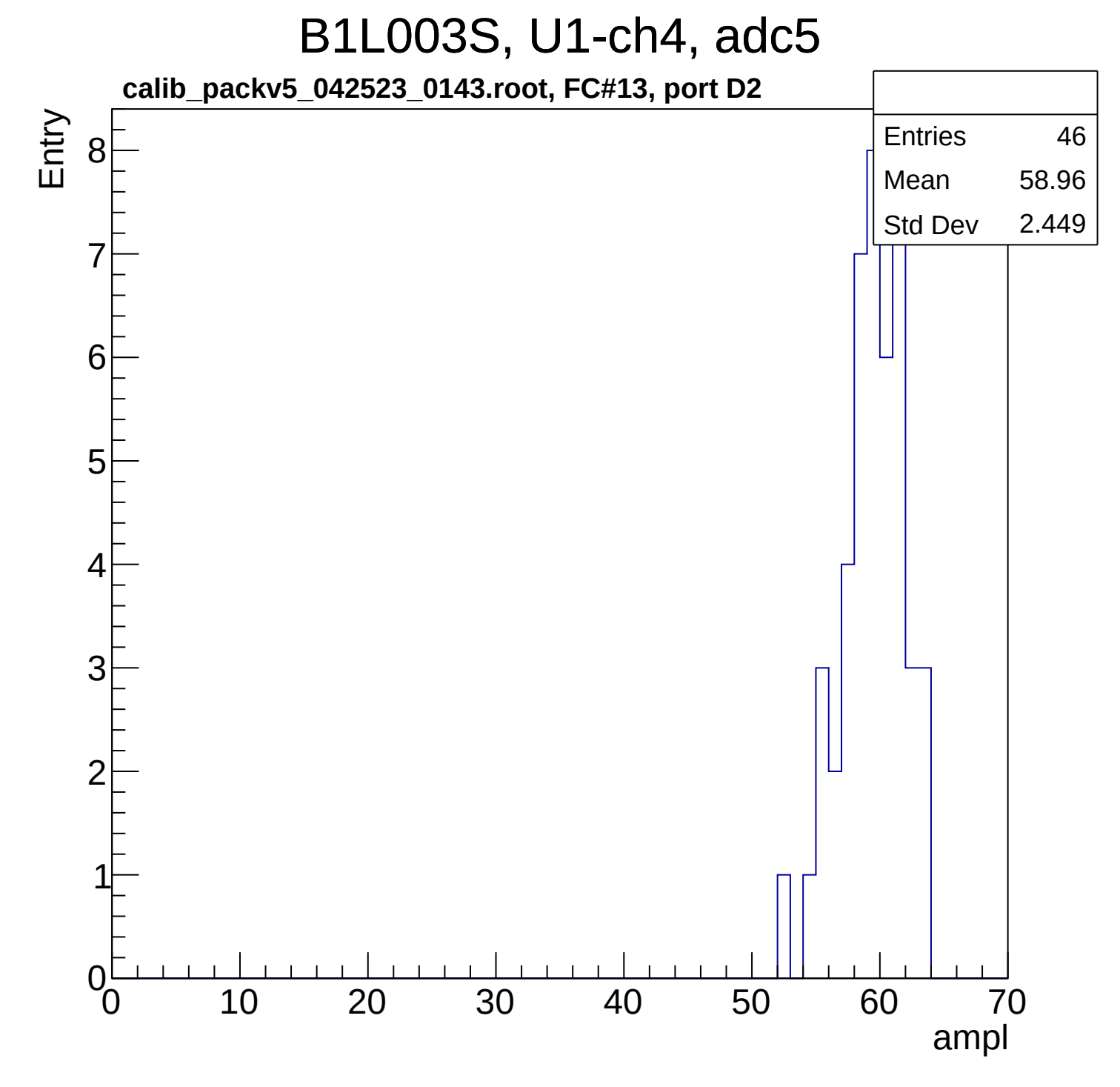
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch4, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.96
Std Dev 2.449
Entry
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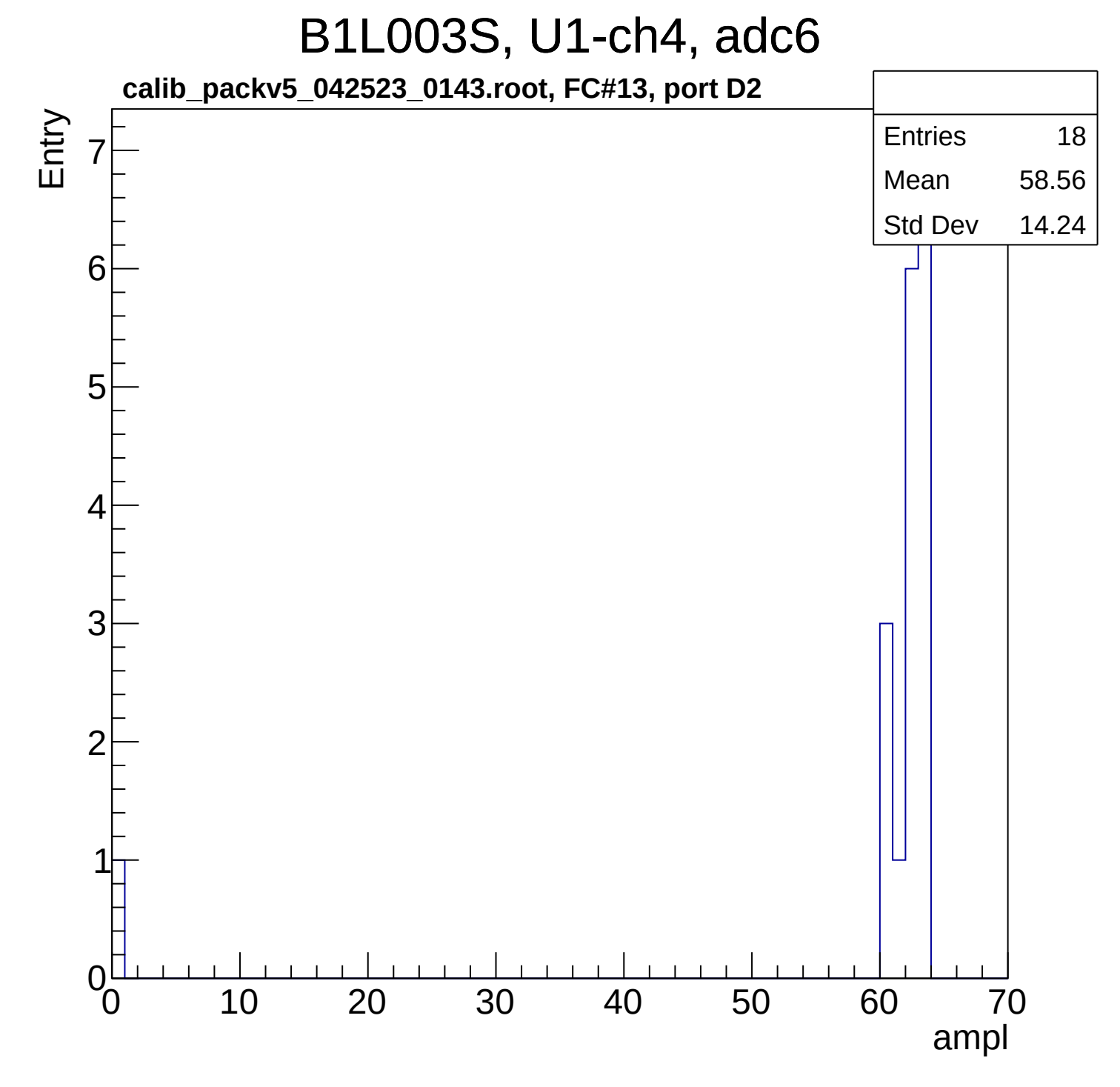
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch4, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 18
Mean 58.56
Std Dev 14.24
Entry
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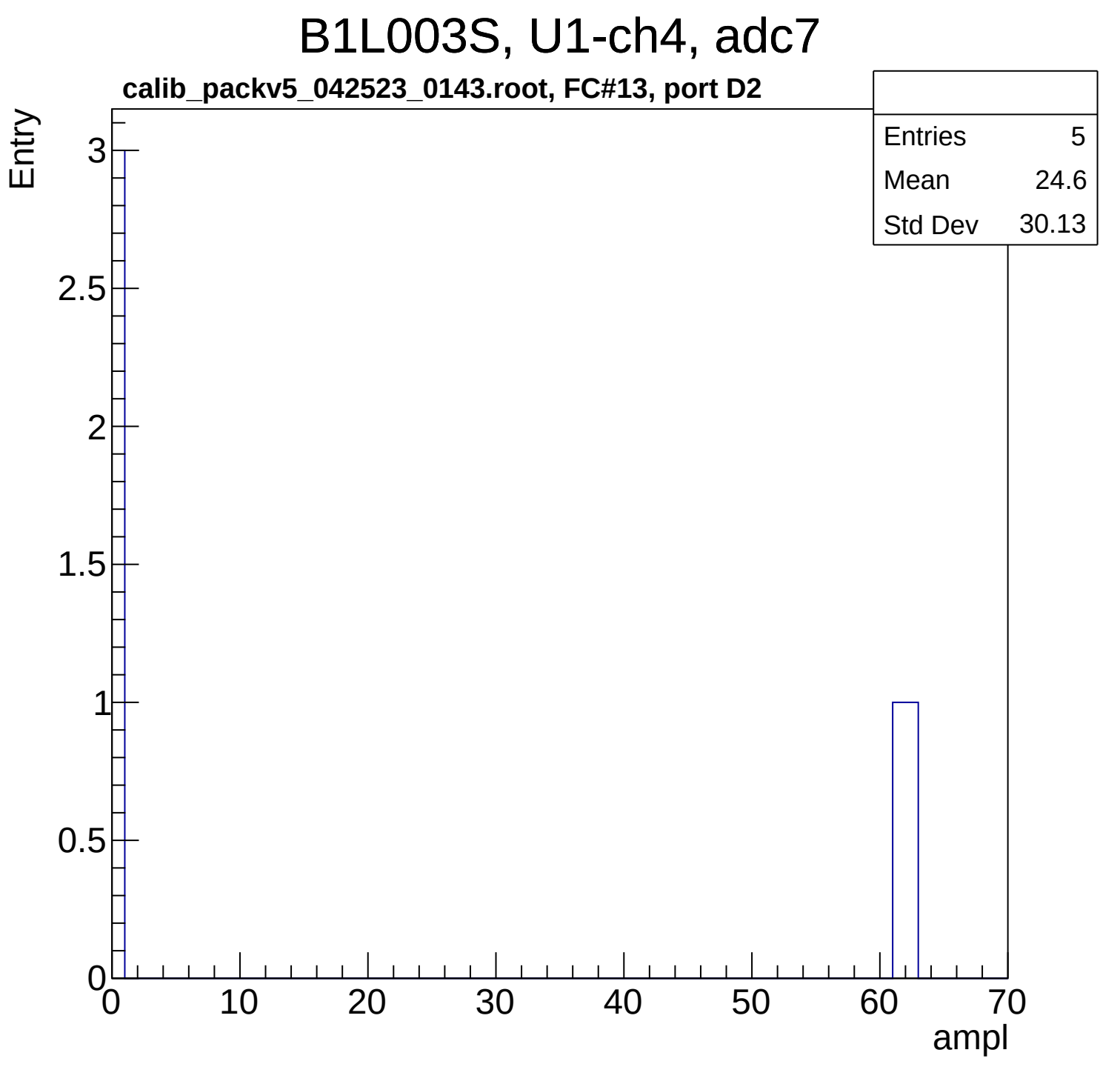
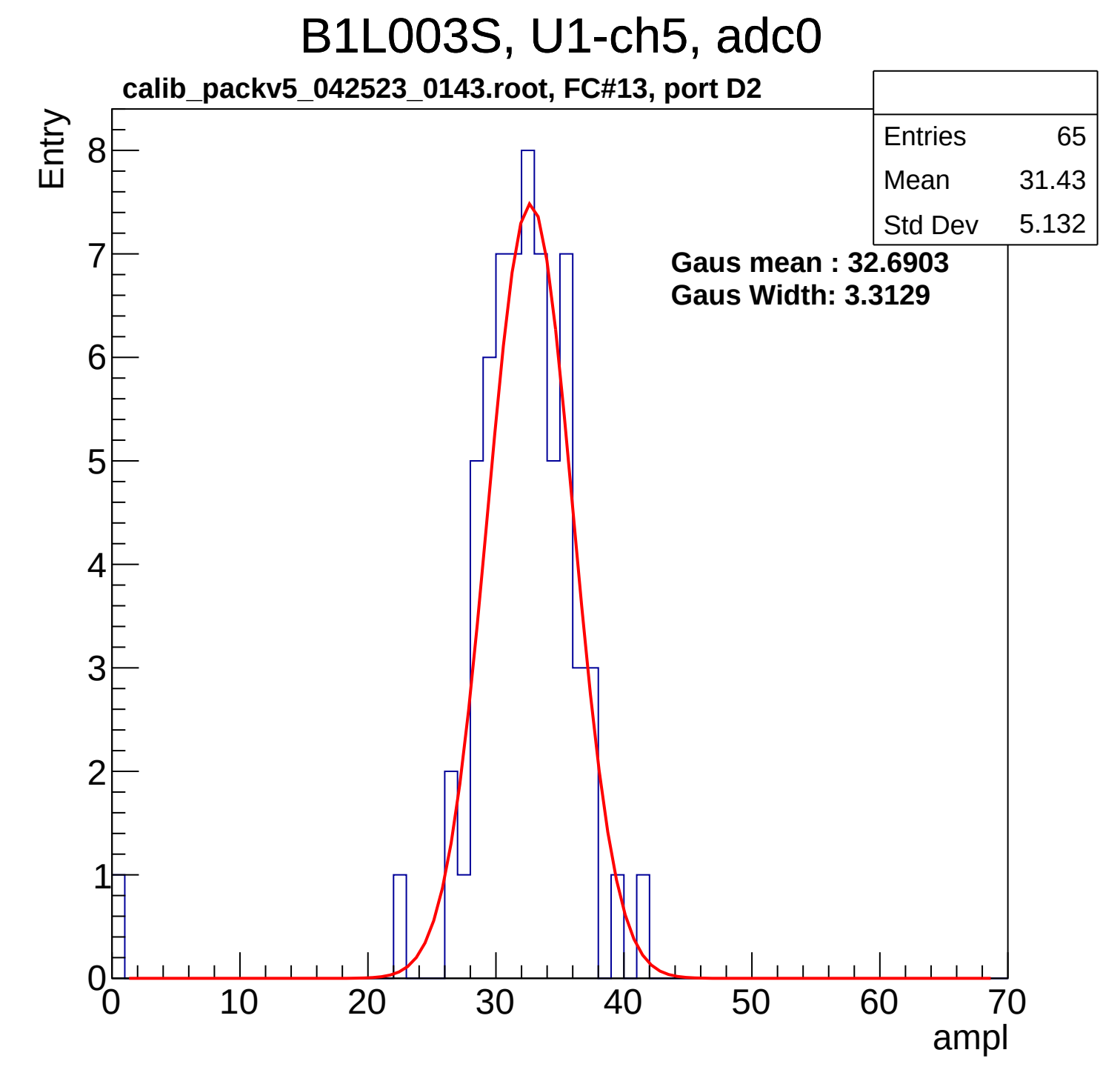
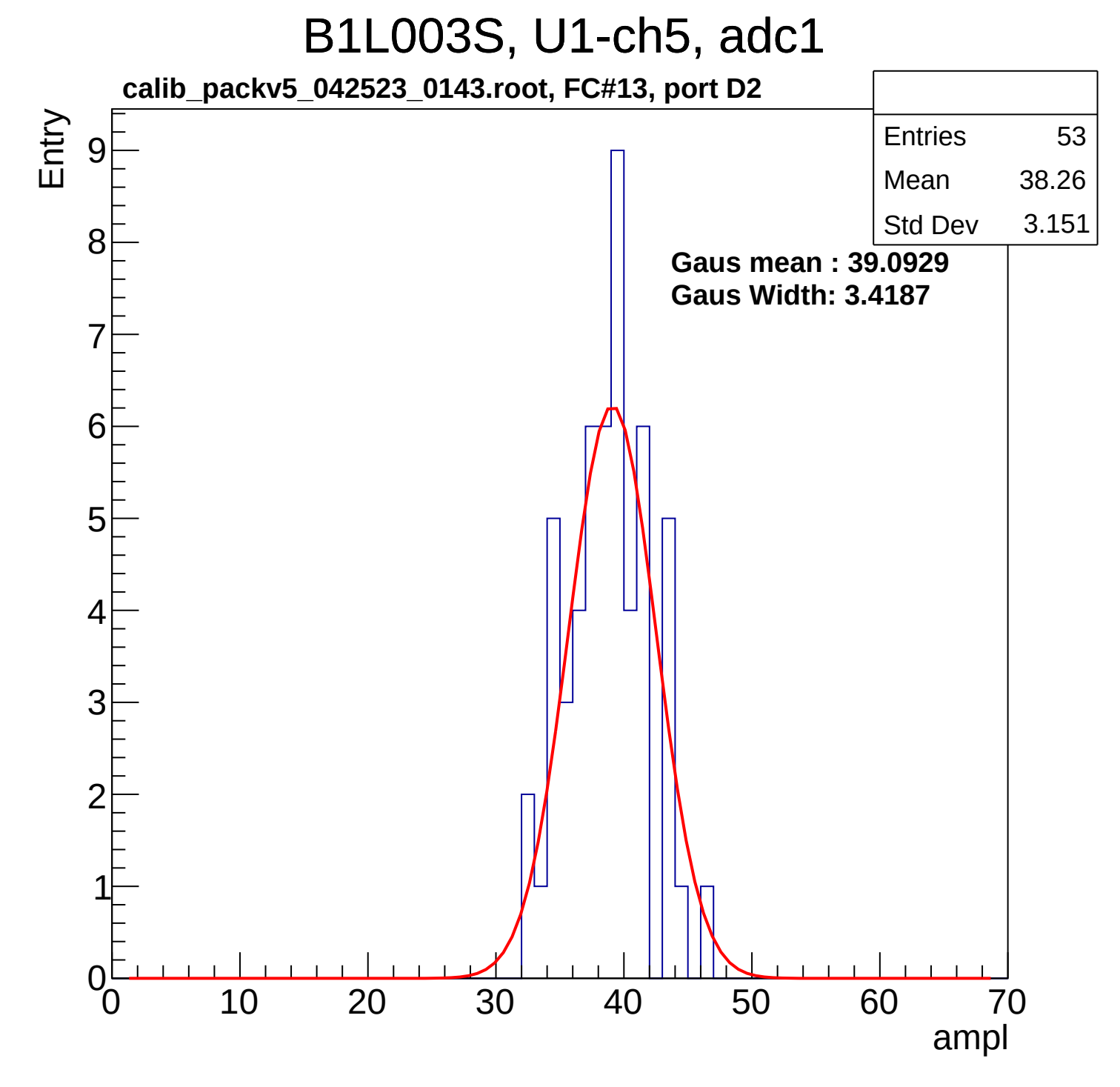
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch4, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 24.6
Std Dev 30.13
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 31.43
Std Dev 5.132
Gaus mean : 32.6903
Gaus Width: 3.3129
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 38.26
Std Dev 3.151
Gaus mean : 39.0929
Gaus Width: 3.4187
Entry
ampl
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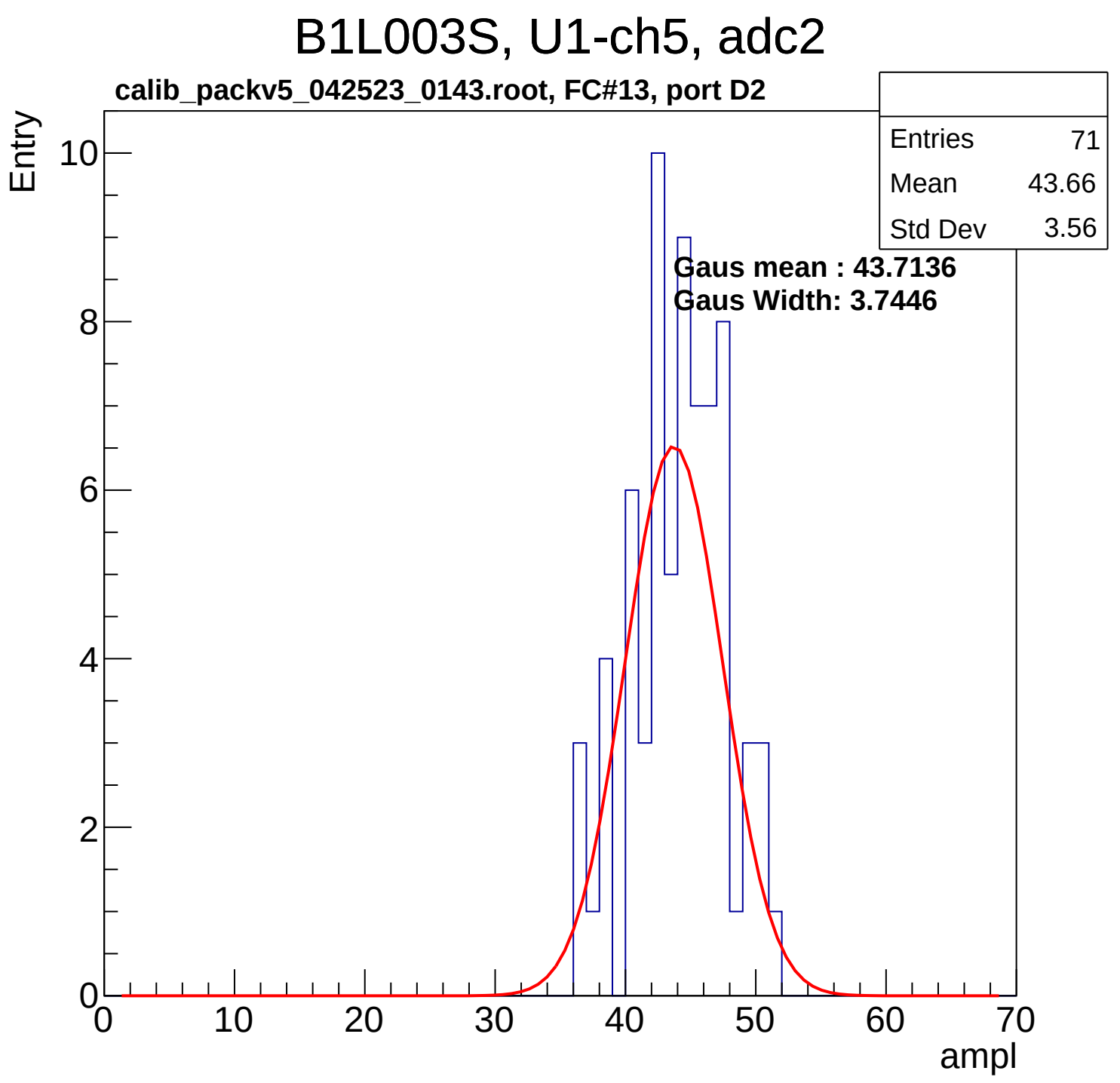
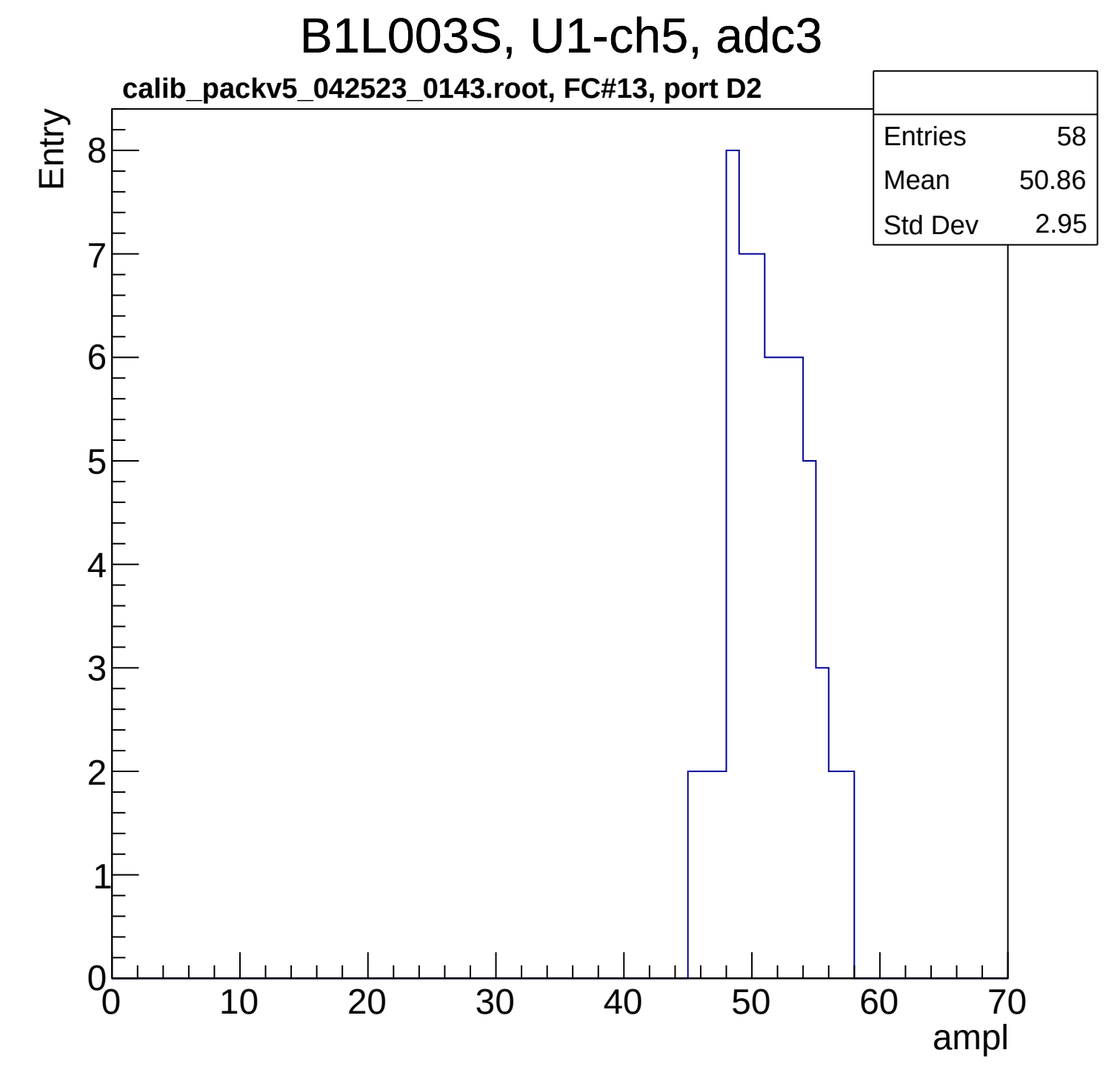
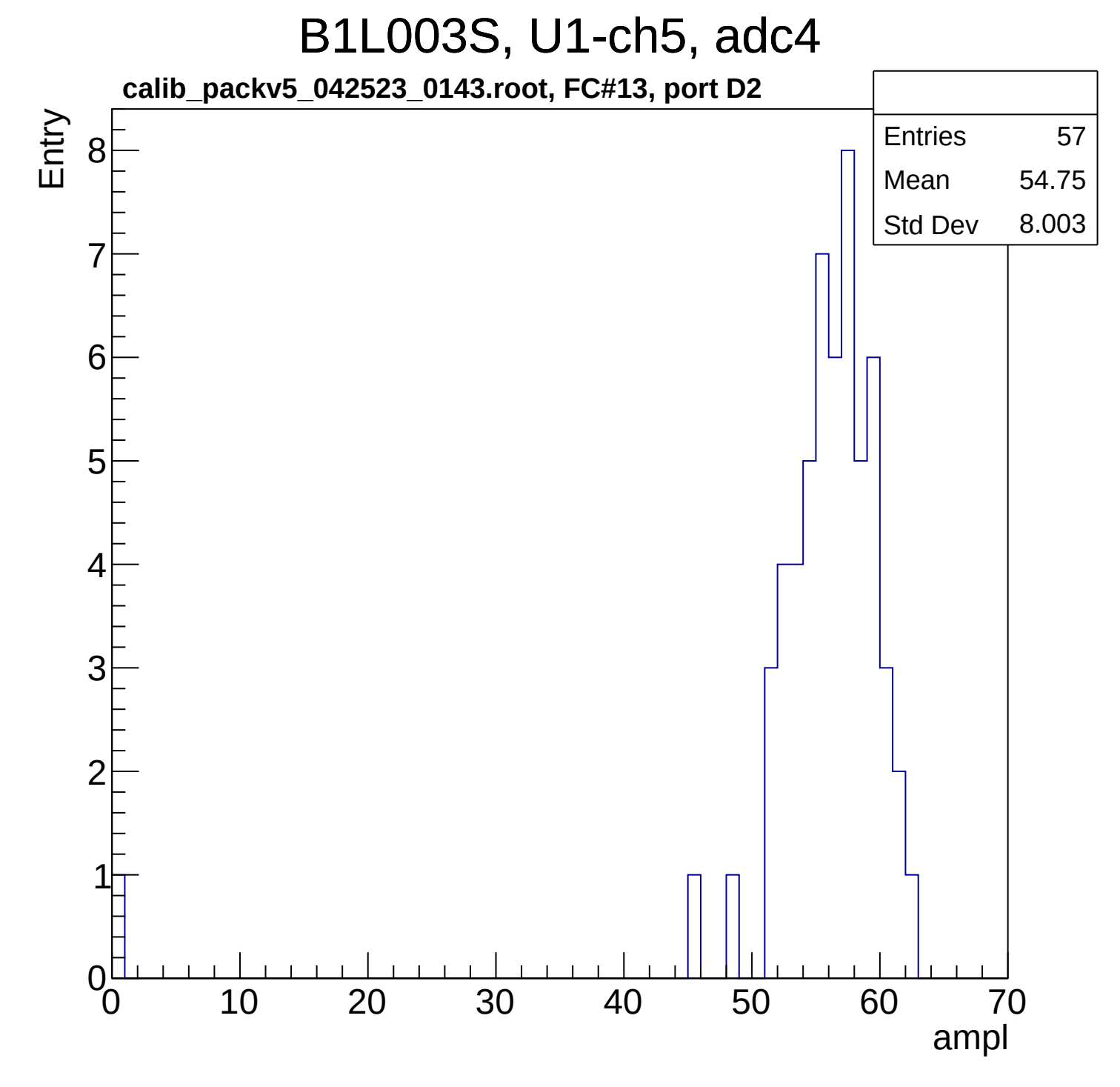
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 43.66
Std Dev 3.56
Gaus mean : 43.7136
Gaus Width: 3.7446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 50.86
Std Dev 2.95
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch5, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.75
Std Dev 8.003
Entry
ampl
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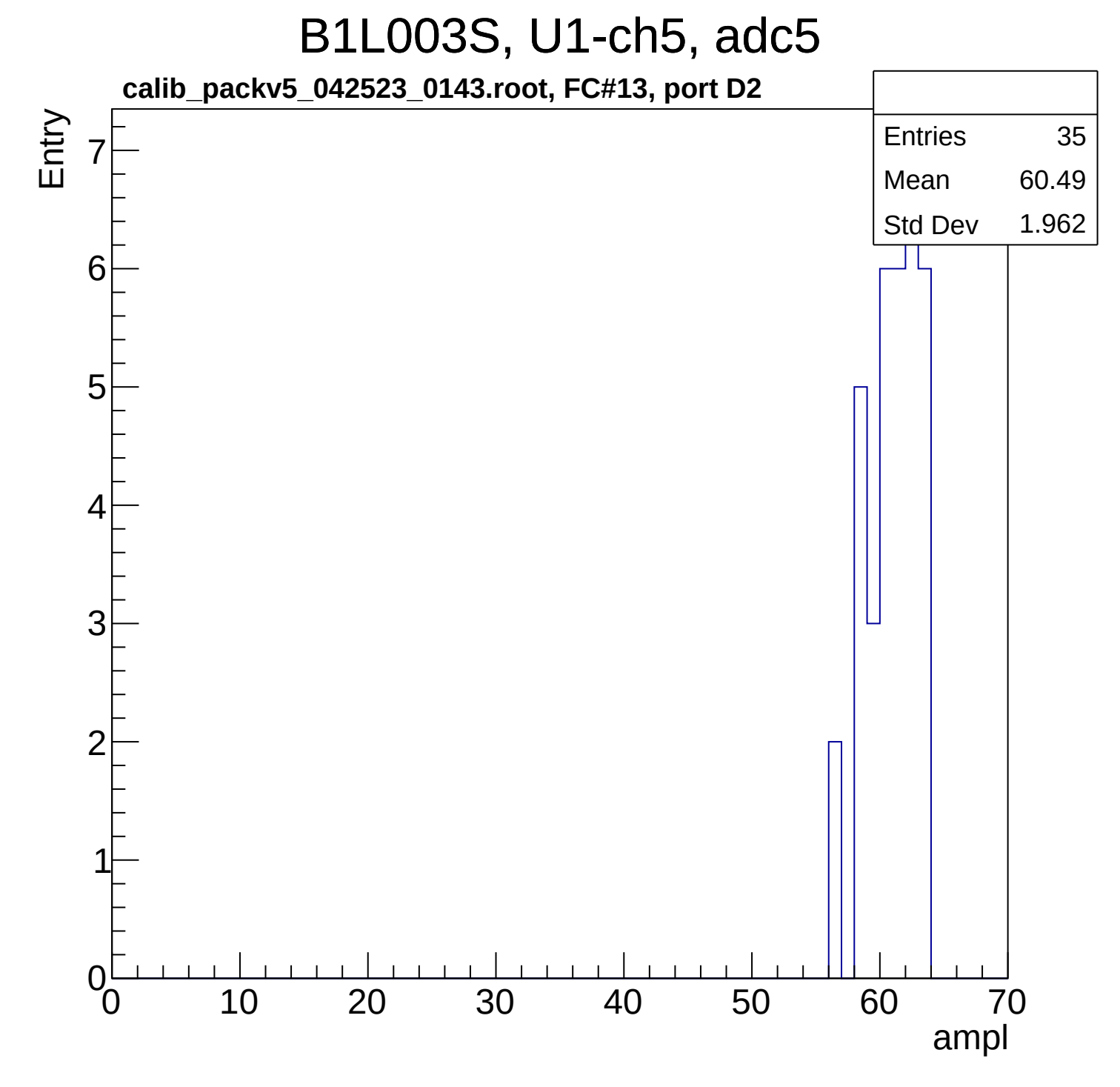
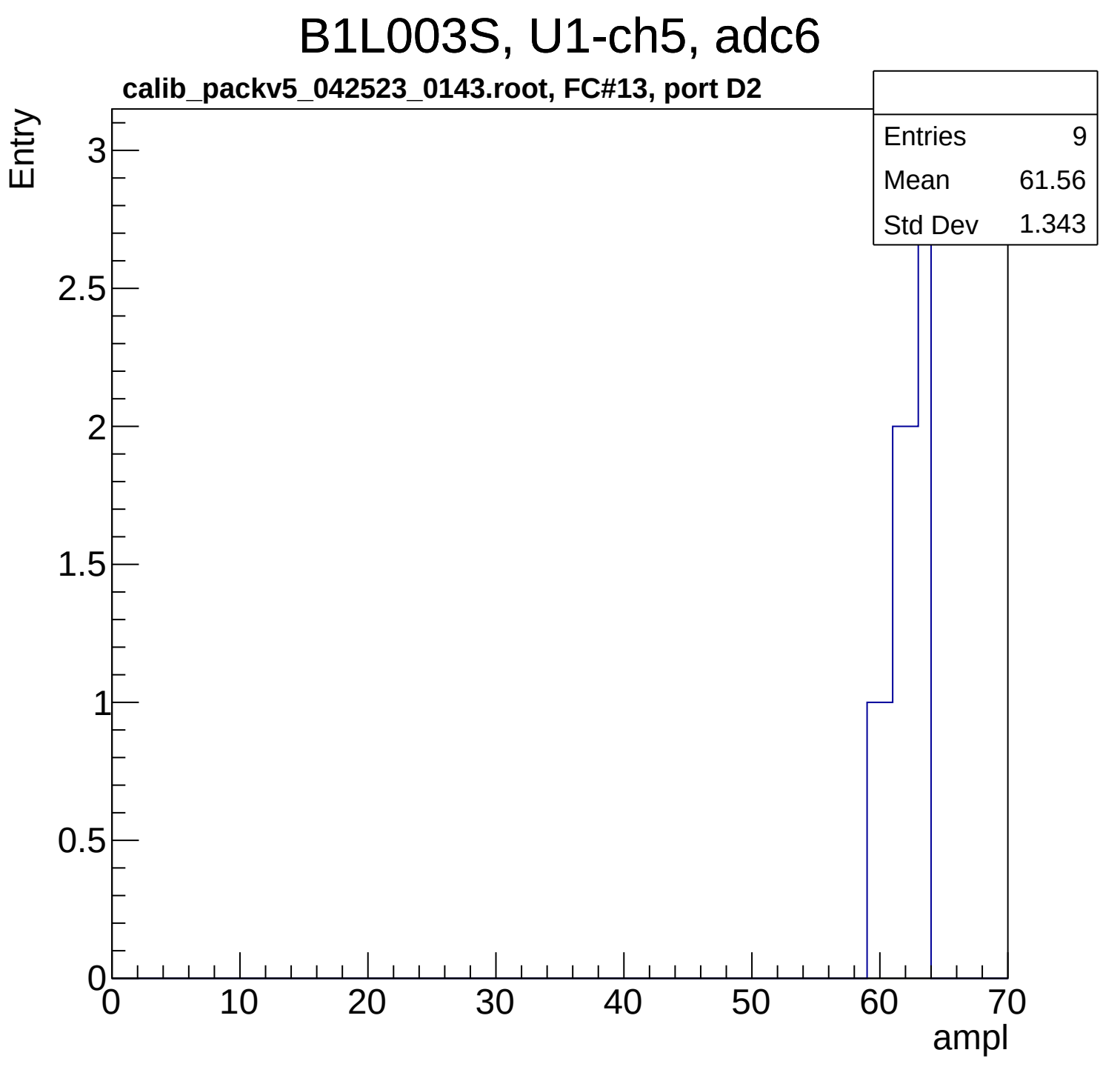
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 60.49
Std Dev 1.962
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch5, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.56
Std Dev 1.343
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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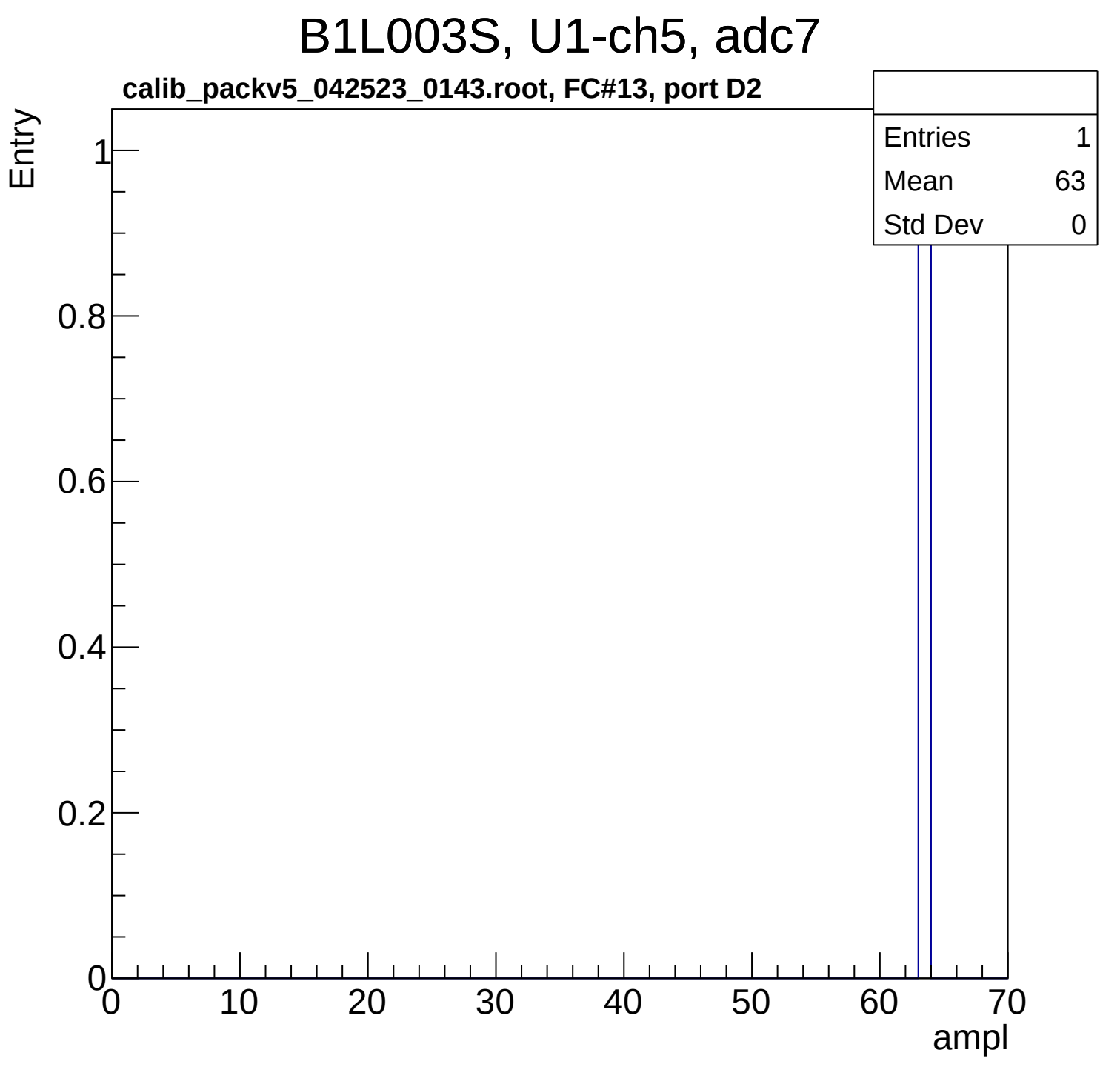
40
50
60
70


B1L003S, U1-ch5, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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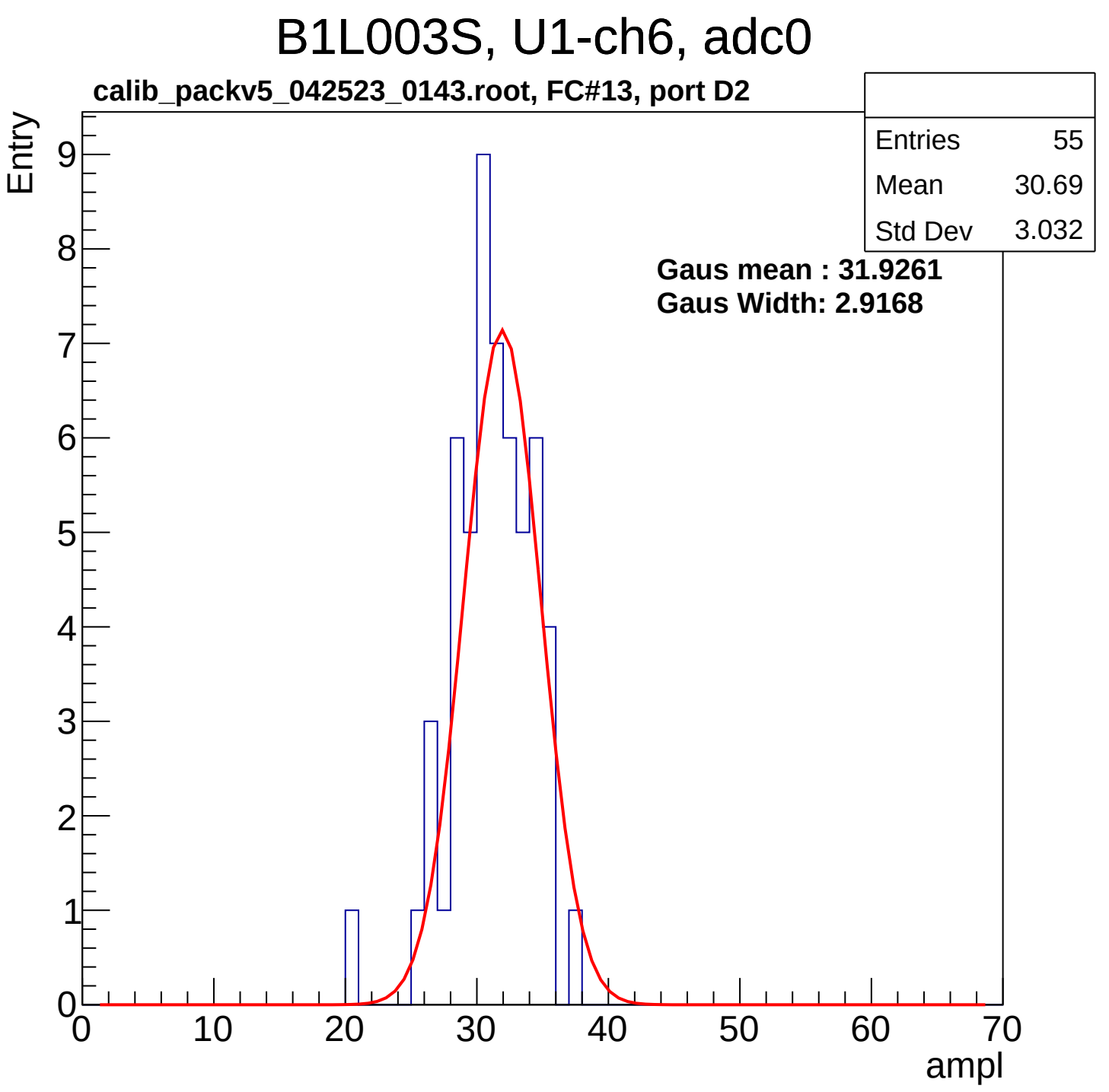
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch6, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 30.69
Std Dev 3.032
Gaus mean : 31.9261
Gaus Width: 2.9168
Entry
ampl
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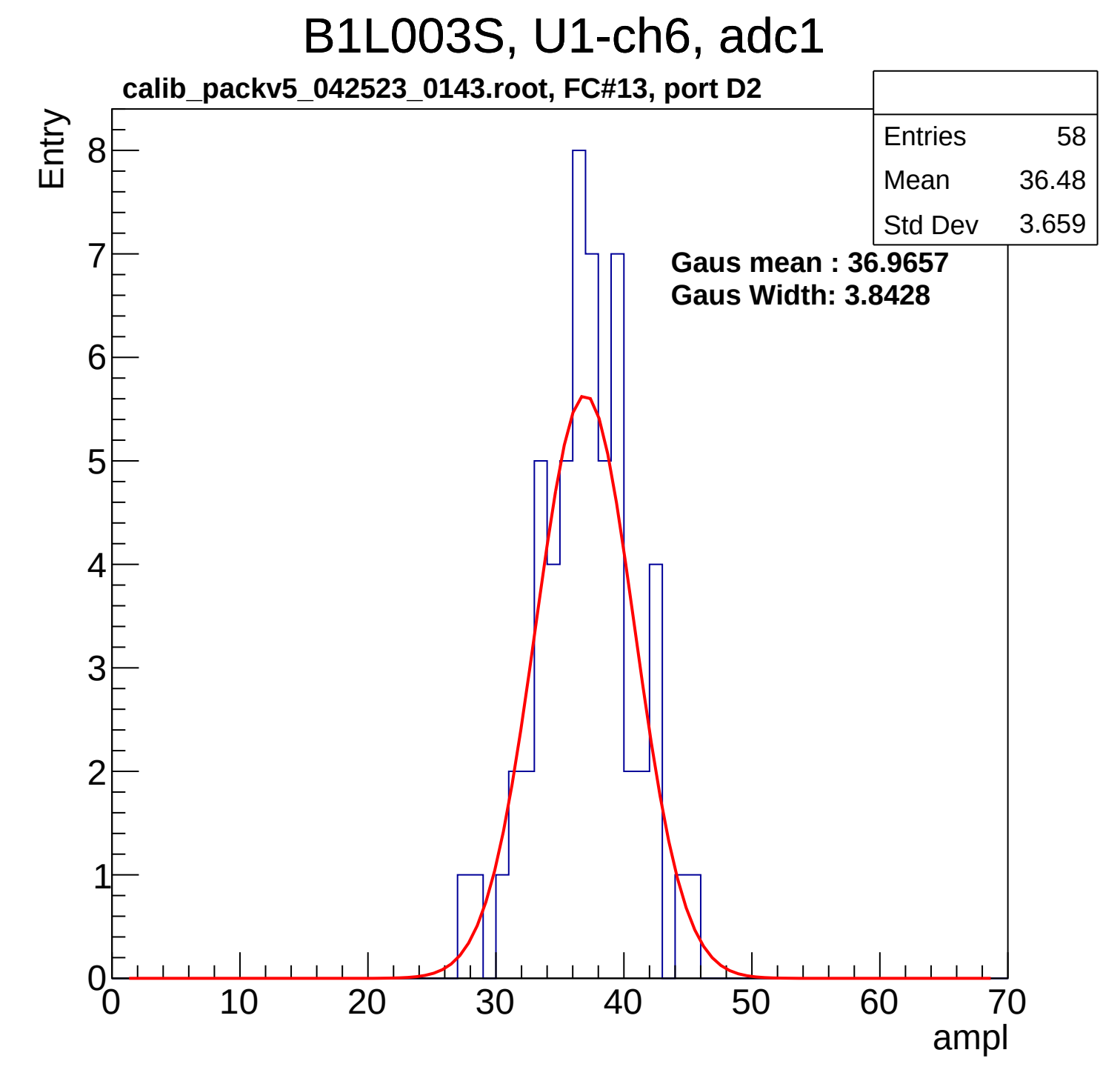
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch6, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 36.48
Std Dev 3.659
Gaus mean : 36.9657
Gaus Width: 3.8428
Entry
ampl
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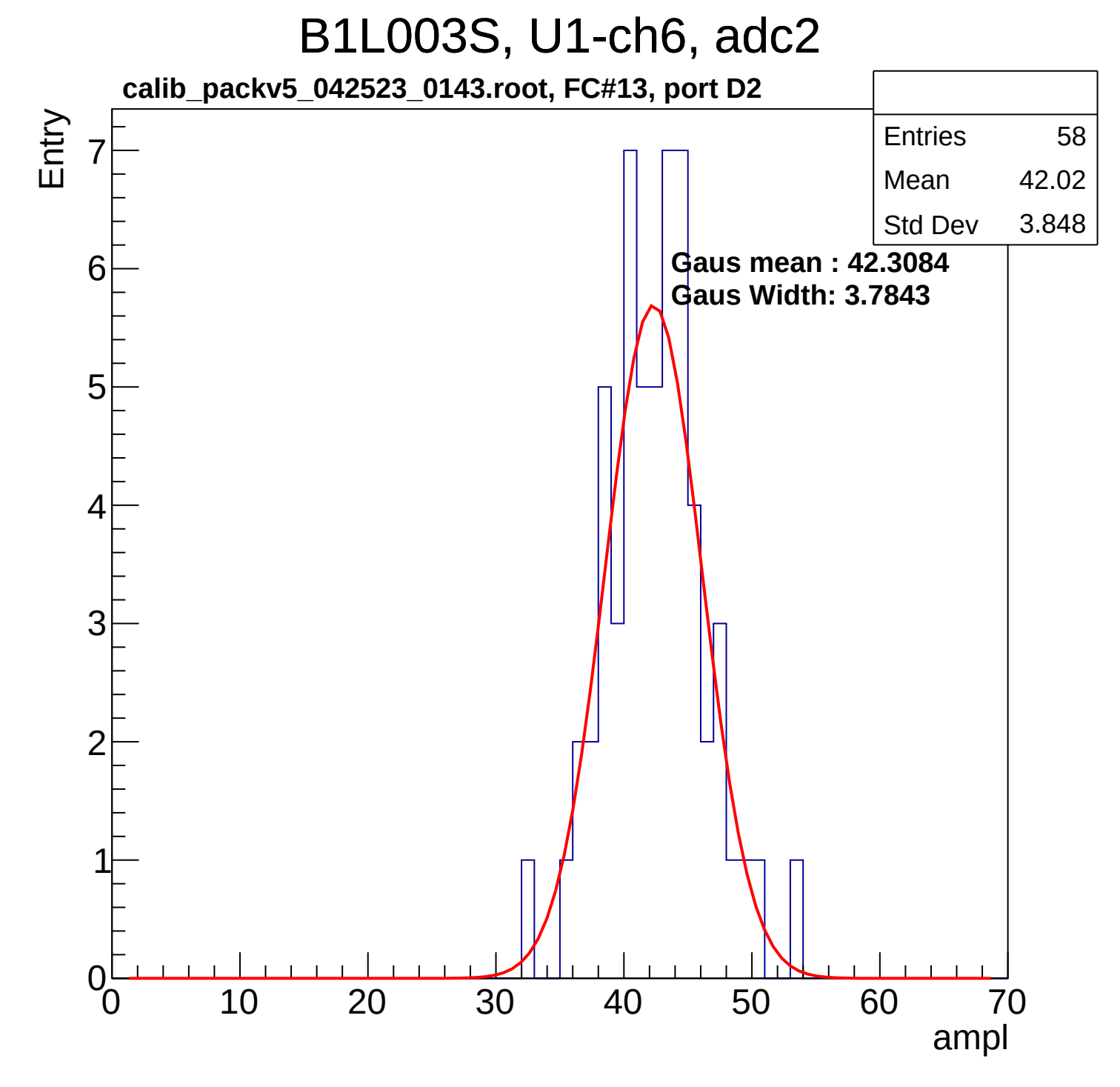
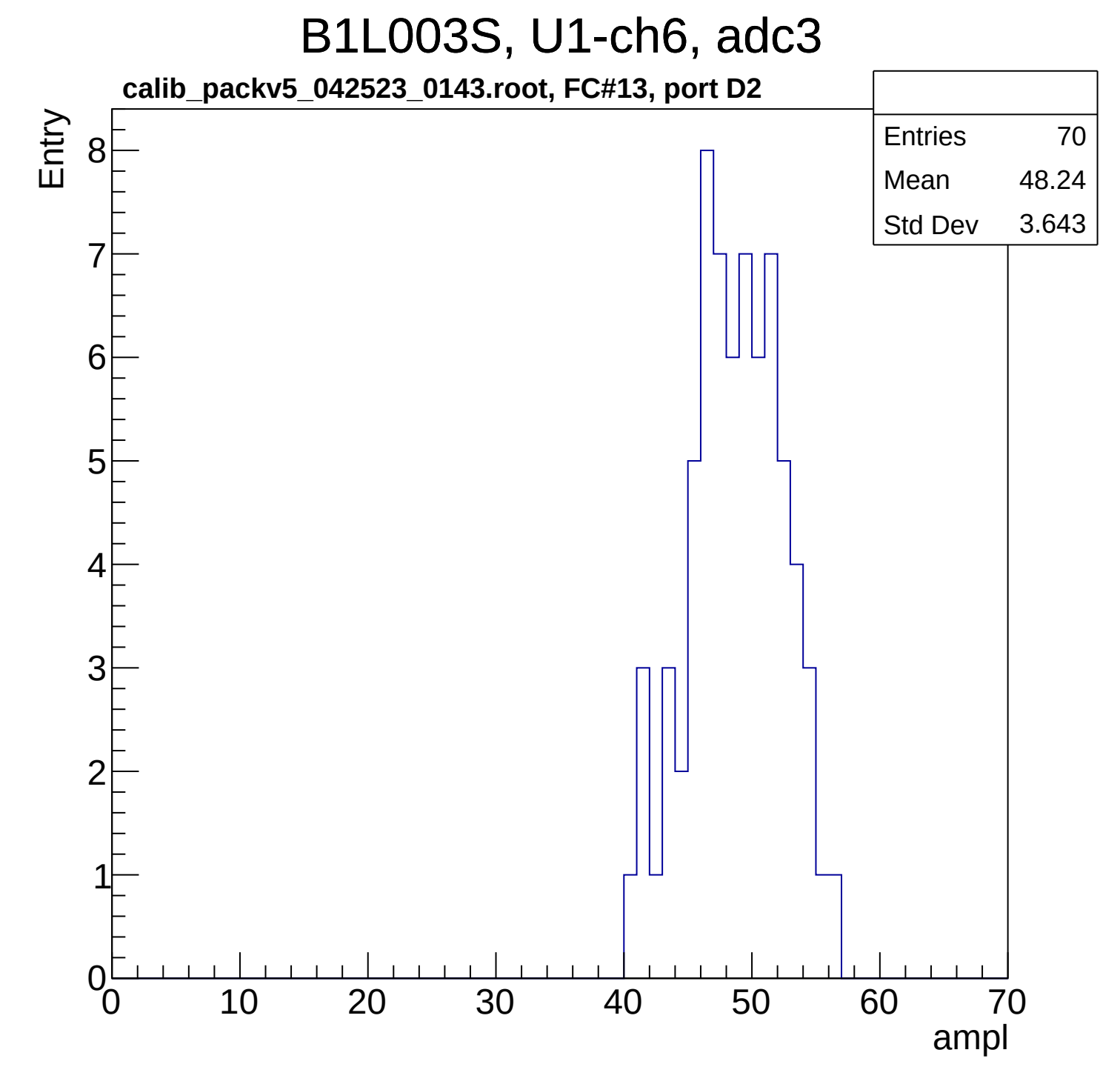
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch6, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.02
Std Dev 3.848
Gaus mean : 42.3084
Gaus Width: 3.7843
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch6, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 48.24
Std Dev 3.643
Entry
ampl
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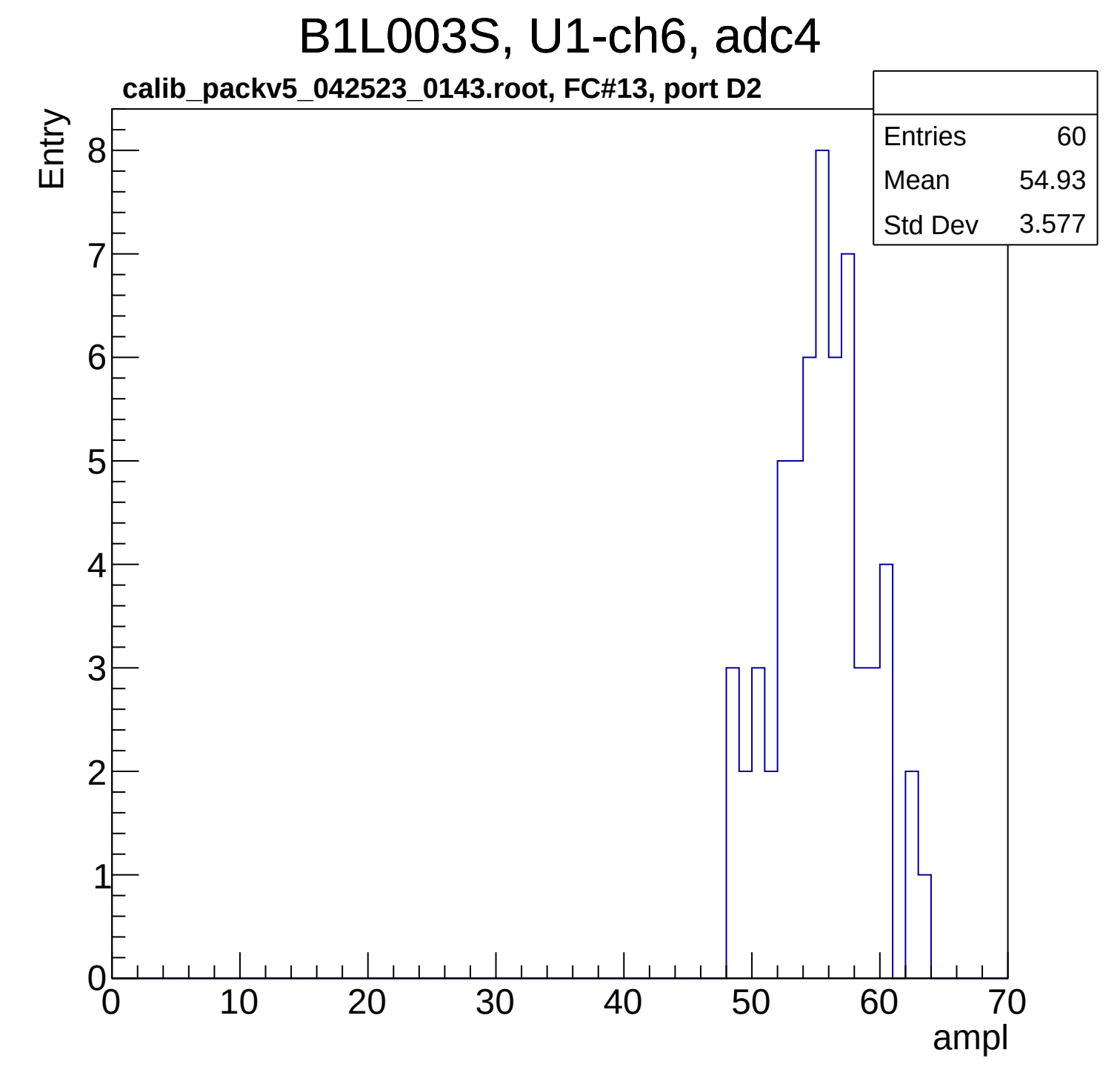
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch6, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.93
Std Dev 3.577
Entry
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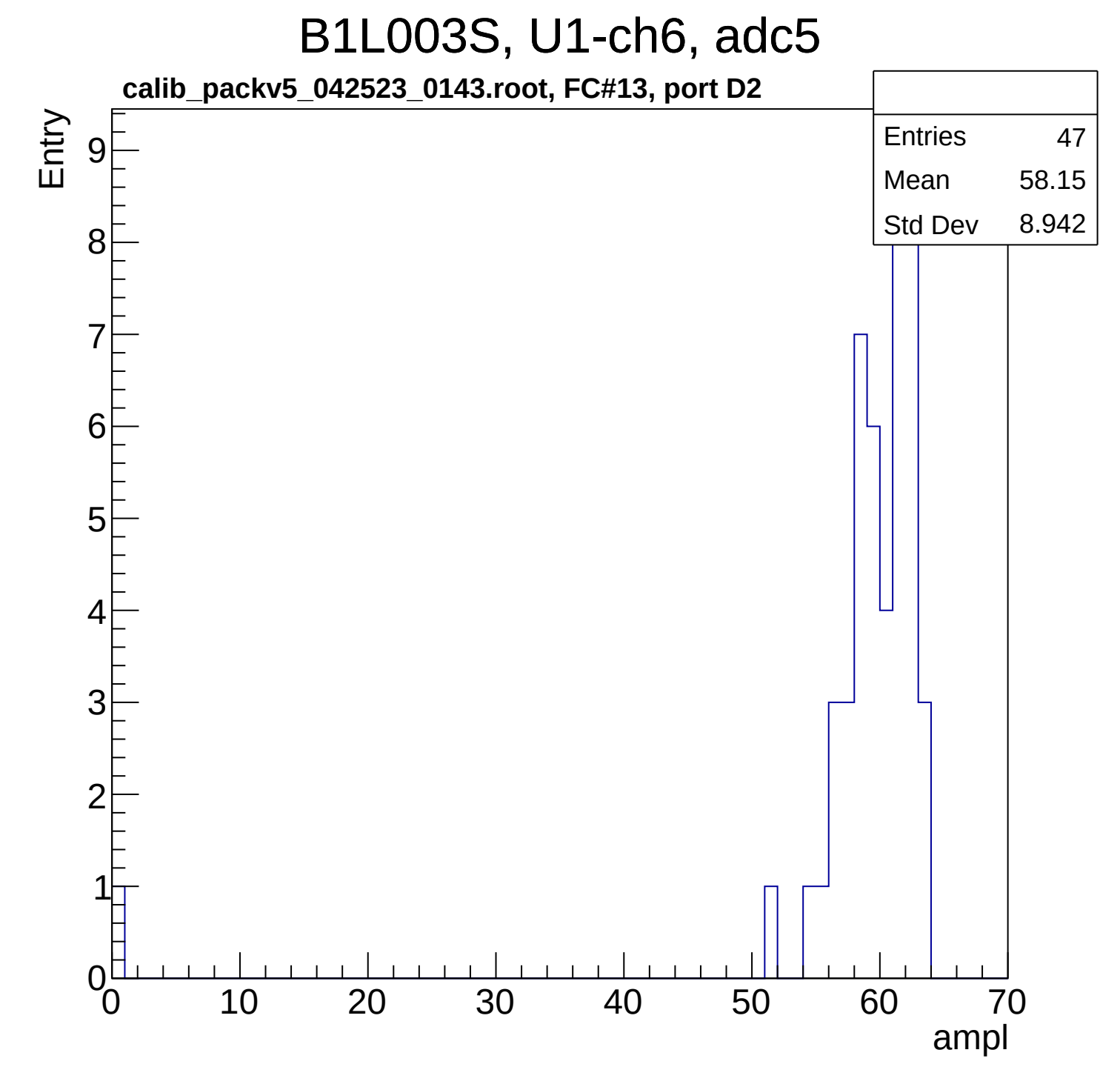
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch6, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.15
Std Dev 8.942
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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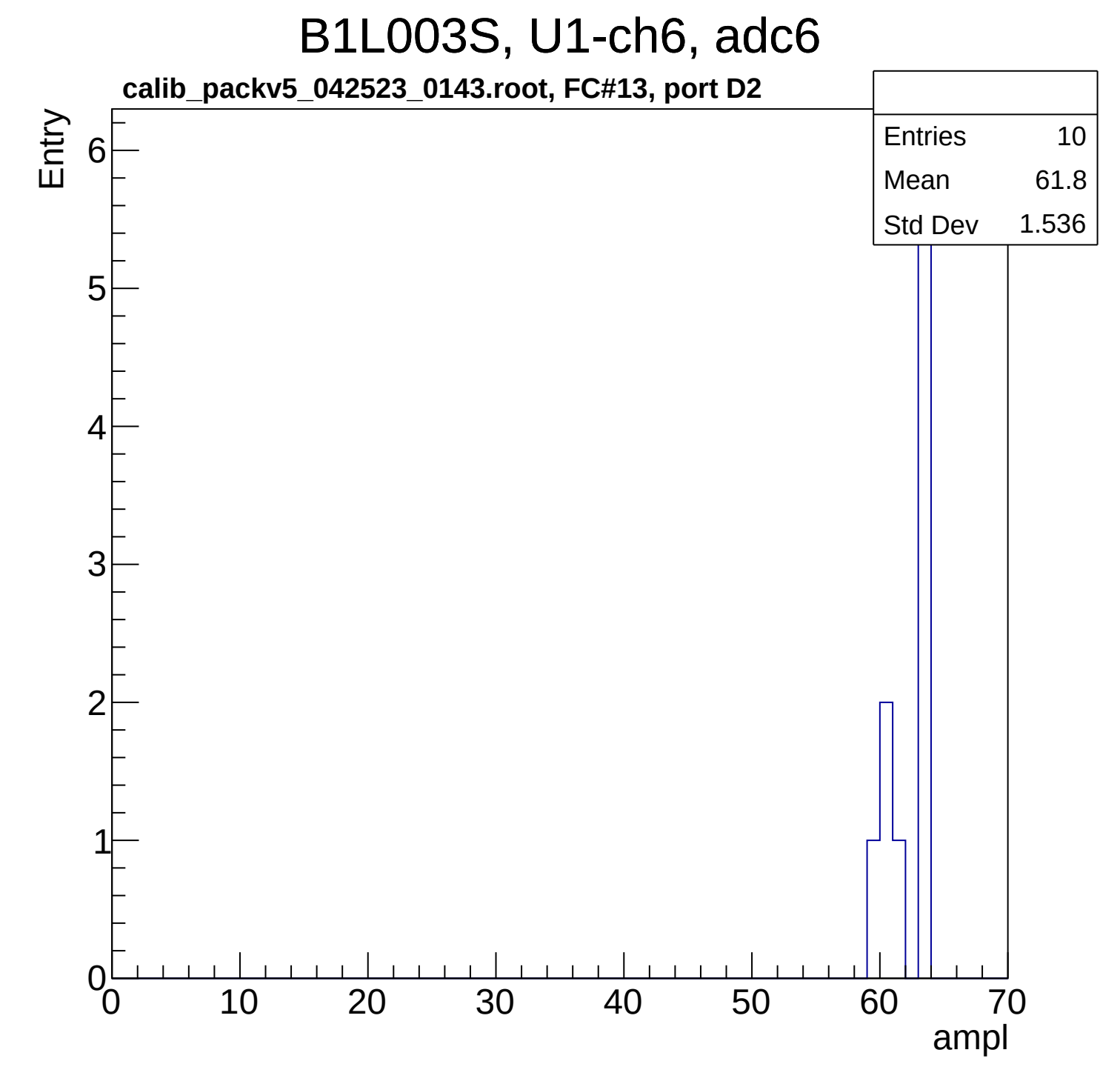
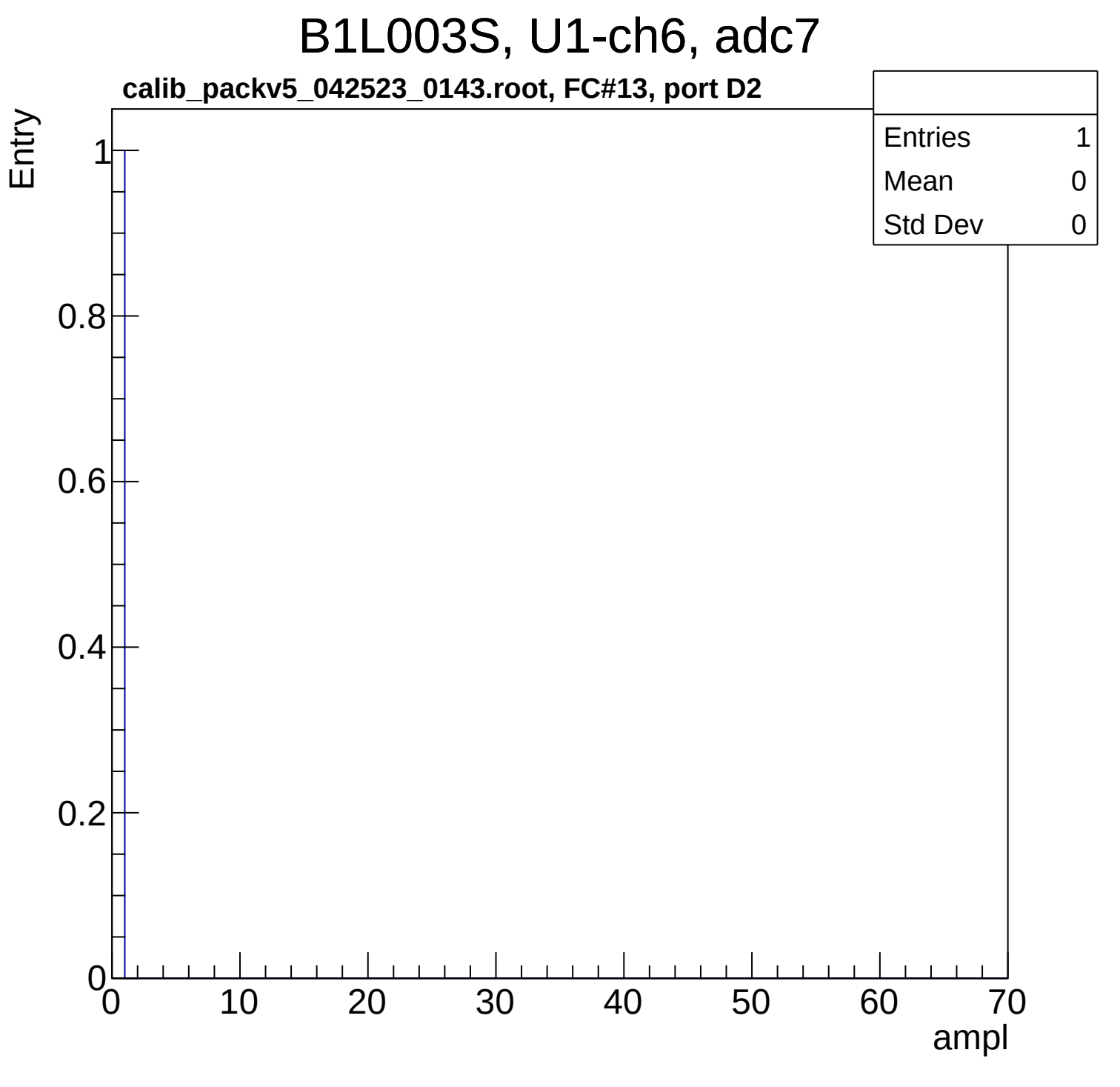
40
50
60
70


B1L003S, U1-ch6, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 1.536
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch6, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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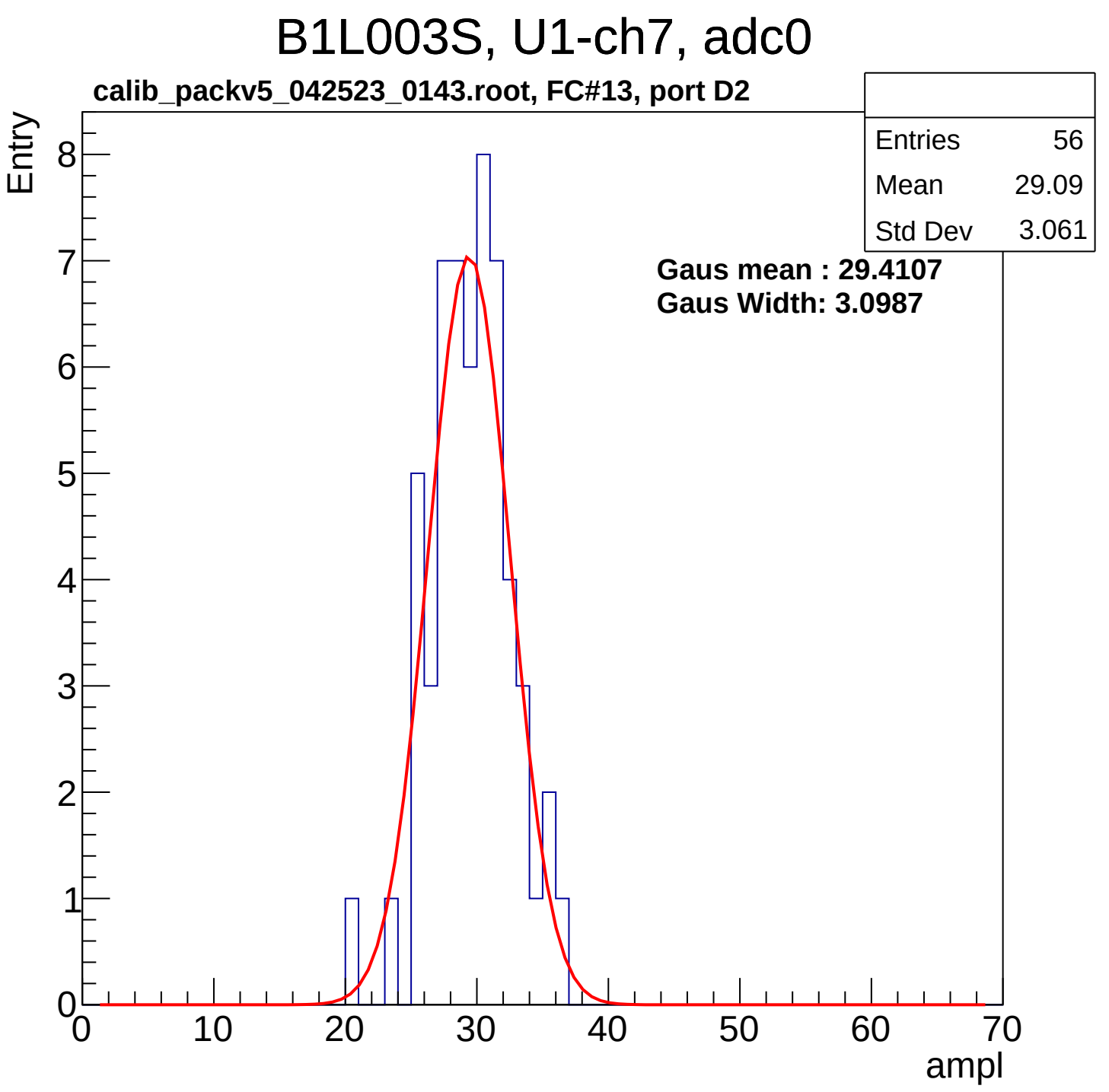
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch7, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 29.09
Std Dev 3.061
Gaus mean : 29.4107
Gaus Width: 3.0987
Entry
ampl
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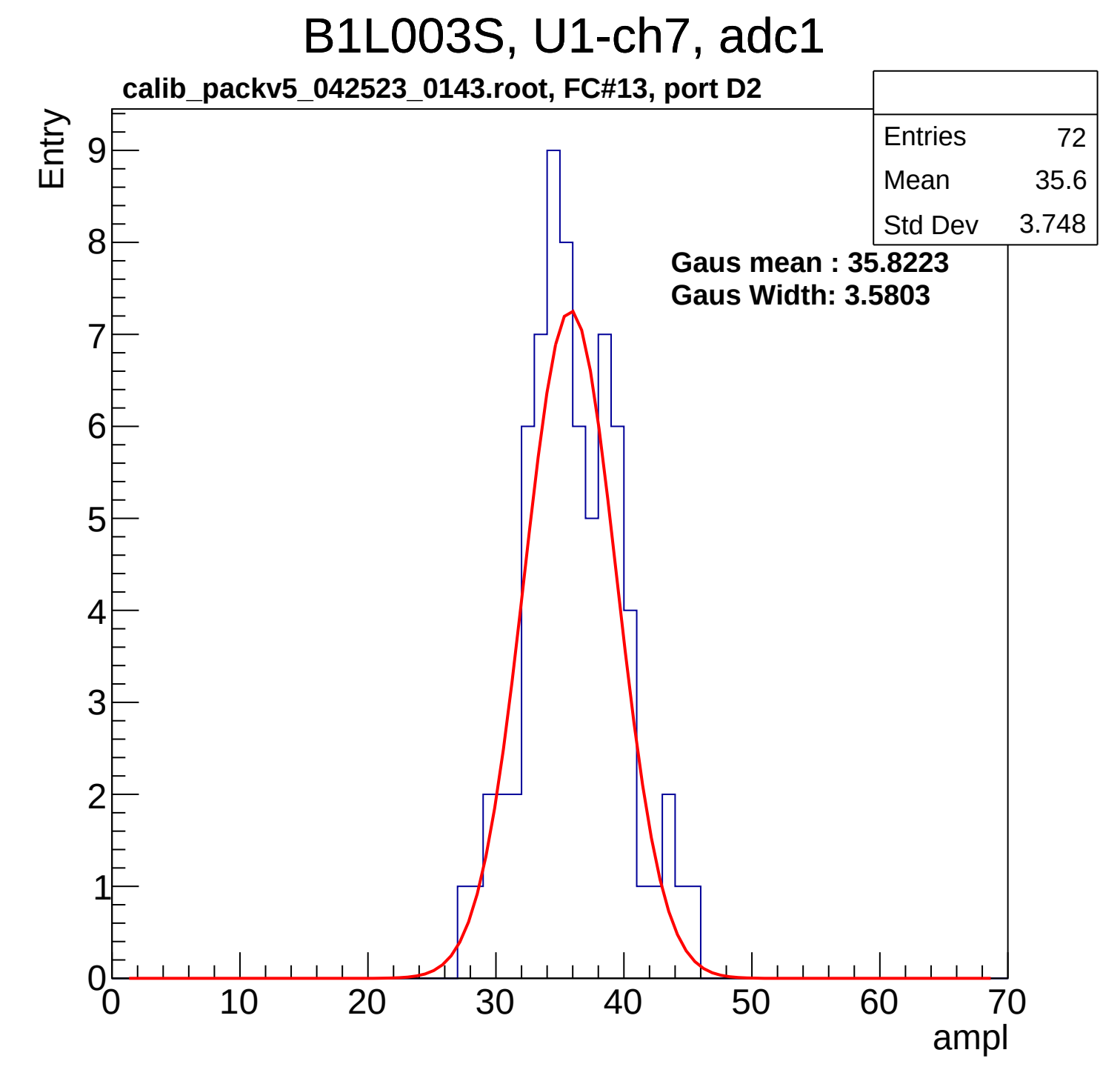
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch7, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 35.6
Std Dev 3.748
Gaus mean : 35.8223
Gaus Width: 3.5803
Entry
ampl
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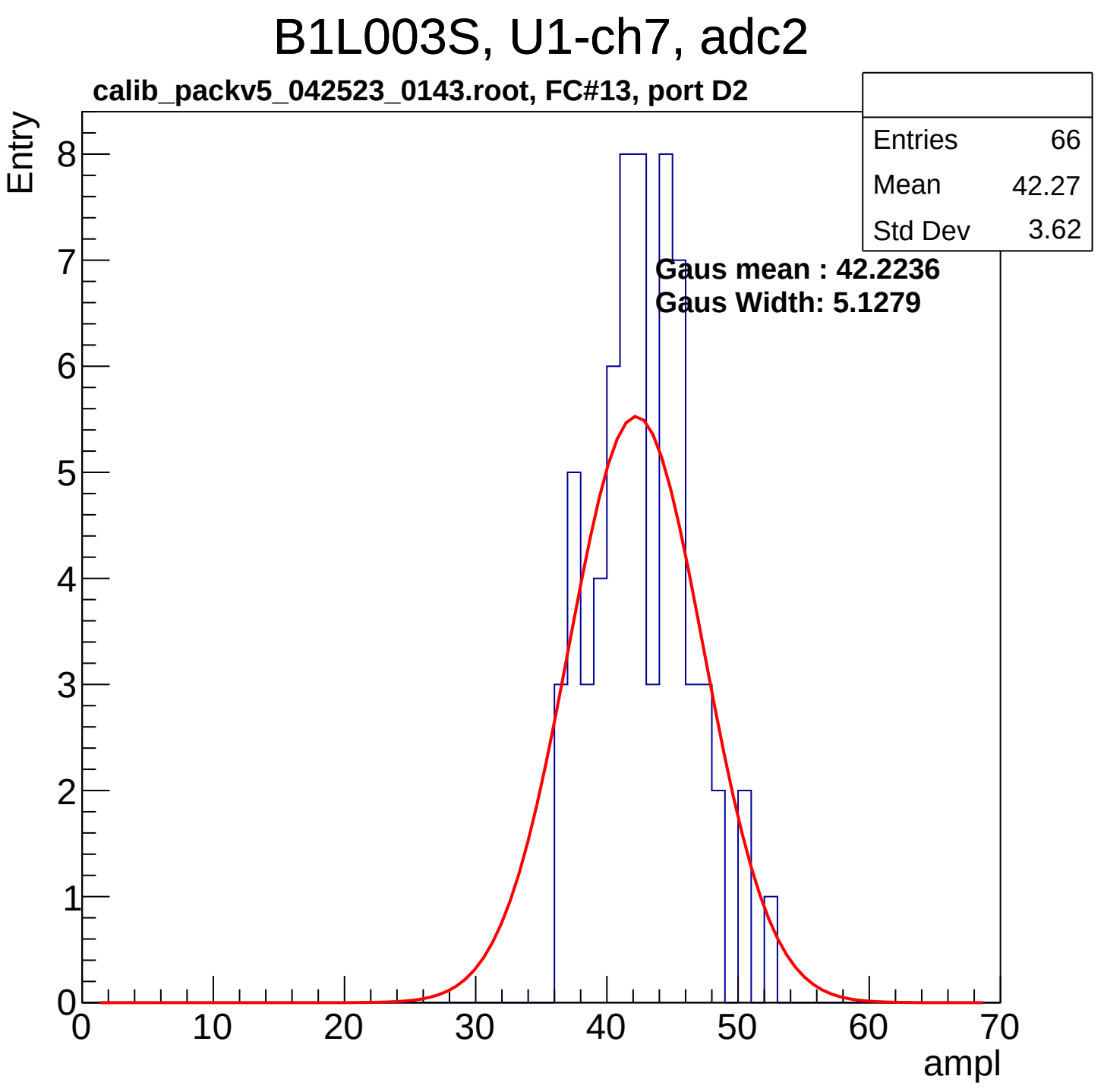
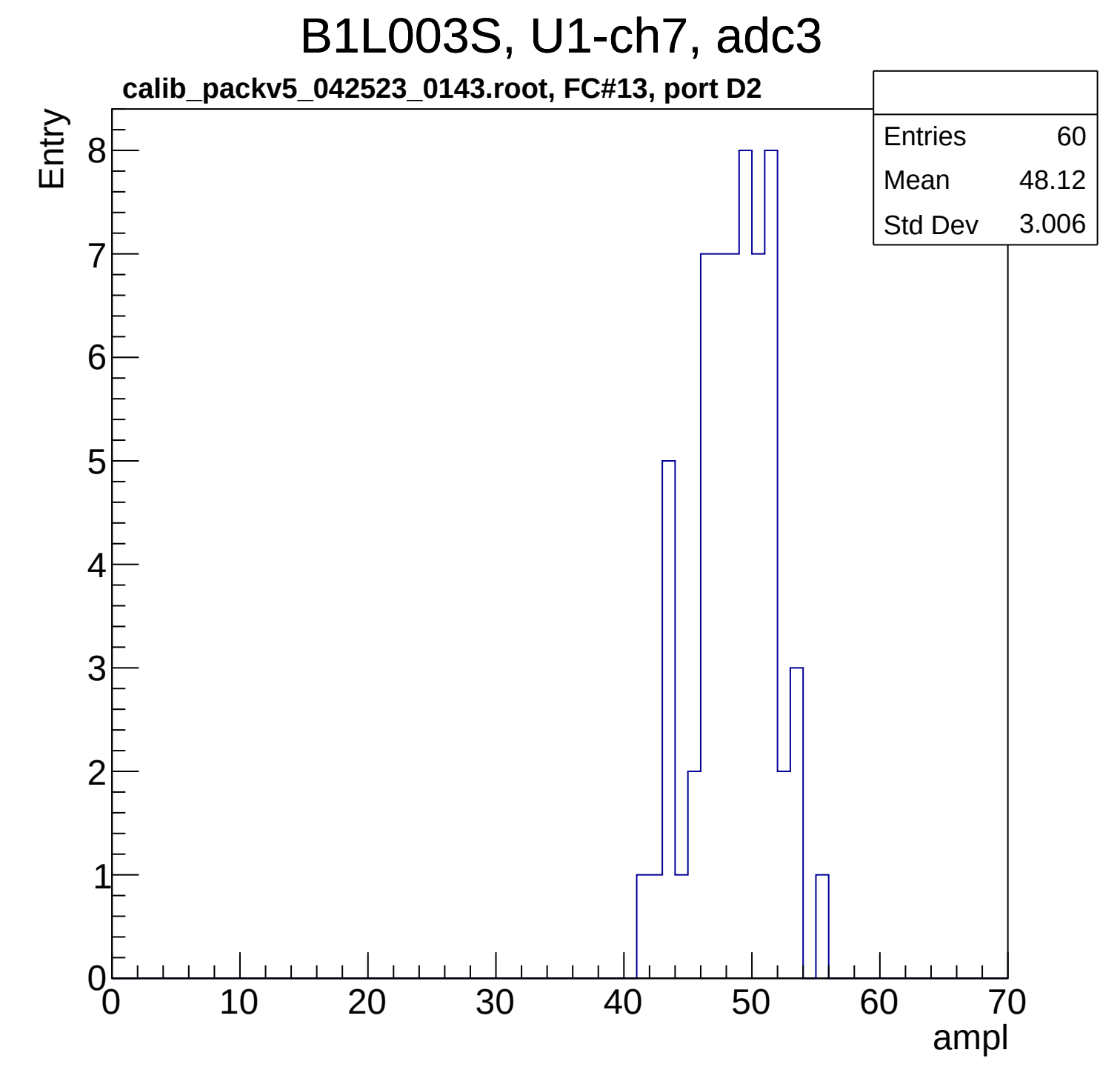
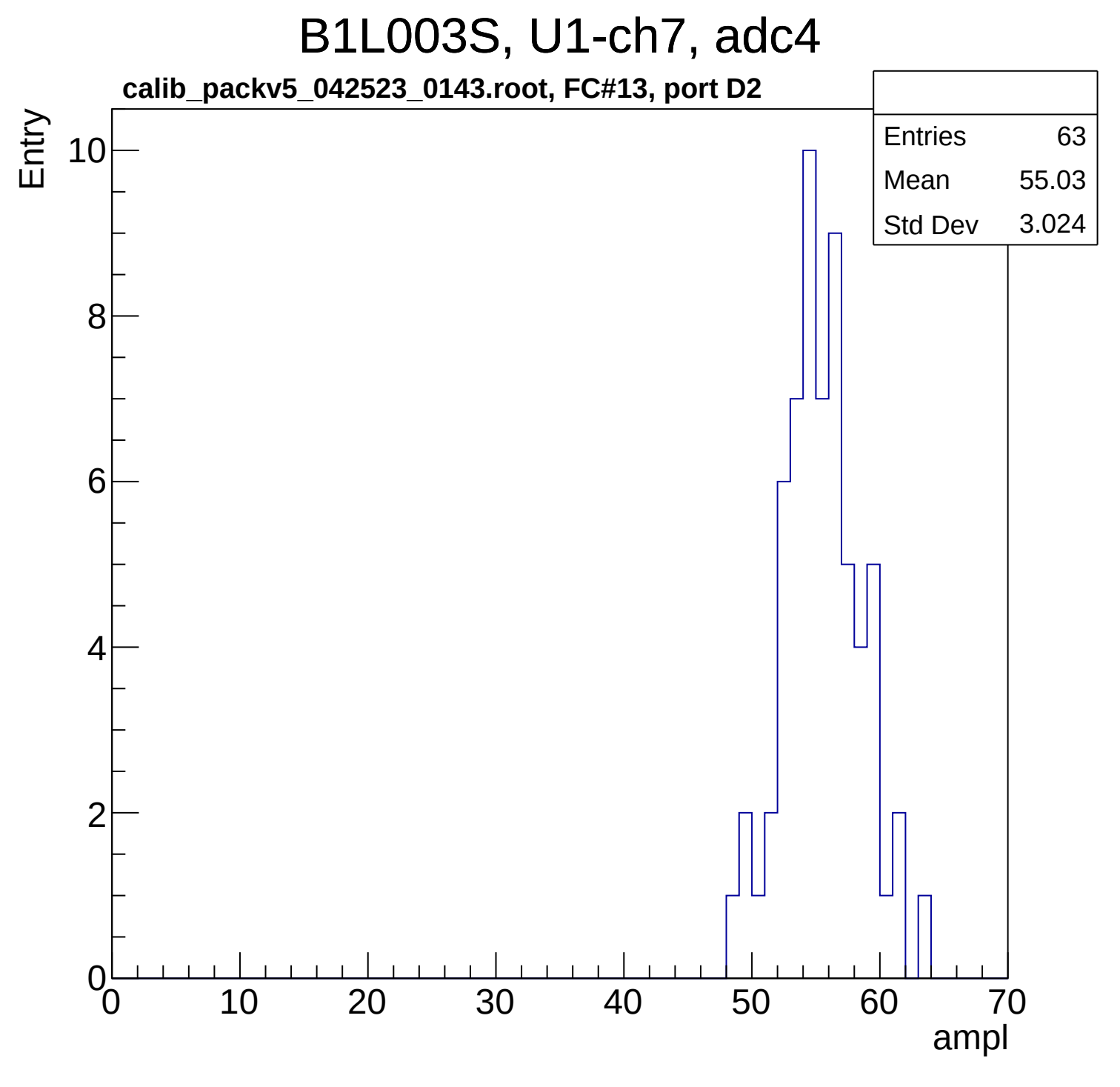
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch7, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.27
Std Dev 3.62
Gaus mean : 42.2236
Gaus Width: 5.1279
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch7, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.12
Std Dev 3.006
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch7, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.03
Std Dev 3.024
Entry
0
2
4
6
8
10
0
10
20
30
40
50
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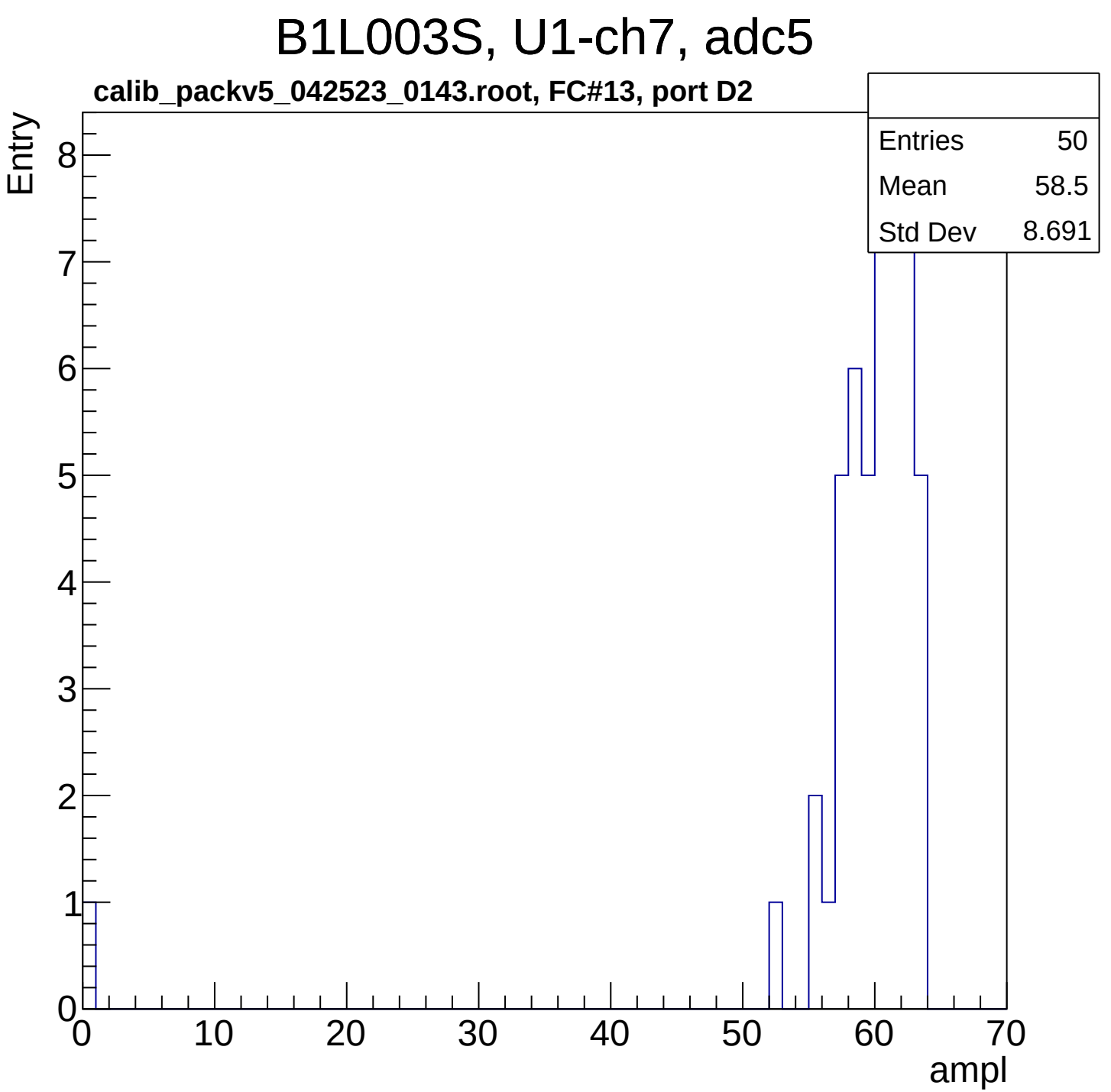
70
ampl


B1L003S, U1-ch7, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.5
Std Dev 8.691
Entry
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2
3
4
5
6
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8
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10
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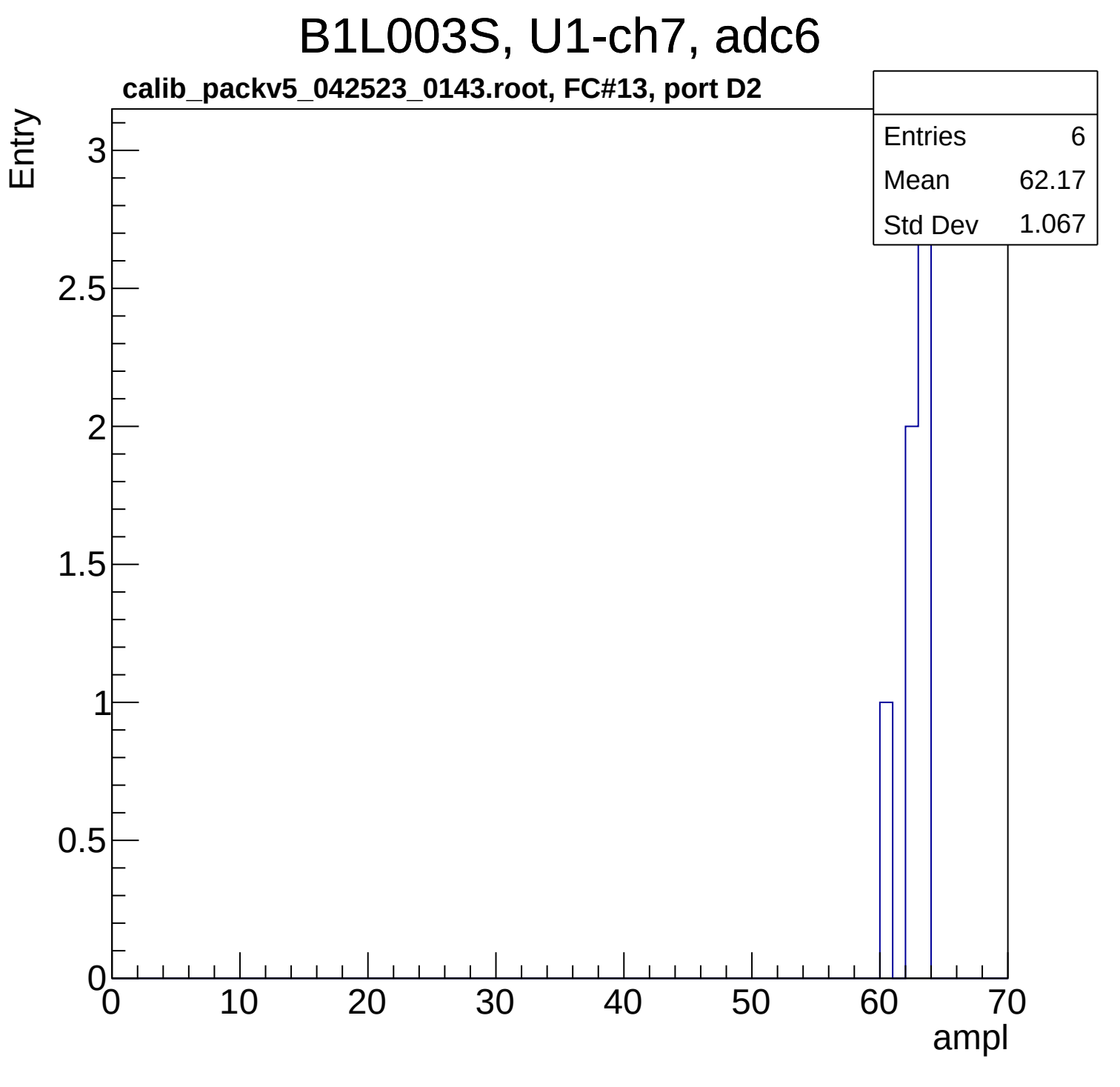
50
60
70
ampl


B1L003S, U1-ch7, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
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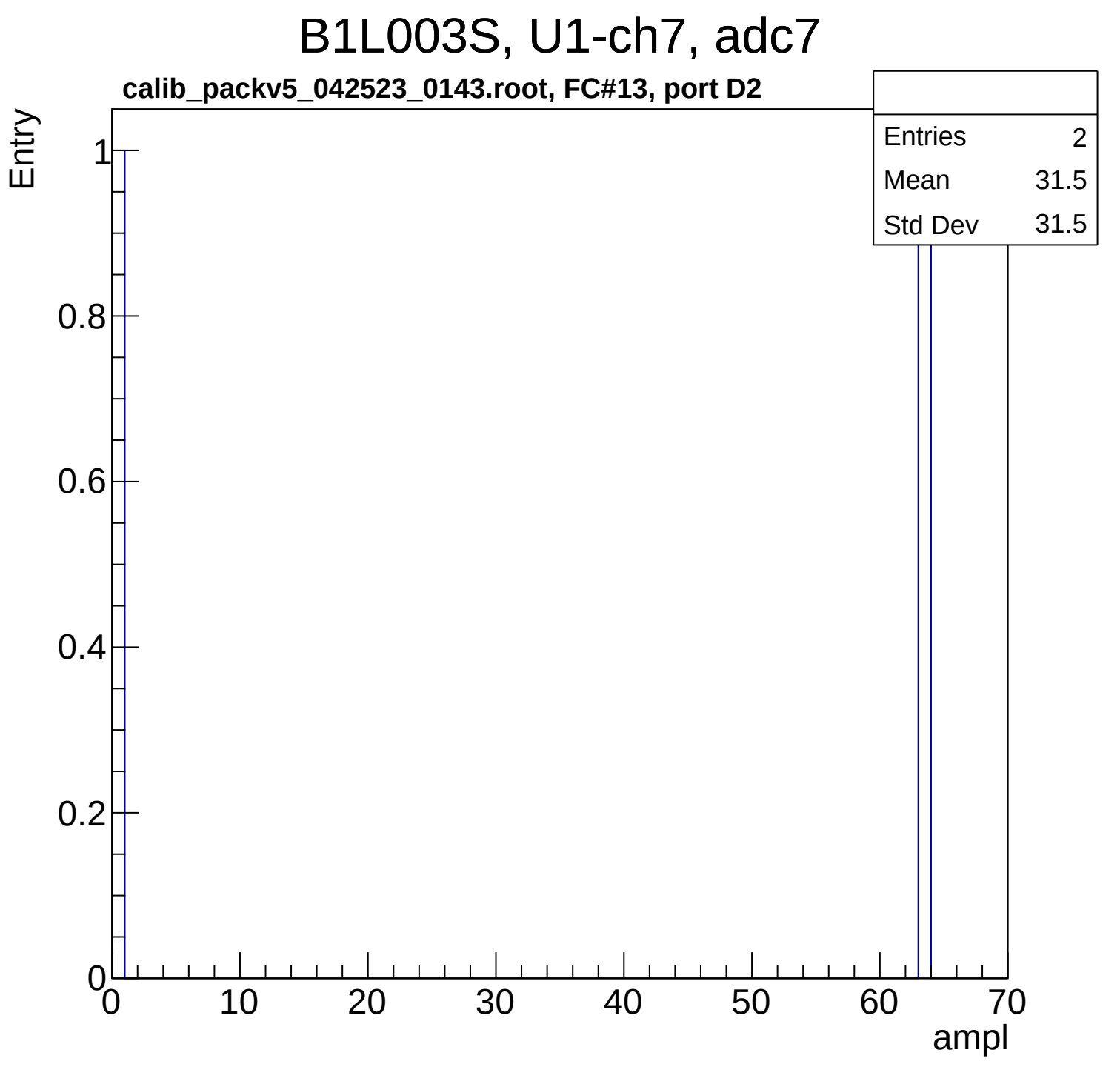
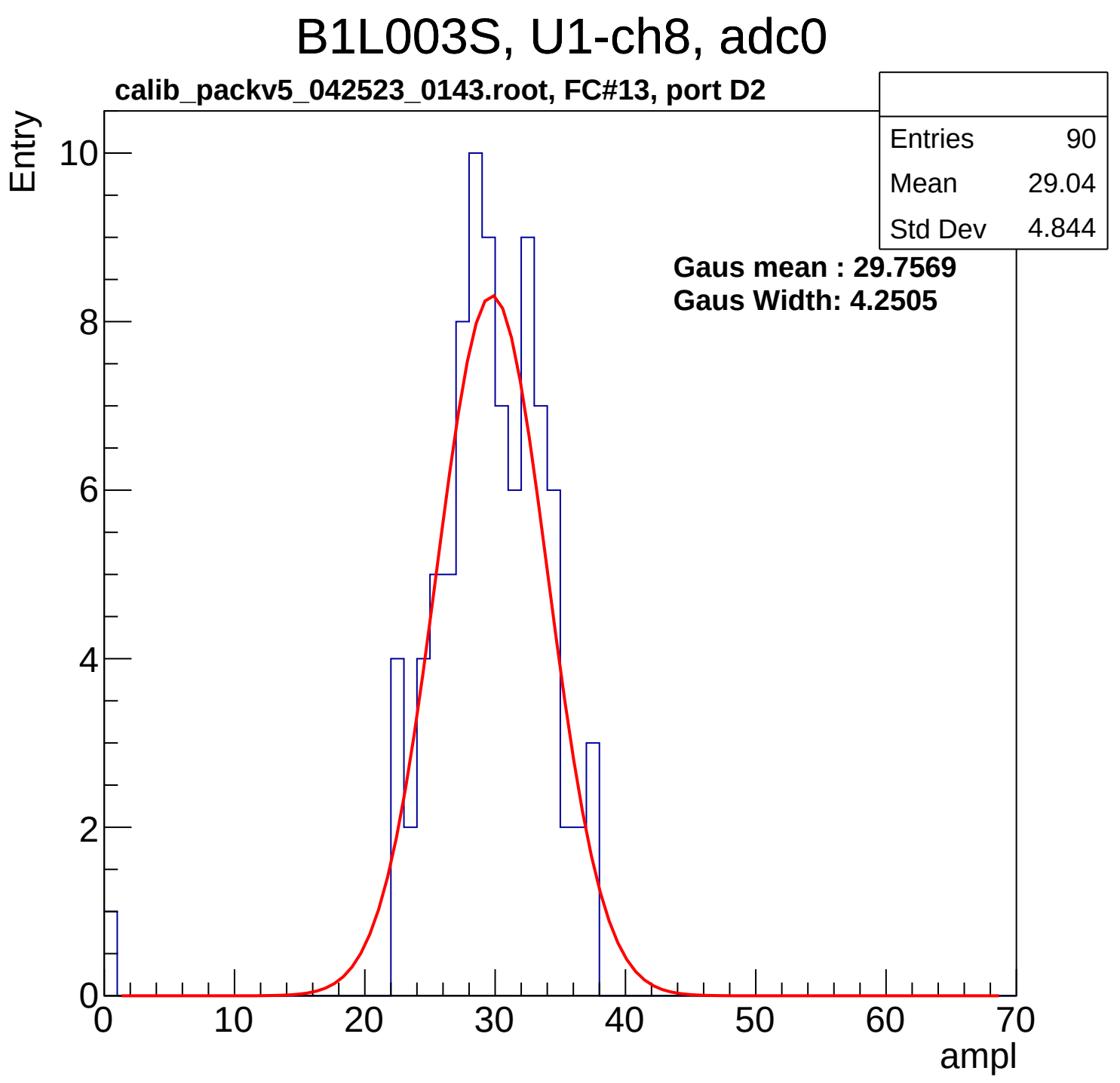
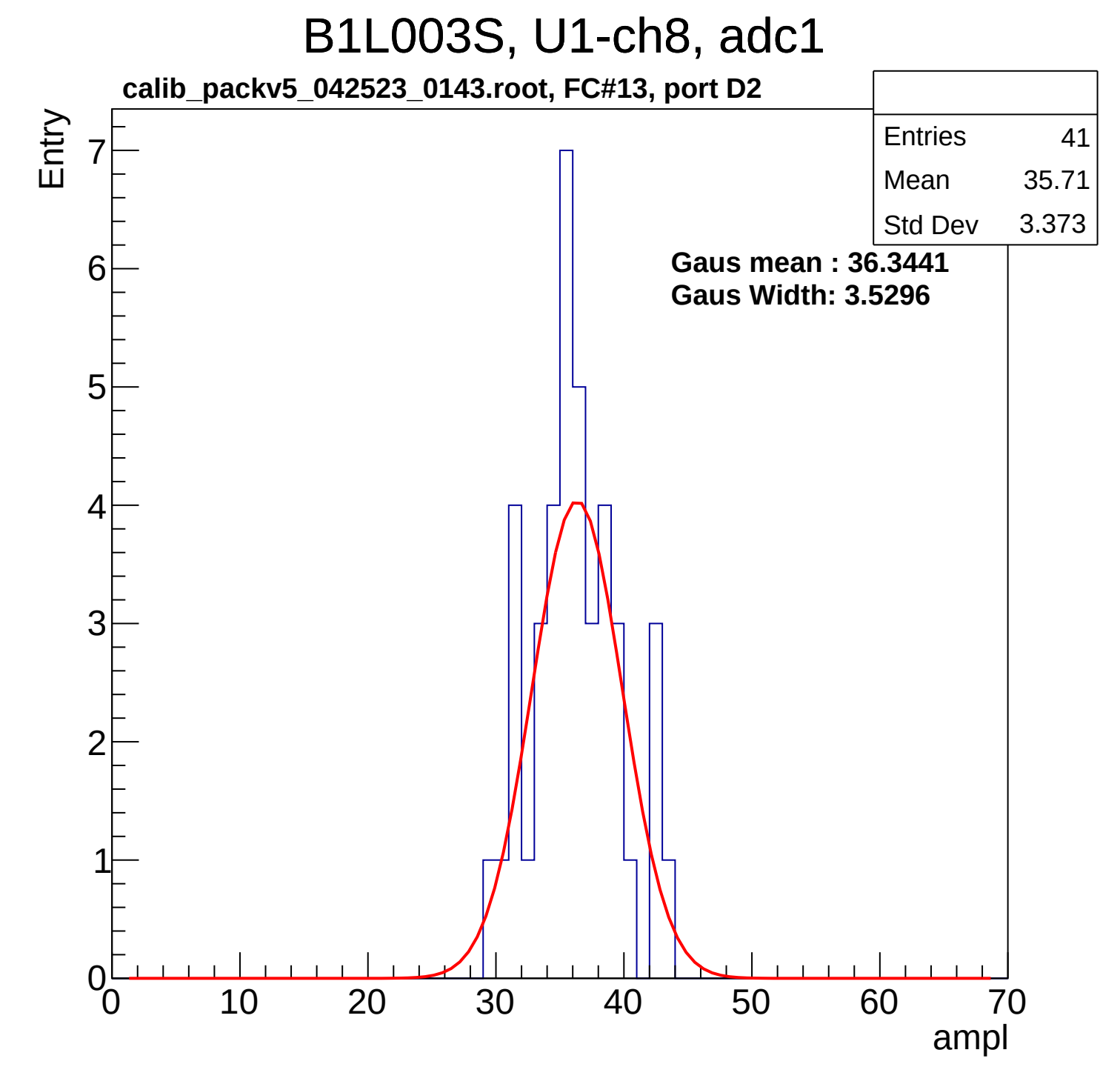
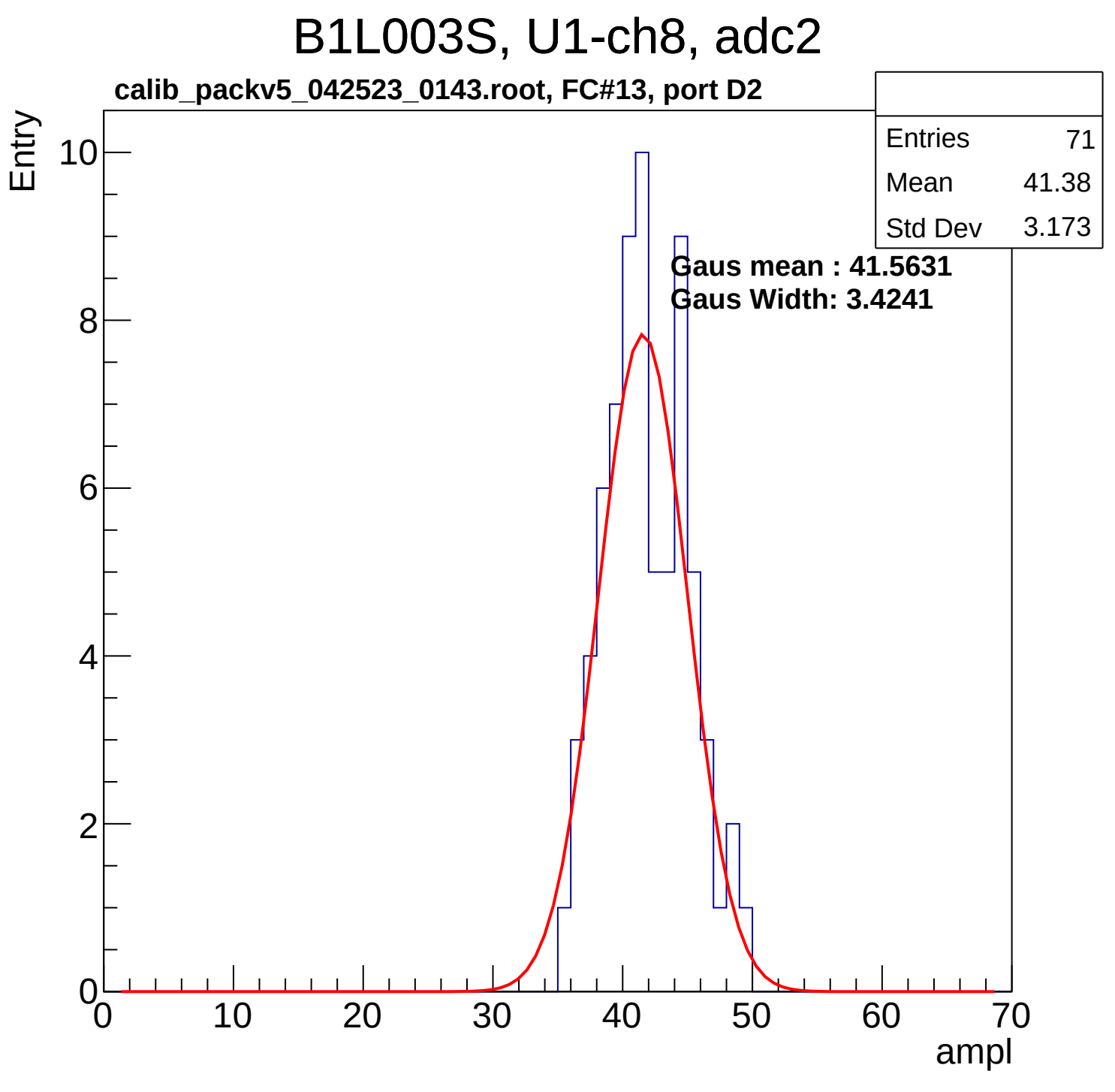
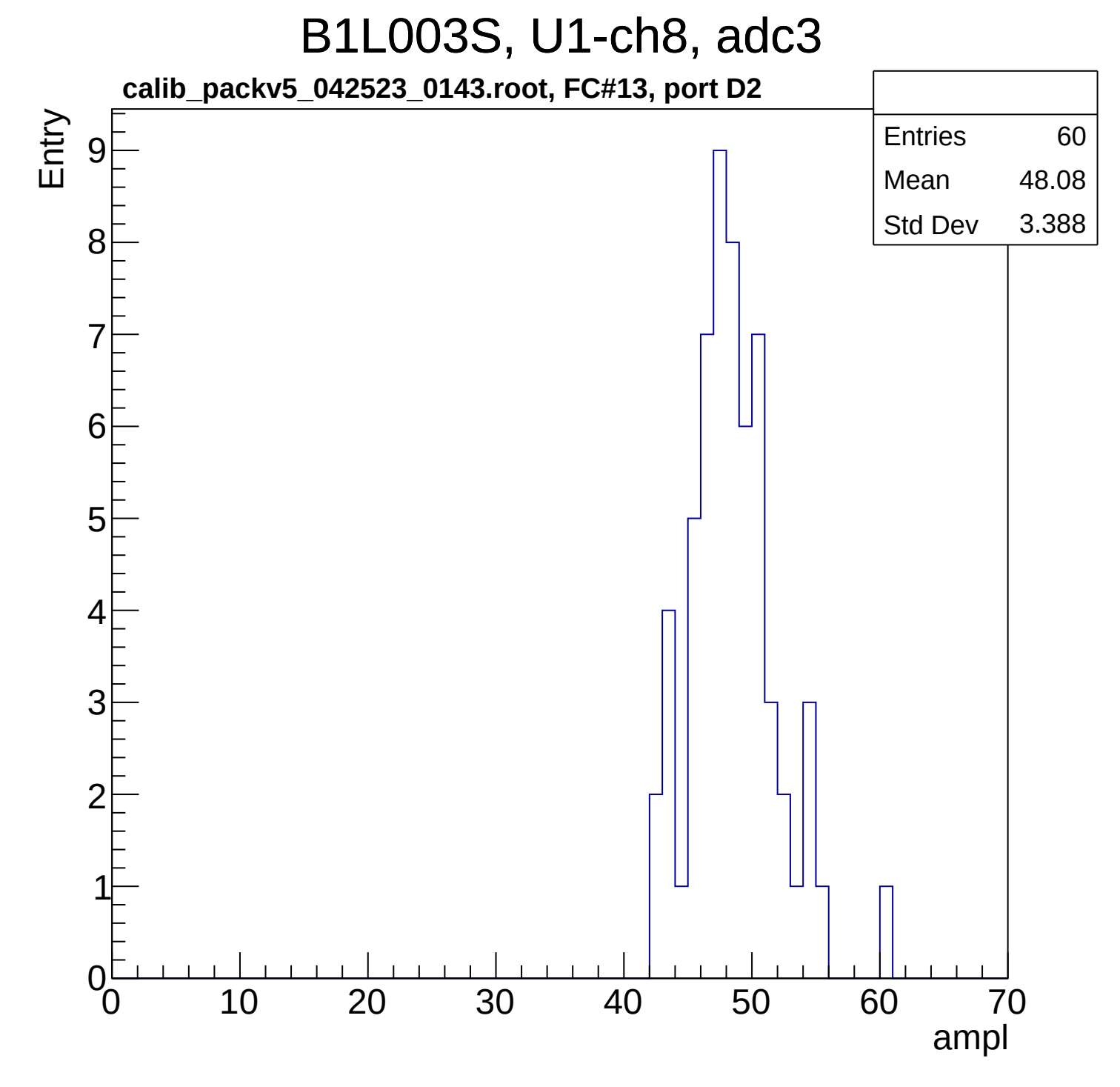
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

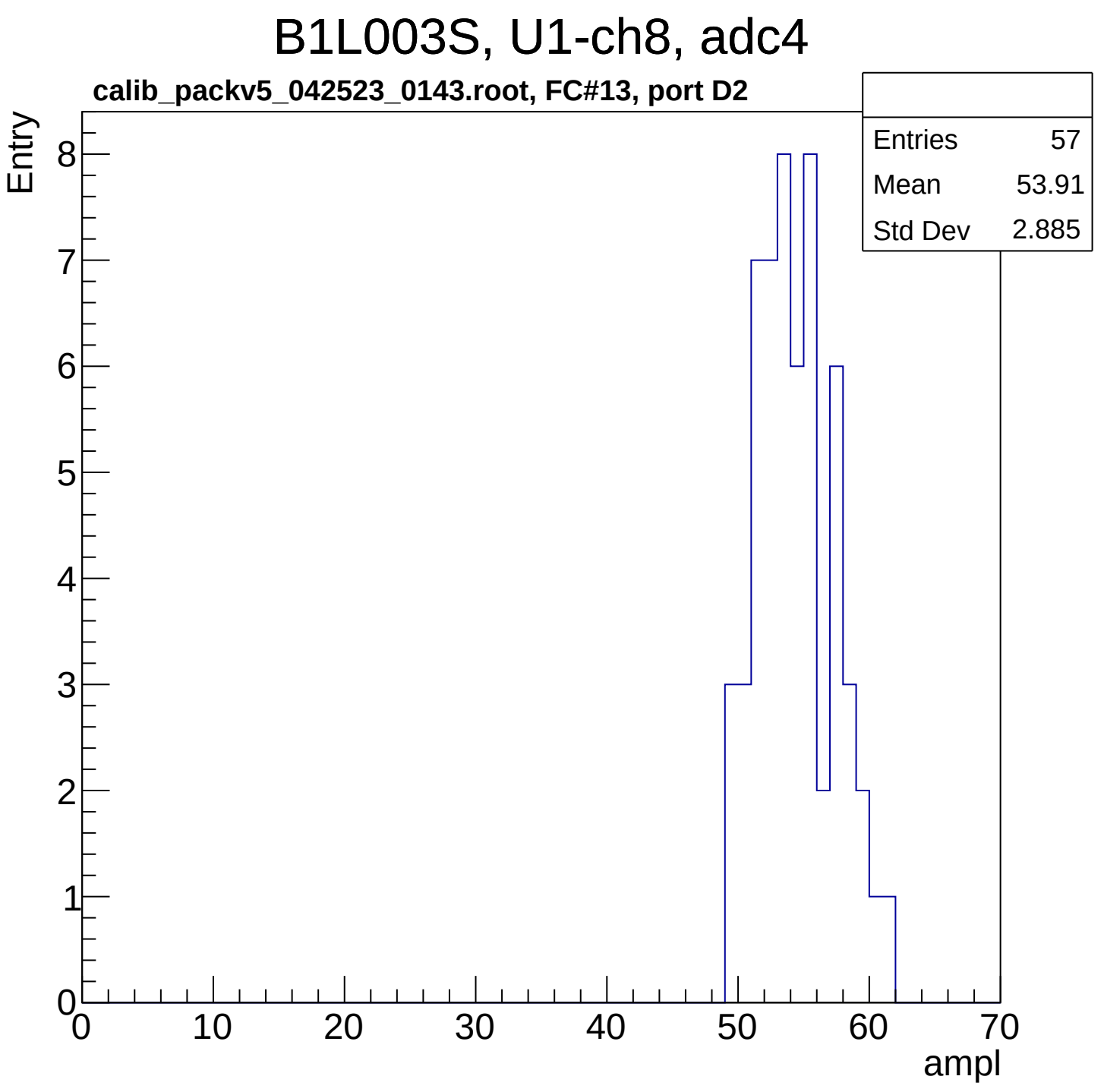

B1L003S, U1-ch7, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

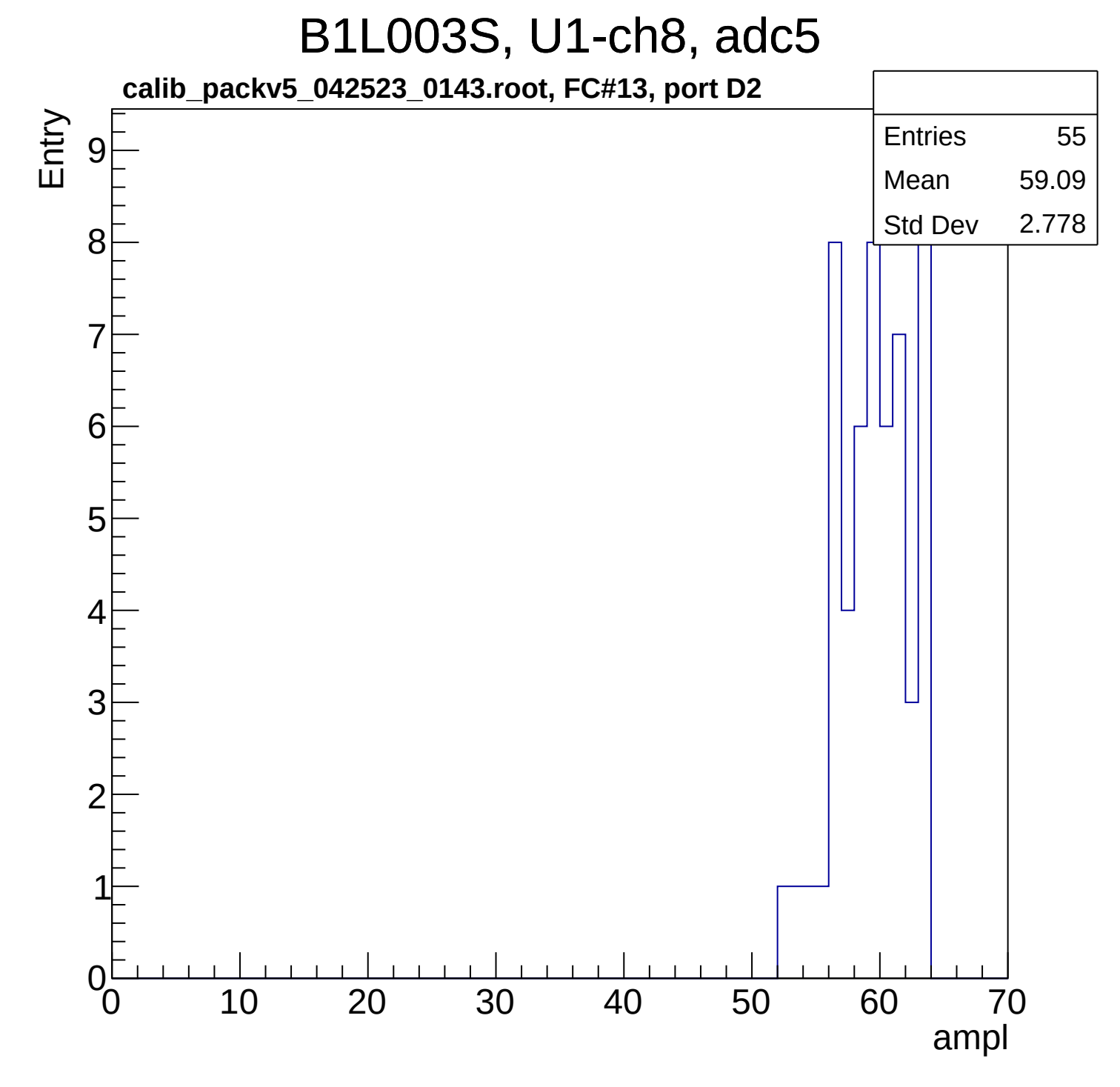

B1L003S, U1-ch8, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 90
Mean 29.04
Std Dev 4.844
Gaus mean : 29.7569
Gaus Width: 4.2505
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

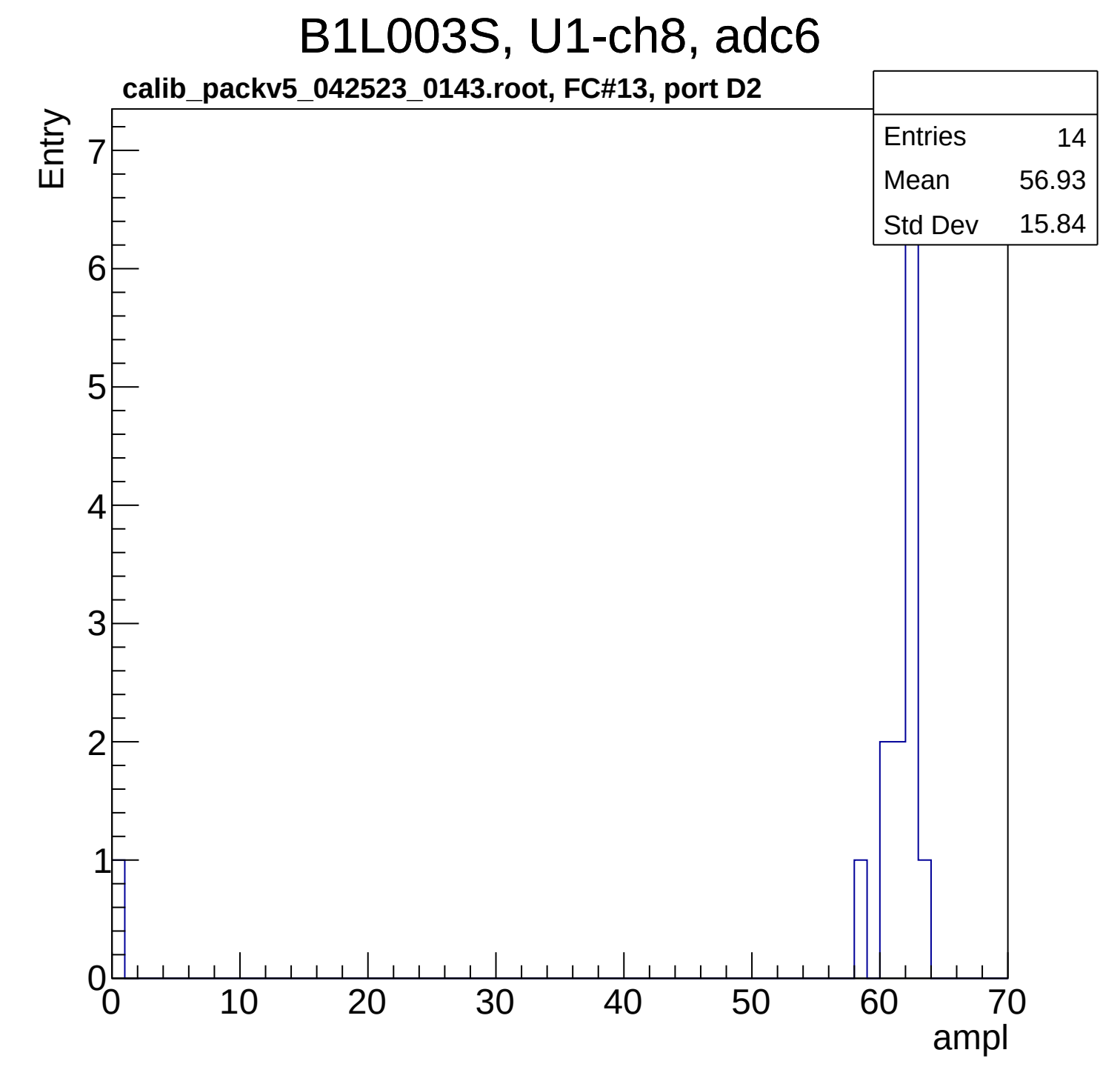

B1L003S, U1-ch8, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 35.71
Std Dev 3.373
Gaus mean : 36.3441
Gaus Width: 3.5296
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

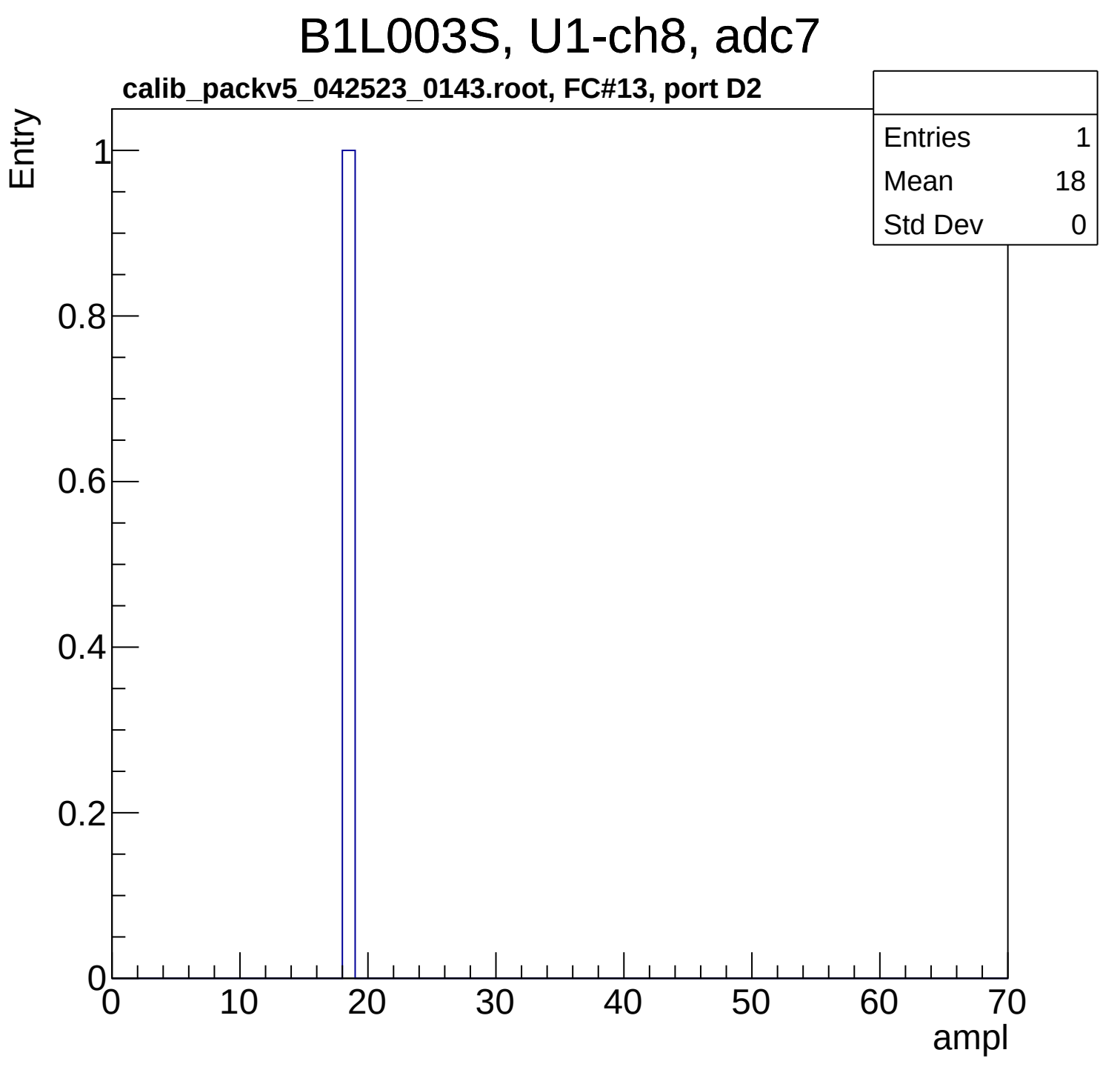

B1L003S, U1-ch8, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 41.38
Std Dev 3.173
Gaus mean : 41.5631
Gaus Width: 3.4241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

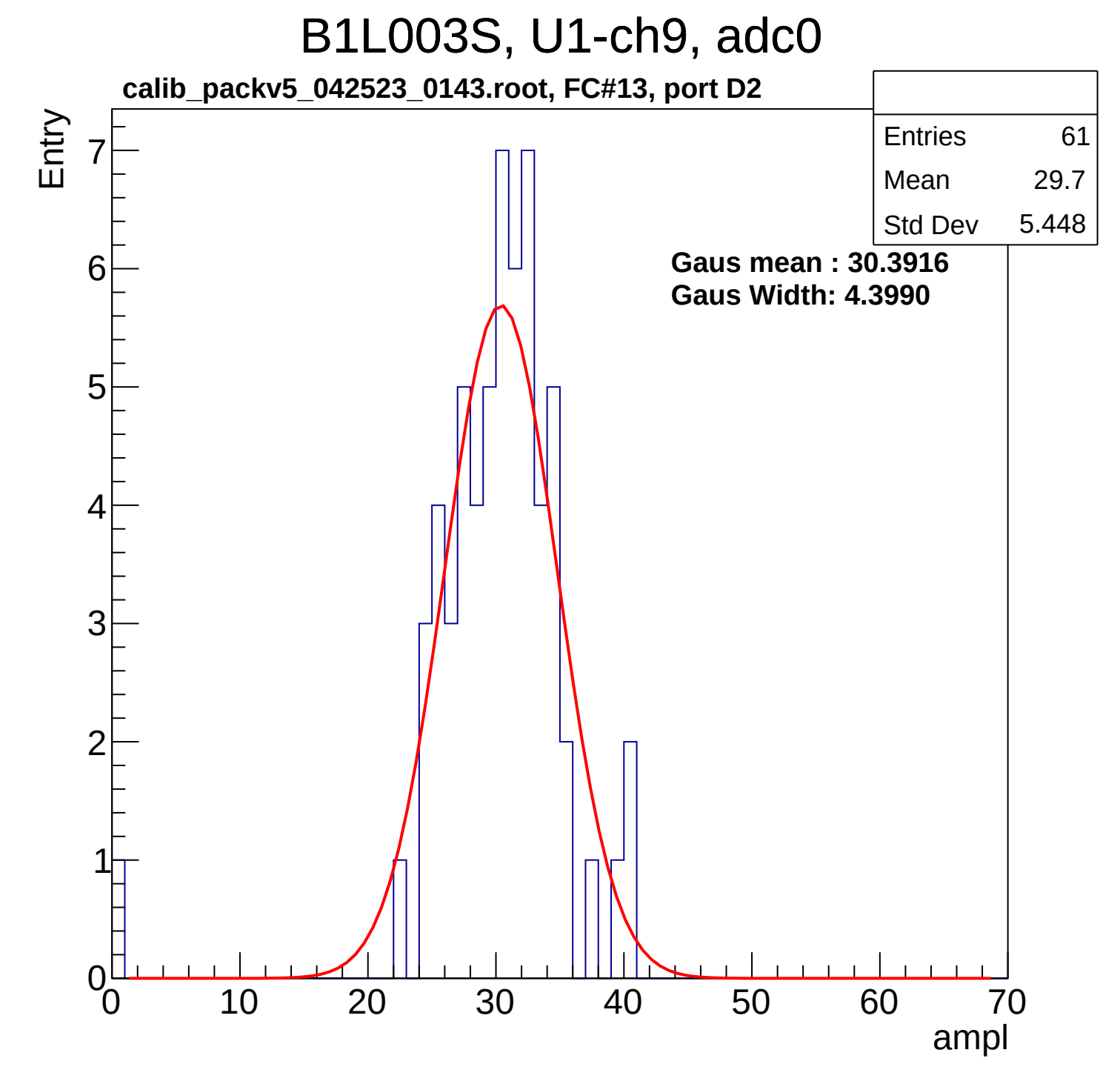

B1L003S, U1-ch8, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.08
Std Dev 3.388
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

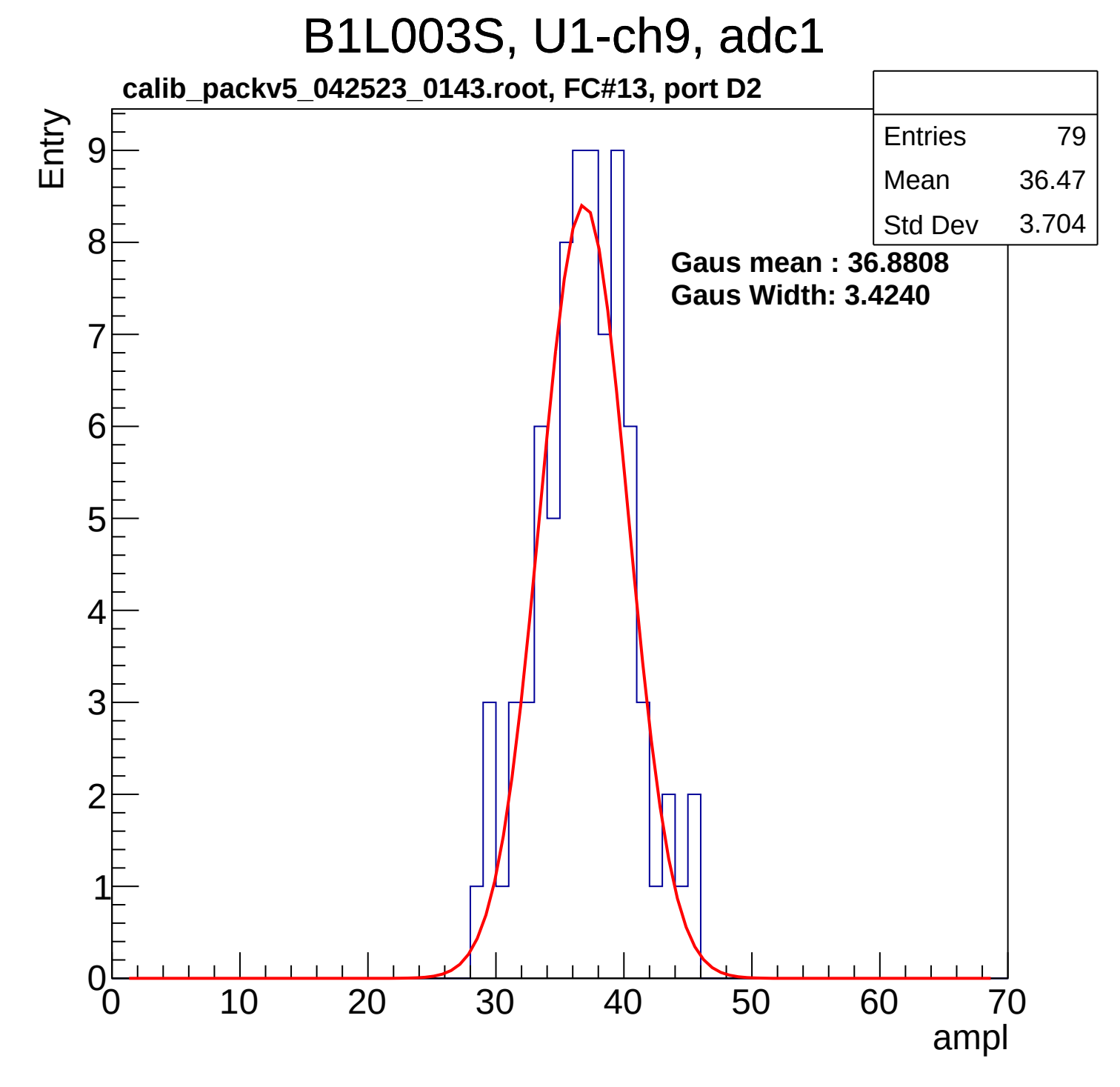

B1L003S, U1-ch8, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 53.91
Std Dev 2.885
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

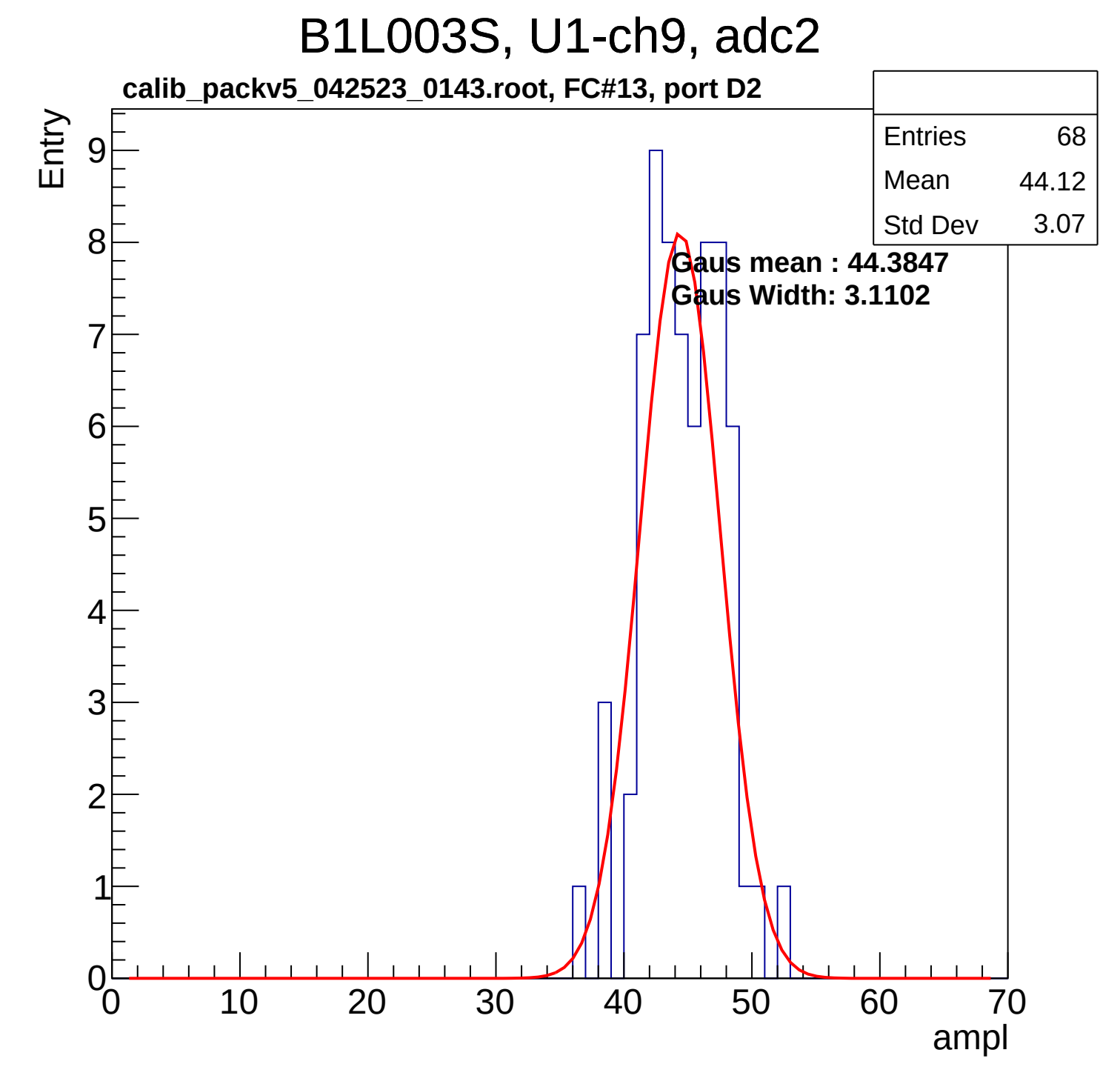

B1L003S, U1-ch8, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 59.09
Std Dev 2.778
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

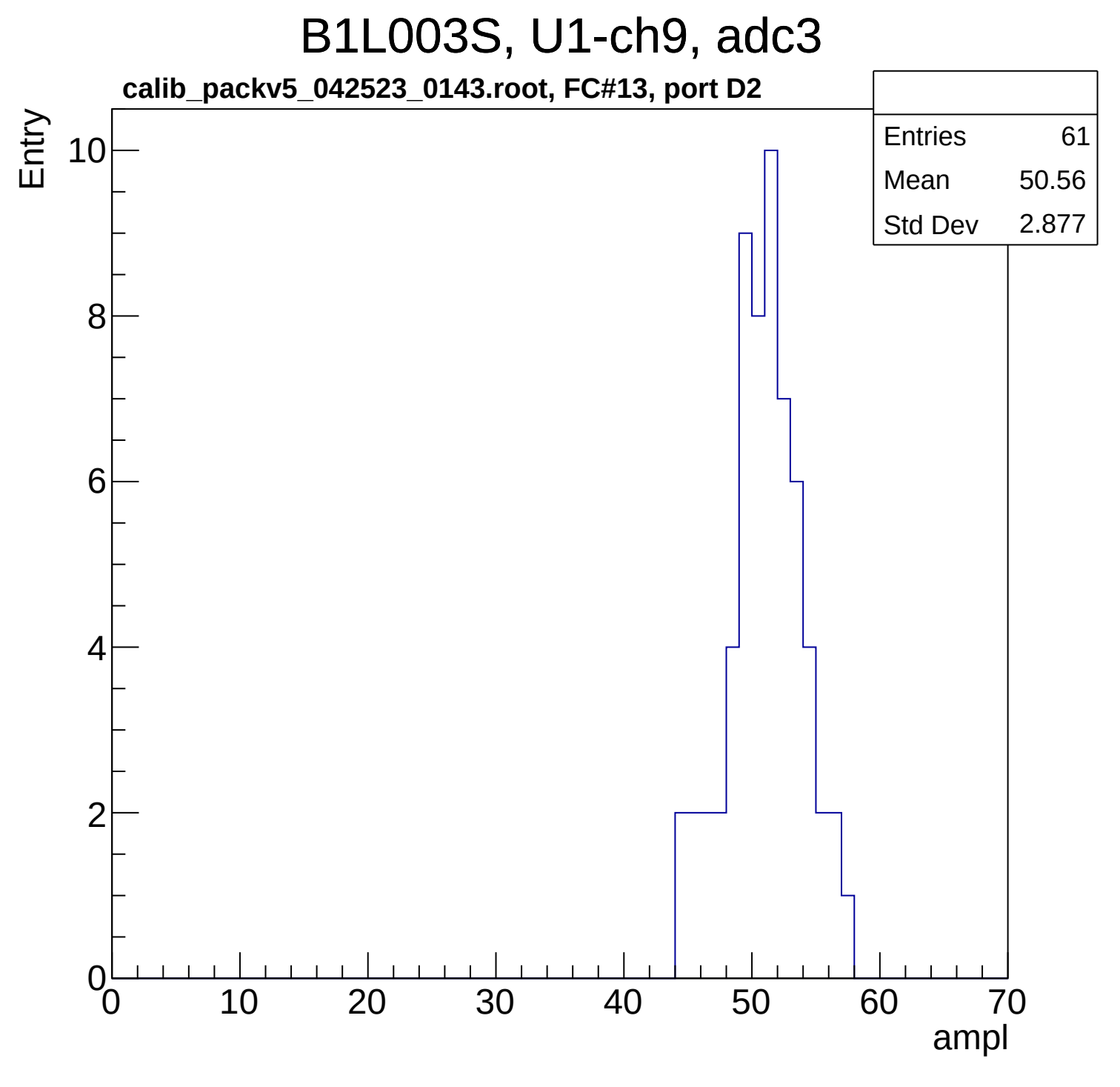

B1L003S, U1-ch8, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 56.93
Std Dev 15.84
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

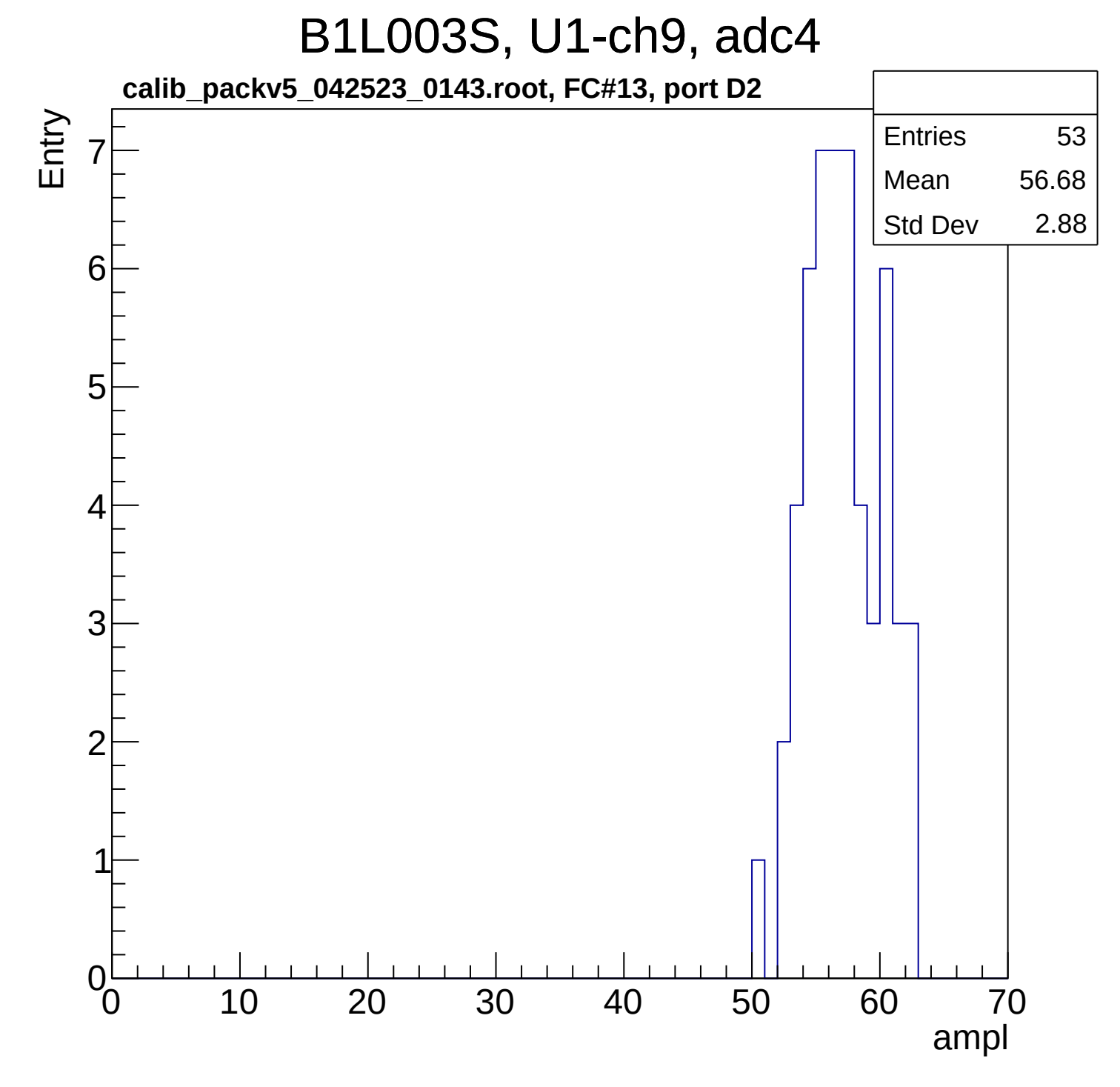

B1L003S, U1-ch8, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 18
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

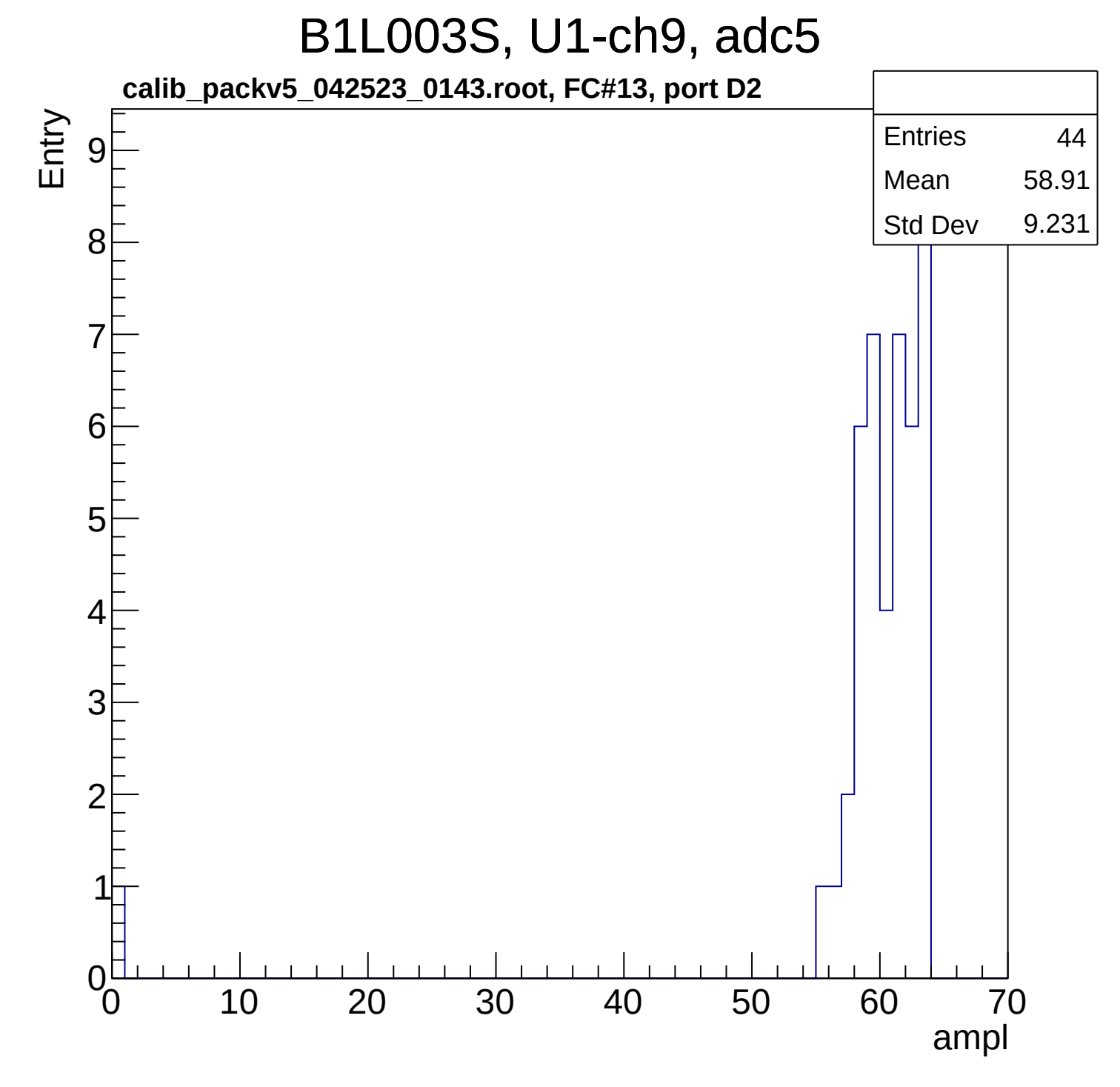

B1L003S, U1-ch9, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.7
Std Dev 5.448
Gaus mean : 30.3916
Gaus Width: 4.3990
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch9, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.47
Std Dev 3.704
Gaus mean : 36.8808
Gaus Width: 3.4240
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch9, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 44.12
Std Dev 3.07
Gaus mean : 44.3847
Gaus Width: 3.1102
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch9, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.56
Std Dev 2.877
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch9, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.68
Std Dev 2.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch9, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 58.91
Std Dev 9.231
Entry
ampl
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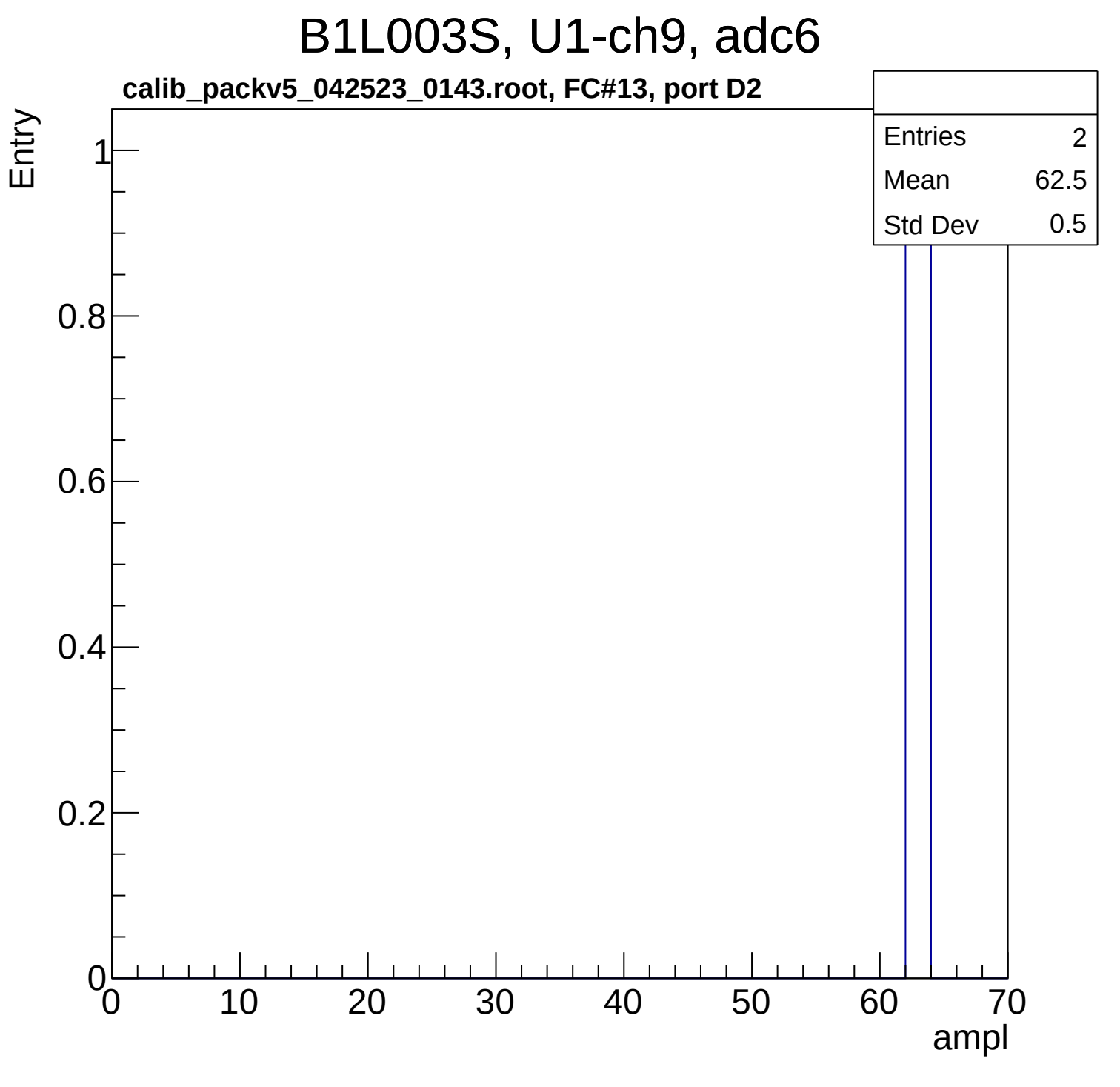
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch9, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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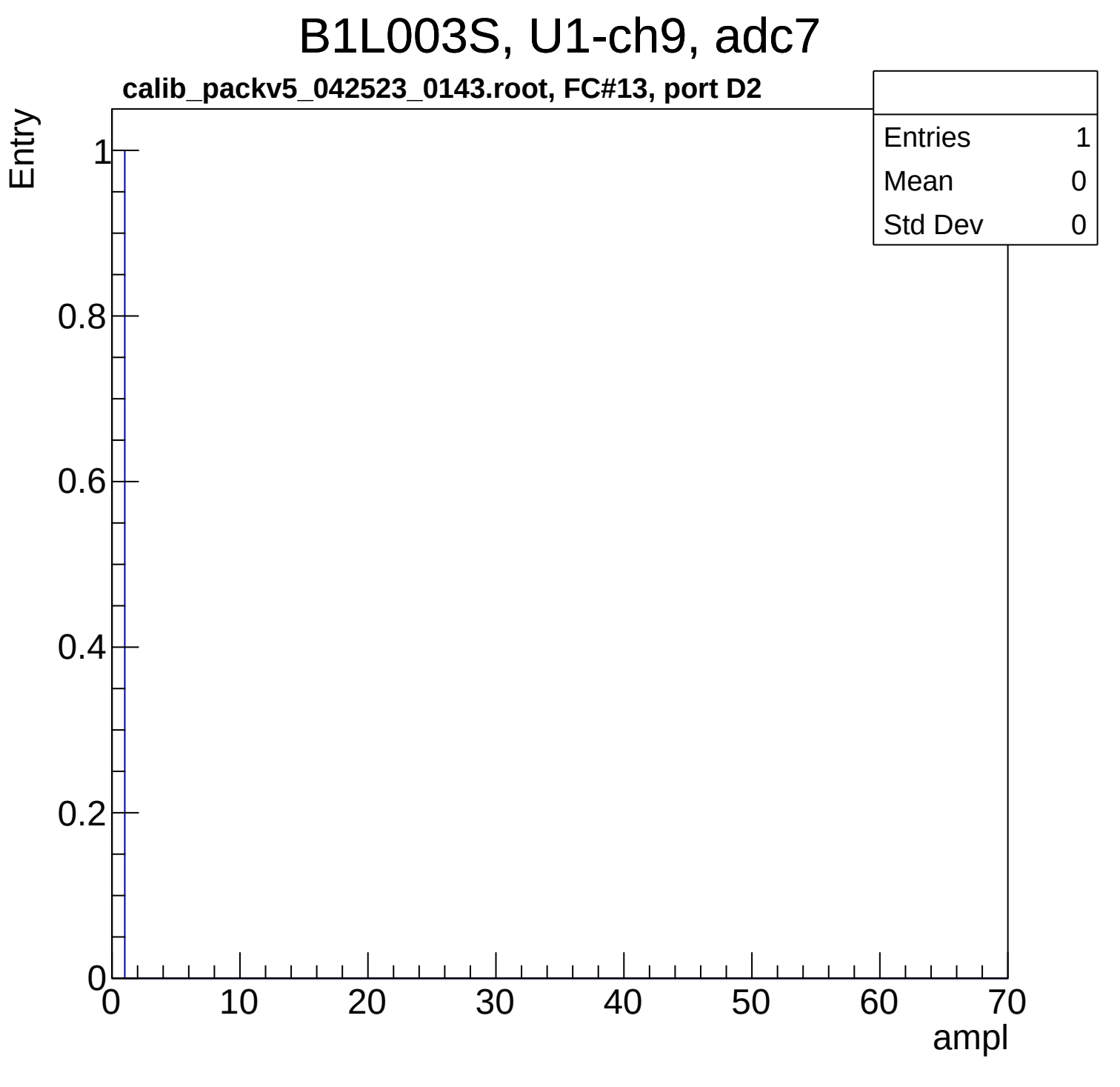
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch9, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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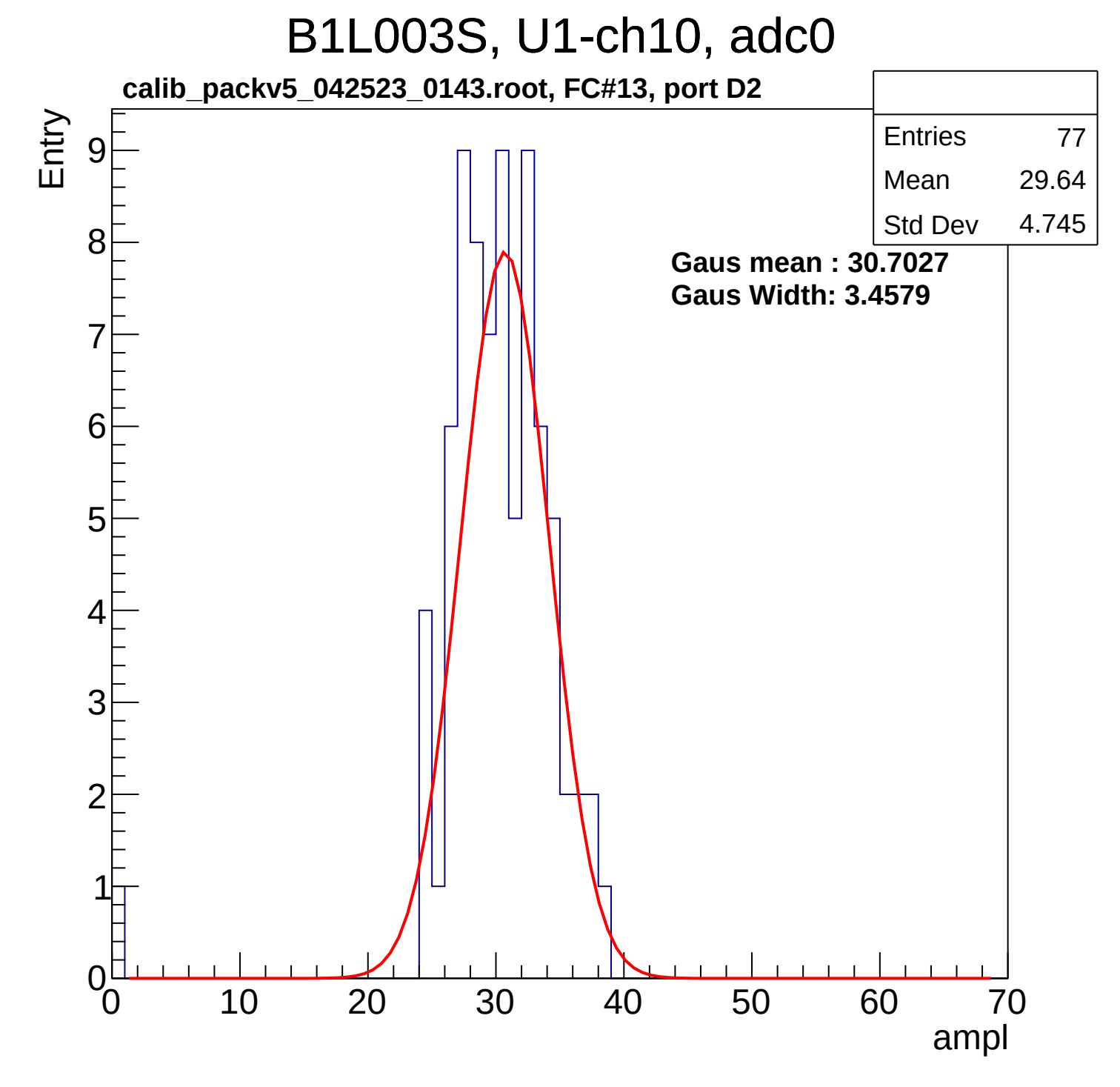
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch10, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 29.64
Std Dev 4.745
Gaus mean : 30.7027
Gaus Width: 3.4579
Entry
ampl
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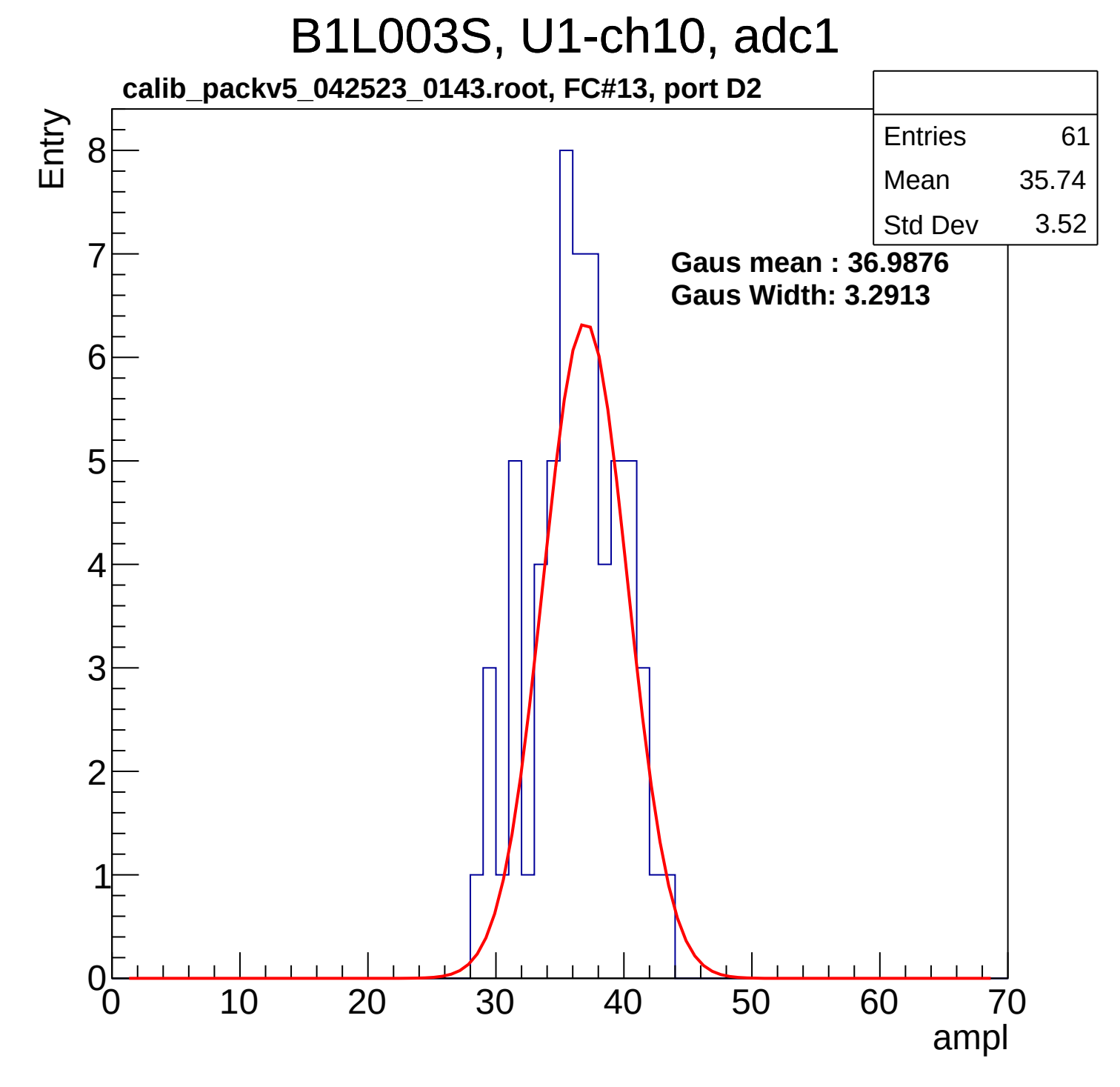
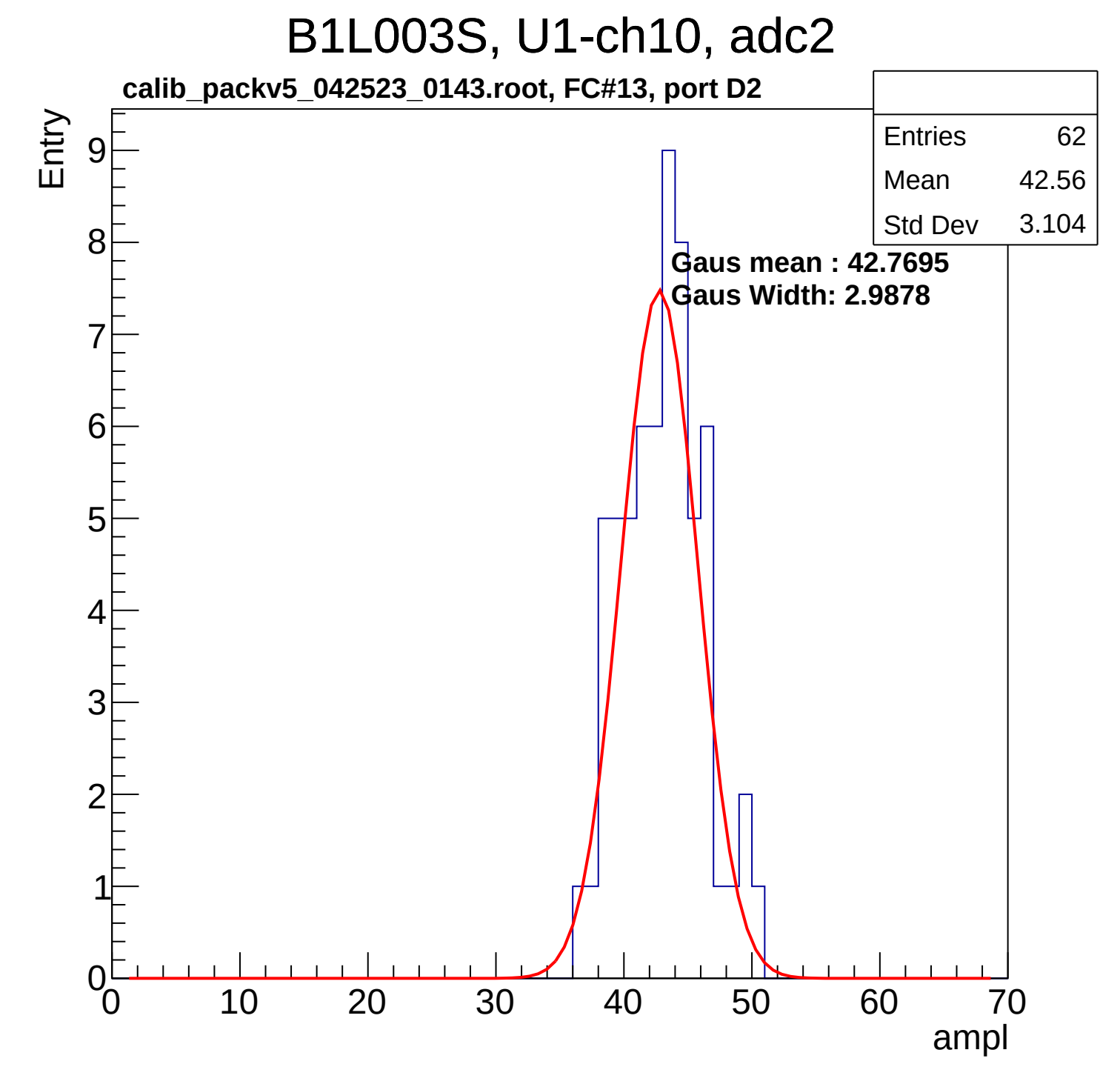
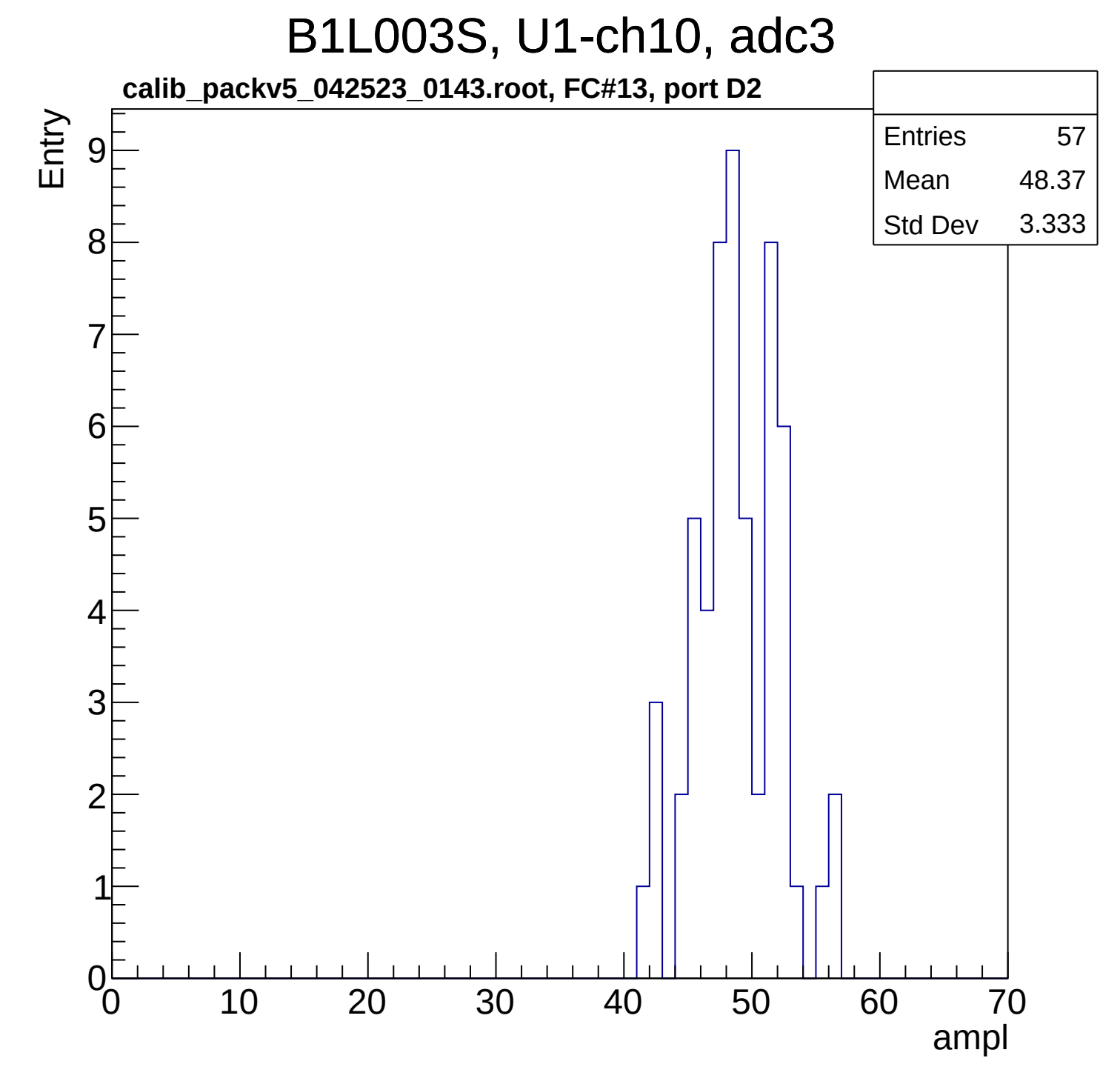
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch10, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.74
Std Dev 3.52
Gaus mean : 36.9876
Gaus Width: 3.2913
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch10, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.56
Std Dev 3.104
Gaus mean : 42.7695
Gaus Width: 2.9878
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch10, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.37
Std Dev 3.333
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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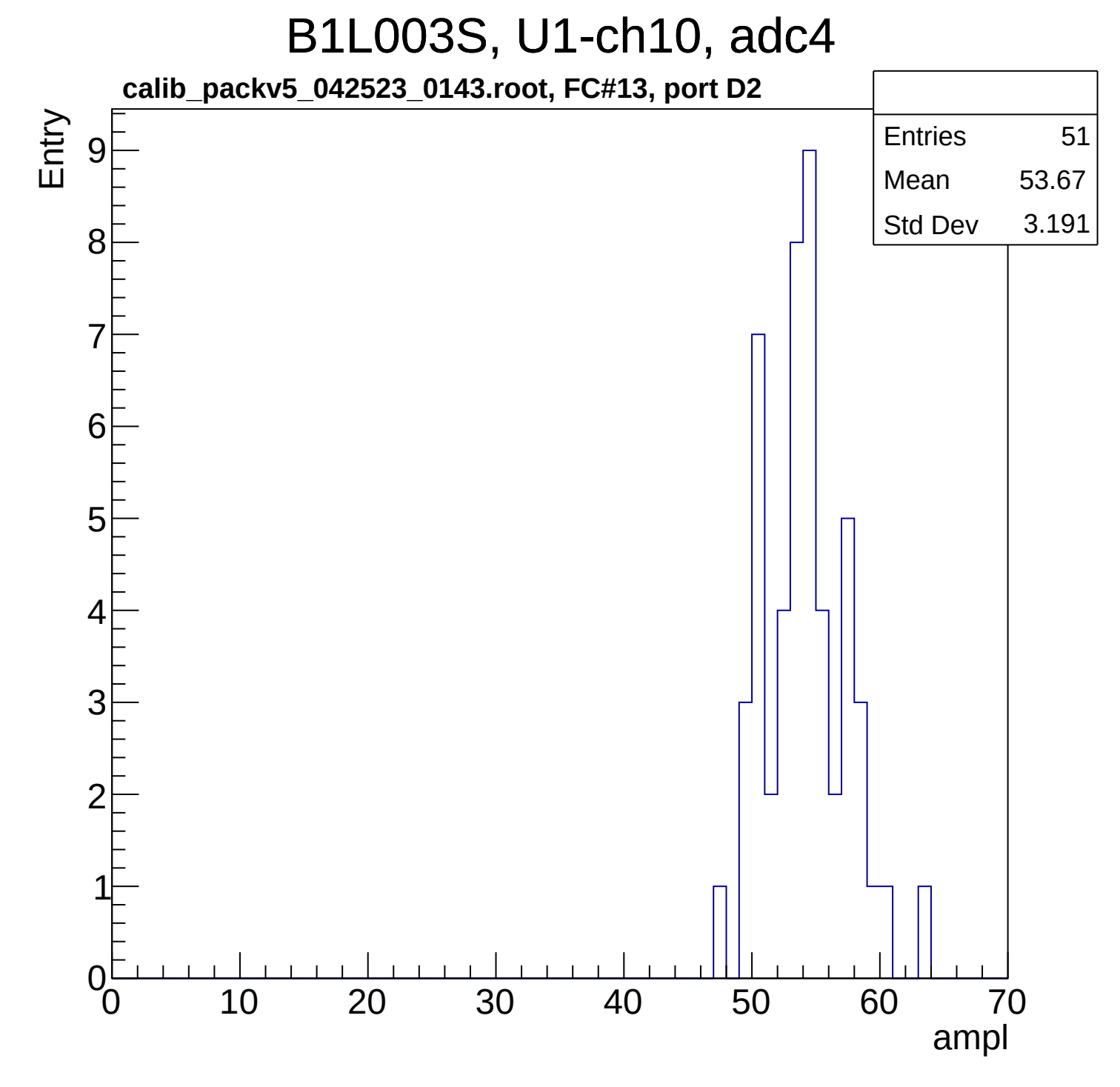
B1L003S, U1-ch10, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.67
Std Dev 3.191
Entry
ampl
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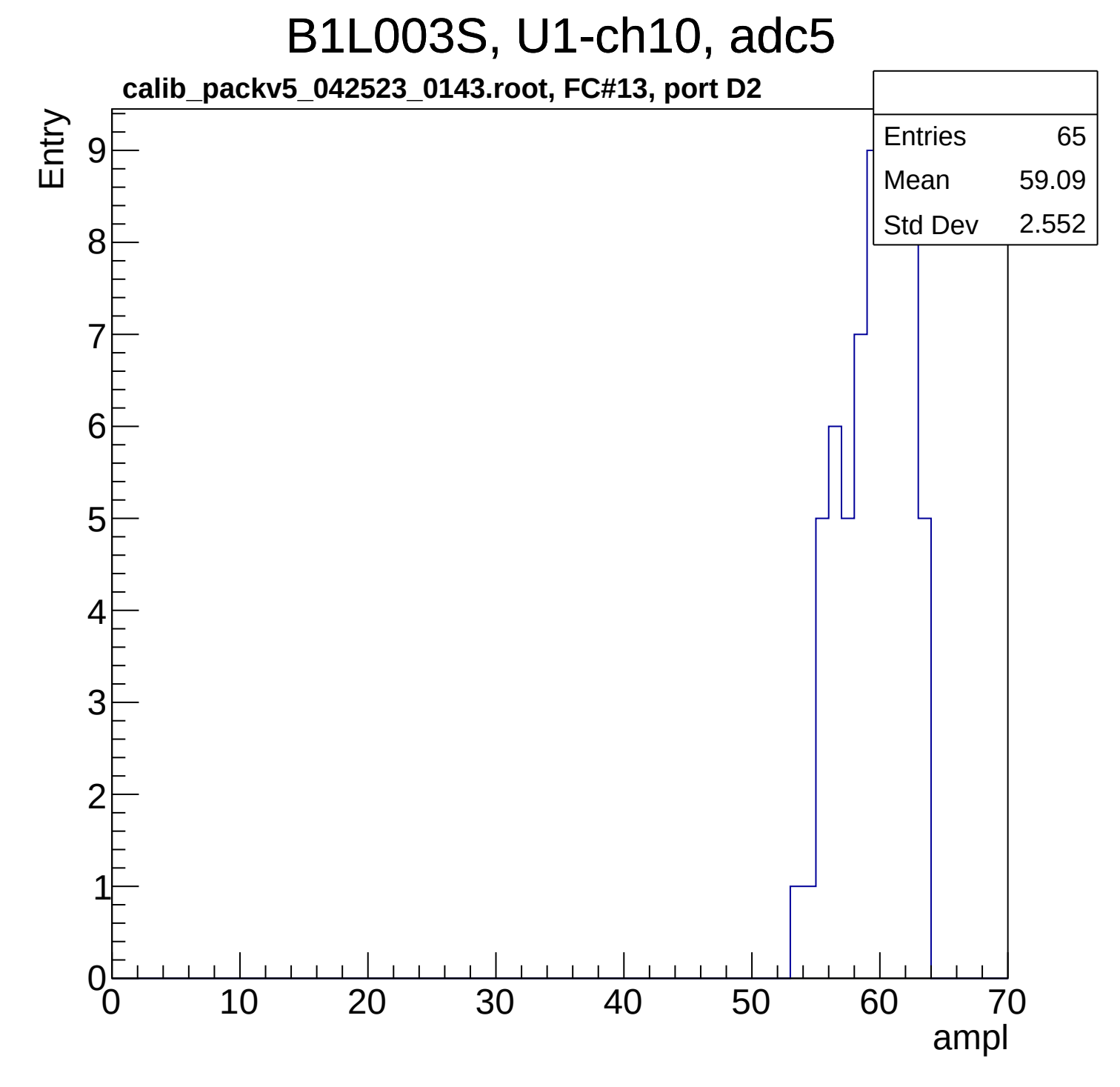
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch10, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 59.09
Std Dev 2.552
Entry
ampl
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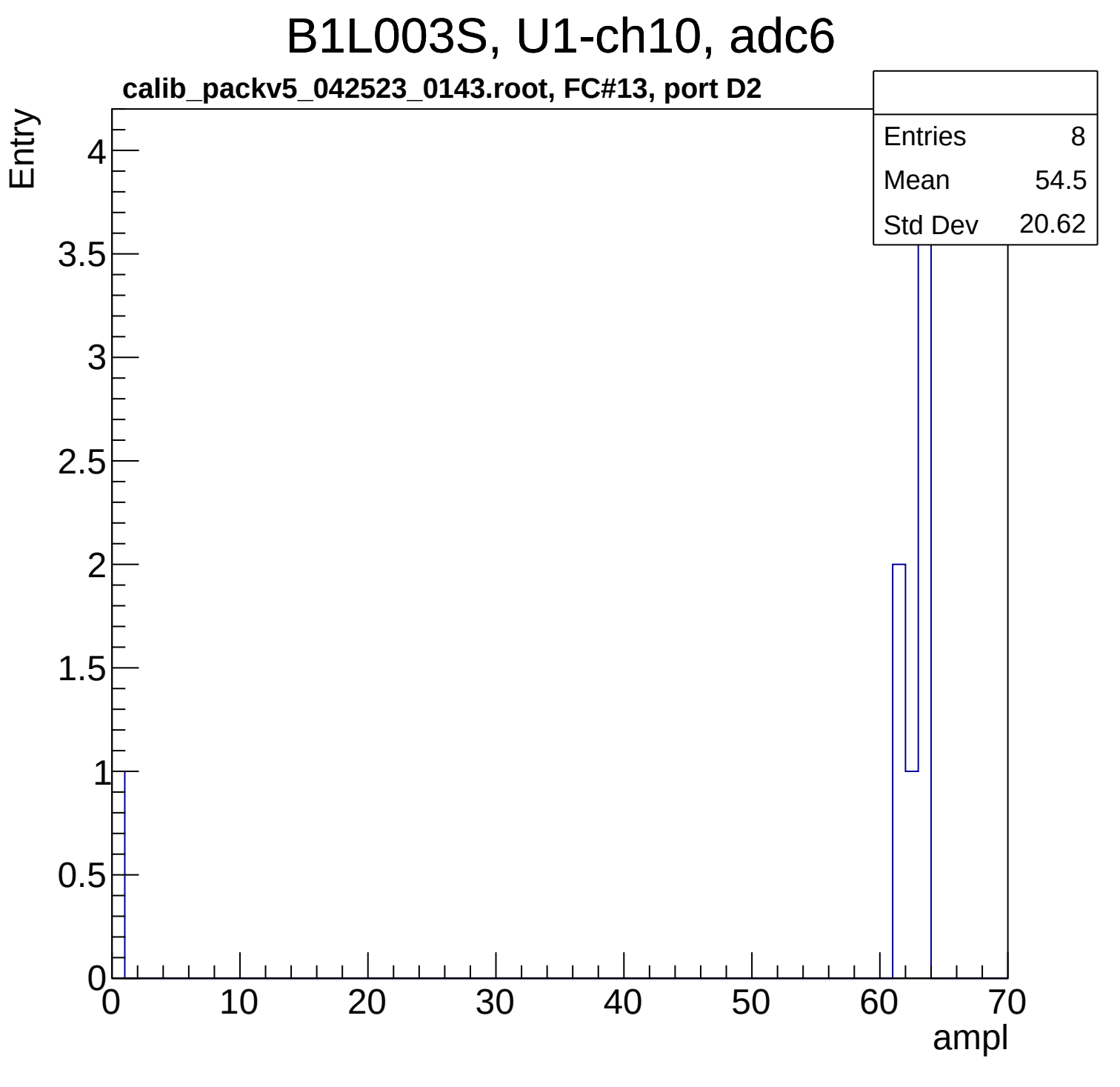
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch10, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.5
Std Dev 20.62
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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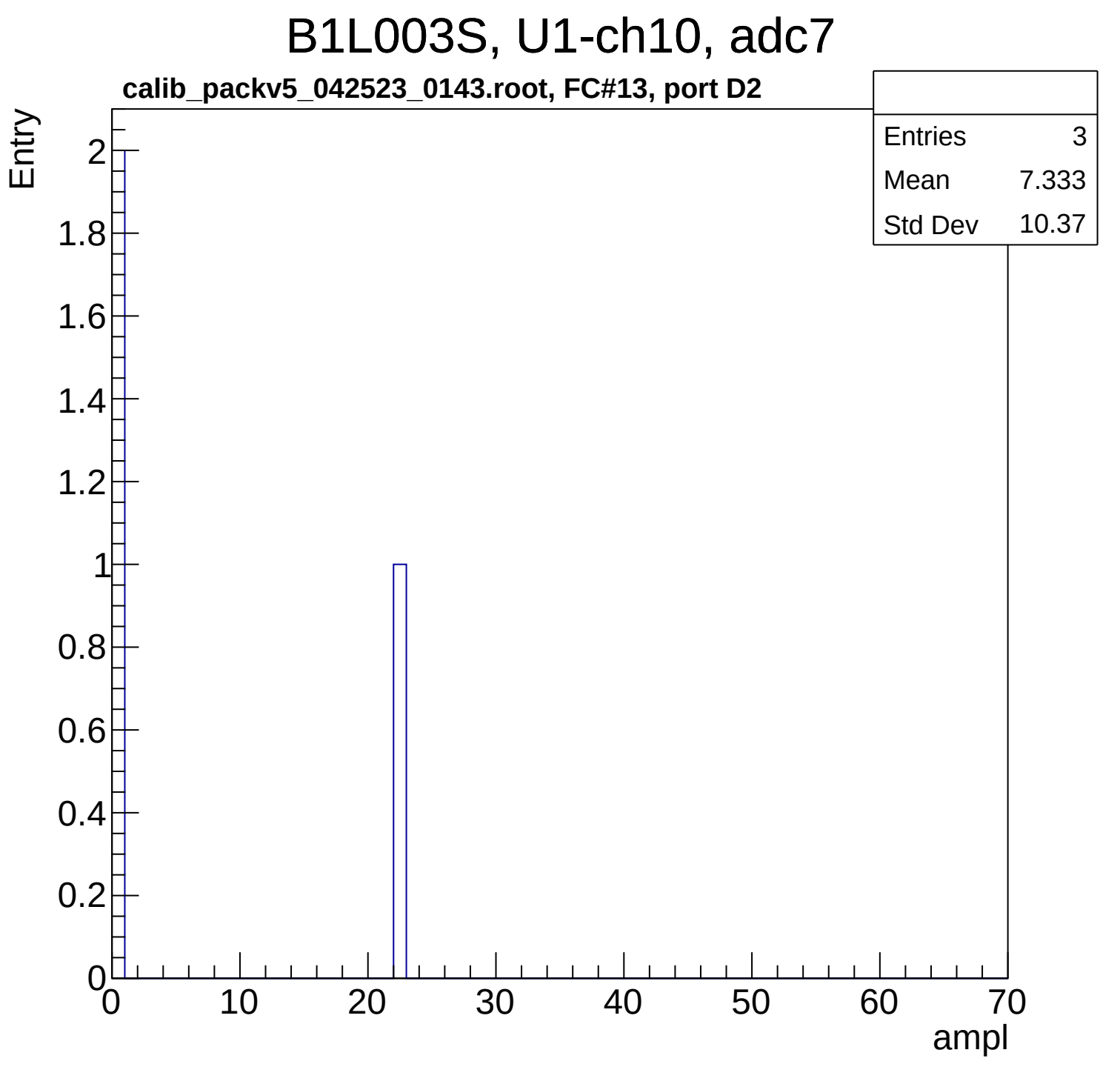
30
40
50
60
70


B1L003S, U1-ch10, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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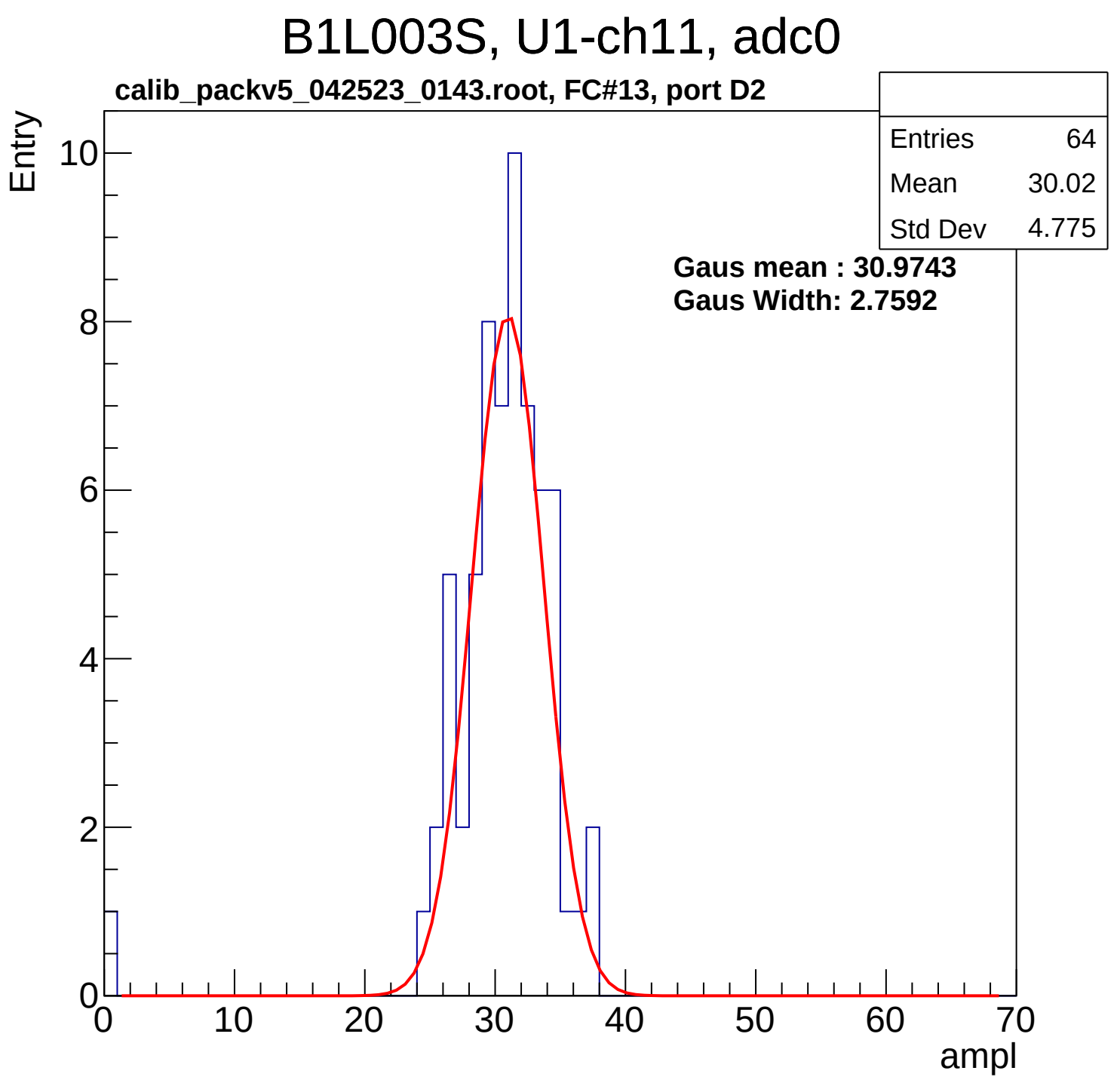
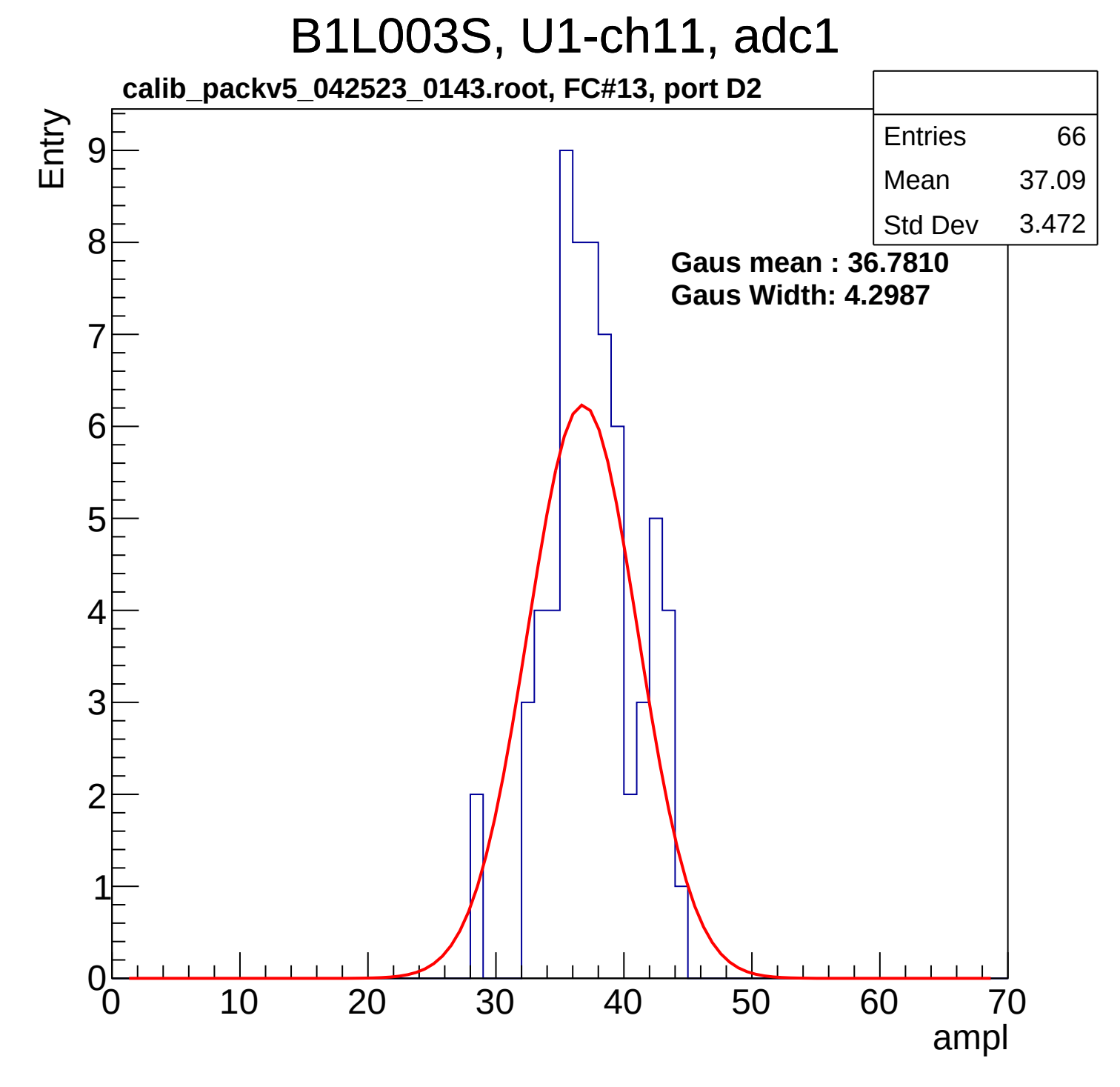

B1L003S, U1-ch11, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 30.02
Std Dev 4.775
Gaus mean : 30.9743
Gaus Width: 2.7592
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch11, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.09
Std Dev 3.472
Gaus mean : 36.7810
Gaus Width: 4.2987
Entry
ampl
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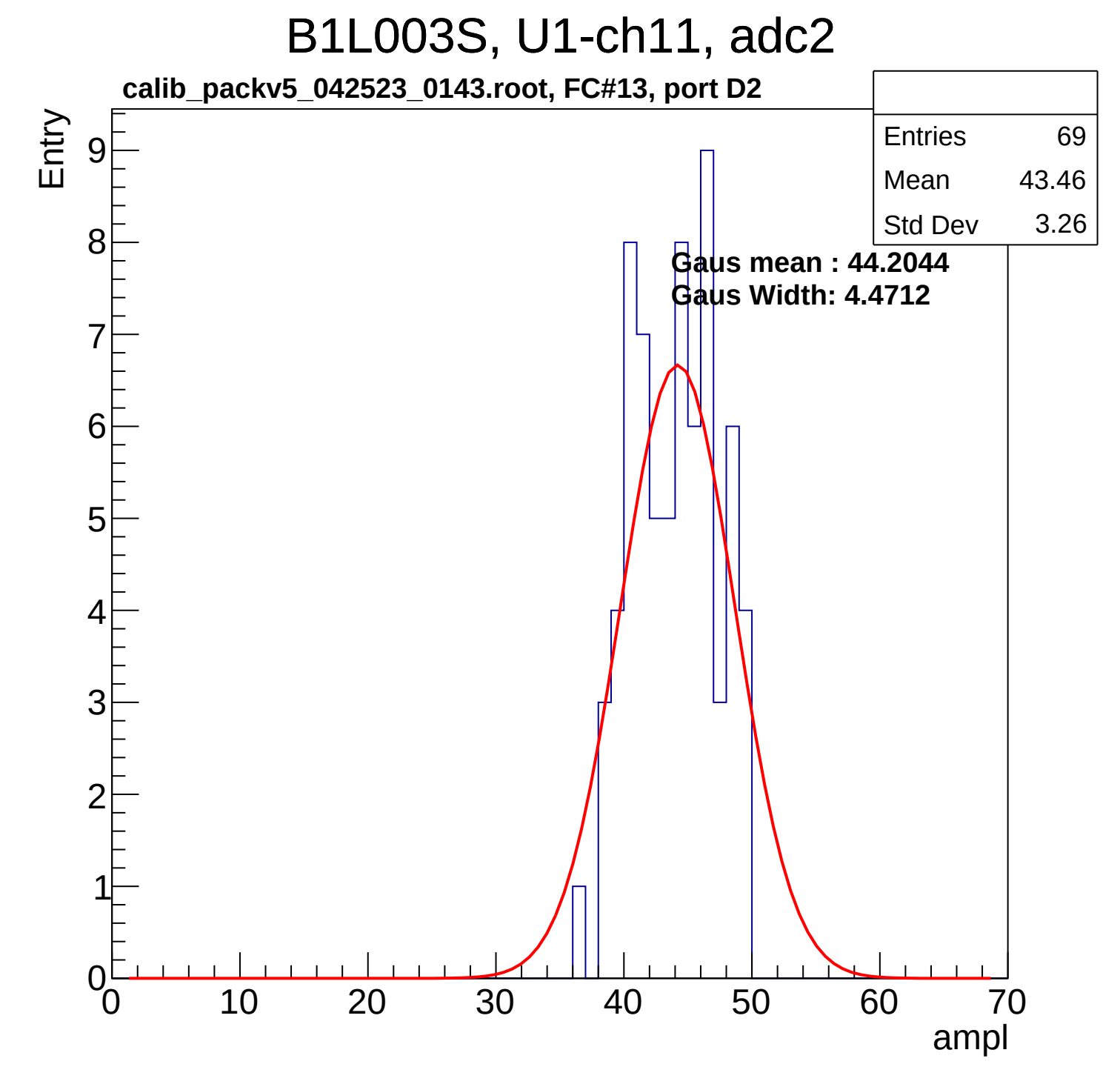
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch11, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.46
Std Dev 3.26
Gaus mean : 44.2044
Gaus Width: 4.4712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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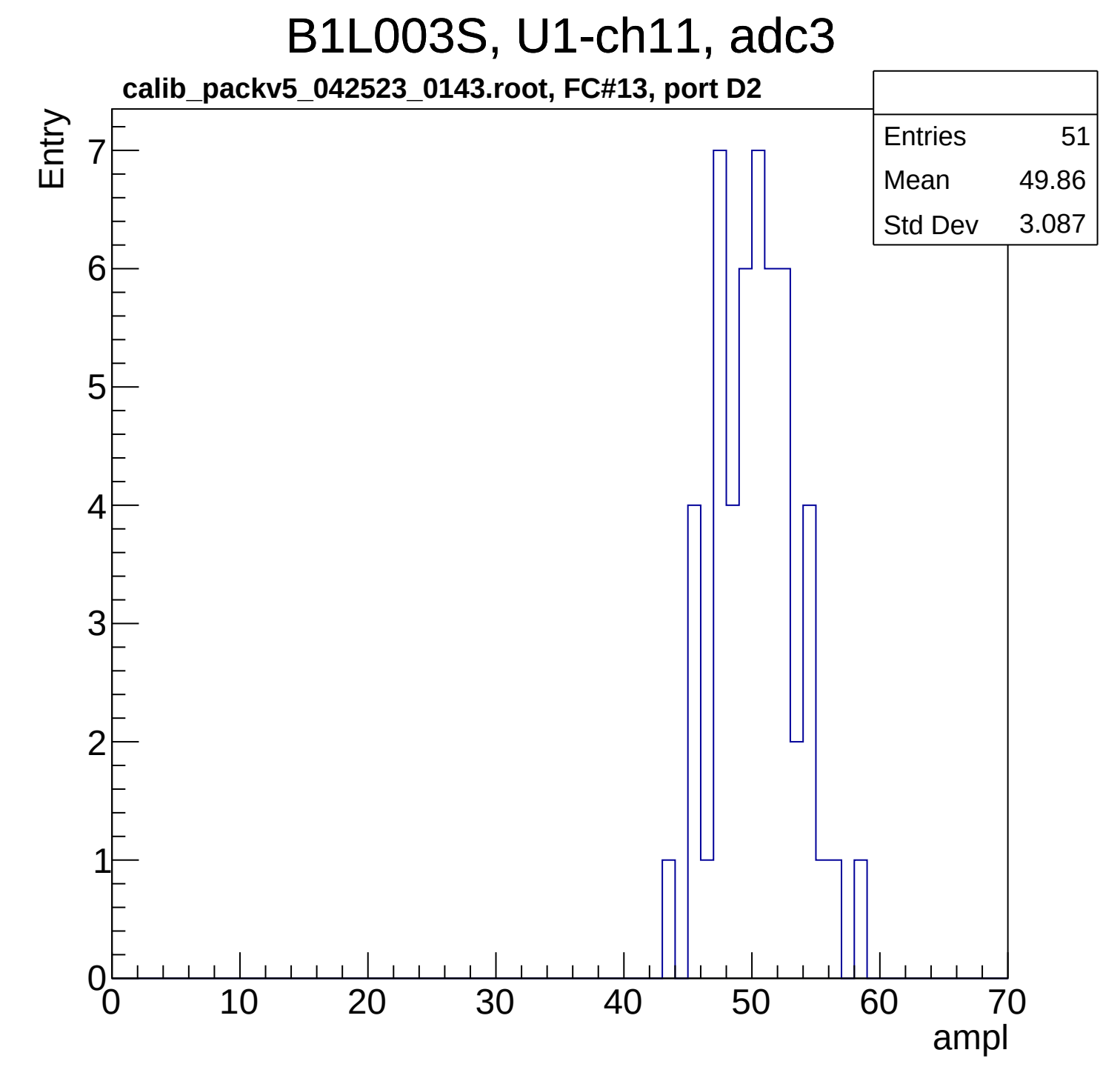
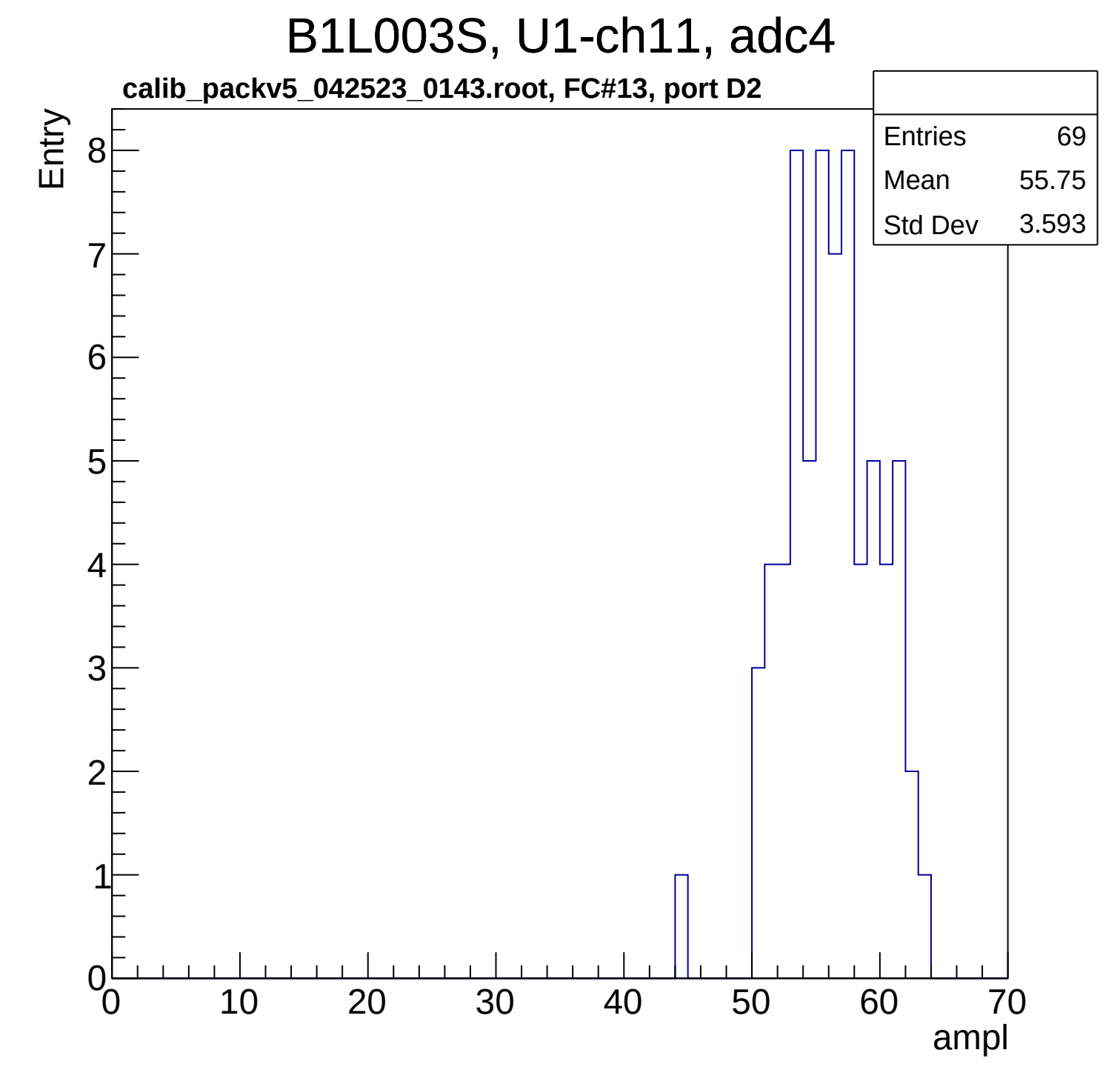
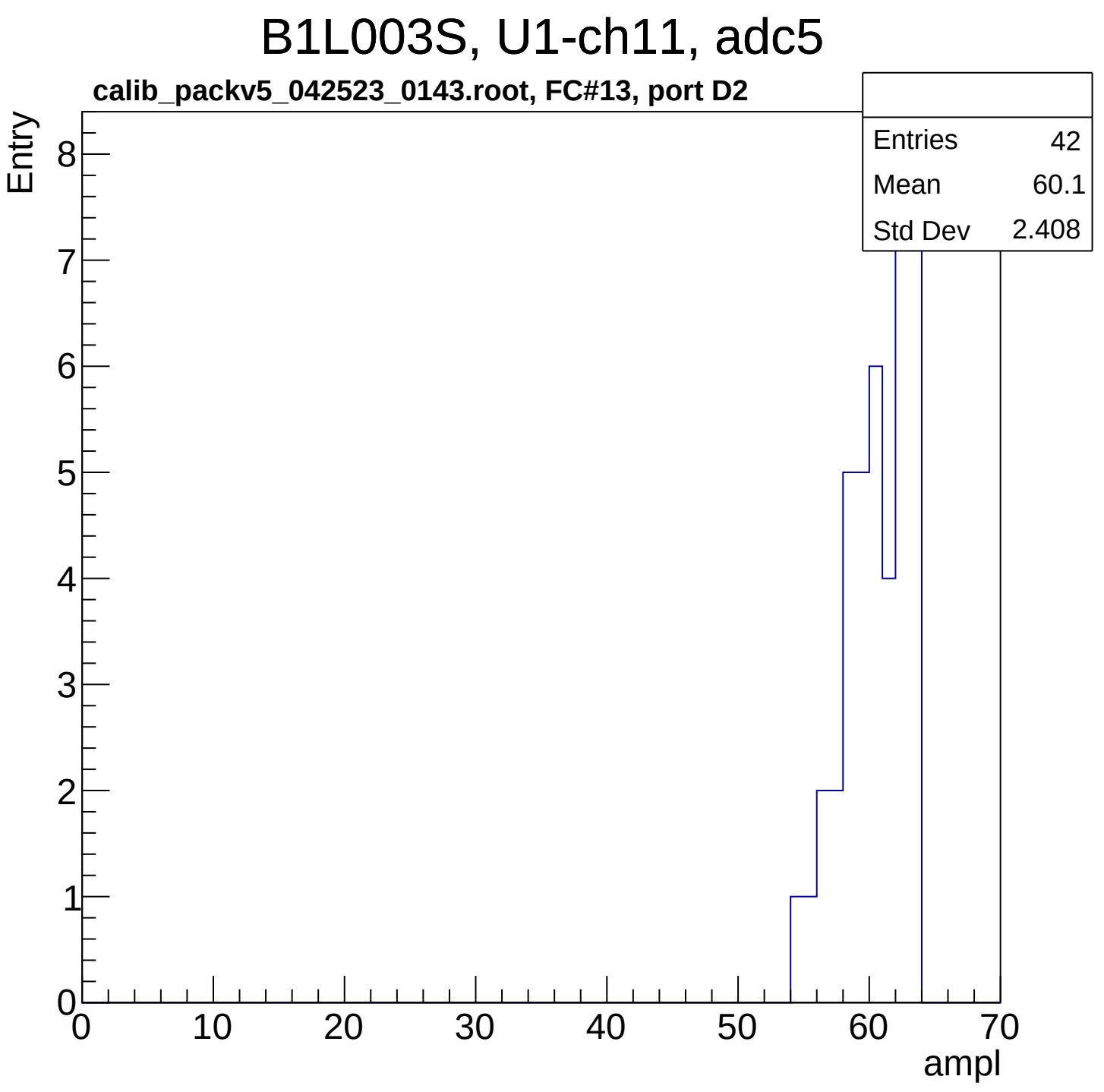
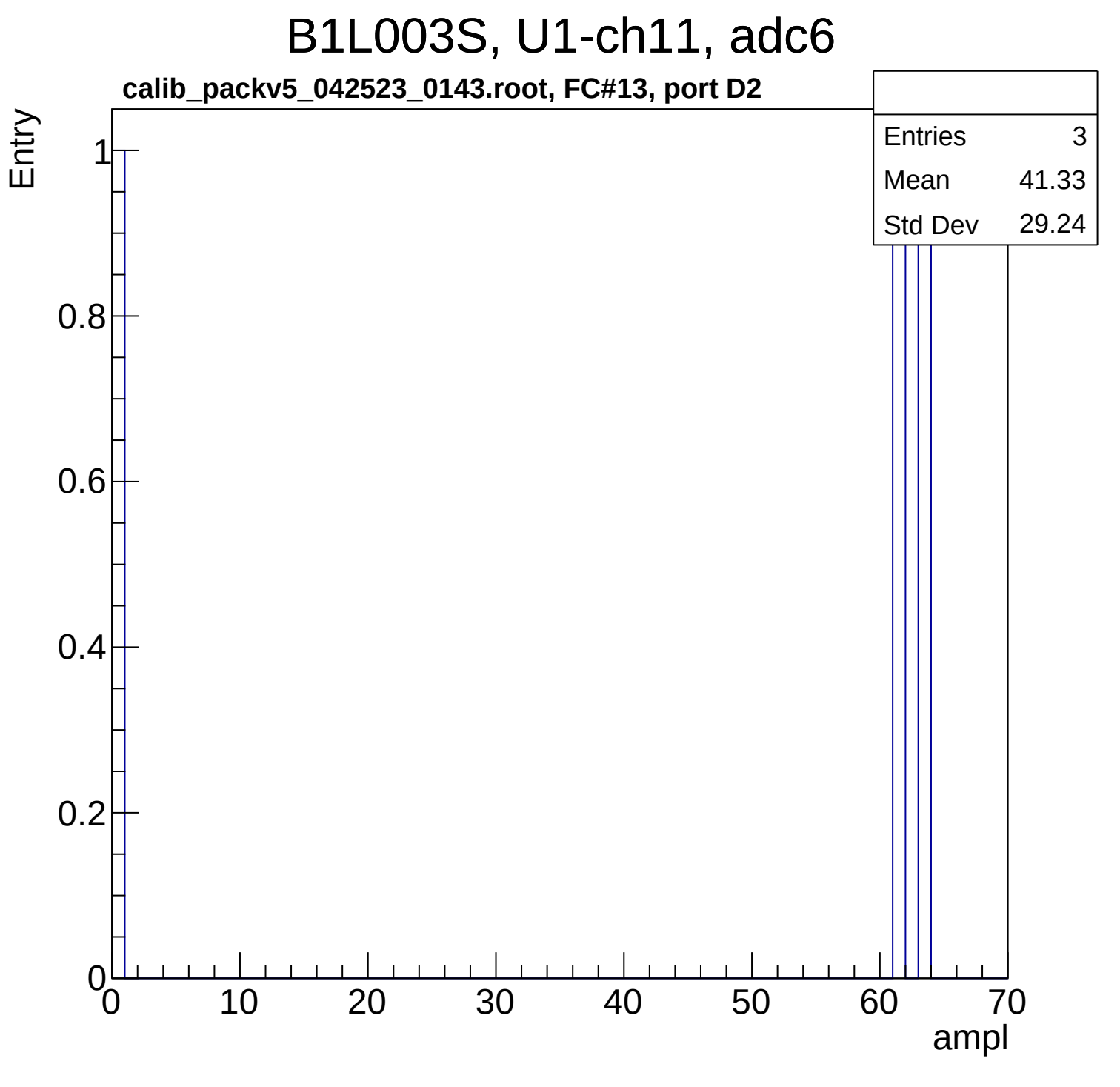
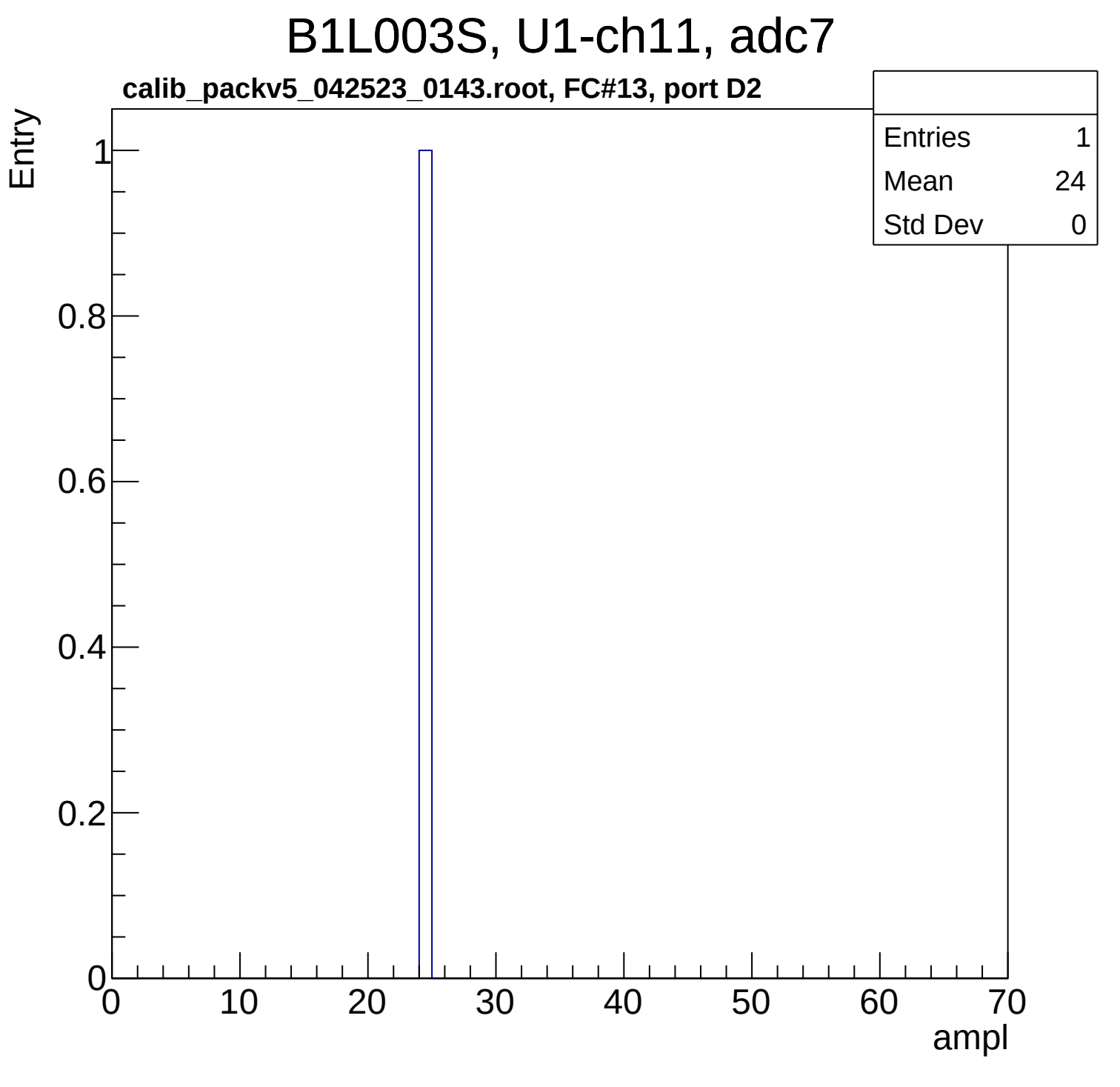

B1L003S, U1-ch11, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 49.86
Std Dev 3.087
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch11, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 55.75
Std Dev 3.593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch11, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 60.1
Std Dev 2.408
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch11, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch11, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
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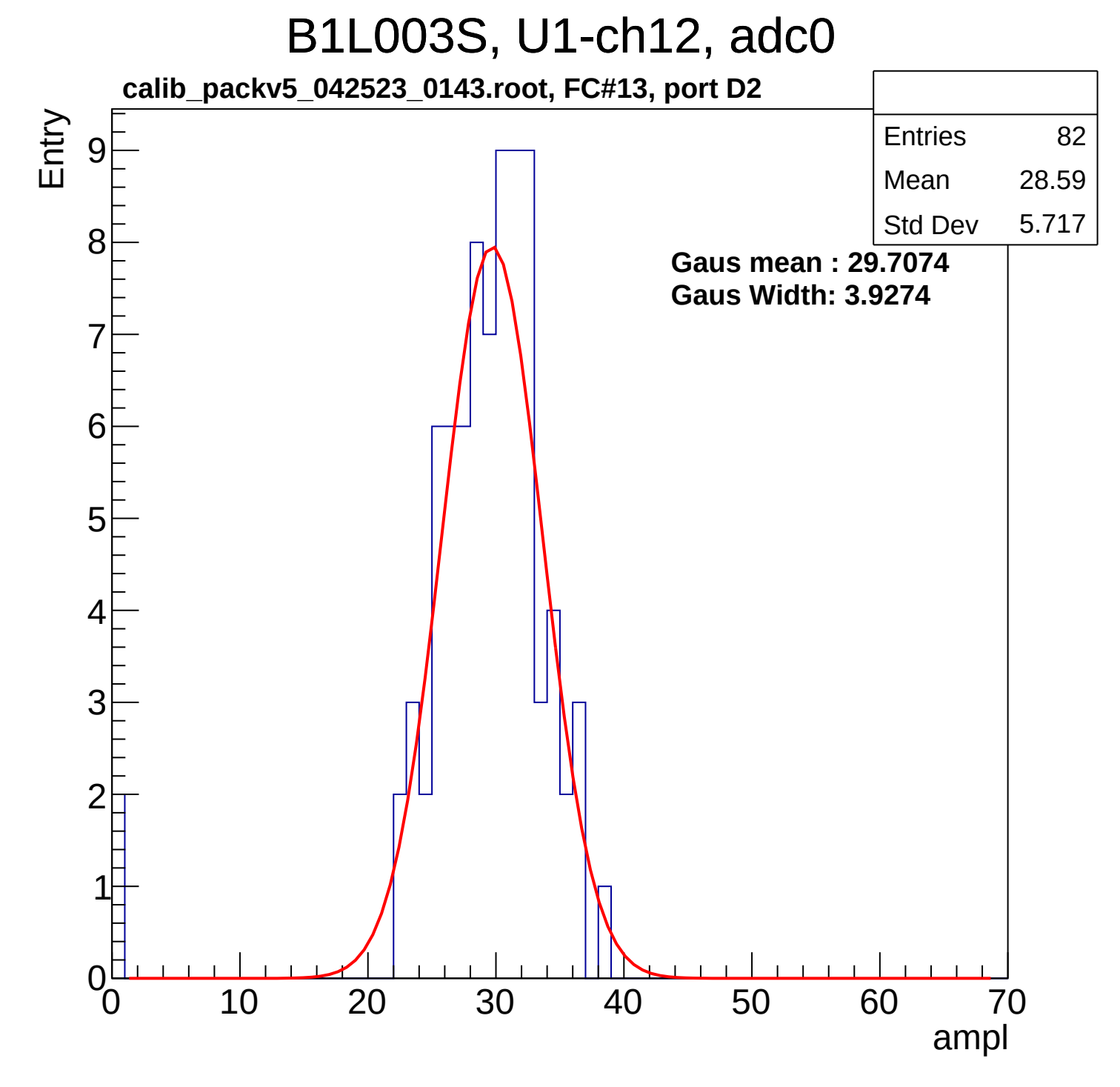
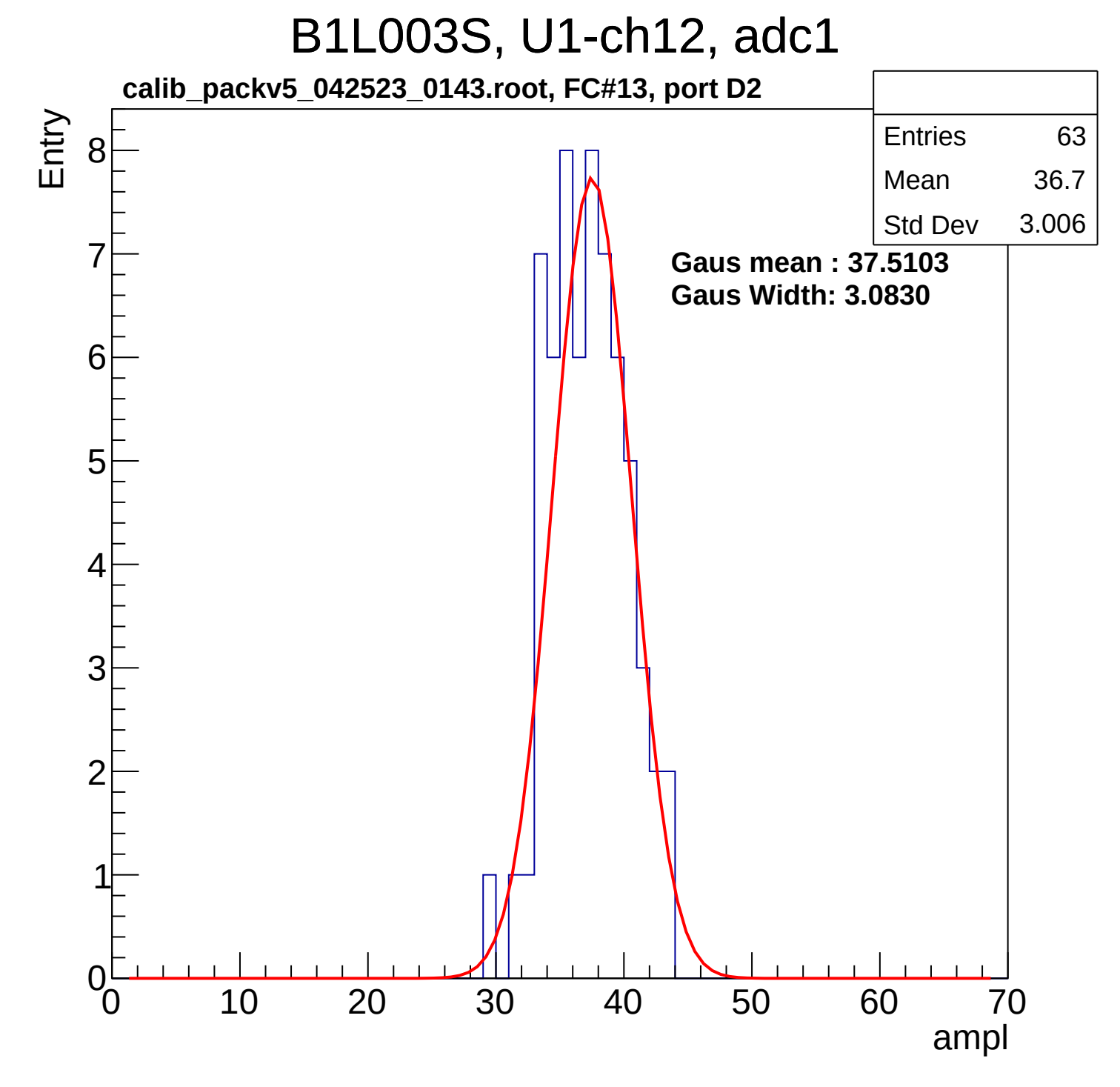
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch12, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 28.59
Std Dev 5.717
Gaus mean : 29.7074
Gaus Width: 3.9274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch12, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.7
Std Dev 3.006
Gaus mean : 37.5103
Gaus Width: 3.0830
Entry
ampl
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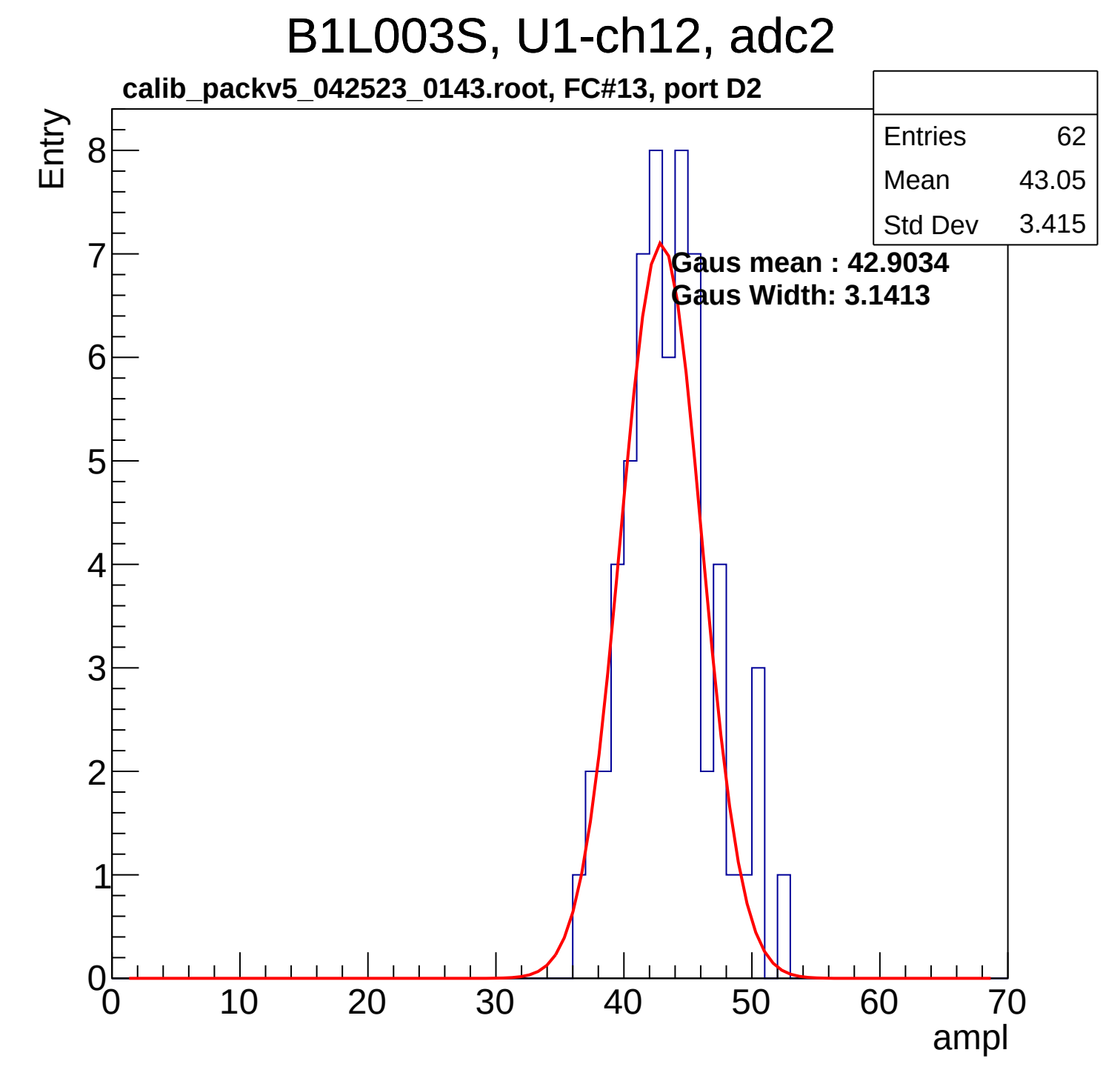
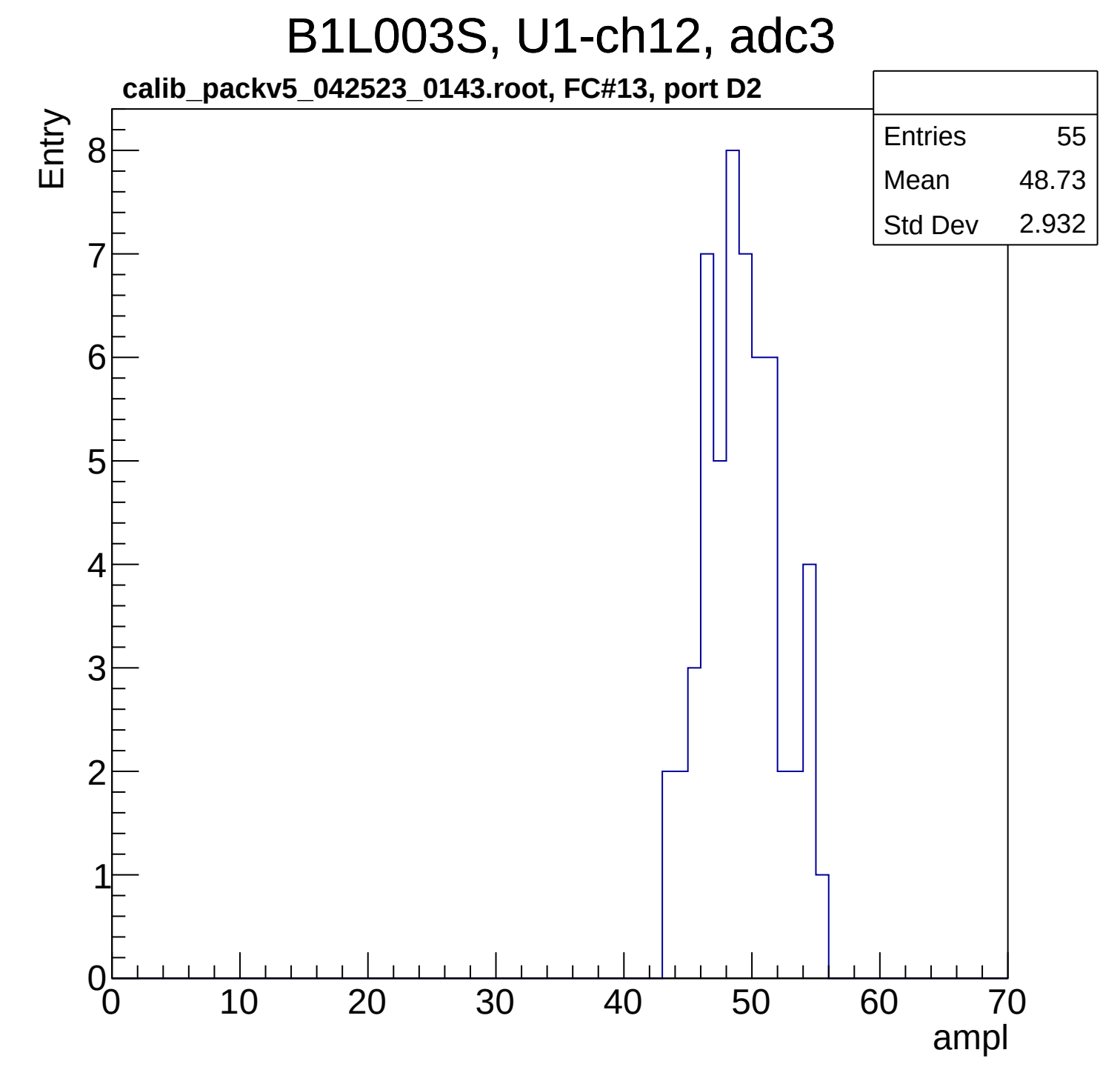
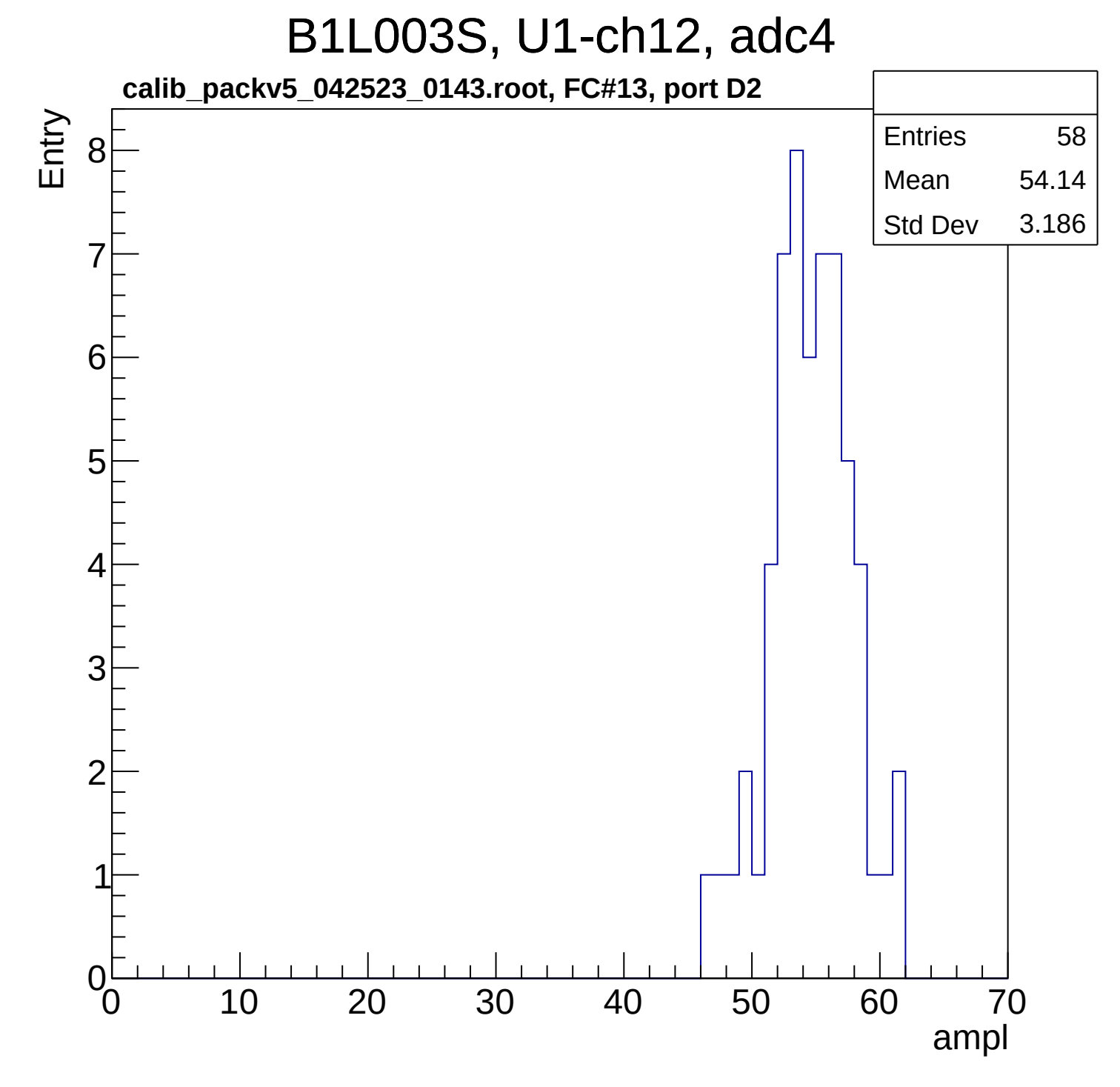
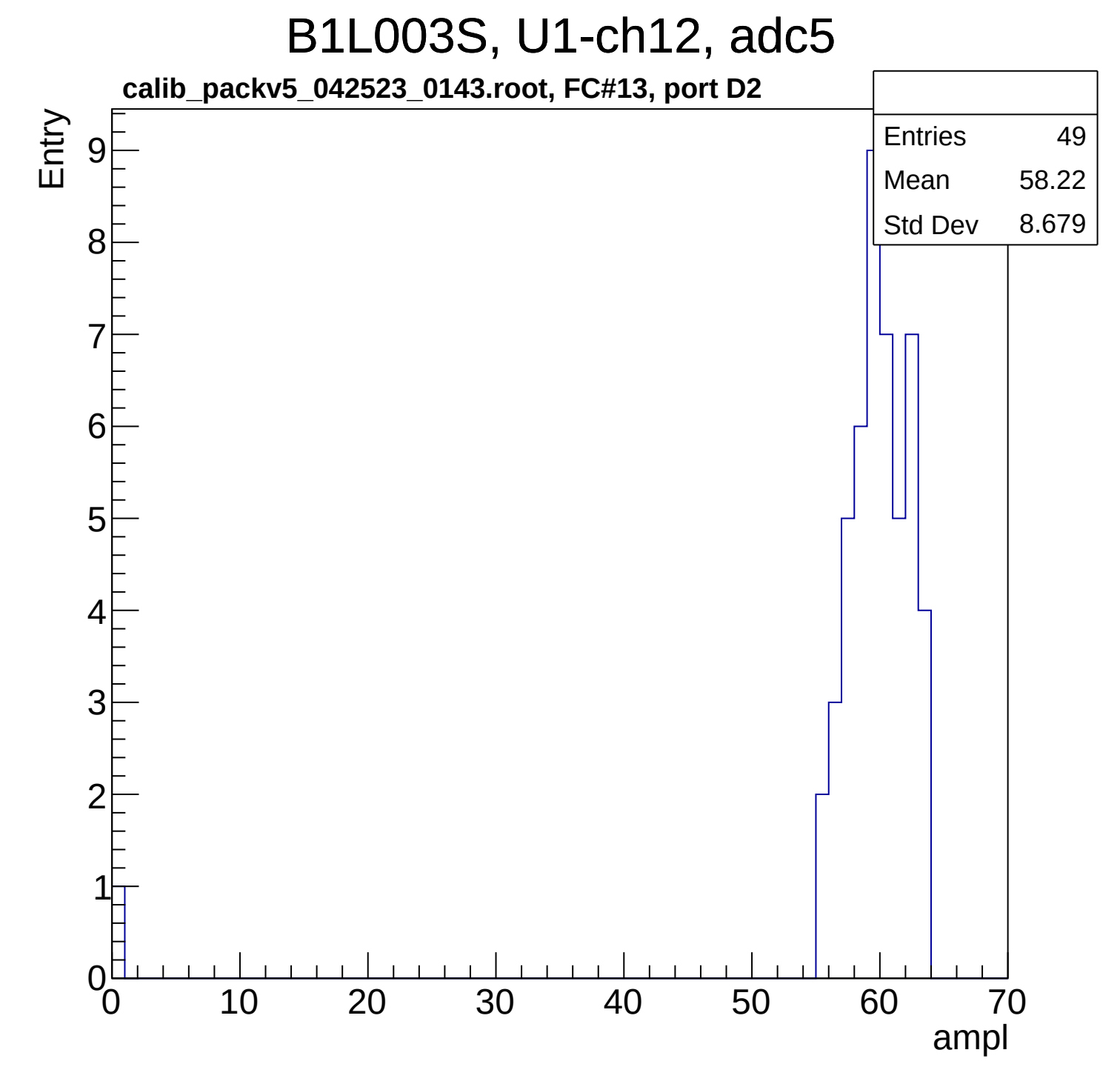
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch12, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.05
Std Dev 3.415
Gaus mean : 42.9034
Gaus Width: 3.1413
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch12, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 48.73
Std Dev 2.932
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch12, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.14
Std Dev 3.186
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch12, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.22
Std Dev 8.679
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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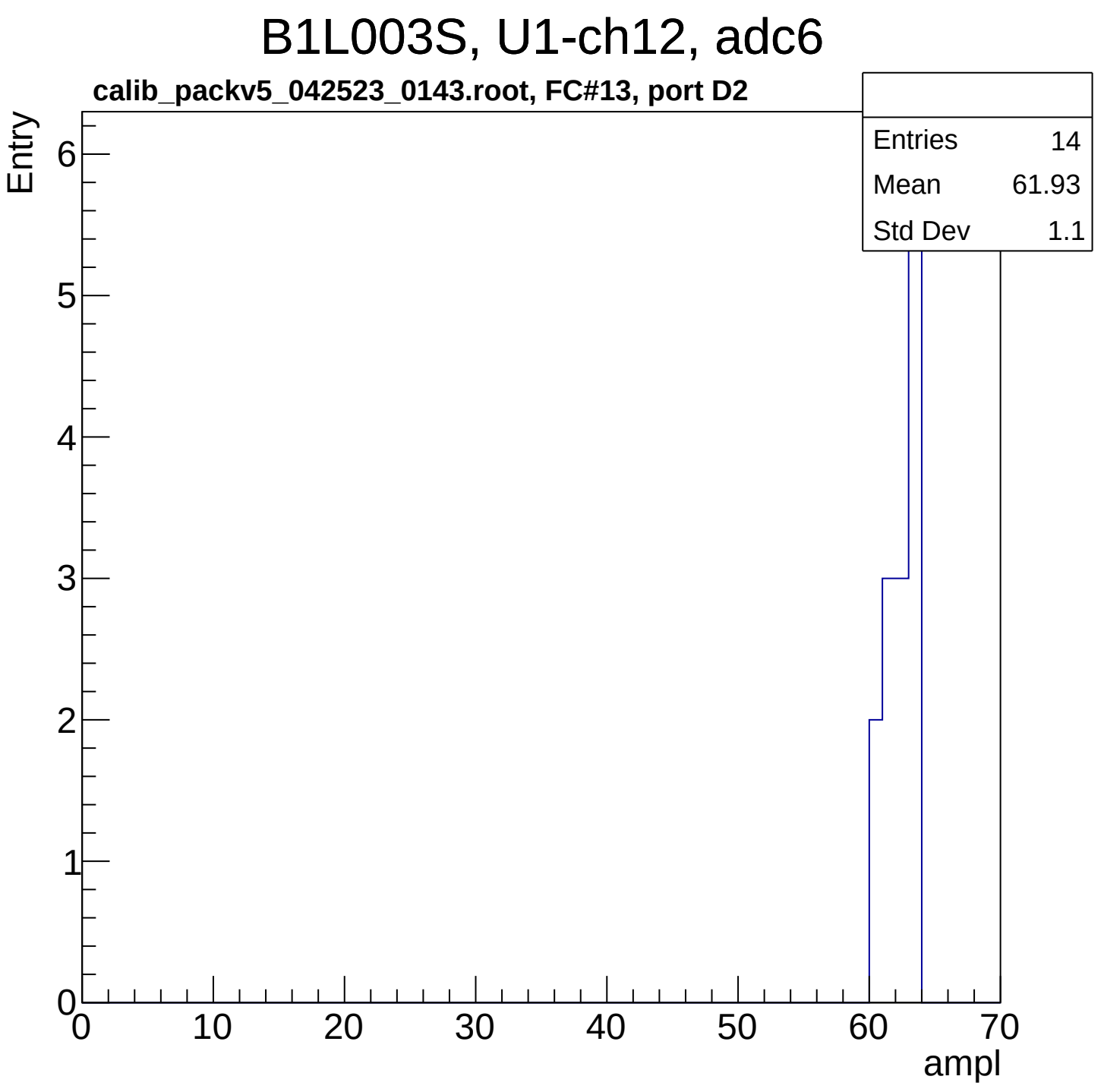
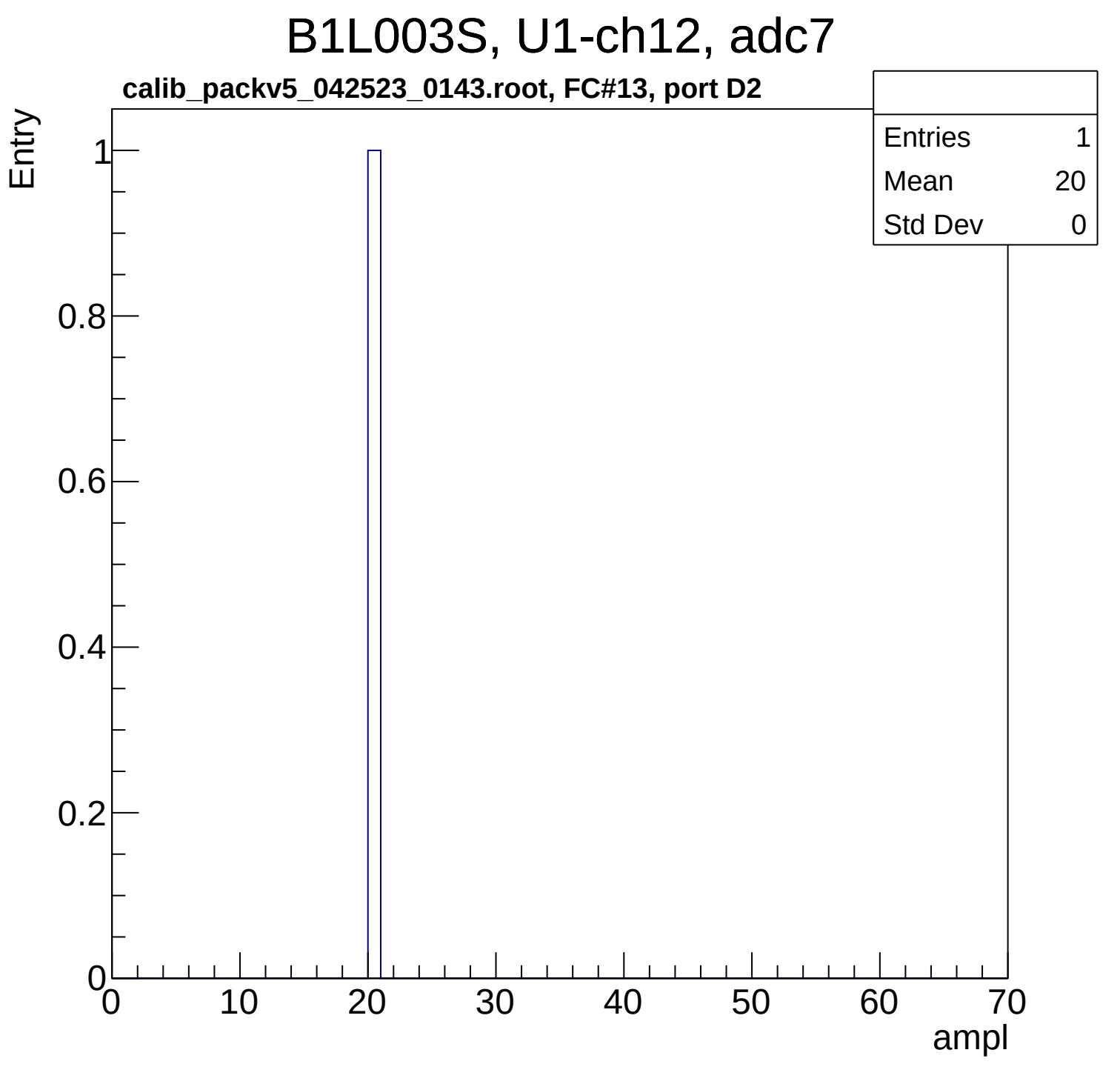
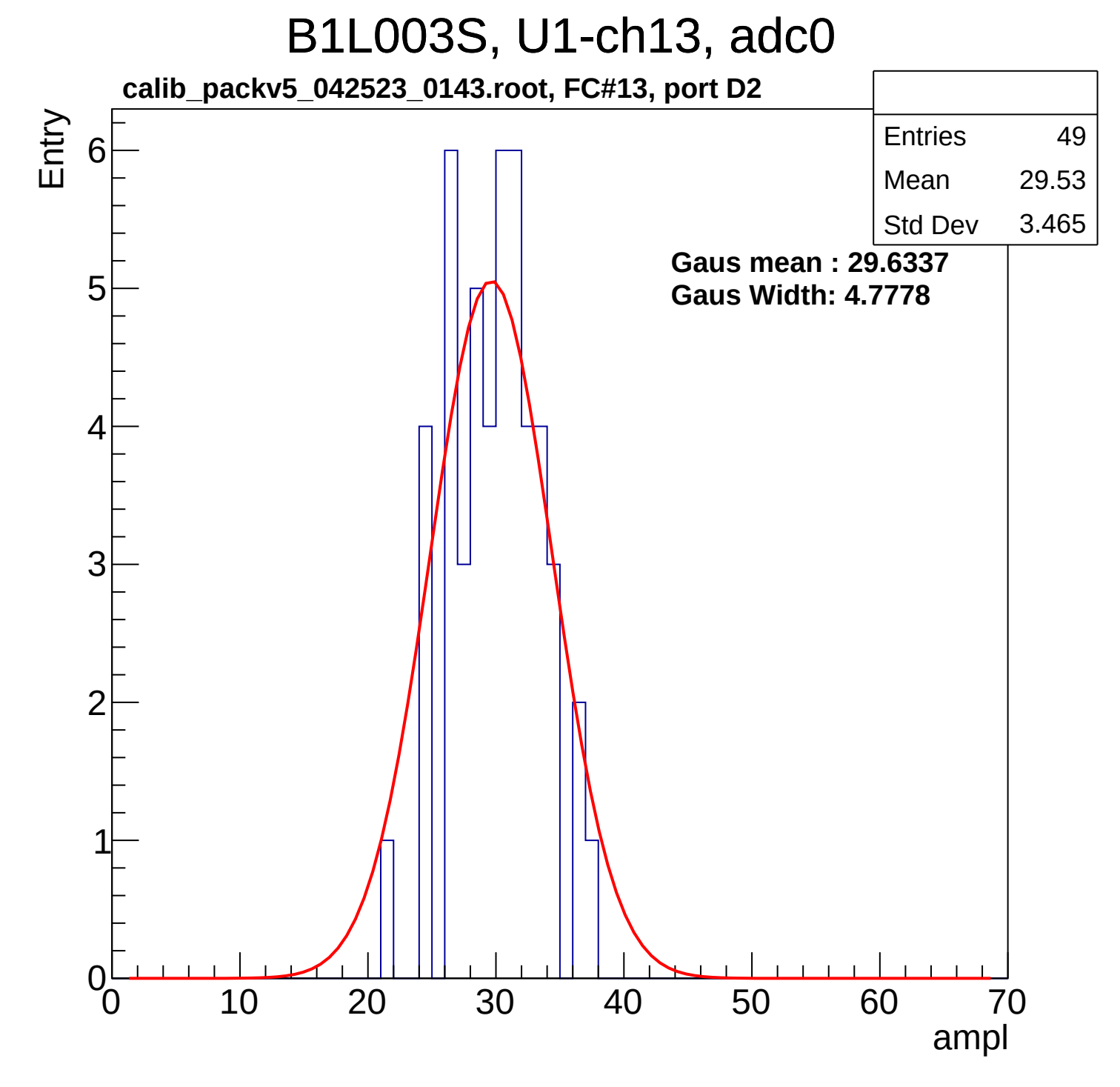
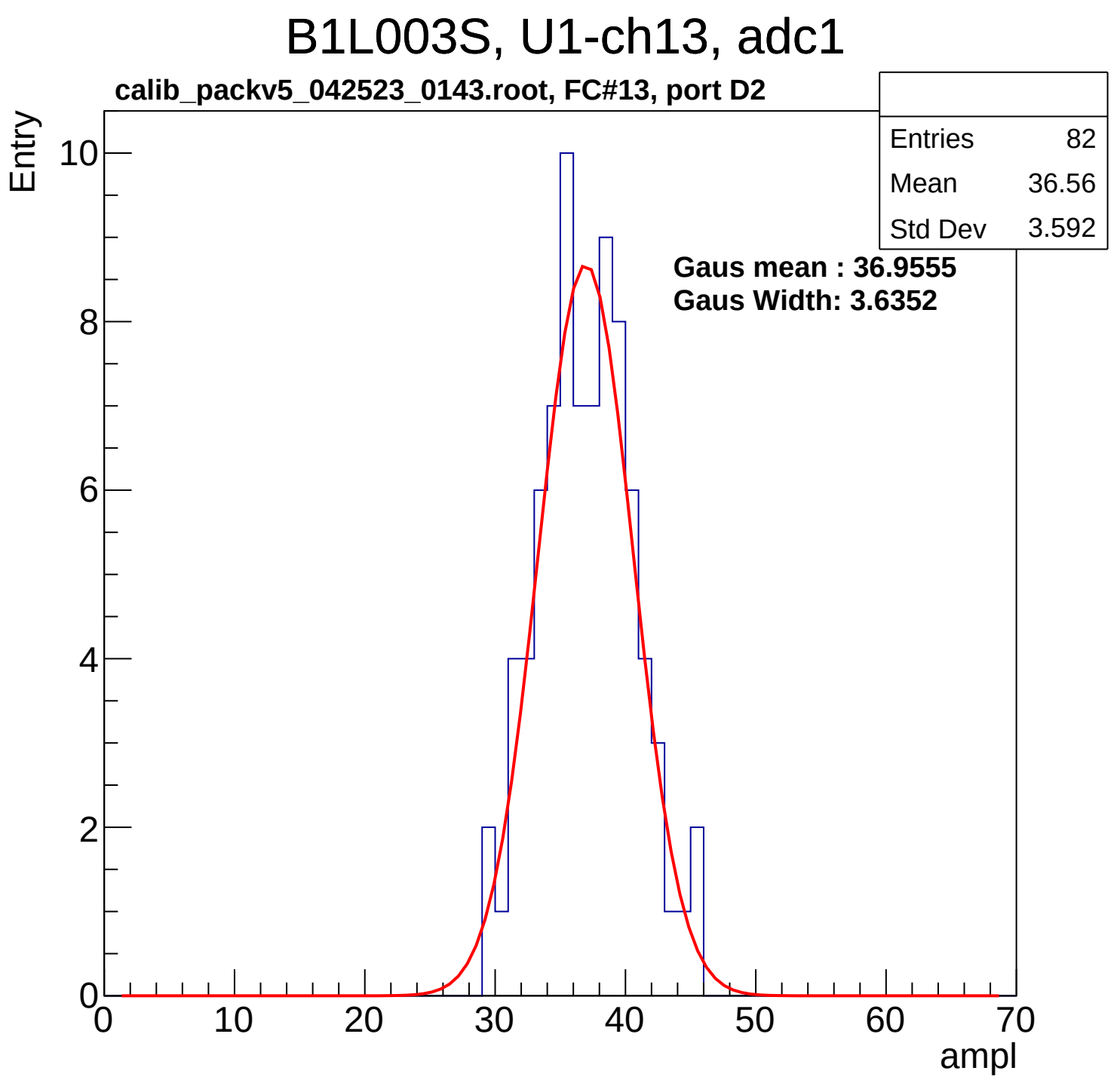
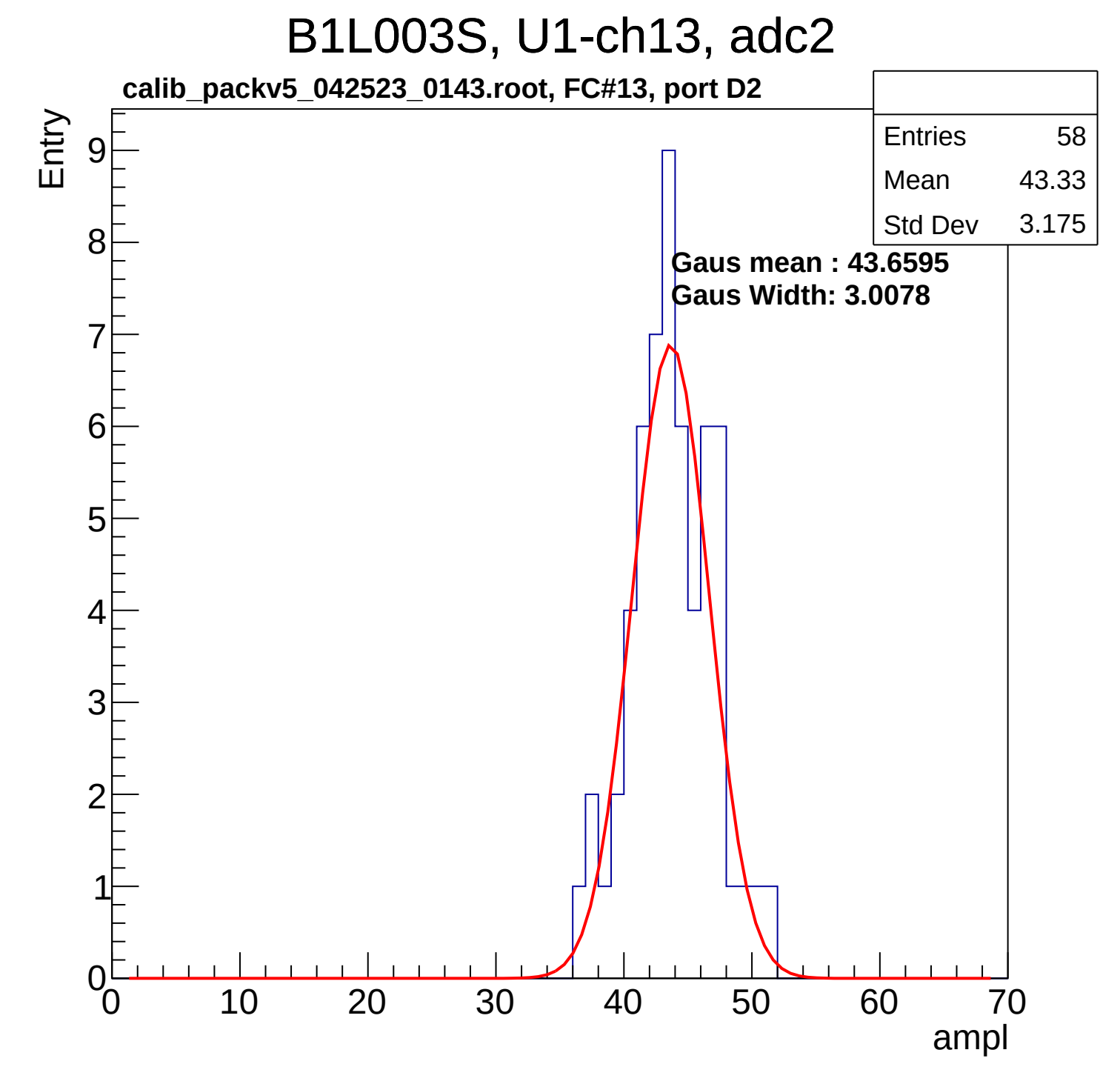
60
70

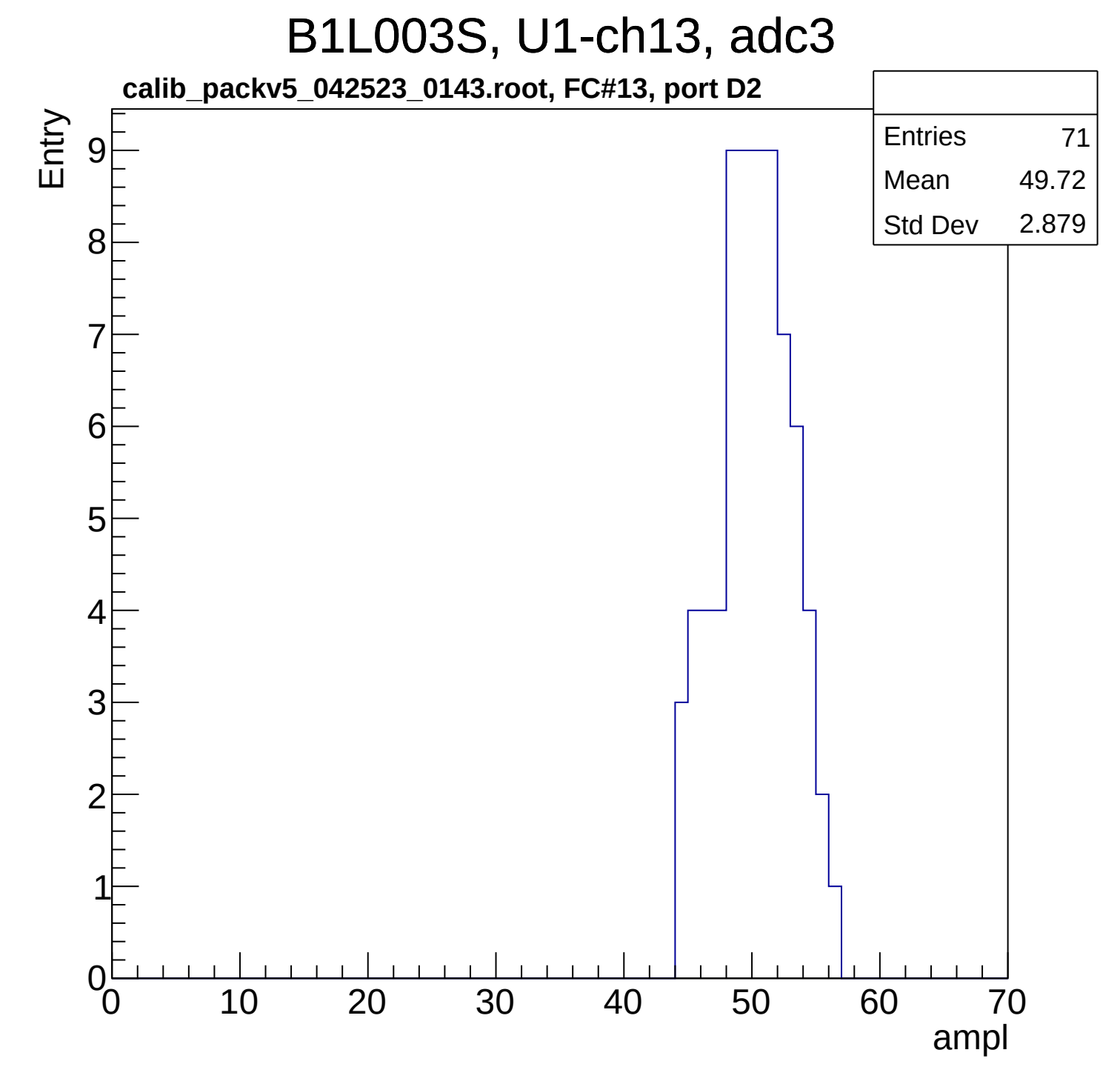

B1L003S, U1-ch12, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.93
Std Dev 1.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

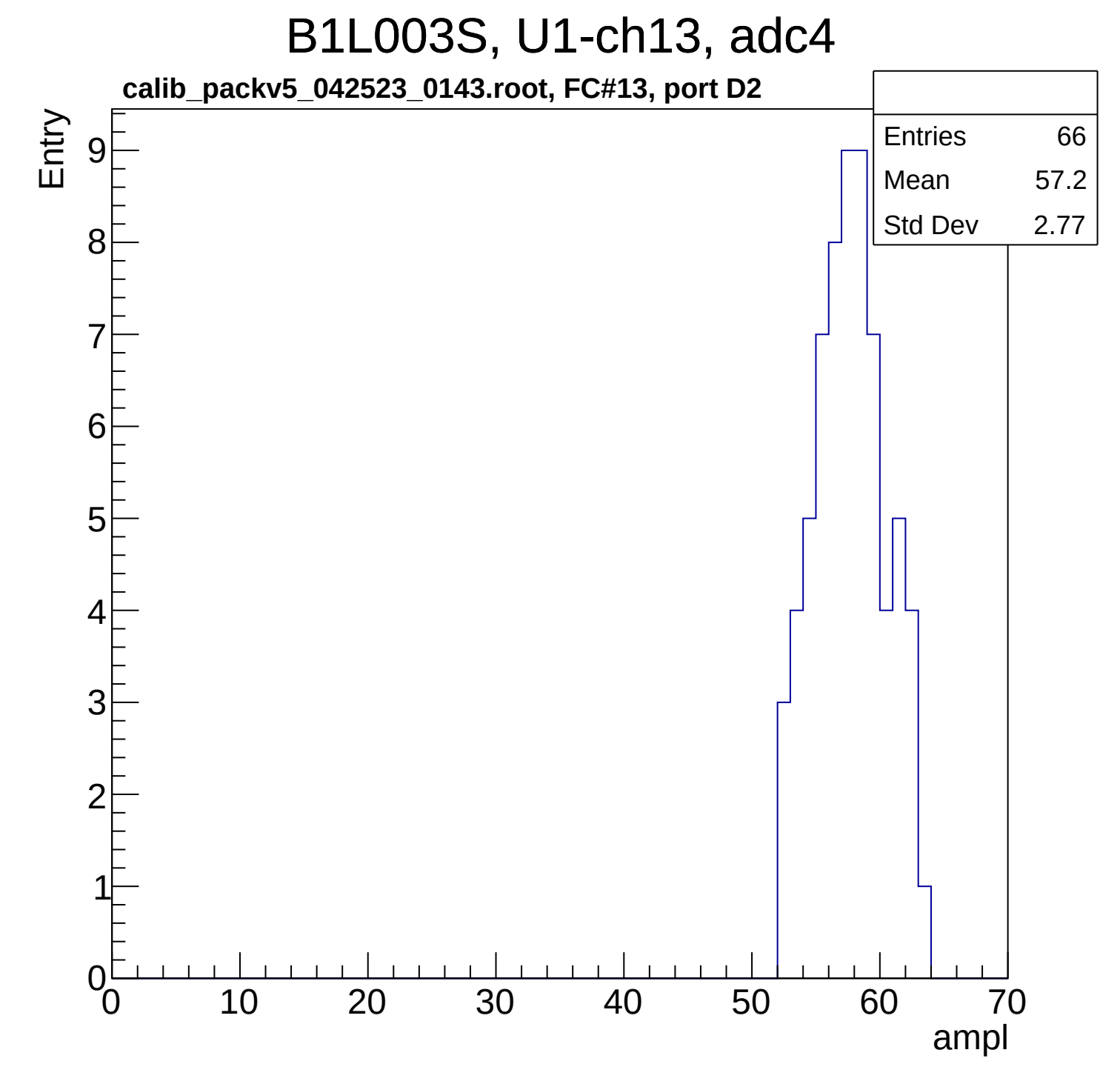

B1L003S, U1-ch12, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch13, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.53
Std Dev 3.465
Gaus mean : 29.6337
Gaus Width: 4.7778
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch13, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 36.56
Std Dev 3.592
Gaus mean : 36.9555
Gaus Width: 3.6352
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch13, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.33
Std Dev 3.175
Gaus mean : 43.6595
Gaus Width: 3.0078
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch13, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.72
Std Dev 2.879
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch13, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 57.2
Std Dev 2.77
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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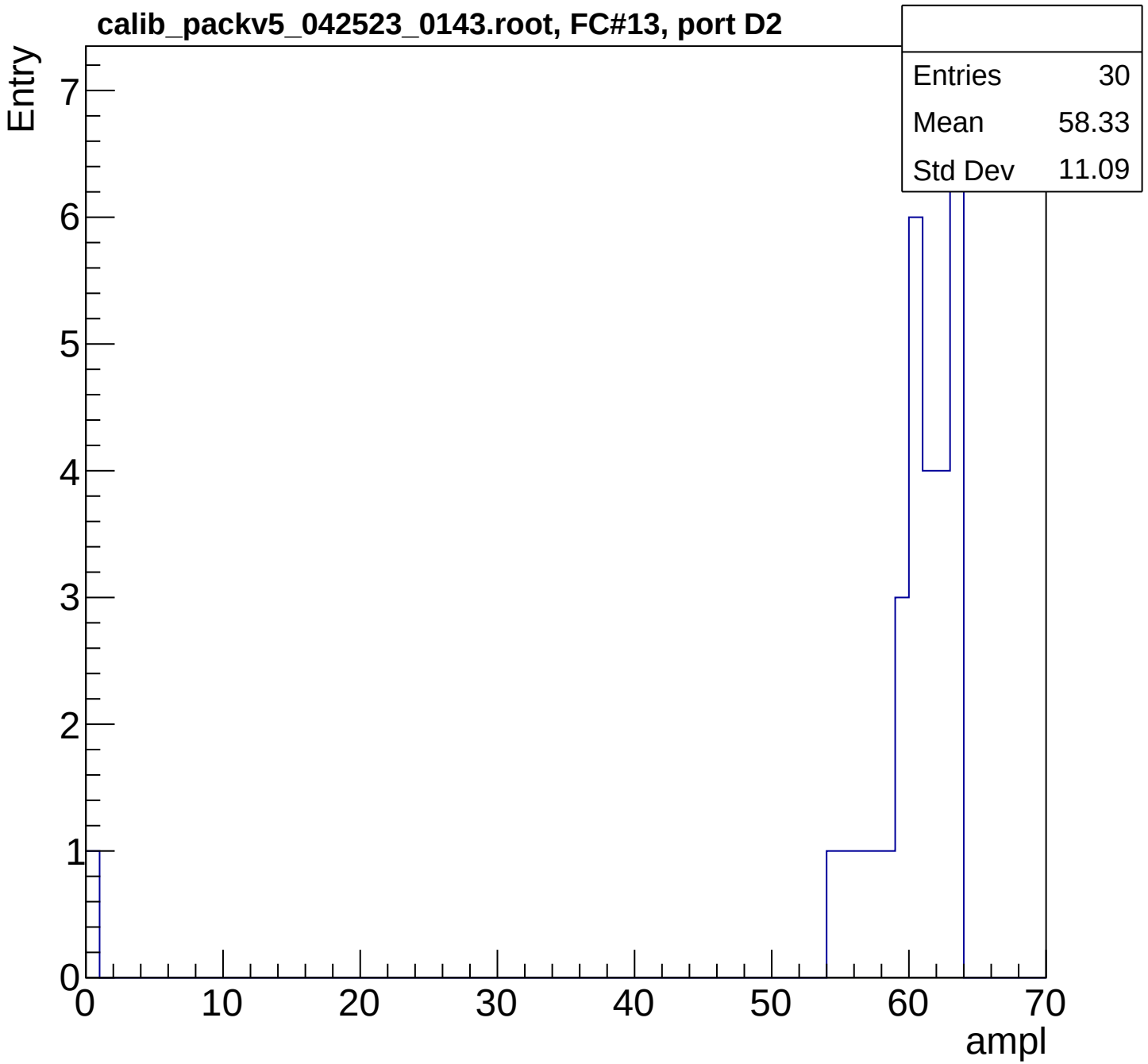
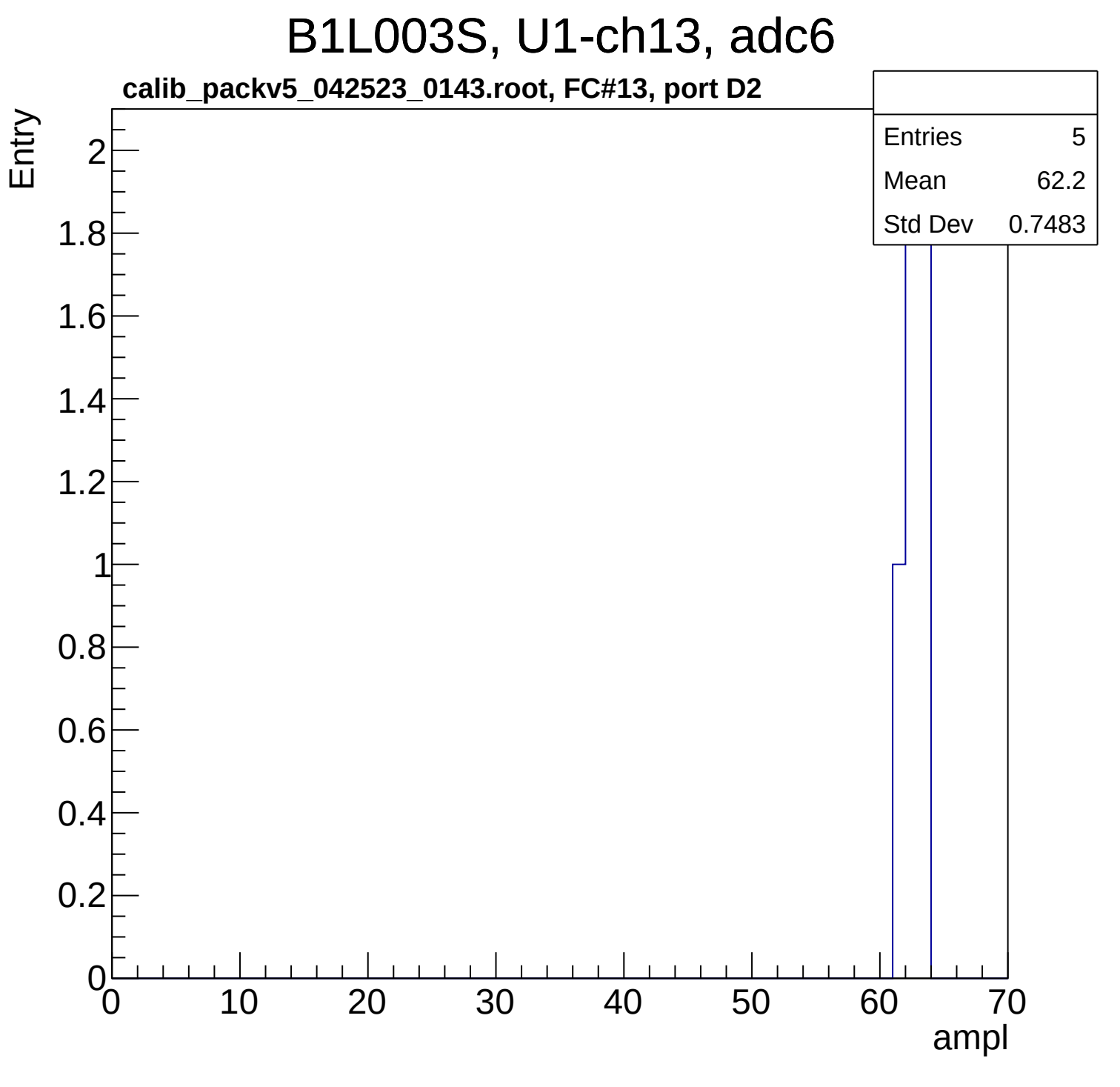
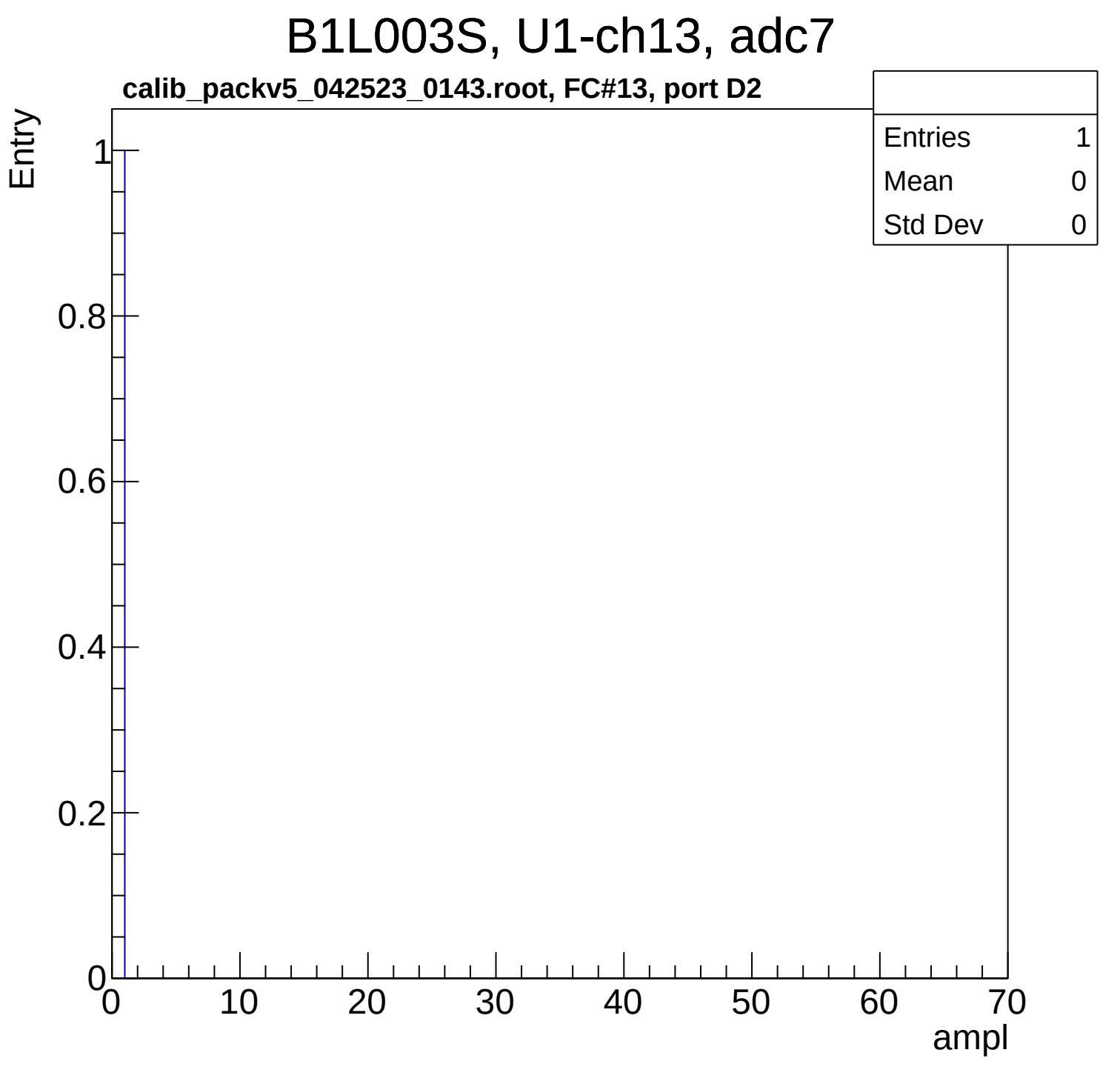
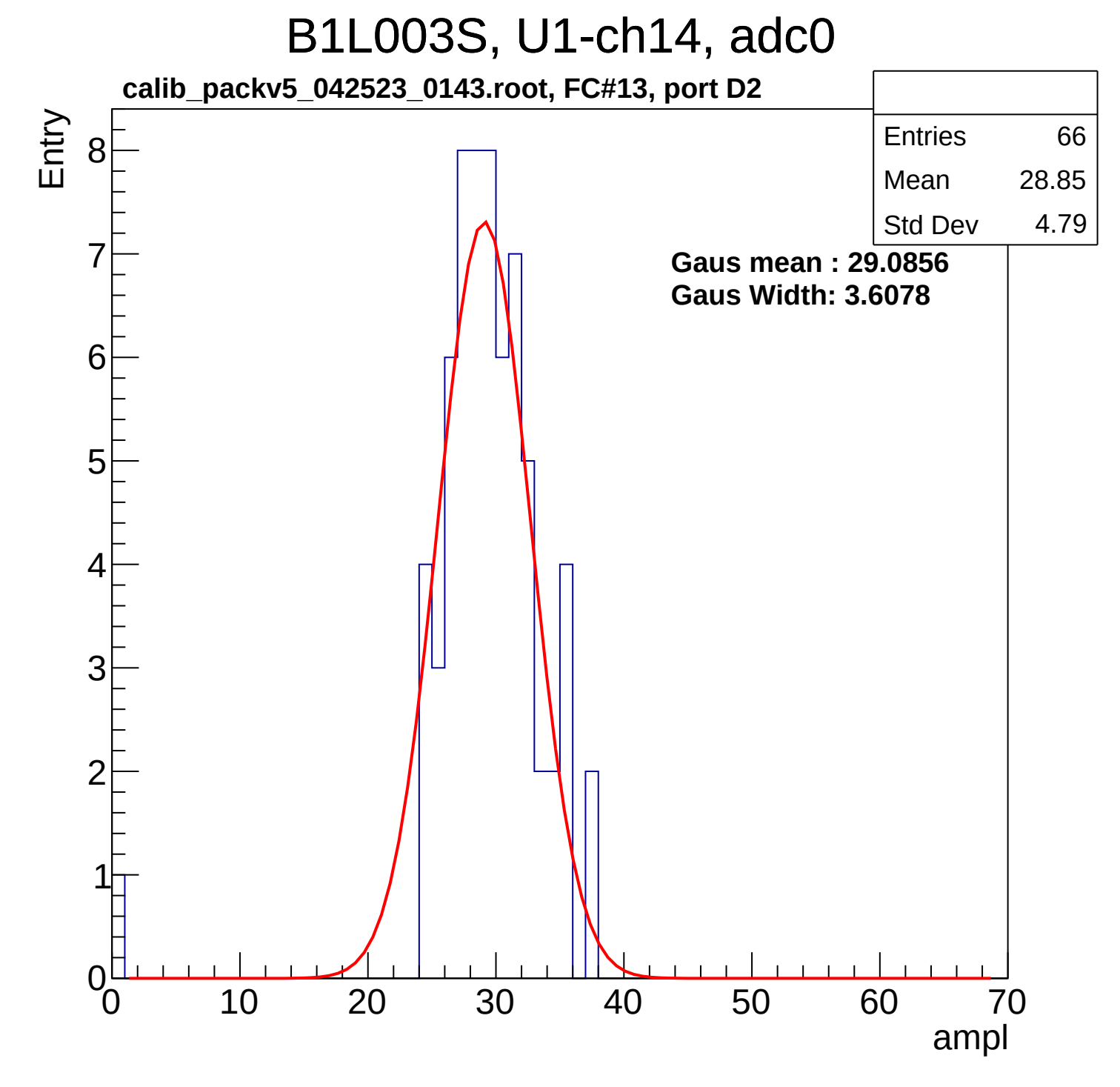
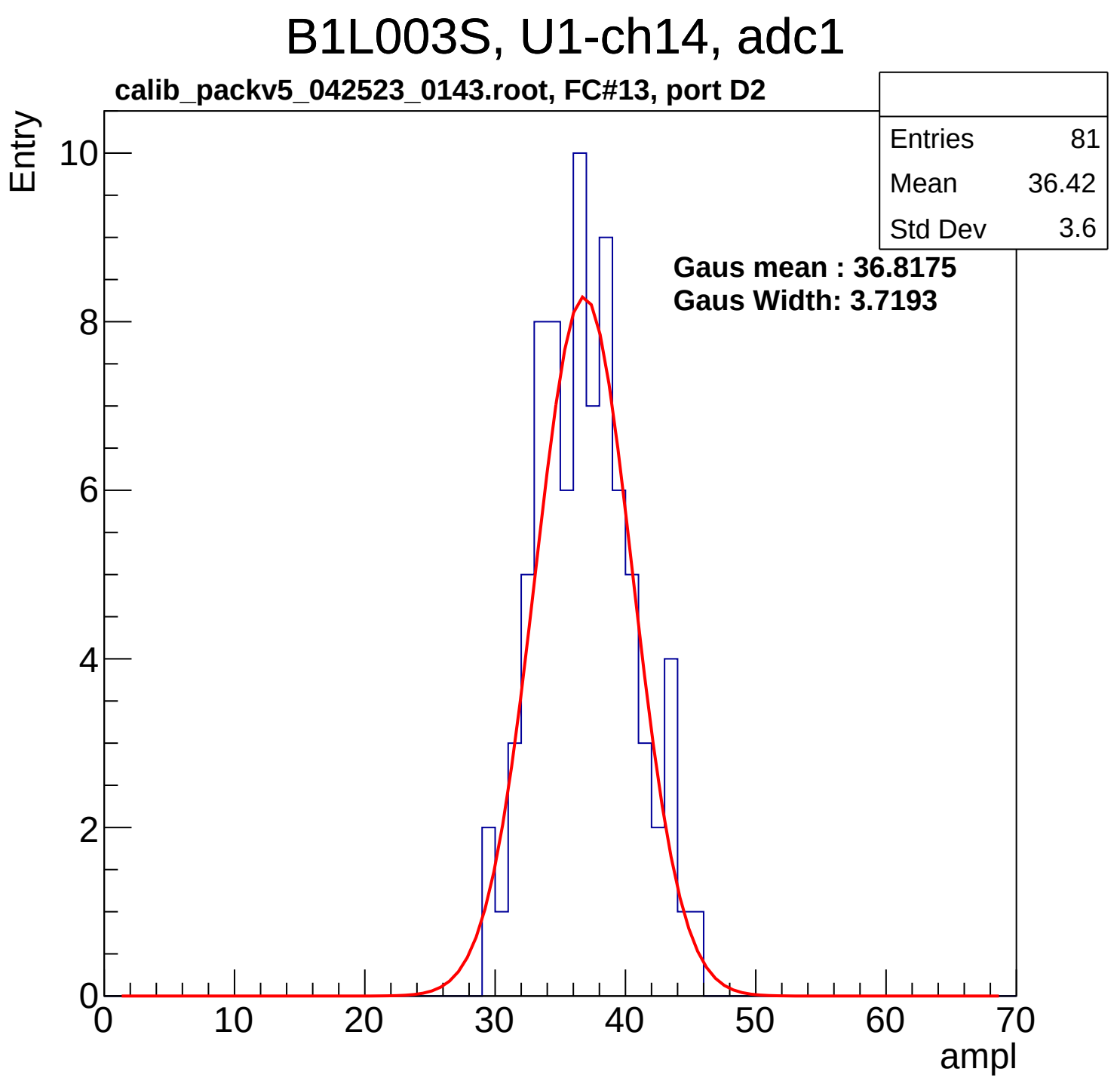
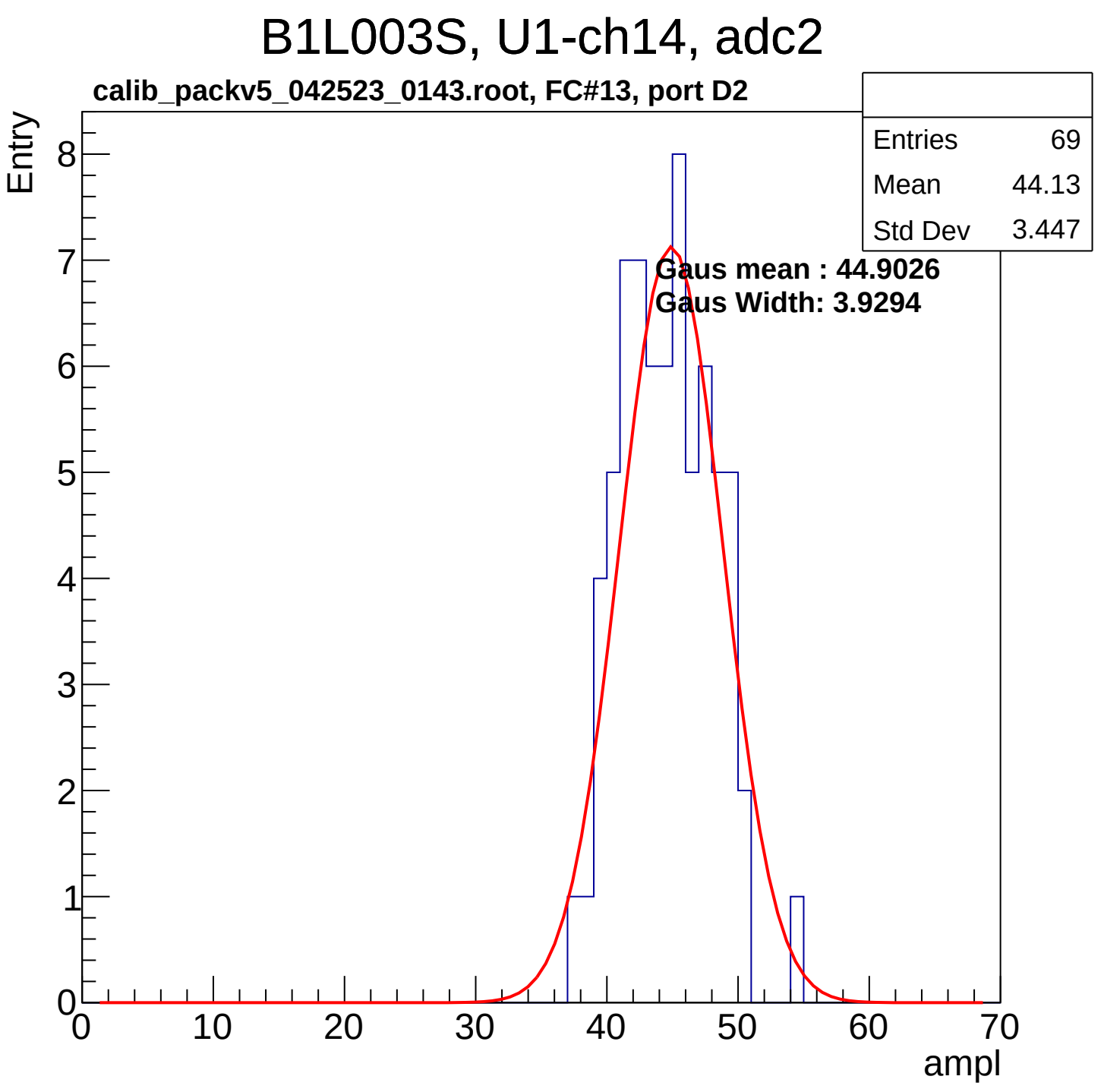

B1L003S, U1-ch13, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 58.33
Std Dev 11.09
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

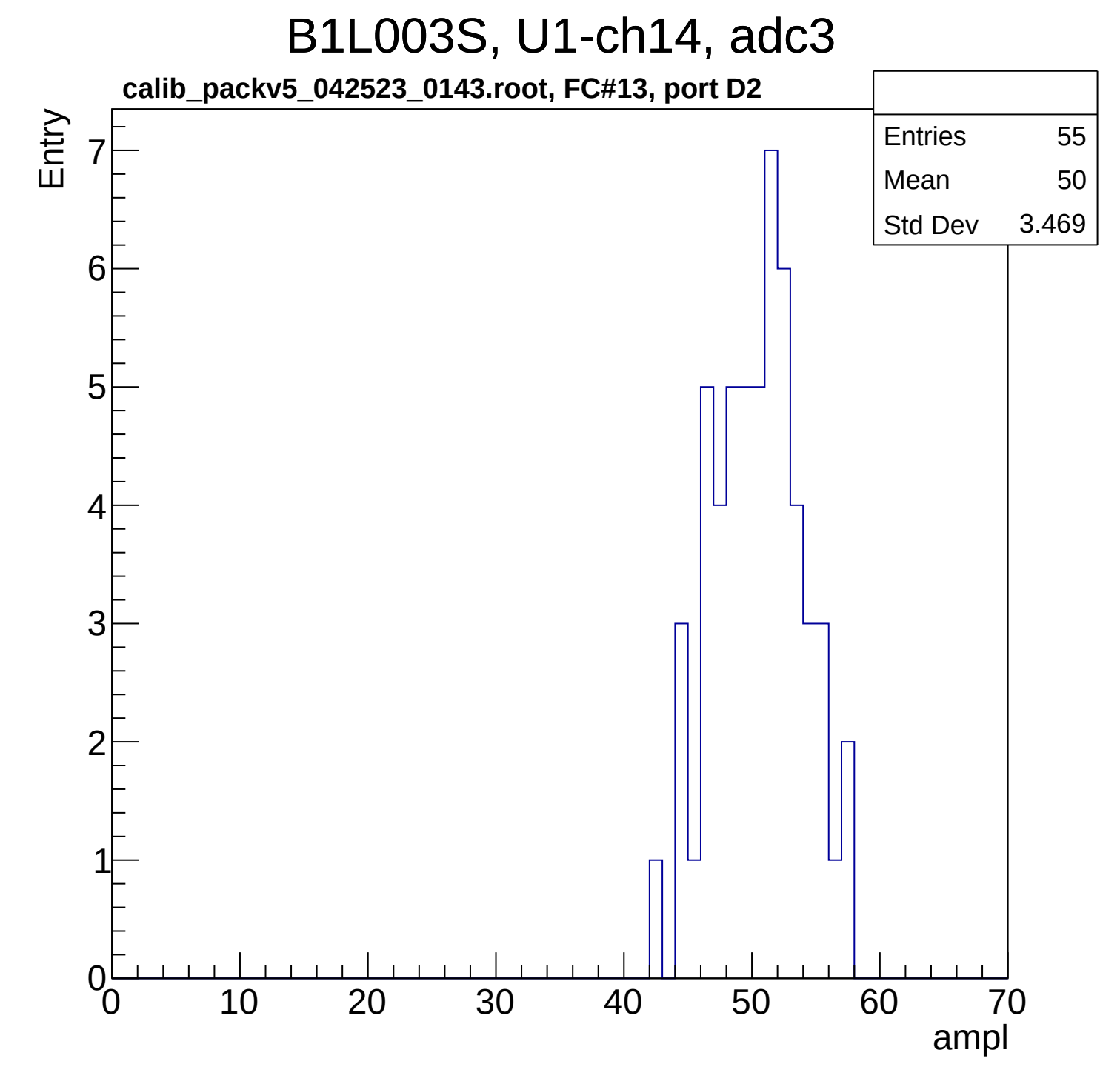

B1L003S, U1-ch13, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

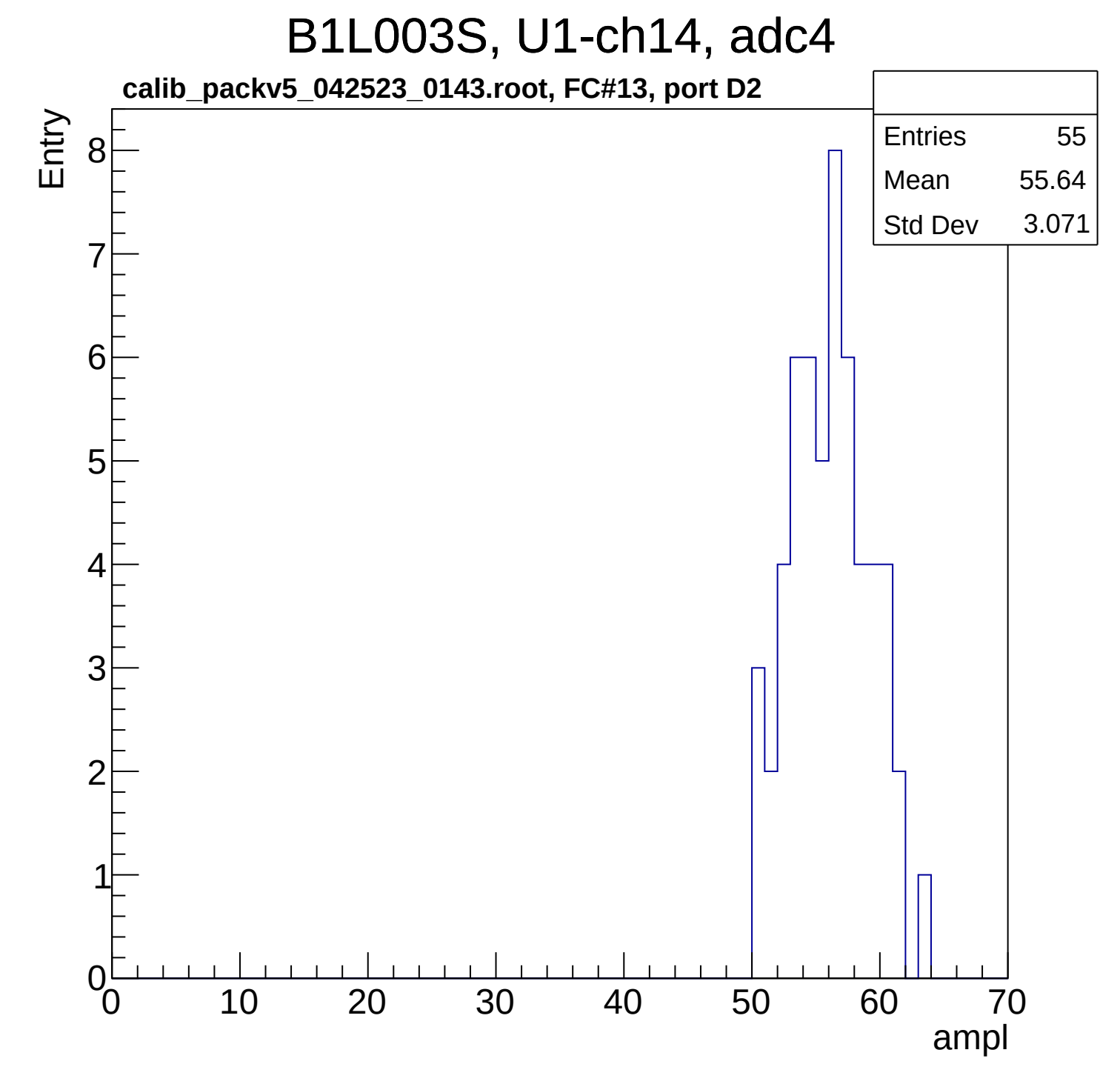

B1L003S, U1-ch13, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

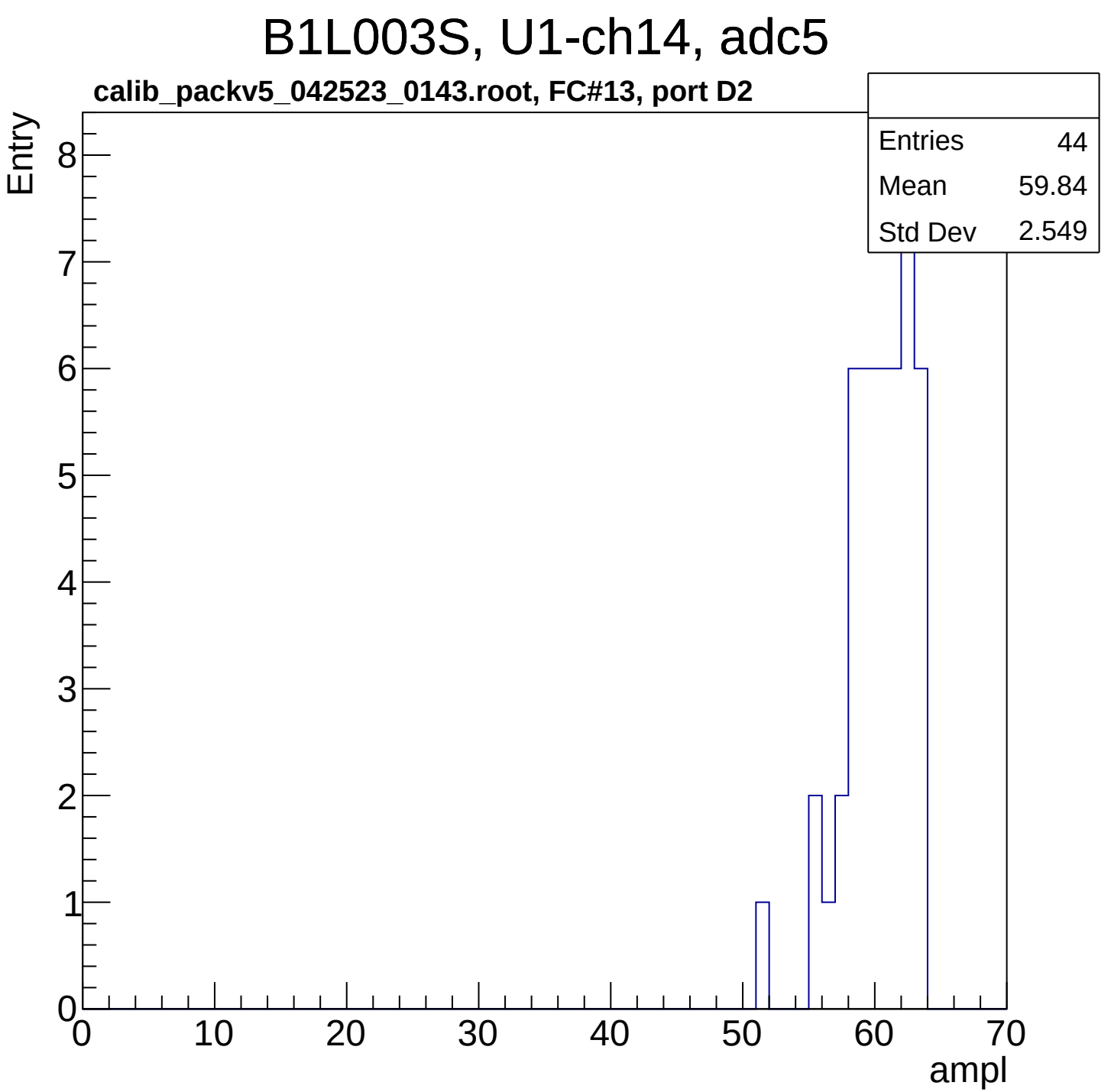

B1L003S, U1-ch14, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.85
Std Dev 4.79
Gaus mean : 29.0856
Gaus Width: 3.6078
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch14, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.42
Std Dev 3.6
Gaus mean : 36.8175
Gaus Width: 3.7193
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch14, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 44.13
Std Dev 3.447
Gaus mean : 44.9026
Gaus Width: 3.9294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch14, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 50
Std Dev 3.469
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch14, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.64
Std Dev 3.071
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch14, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.84
Std Dev 2.549
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
50
60
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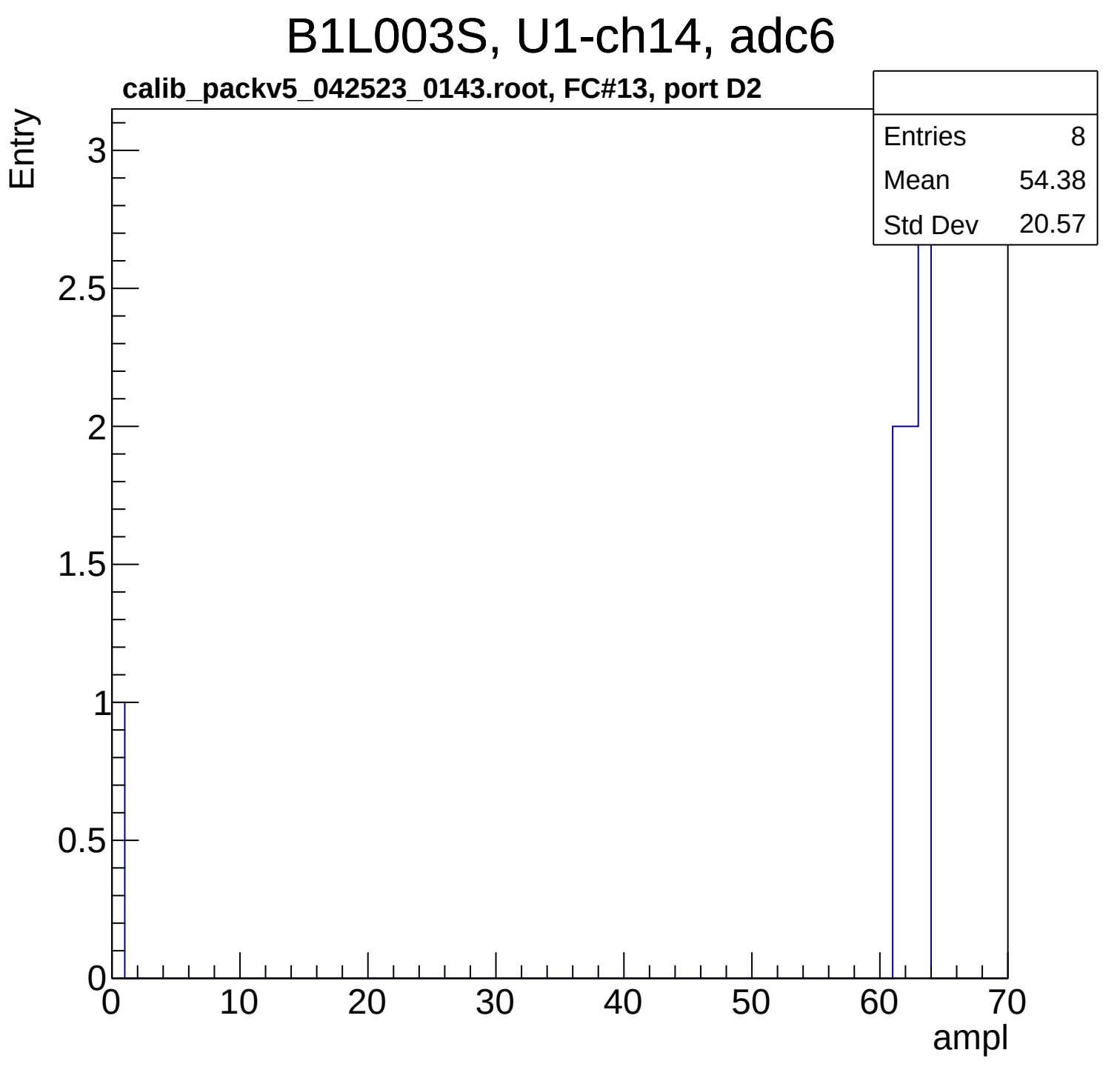
B1L003S, U1-ch14, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.38
Std Dev 20.57
Entry
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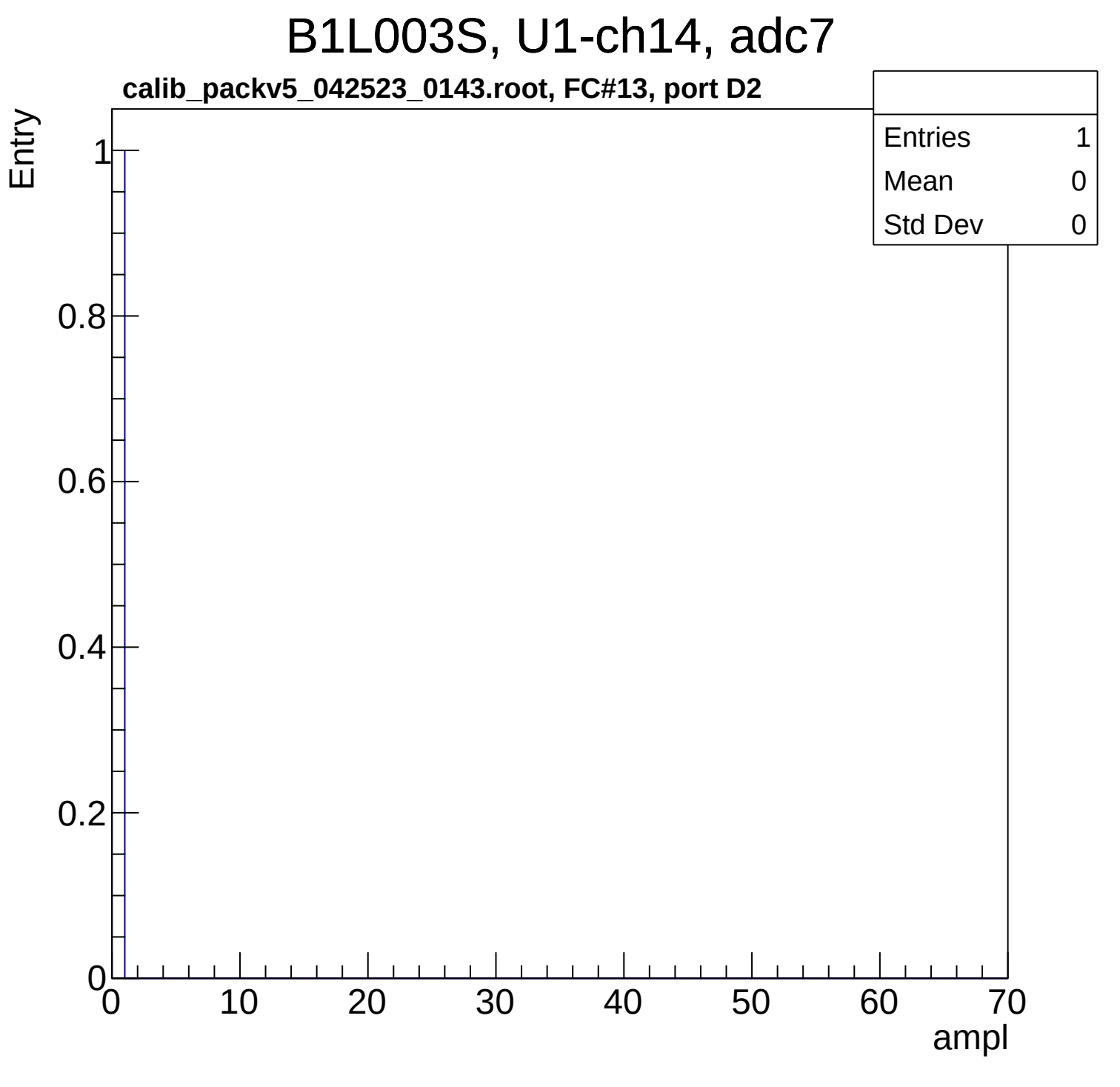
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch14, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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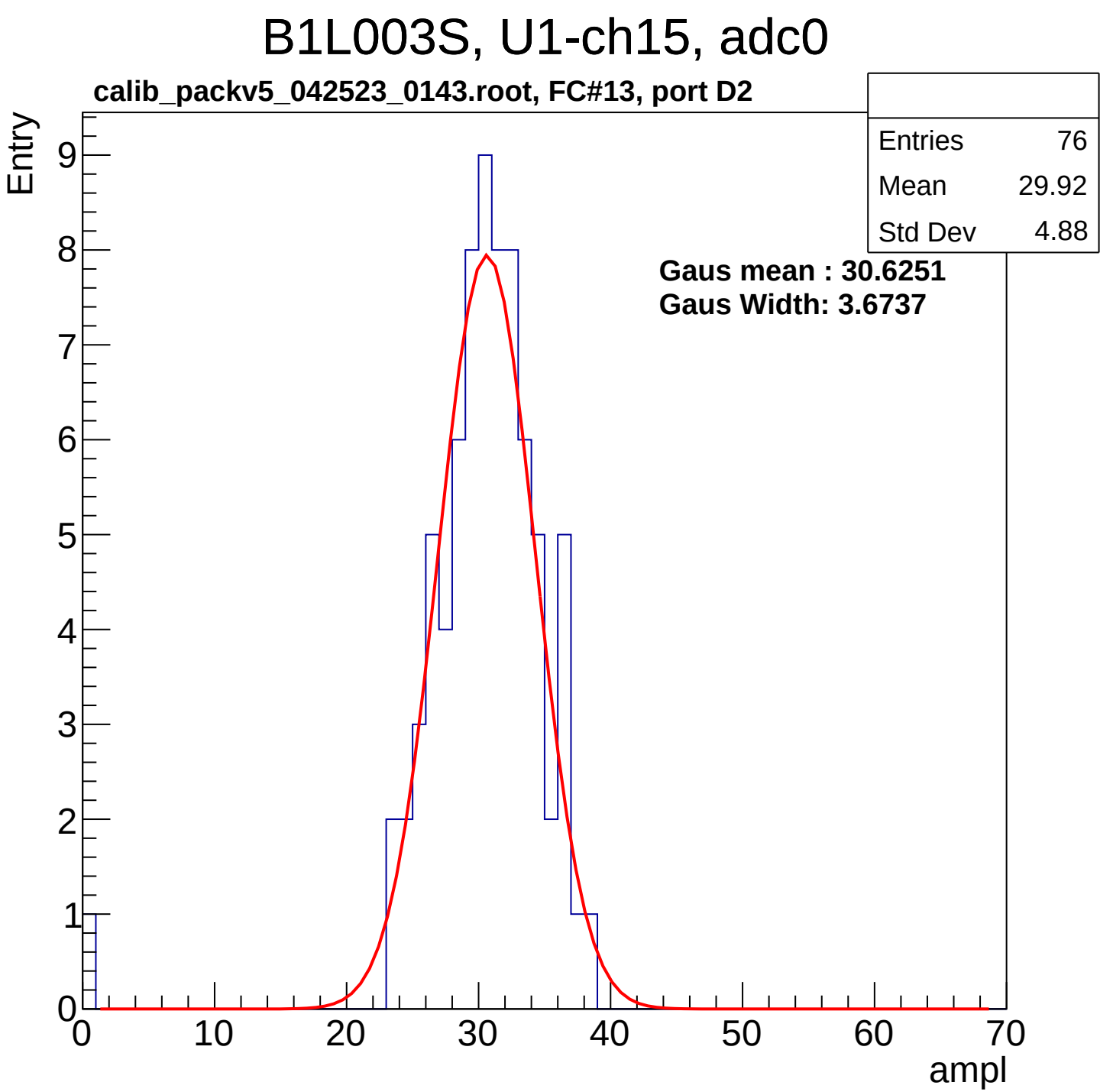
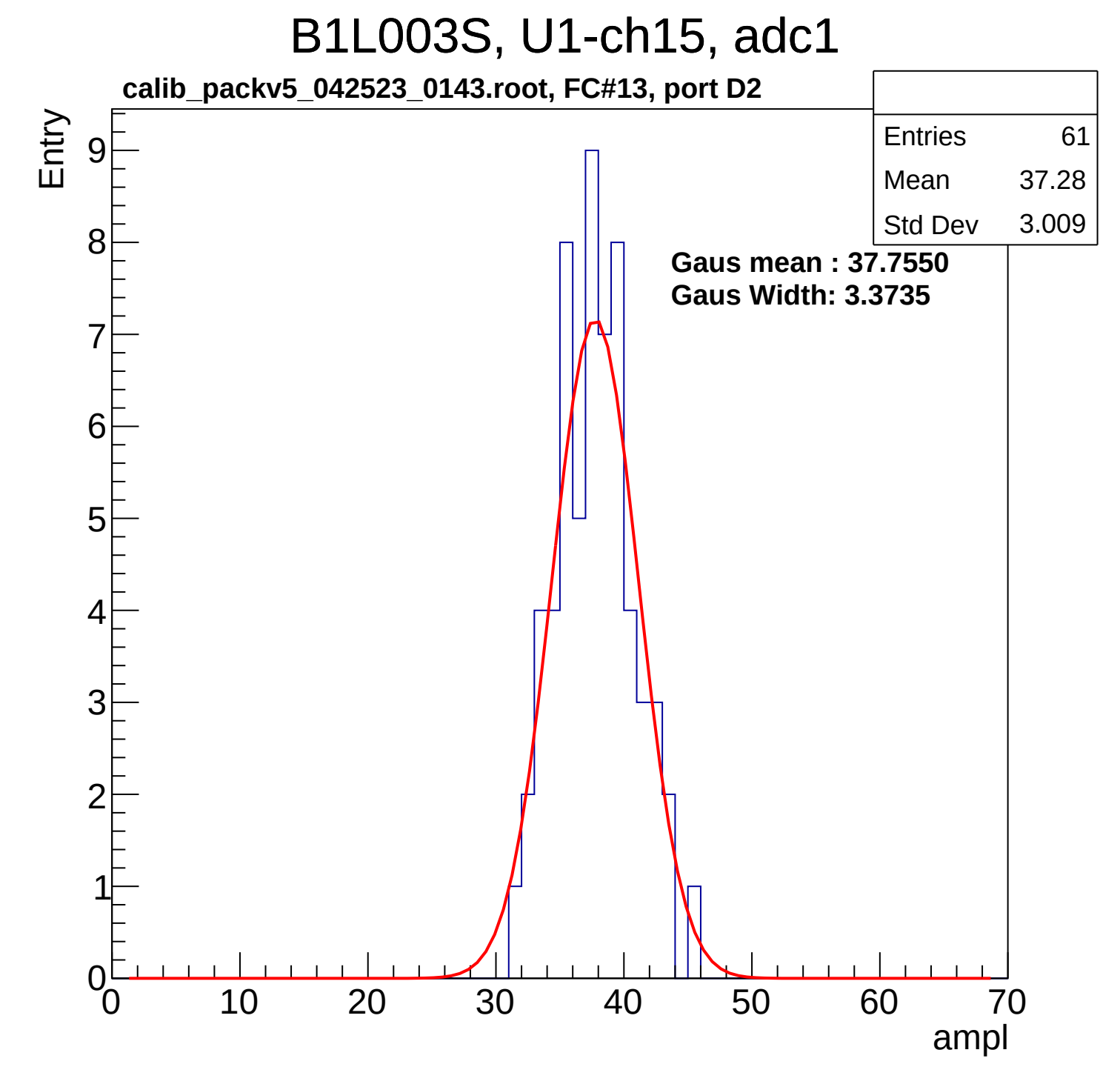
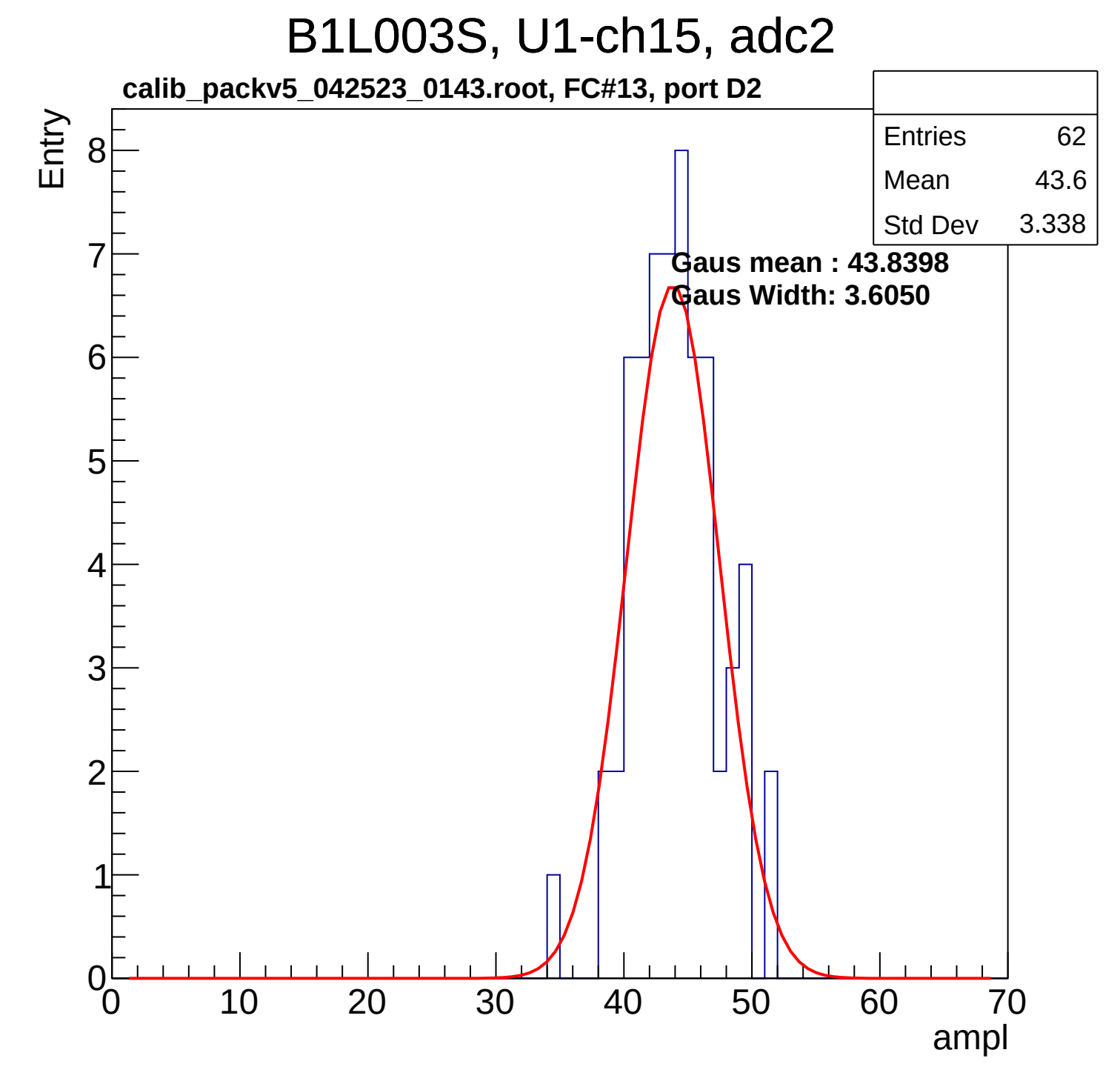
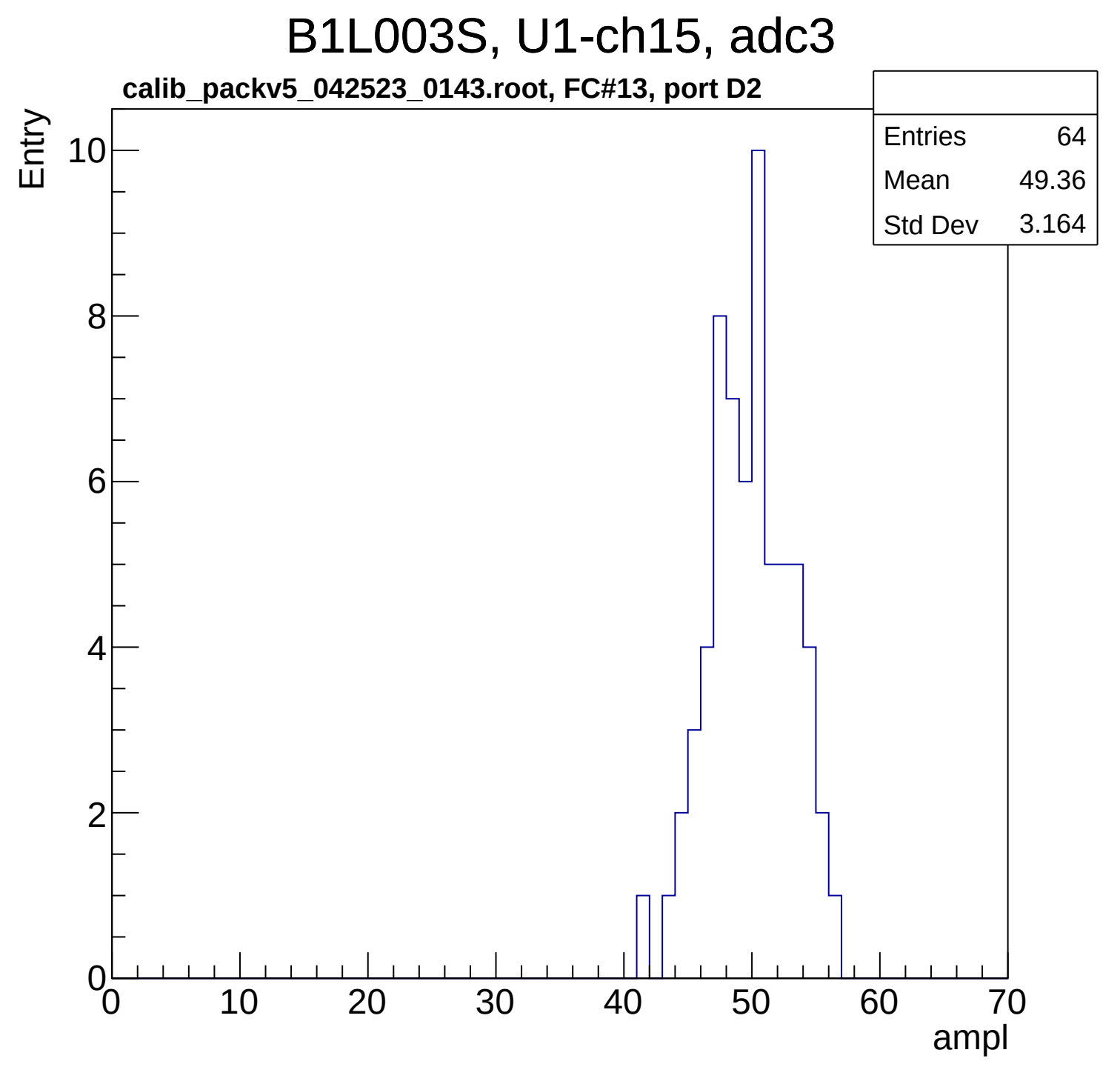
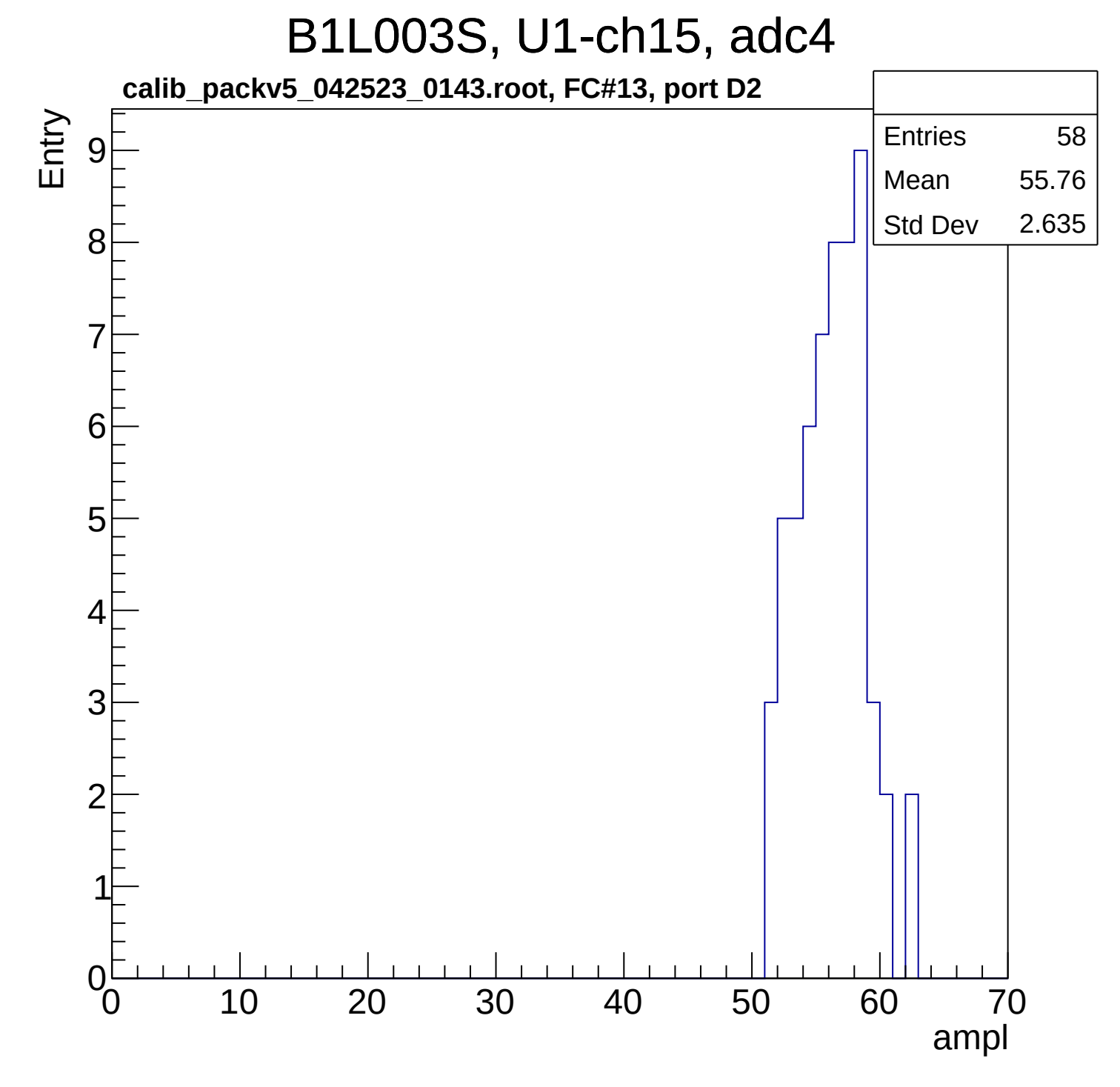
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch15, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 29.92
Std Dev 4.88
Gaus mean : 30.6251
Gaus Width: 3.6737
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch15, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 37.28
Std Dev 3.009
Gaus mean : 37.7550
Gaus Width: 3.3735
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch15, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.6
Std Dev 3.338
Gaus mean : 43.8398
Gaus Width: 3.6050
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch15, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.36
Std Dev 3.164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch15, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.76
Std Dev 2.635
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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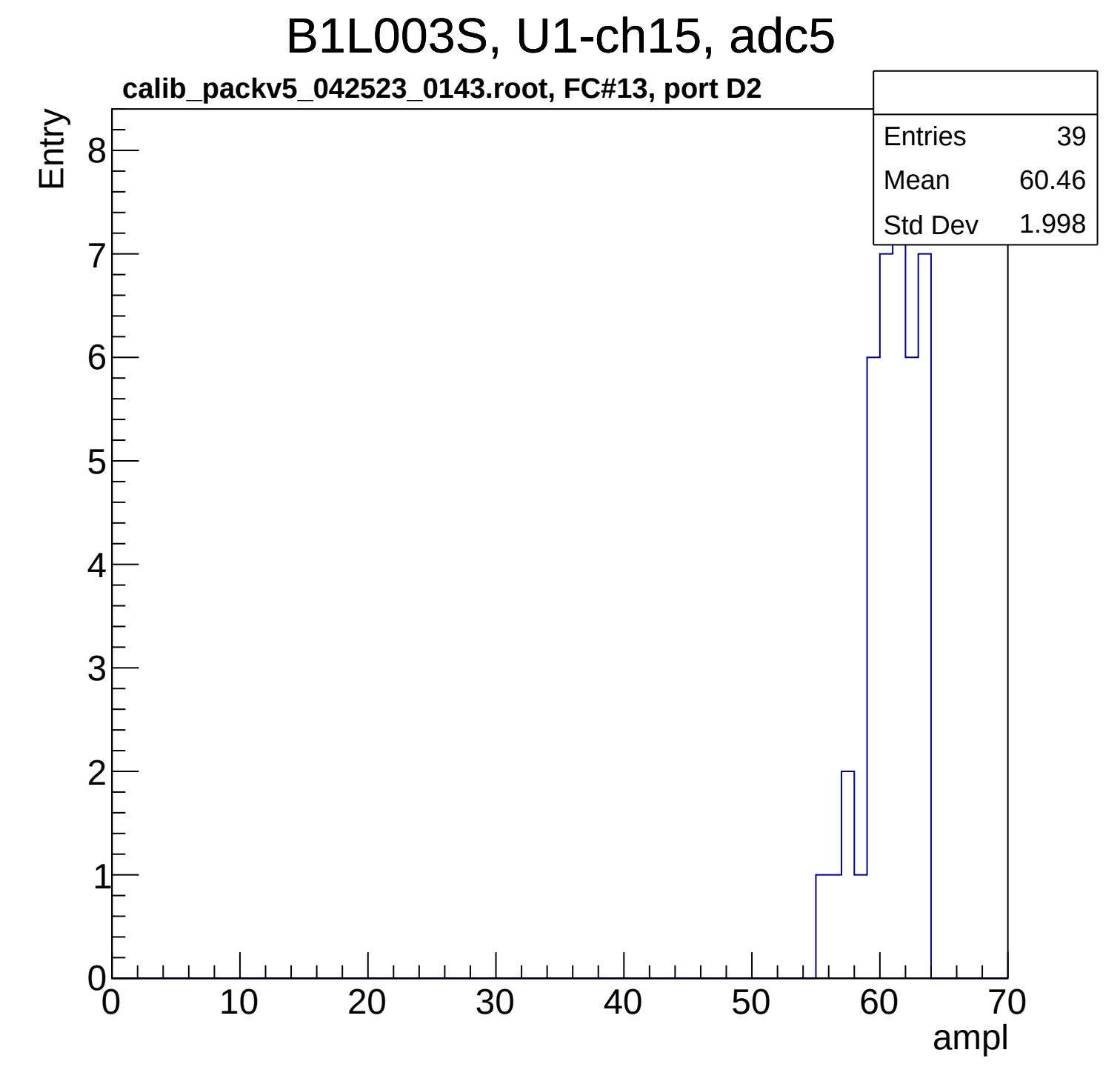
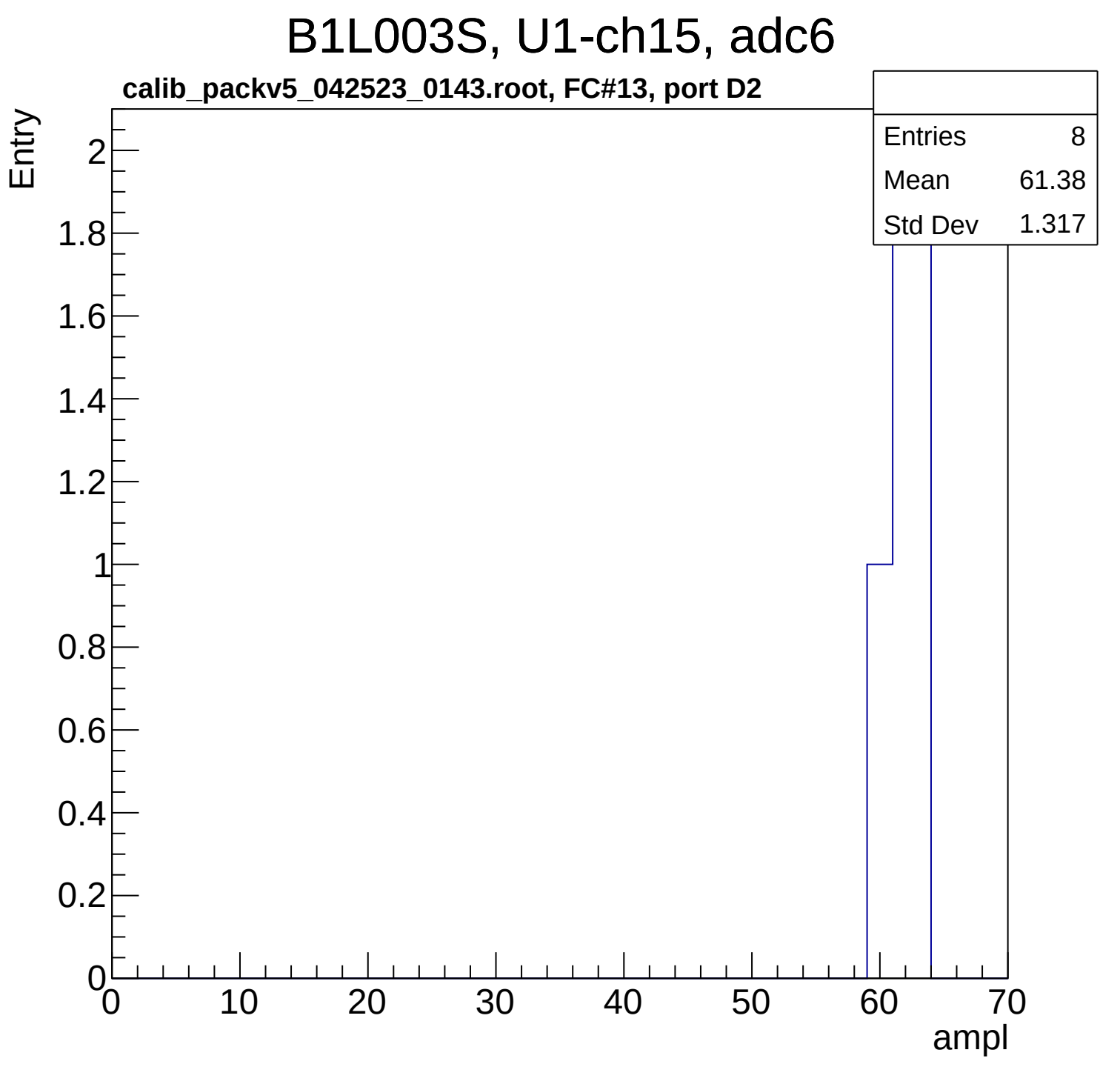
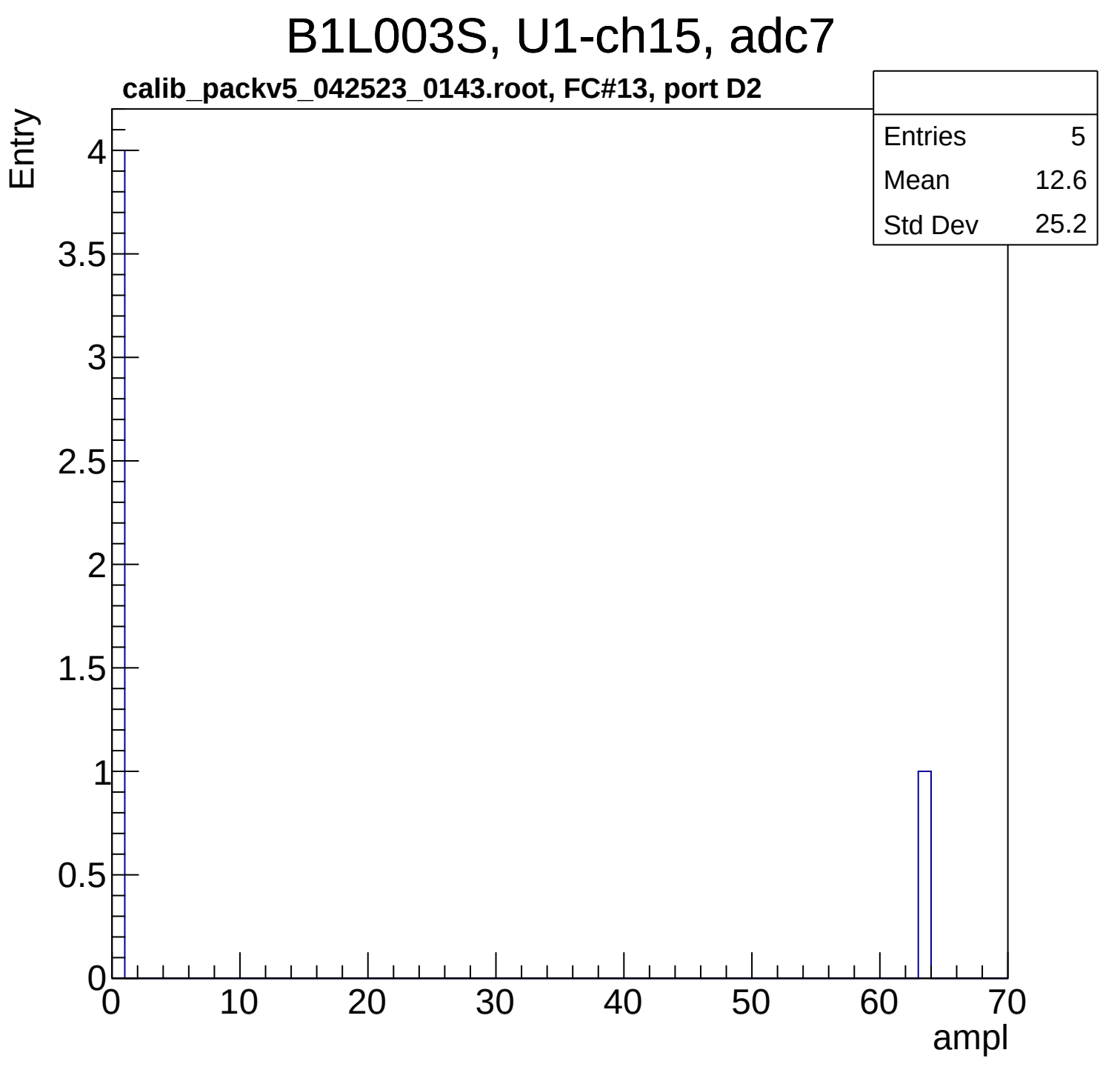
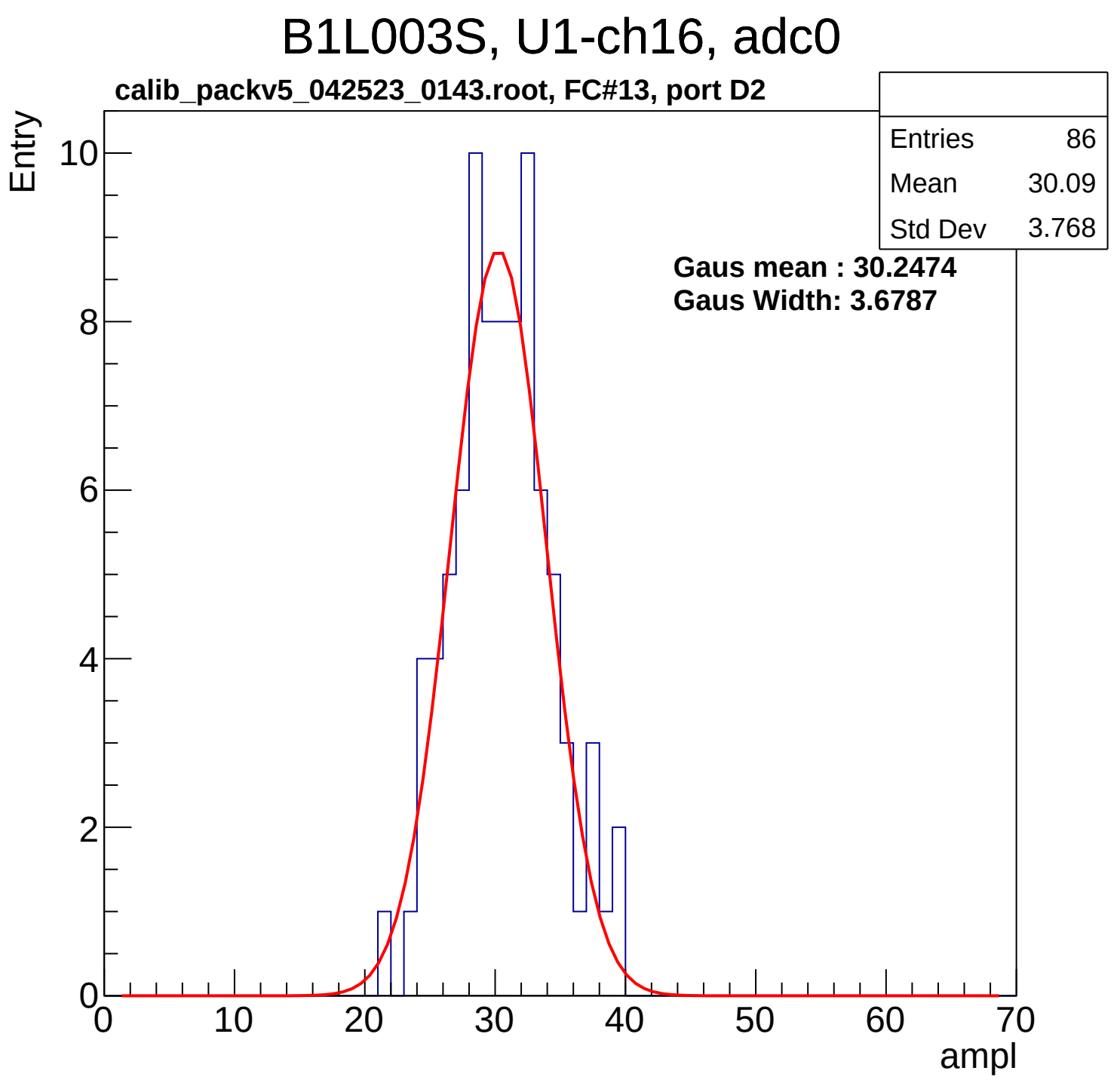
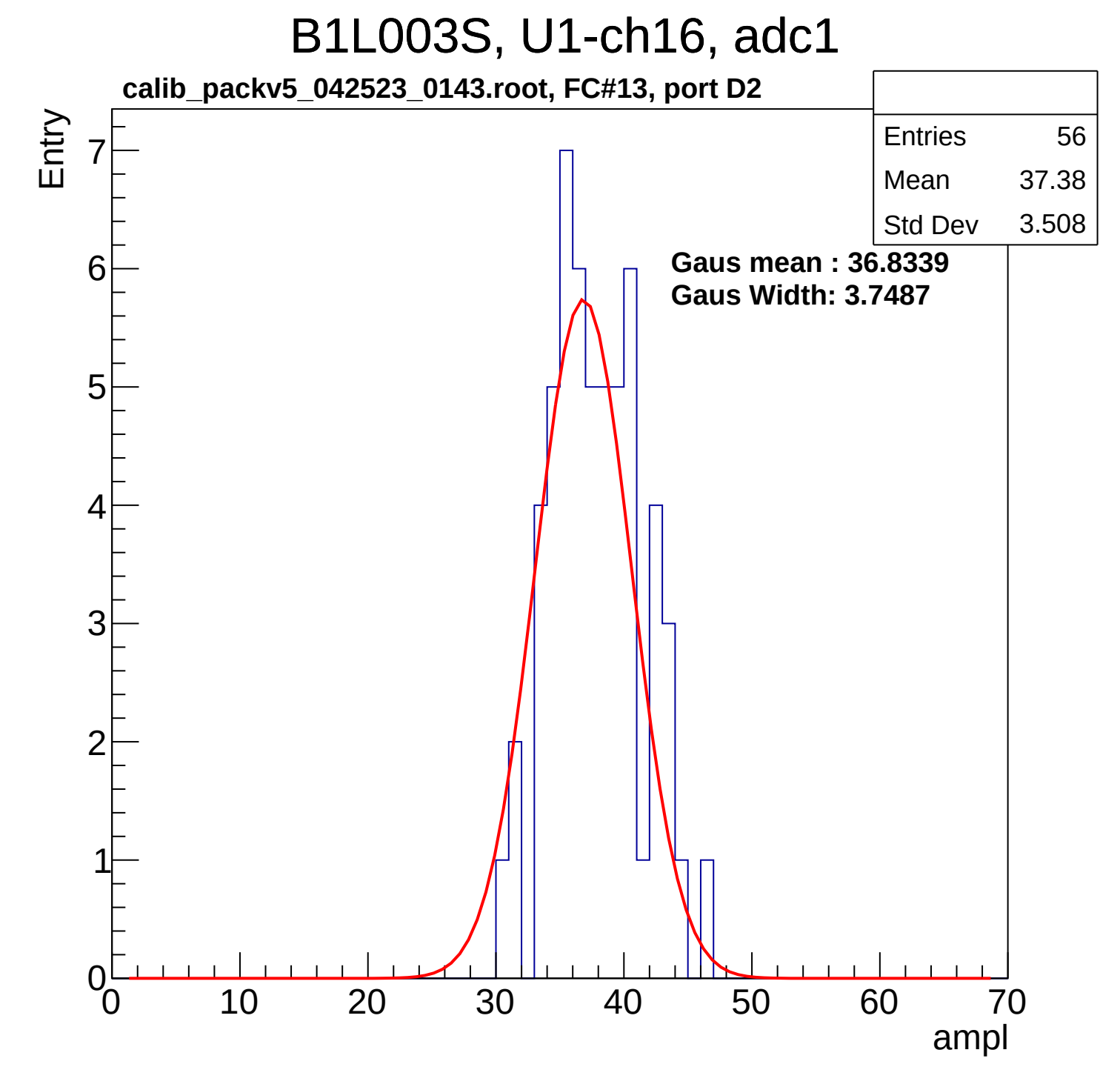
30
40
50
60
70

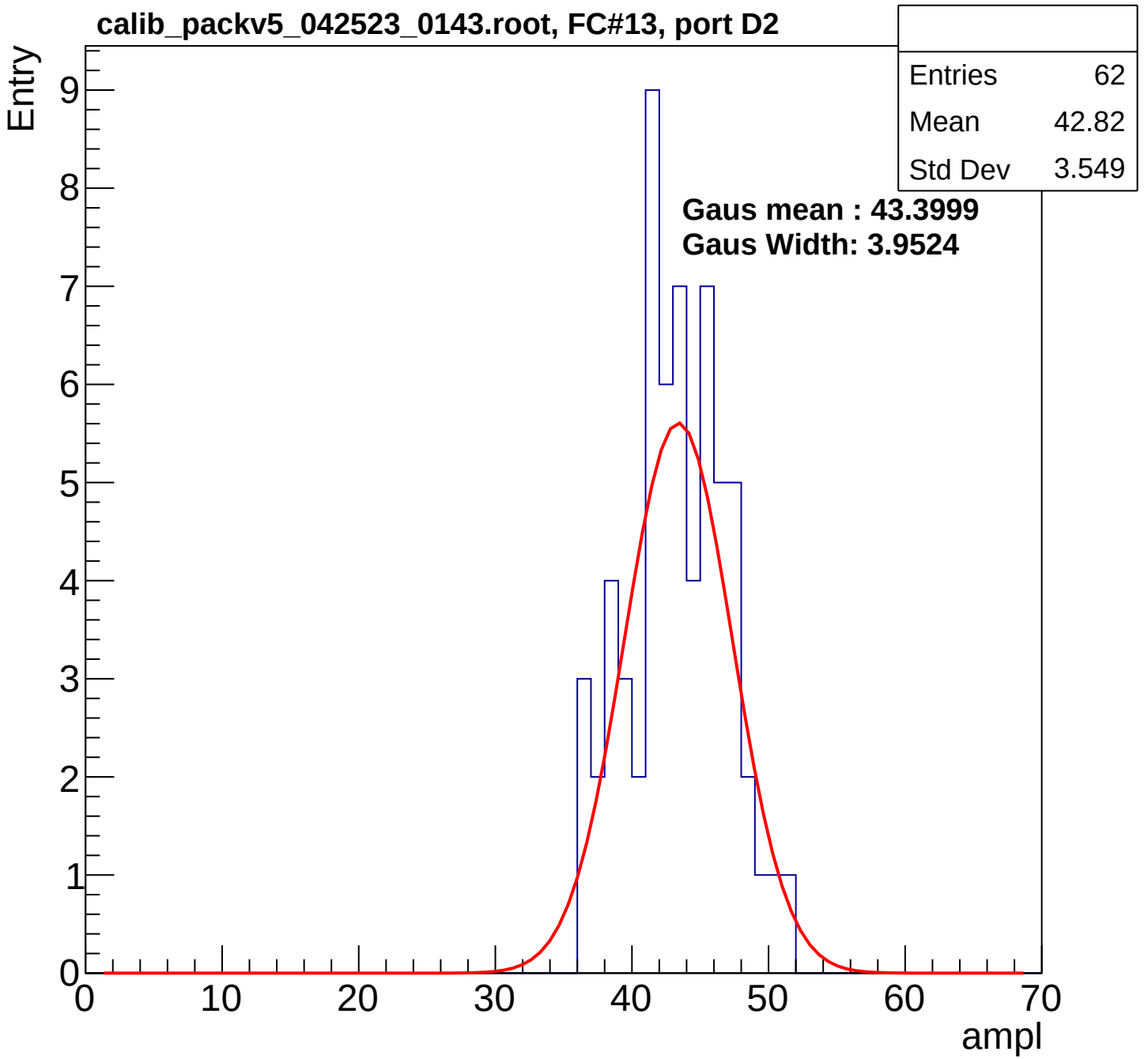

B1L003S, U1-ch15, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 60.46
Std Dev 1.998
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

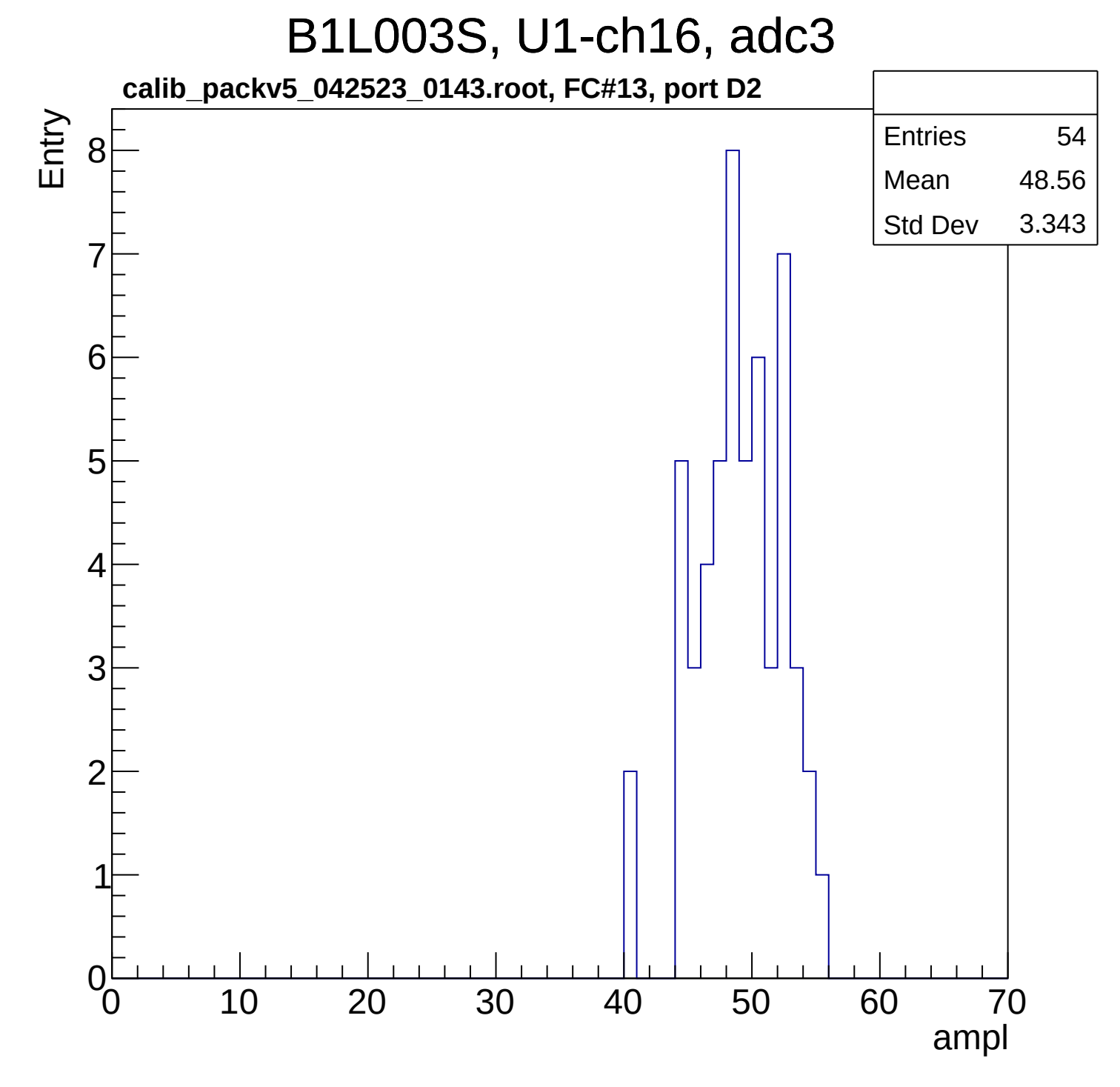

B1L003S, U1-ch15, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.38
Std Dev 1.317
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

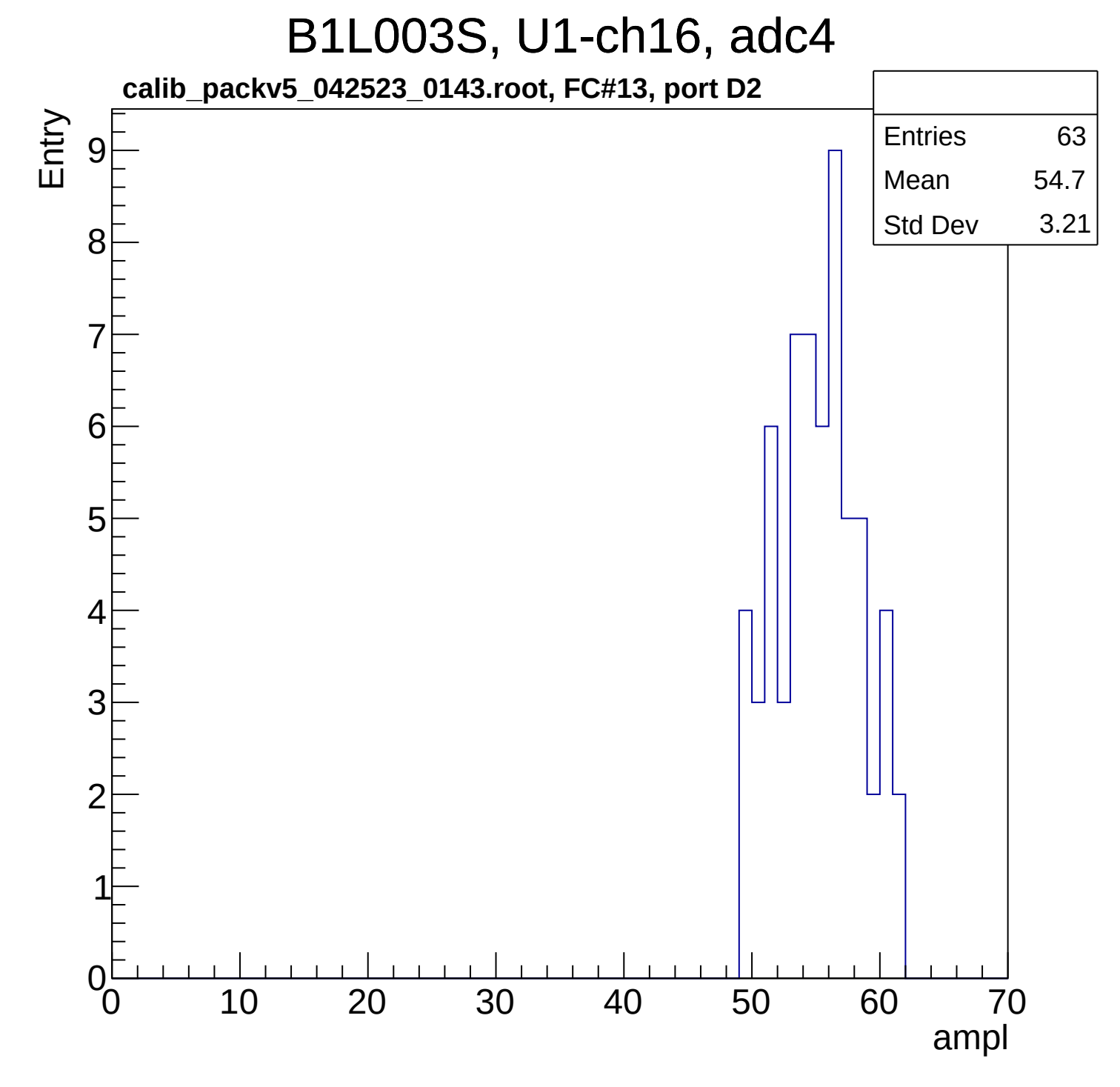

B1L003S, U1-ch15, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 12.6
Std Dev 25.2
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl

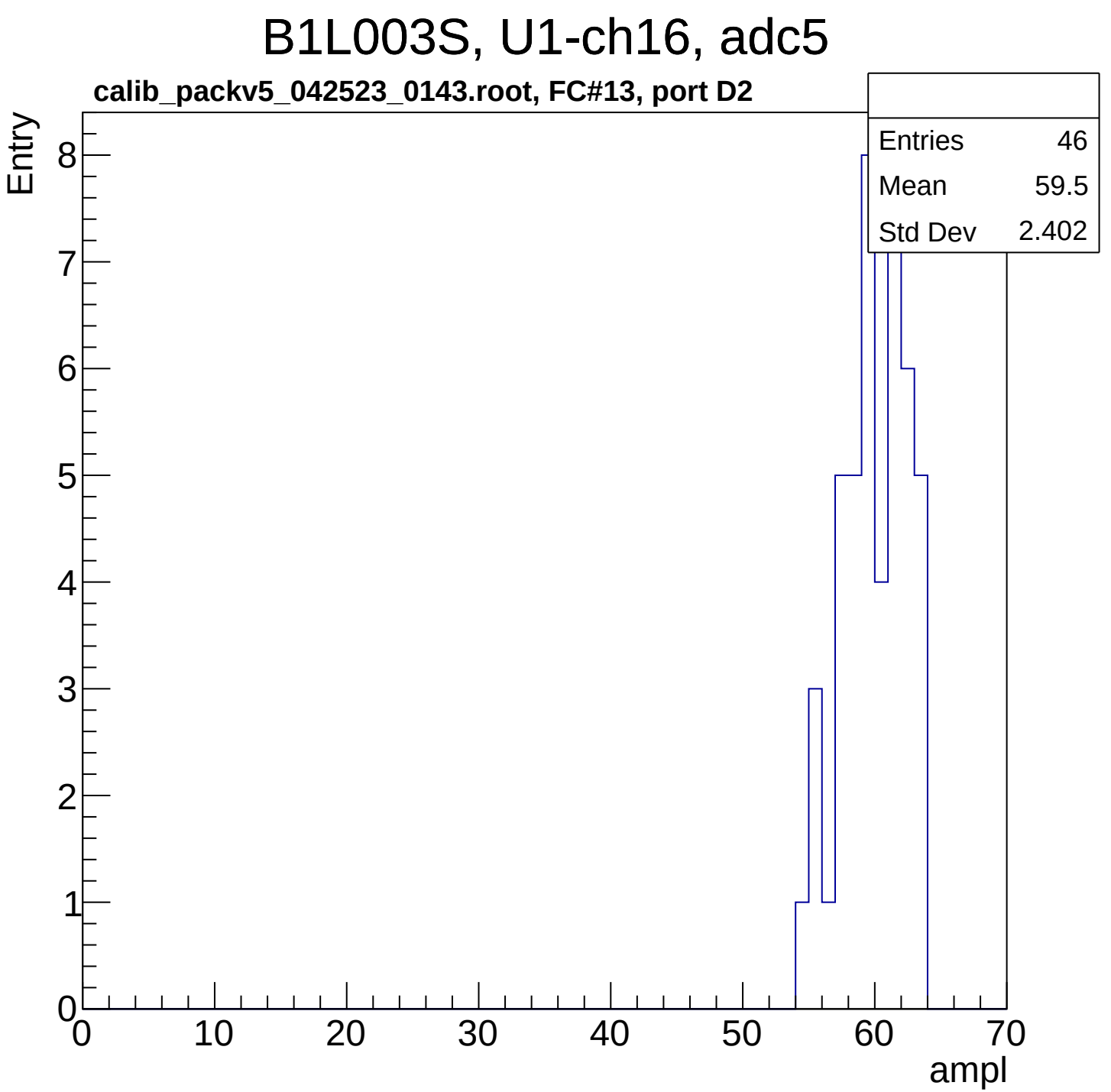

B1L003S, U1-ch16, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 86
Mean 30.09
Std Dev 3.768
Gaus mean : 30.2474
Gaus Width: 3.6787
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

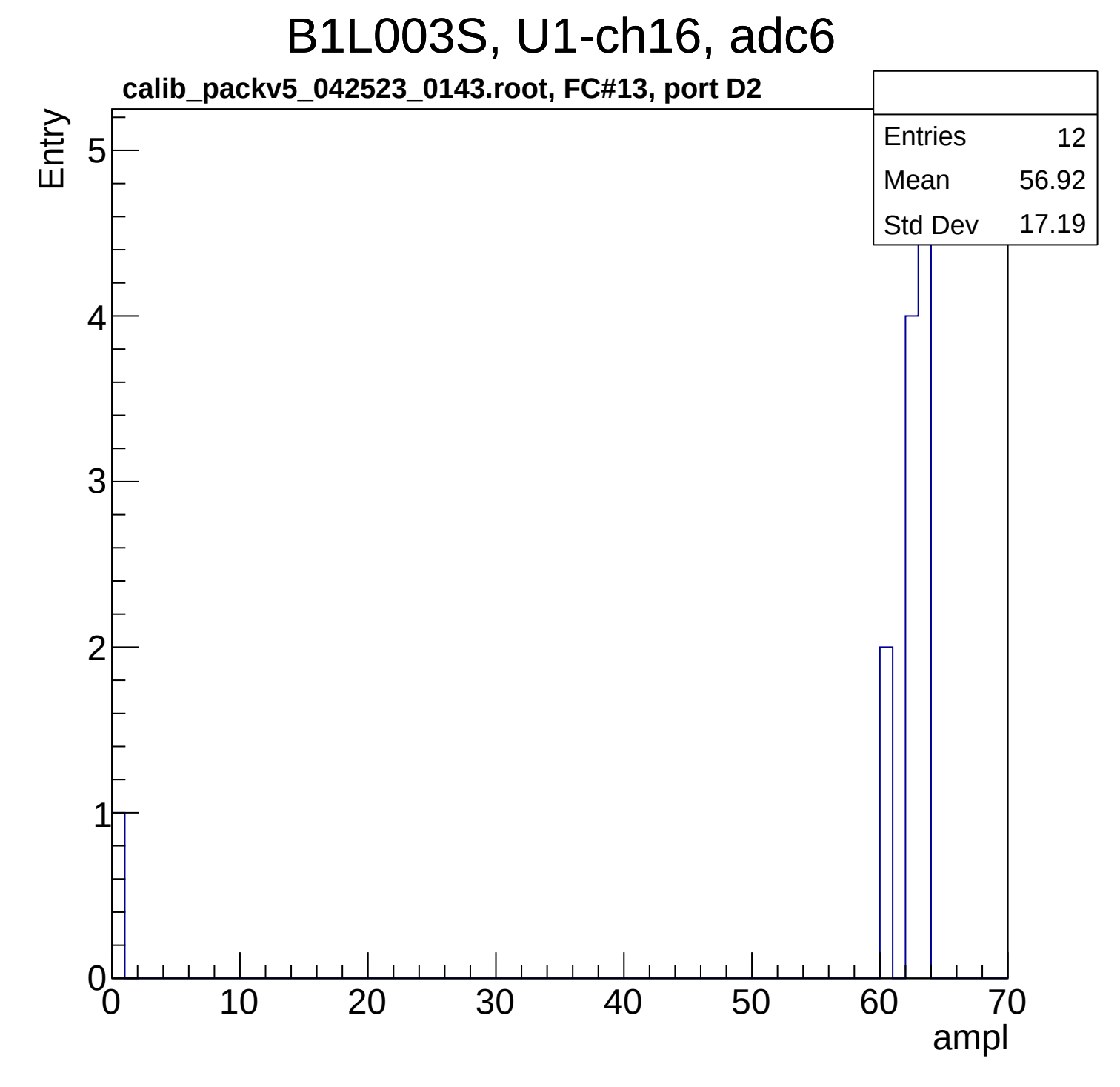

B1L003S, U1-ch16, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 37.38
Std Dev 3.508
Gaus mean : 36.8339
Gaus Width: 3.7487
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

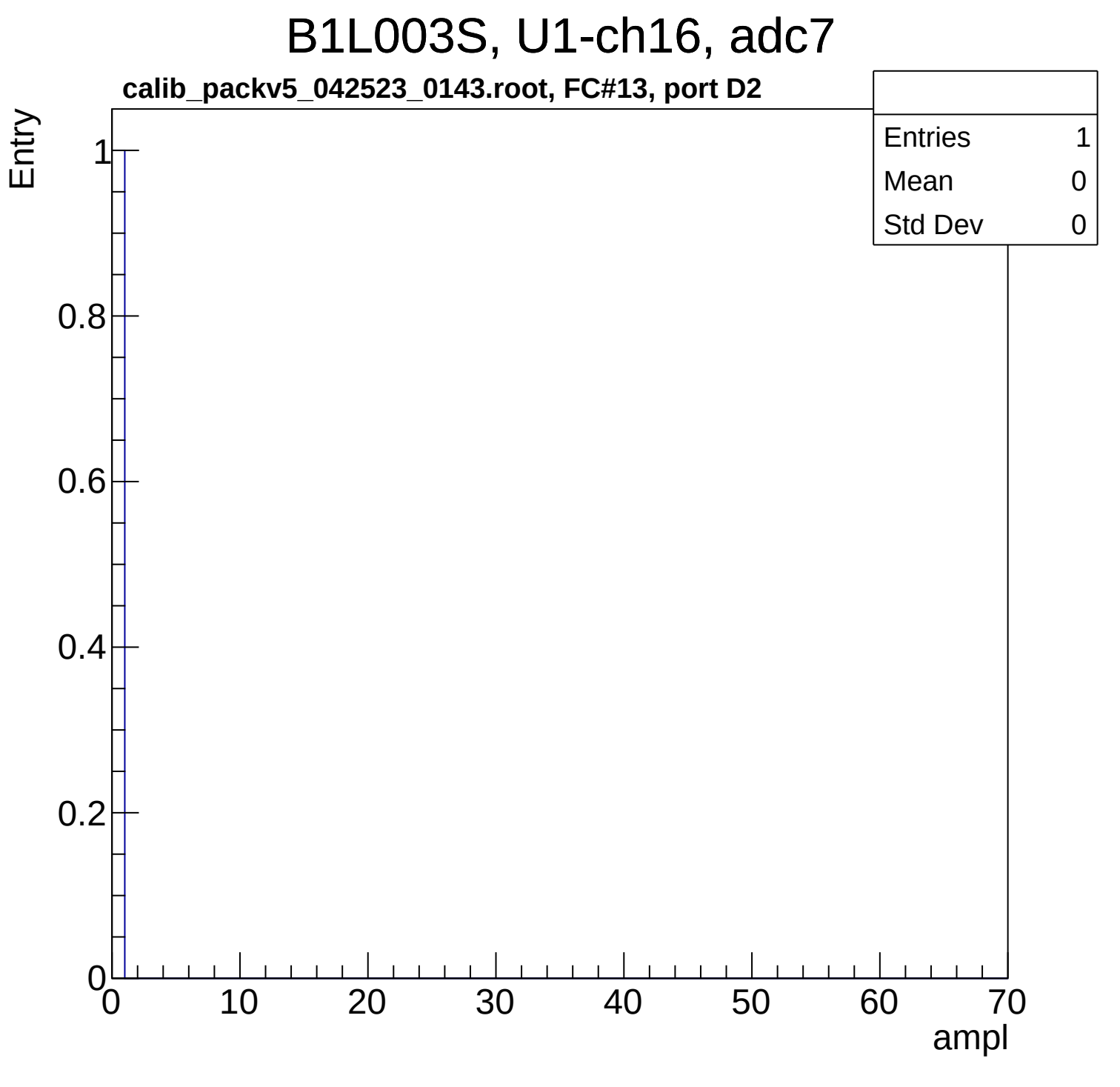

B1L003S, U1-ch16, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.82
Std Dev 3.549
Gaus mean : 43.3999
Gaus Width: 3.9524
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

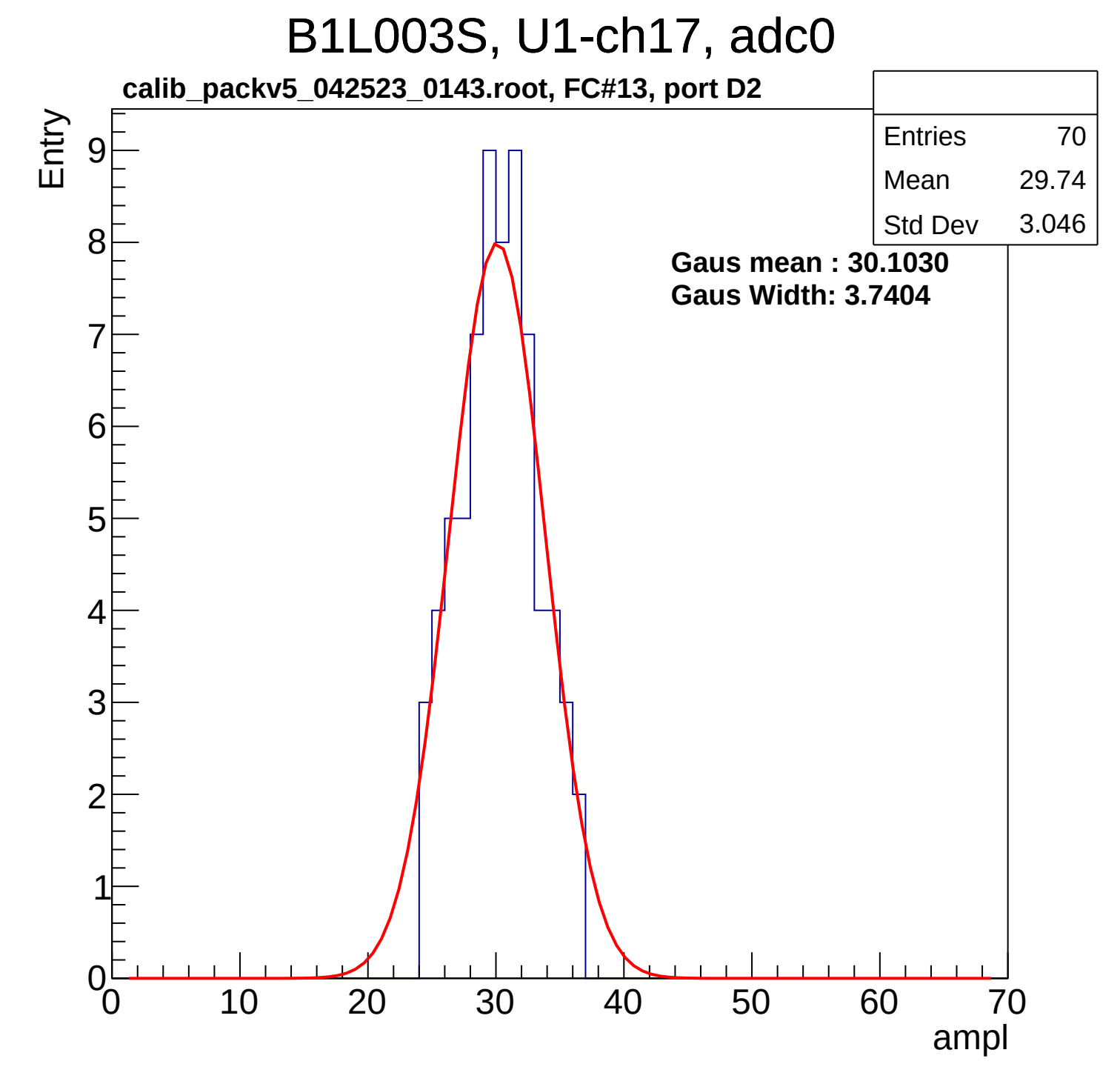

B1L003S, U1-ch16, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 48.56
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

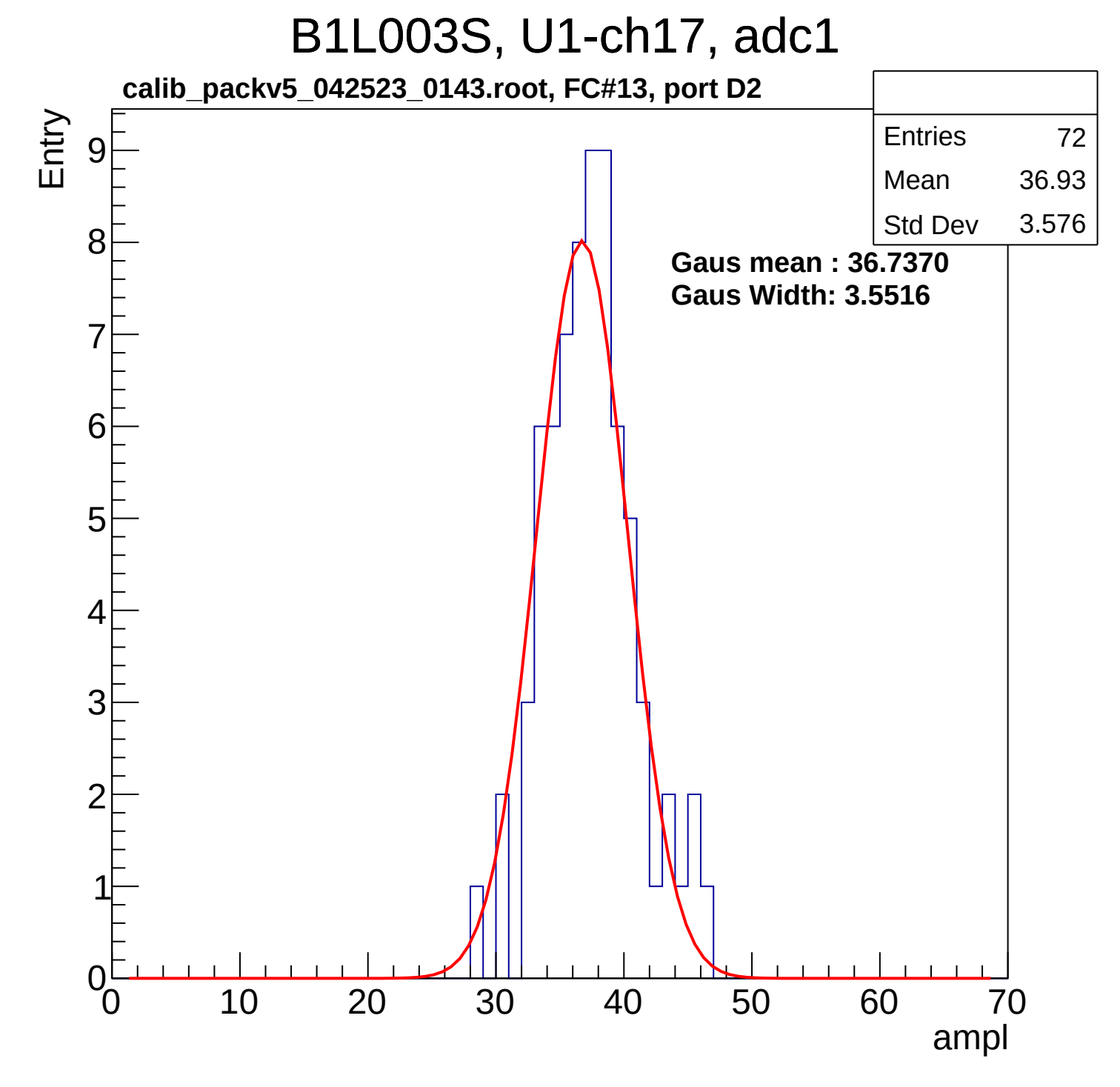

B1L003S, U1-ch16, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.7
Std Dev 3.21
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

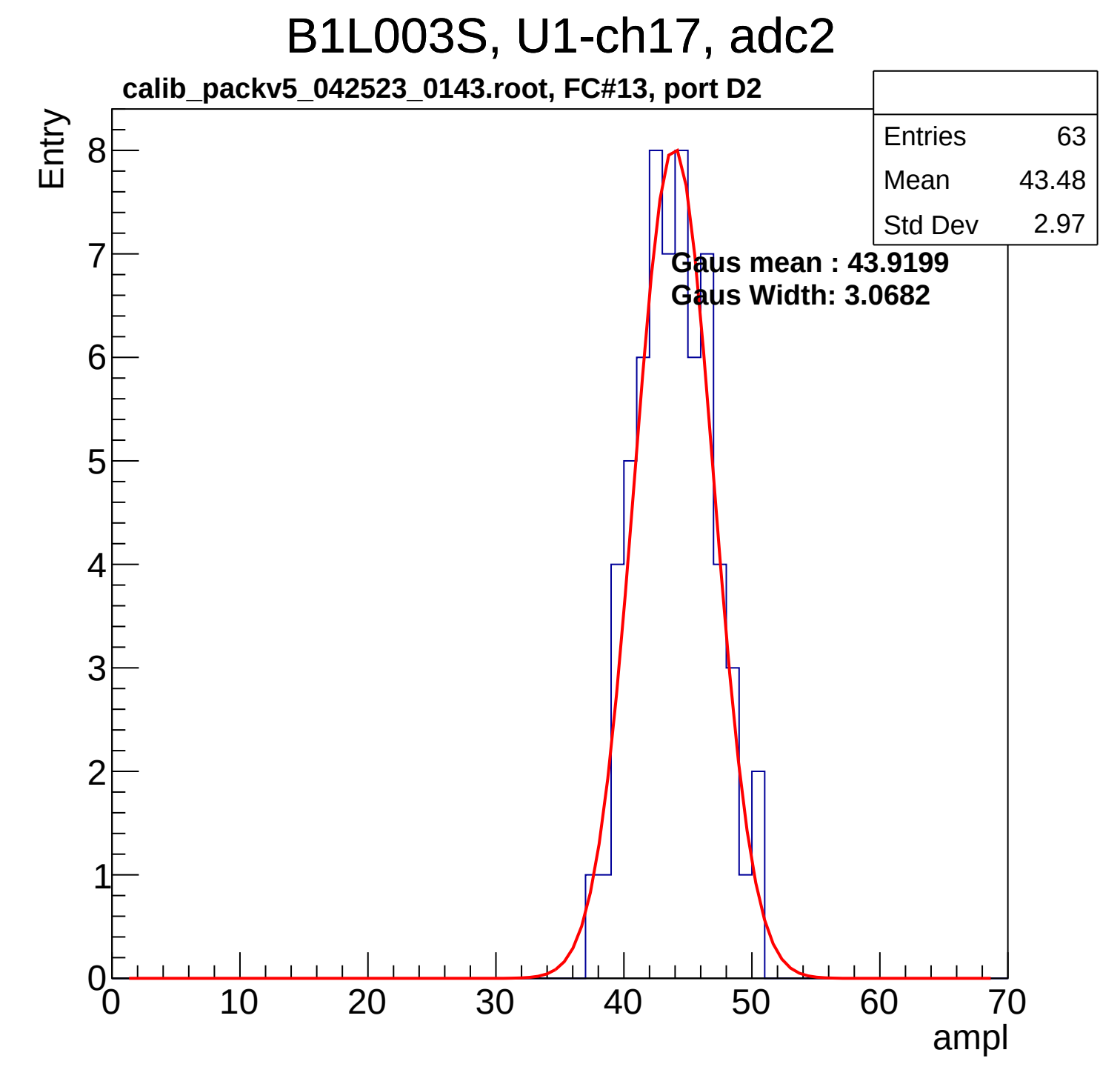

B1L003S, U1-ch16, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.5
Std Dev 2.402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

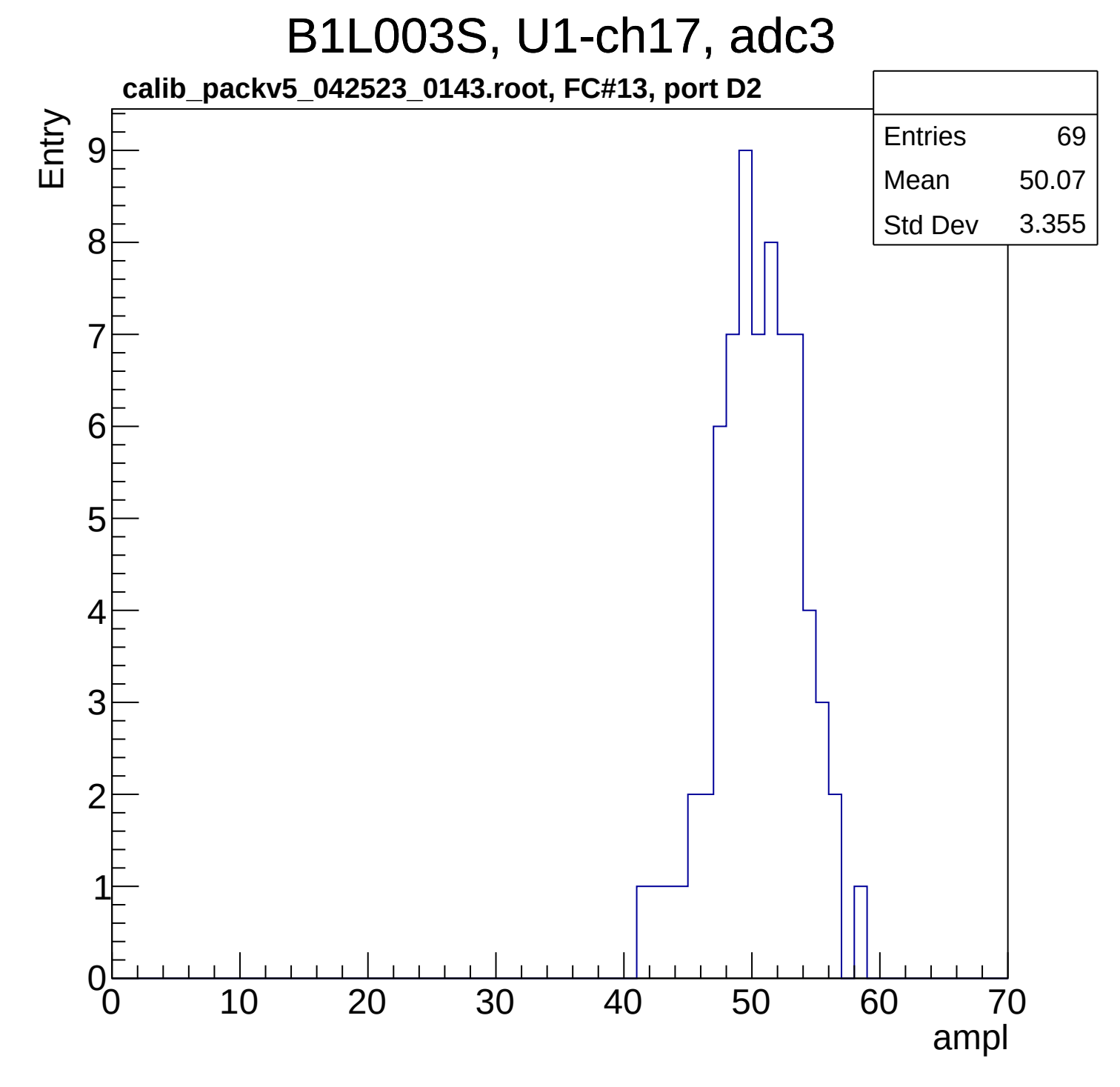

B1L003S, U1-ch16, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.92
Std Dev 17.19
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch16, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch17, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.74
Std Dev 3.046
Gaus mean : 30.1030
Gaus Width: 3.7404
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch17, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.93
Std Dev 3.576
Gaus mean : 36.7370
Gaus Width: 3.5516
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch17, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.48
Std Dev 2.97
Gaus mean : 43.9199
Gaus Width: 3.0682
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch17, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.07
Std Dev 3.355
Entry
ampl
0
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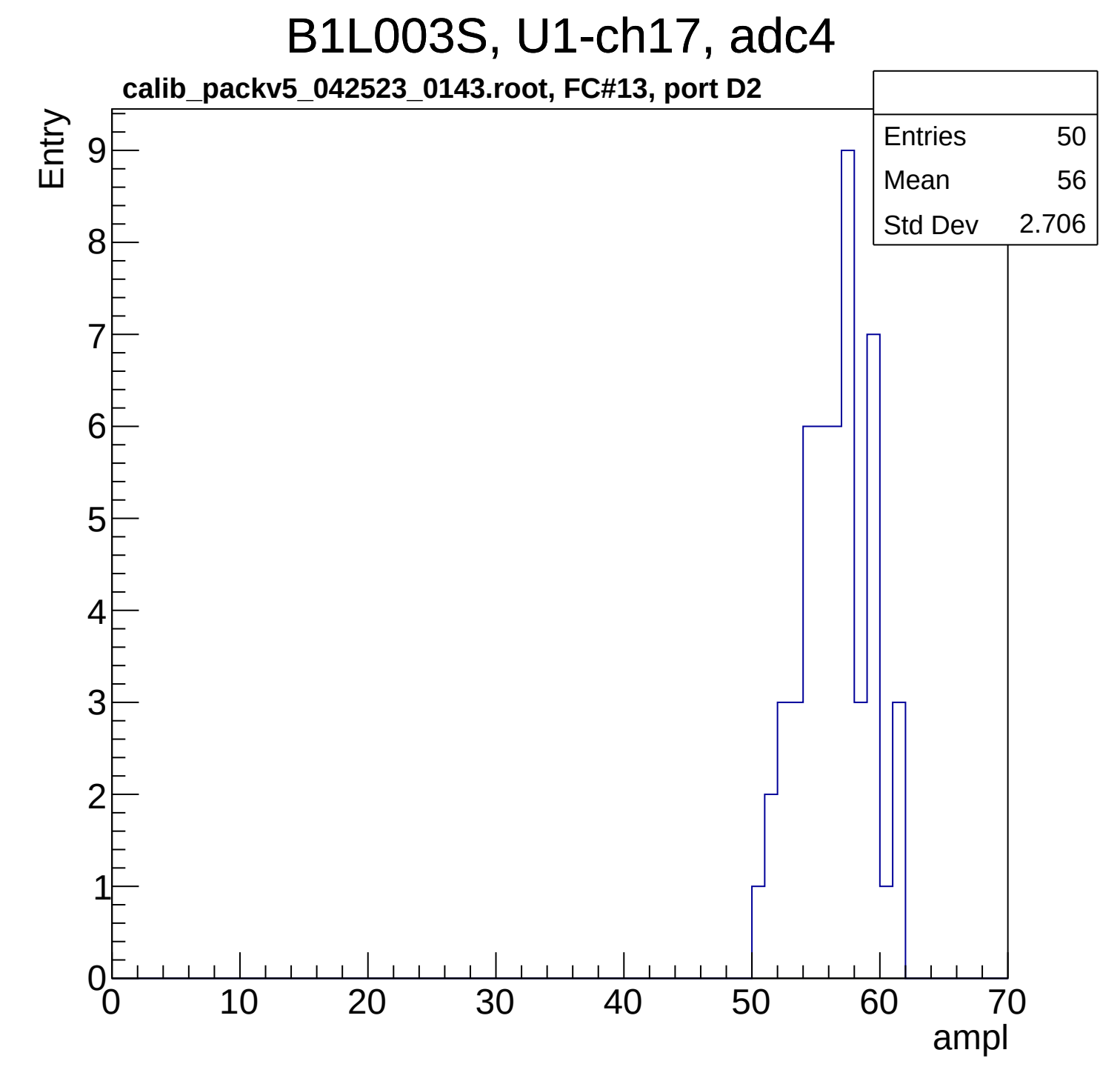
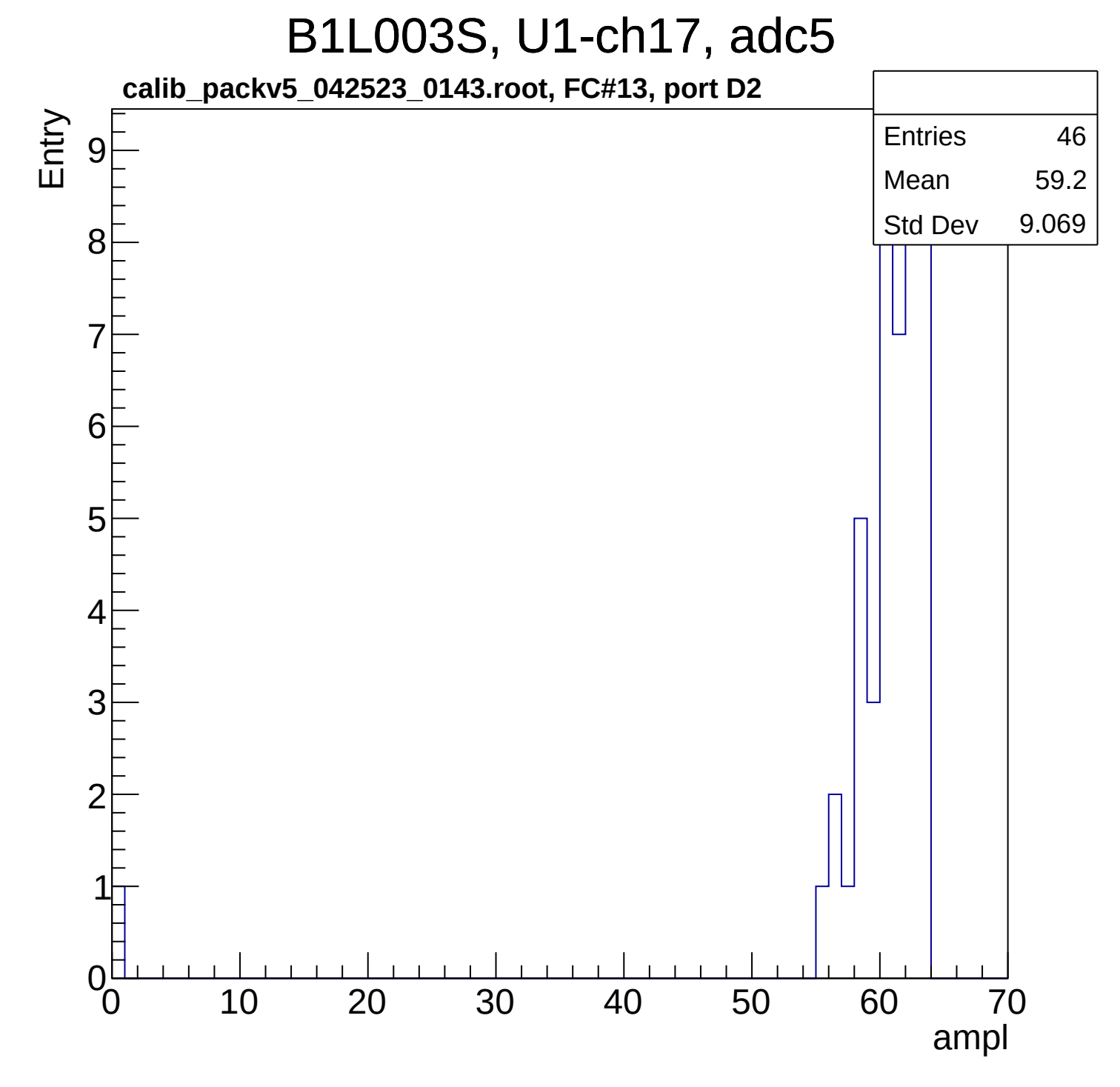
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch17, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 56
Std Dev 2.706
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch17, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 59.2
Std Dev 9.069
Entry
ampl
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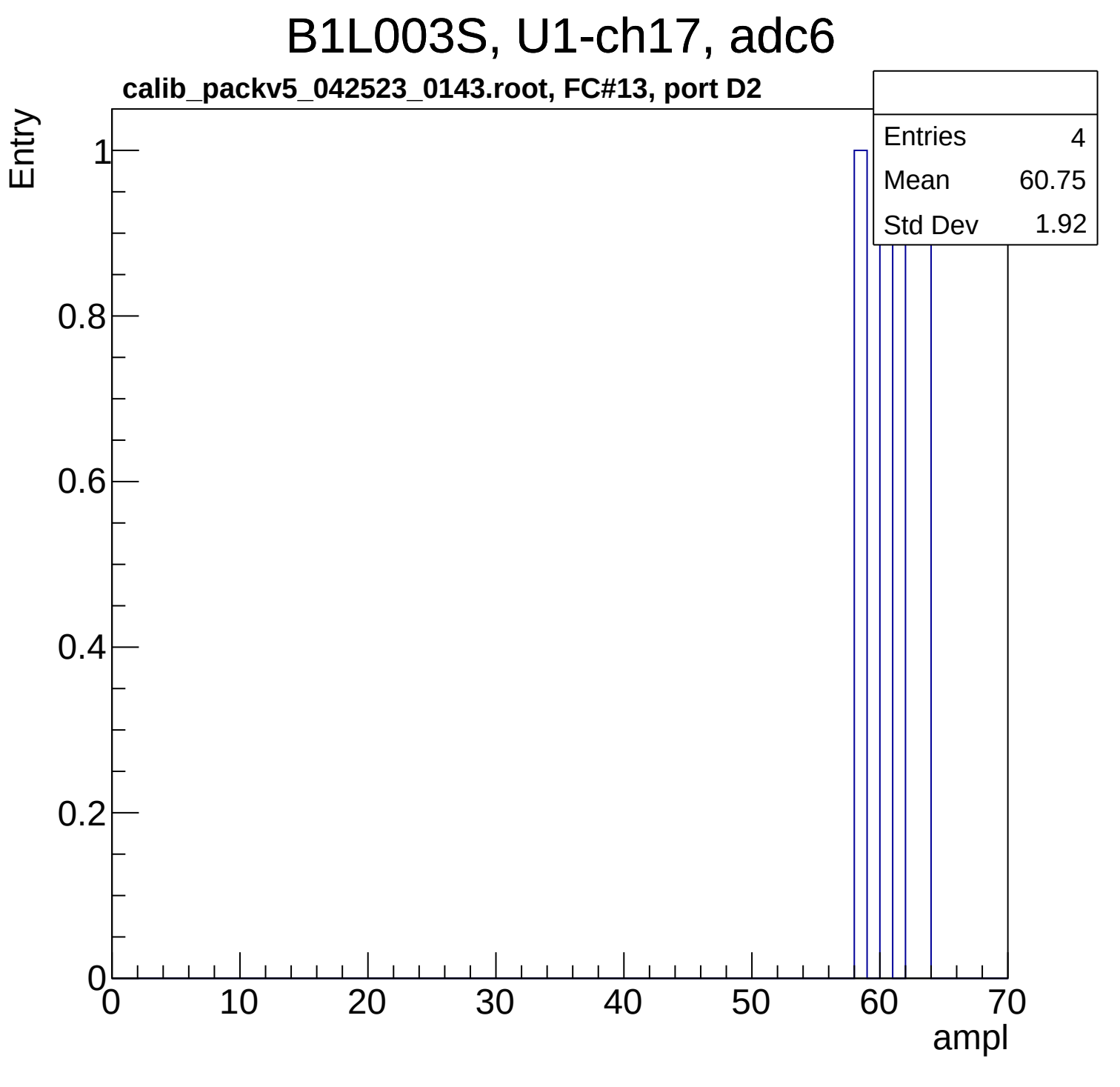
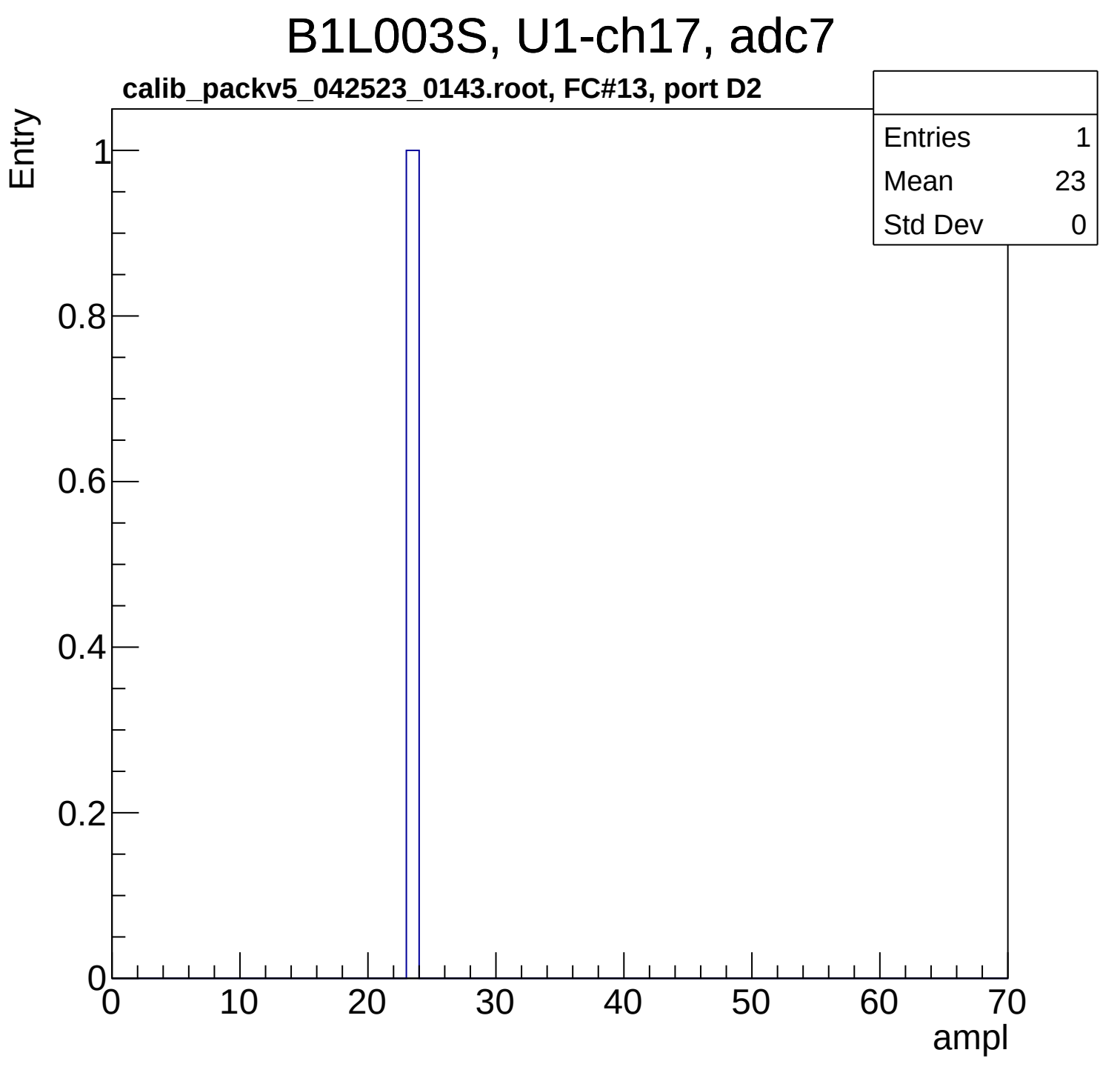
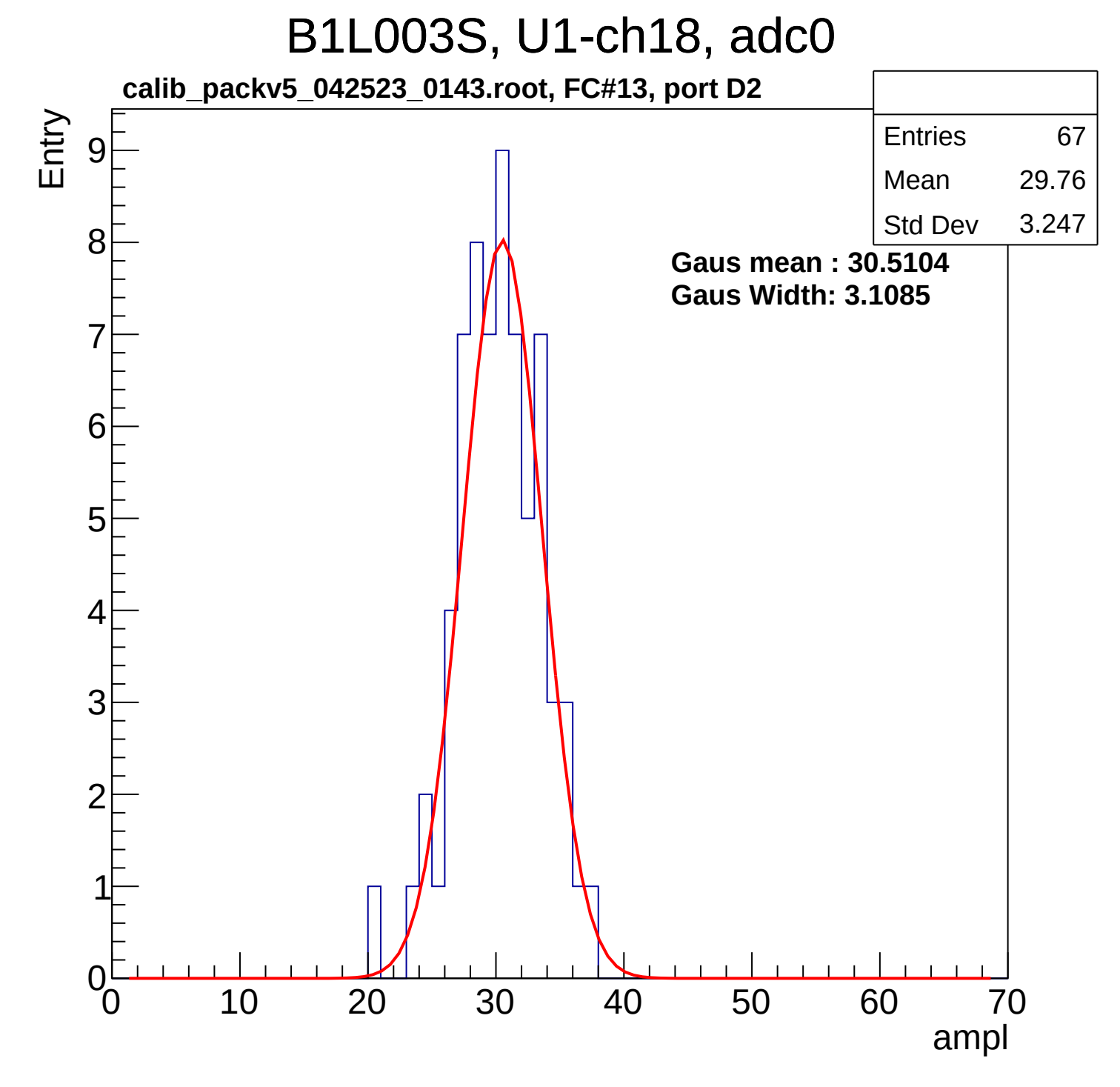
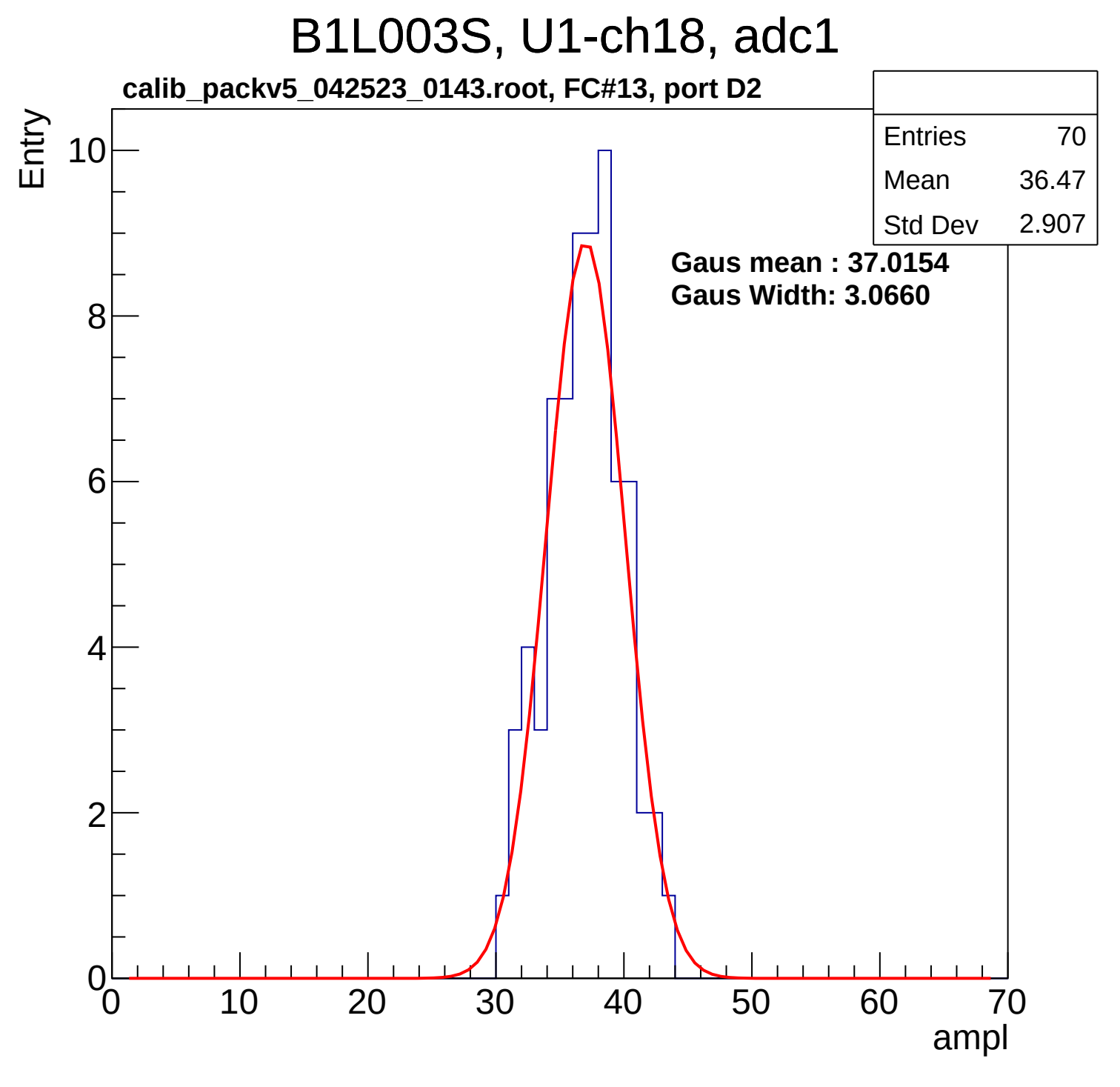
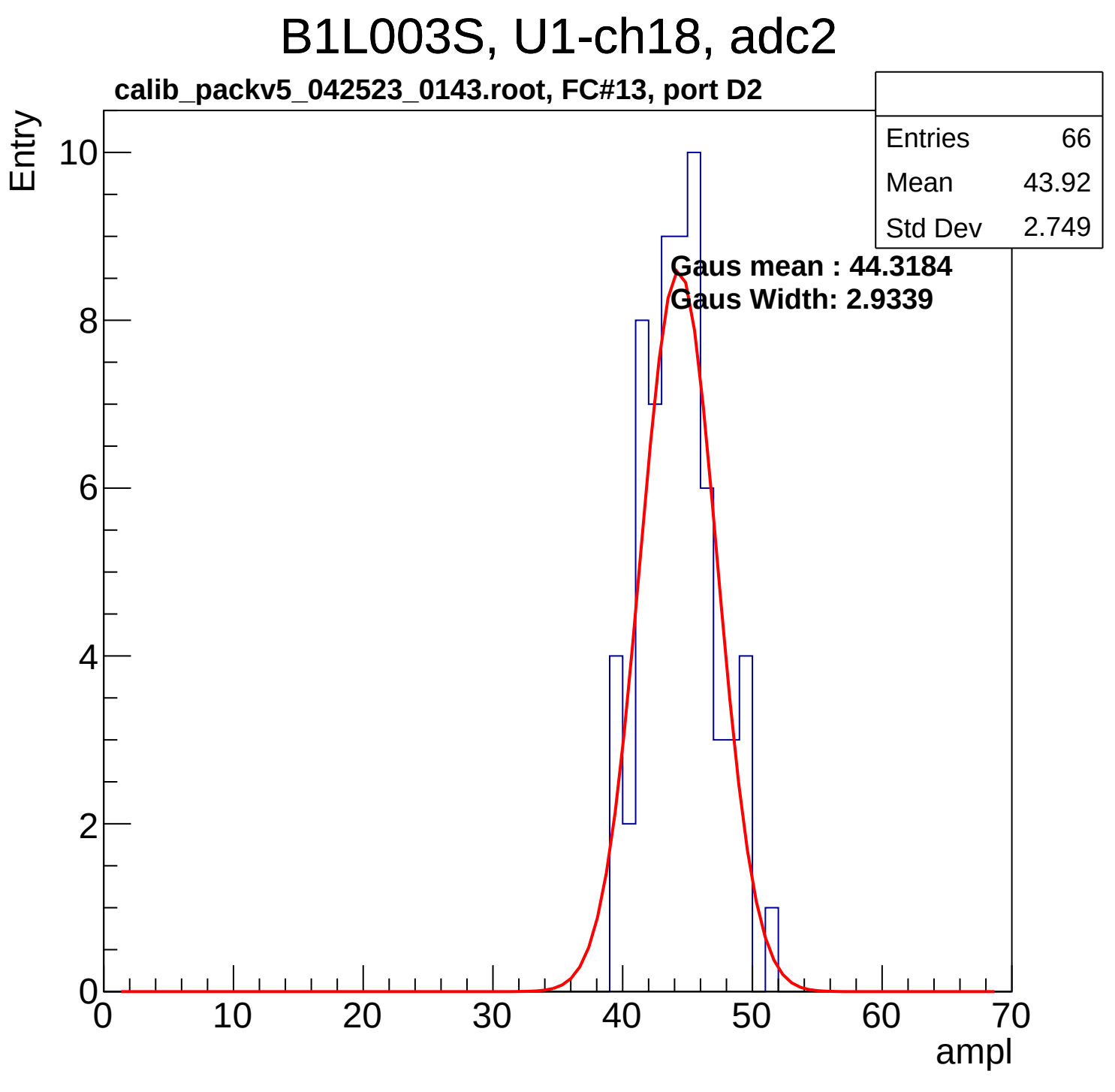
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

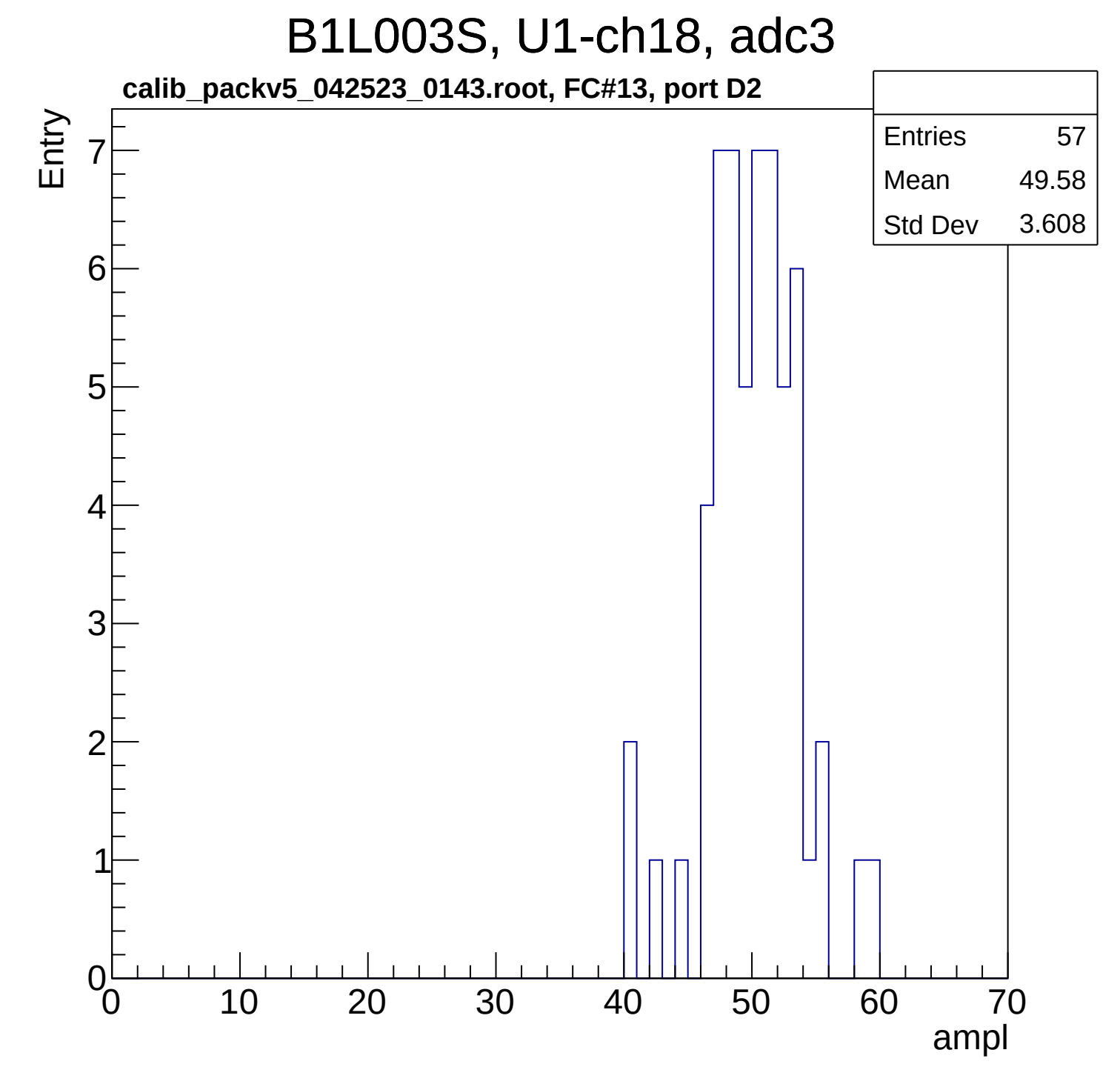

B1L003S, U1-ch17, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 60.75
Std Dev 1.92
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

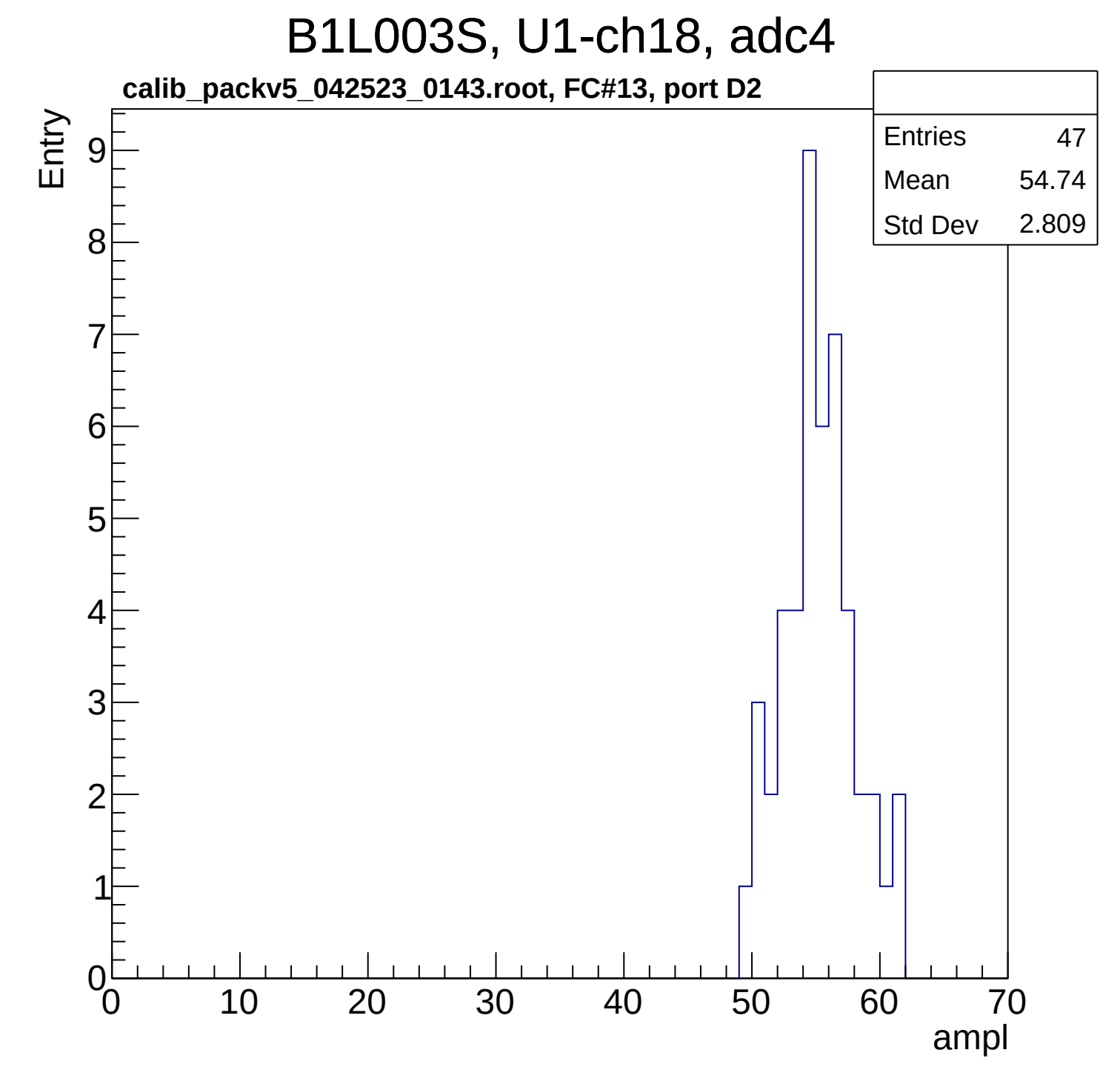

B1L003S, U1-ch17, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch18, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 29.76
Std Dev 3.247
Gaus mean : 30.5104
Gaus Width: 3.1085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch18, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 36.47
Std Dev 2.907
Gaus mean : 37.0154
Gaus Width: 3.0660
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch18, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.92
Std Dev 2.749
Gaus mean : 44.3184
Gaus Width: 2.9339
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch18, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.58
Std Dev 3.608
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch18, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.74
Std Dev 2.809
Entry
ampl
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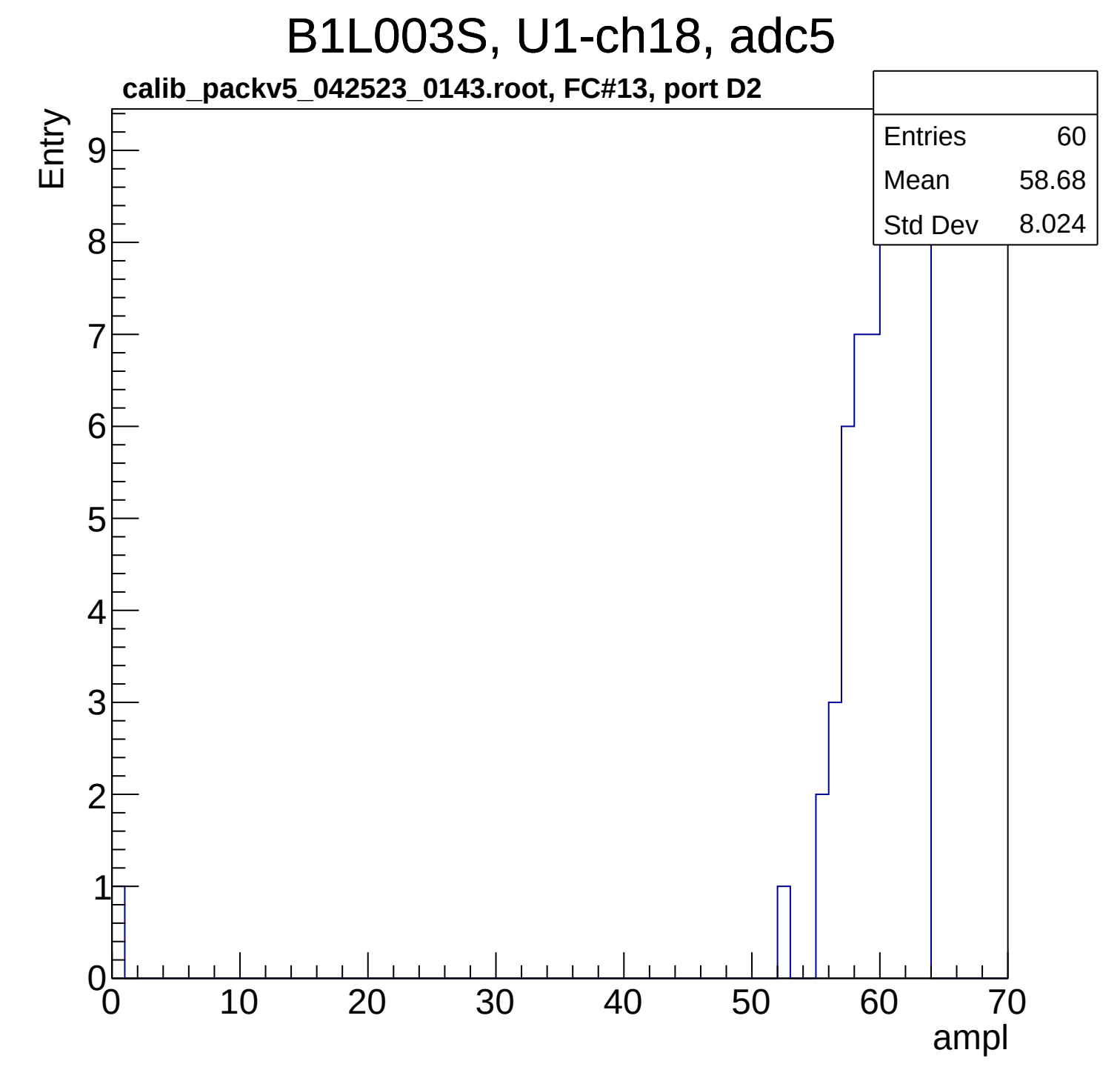
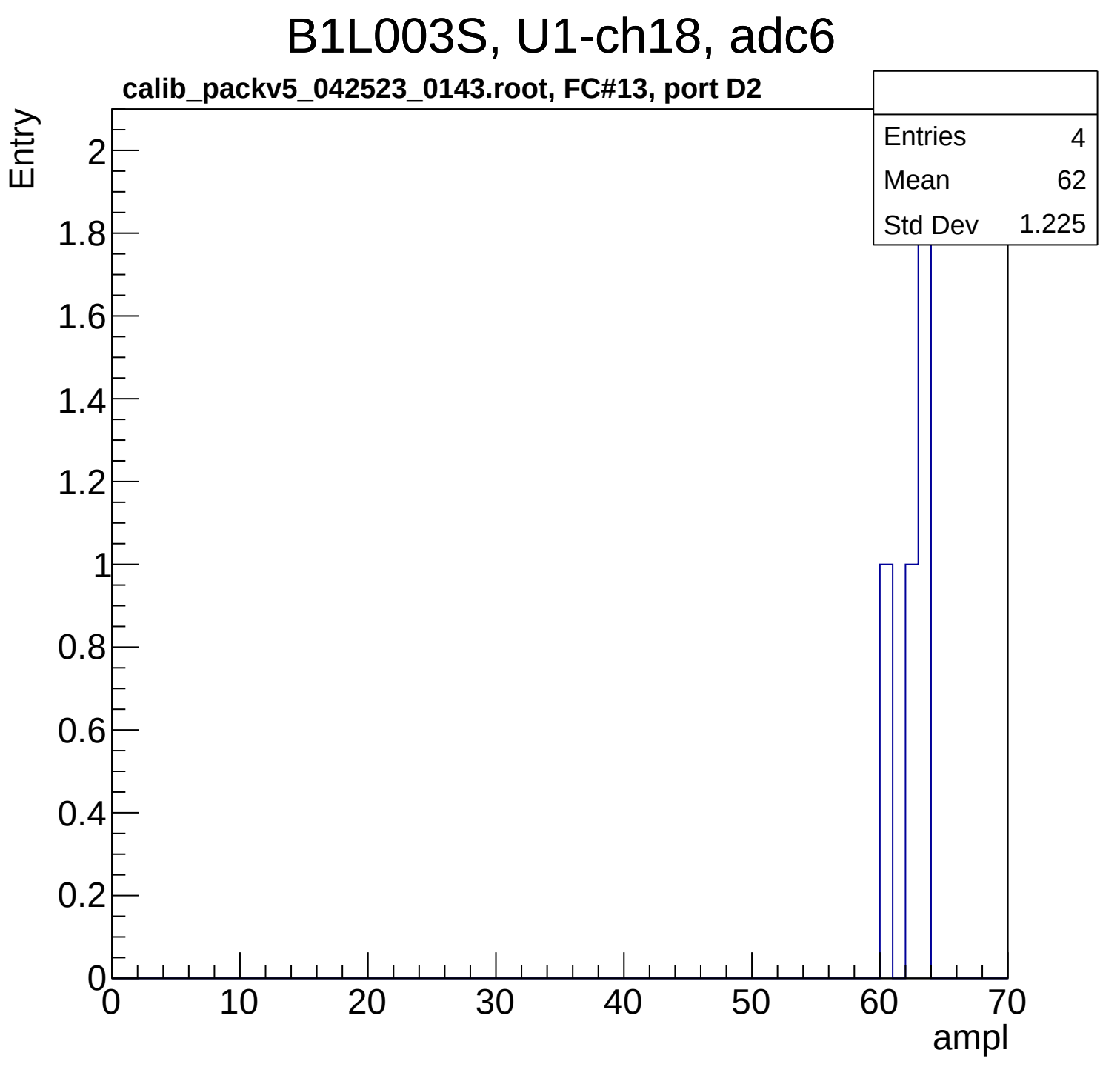
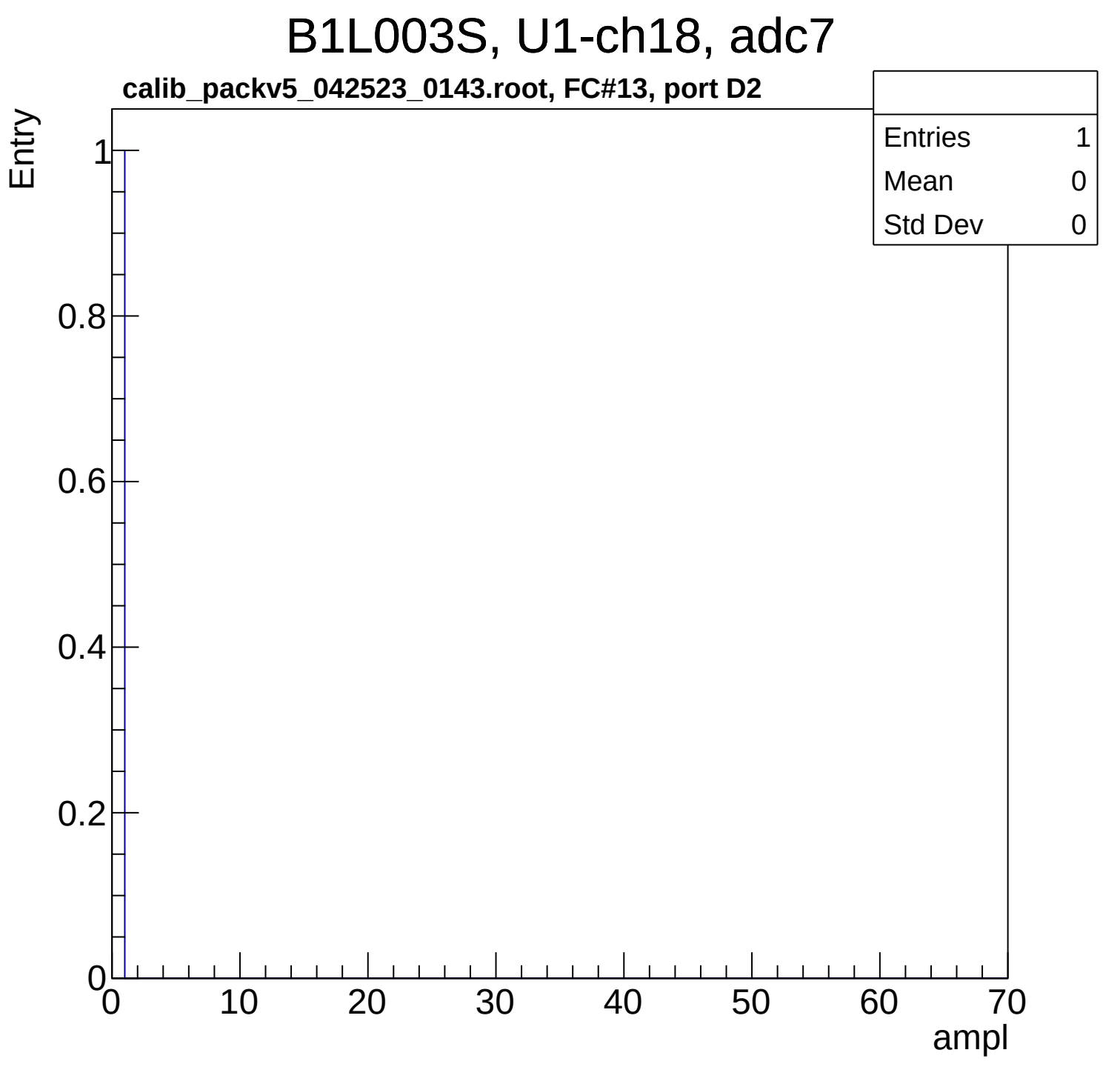
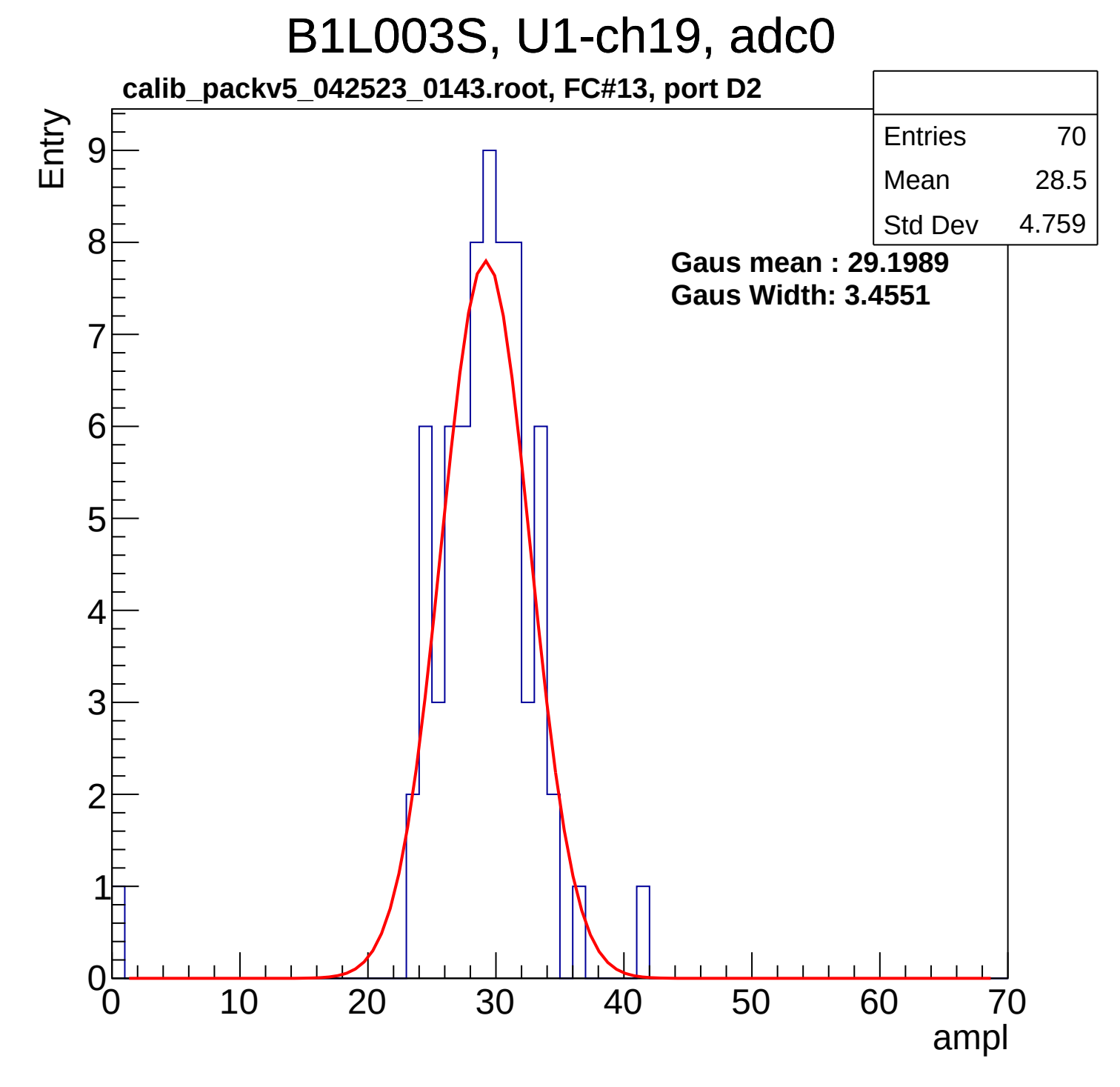
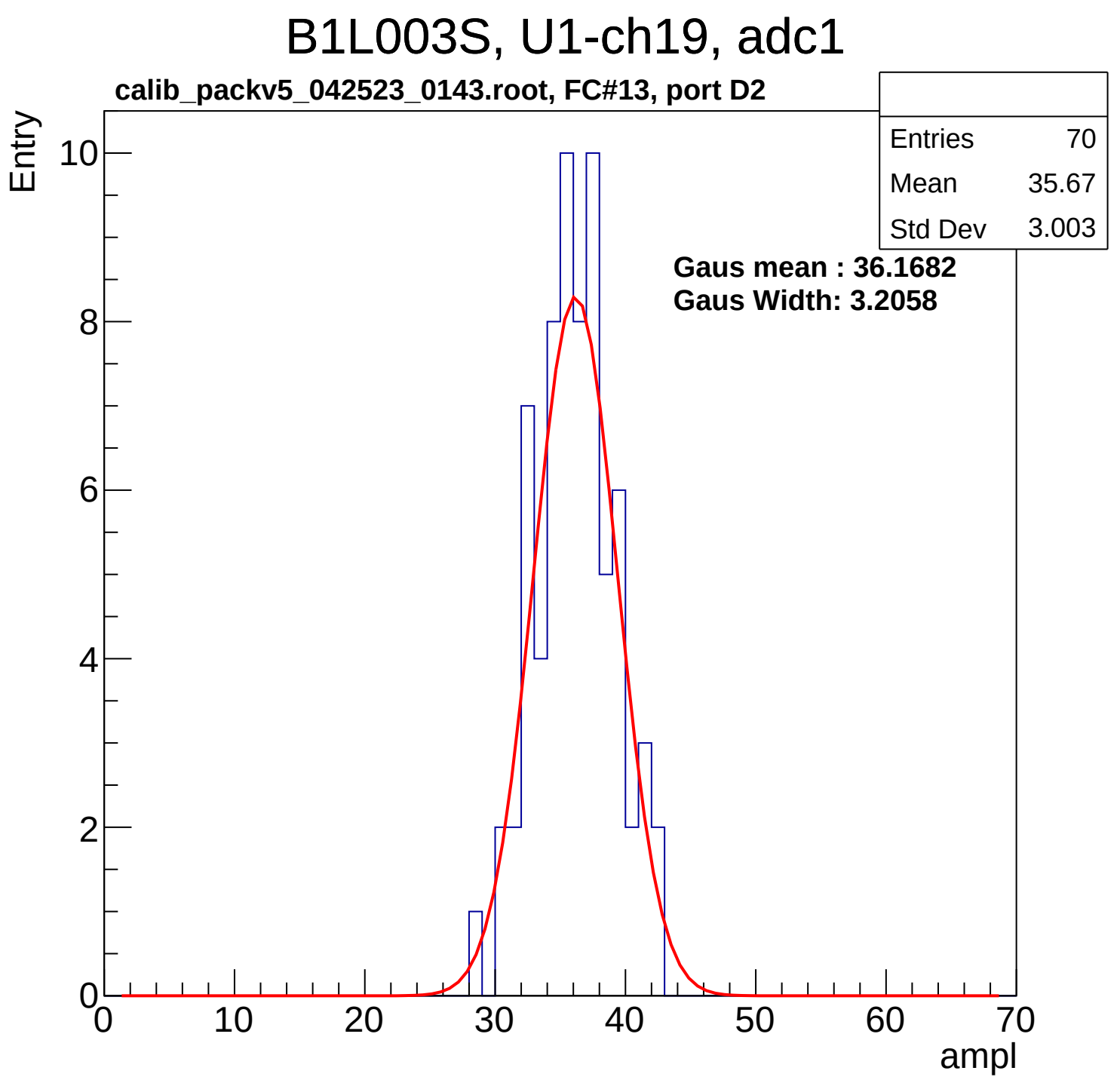
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

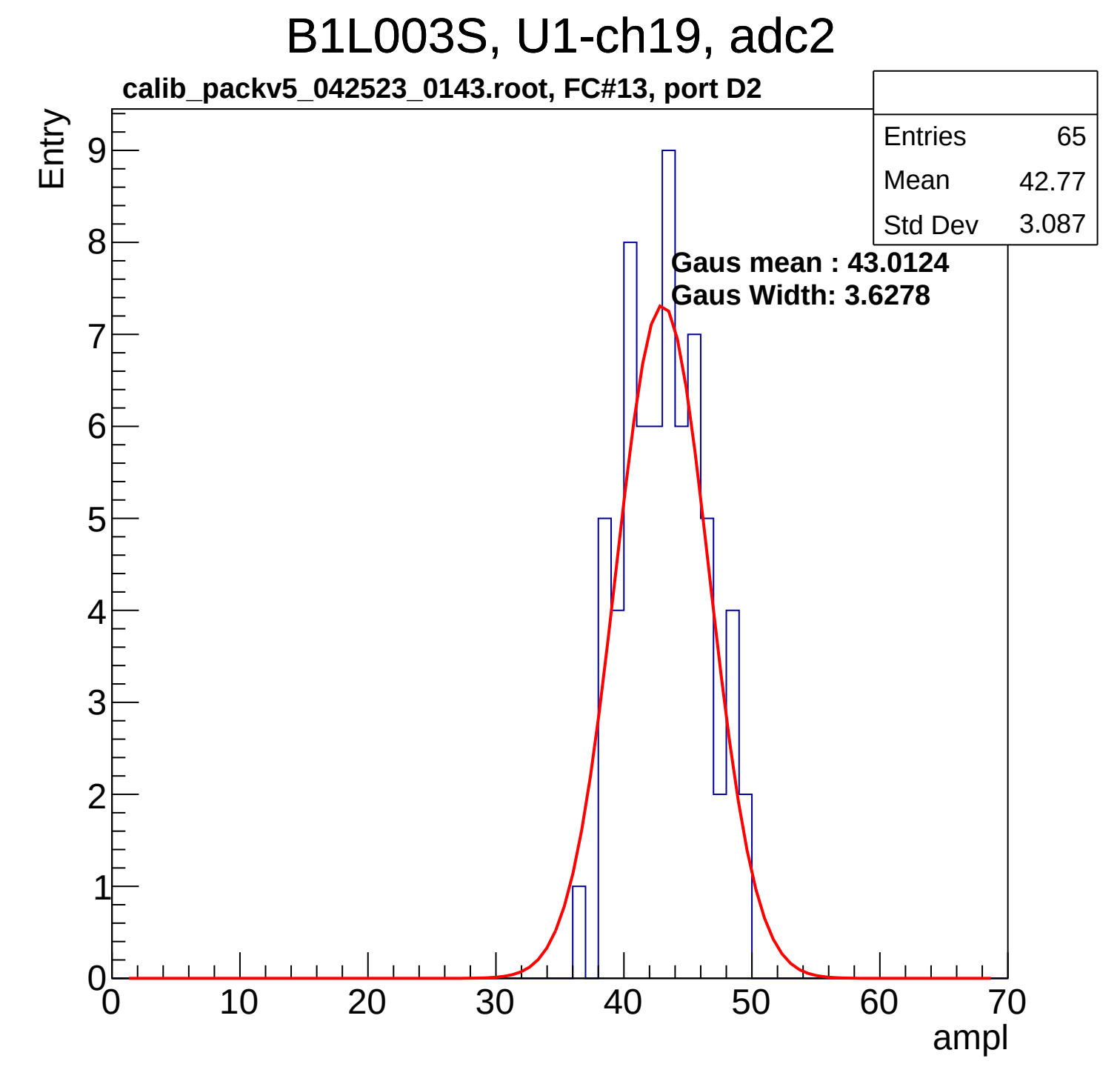

B1L003S, U1-ch18, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 58.68
Std Dev 8.024
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

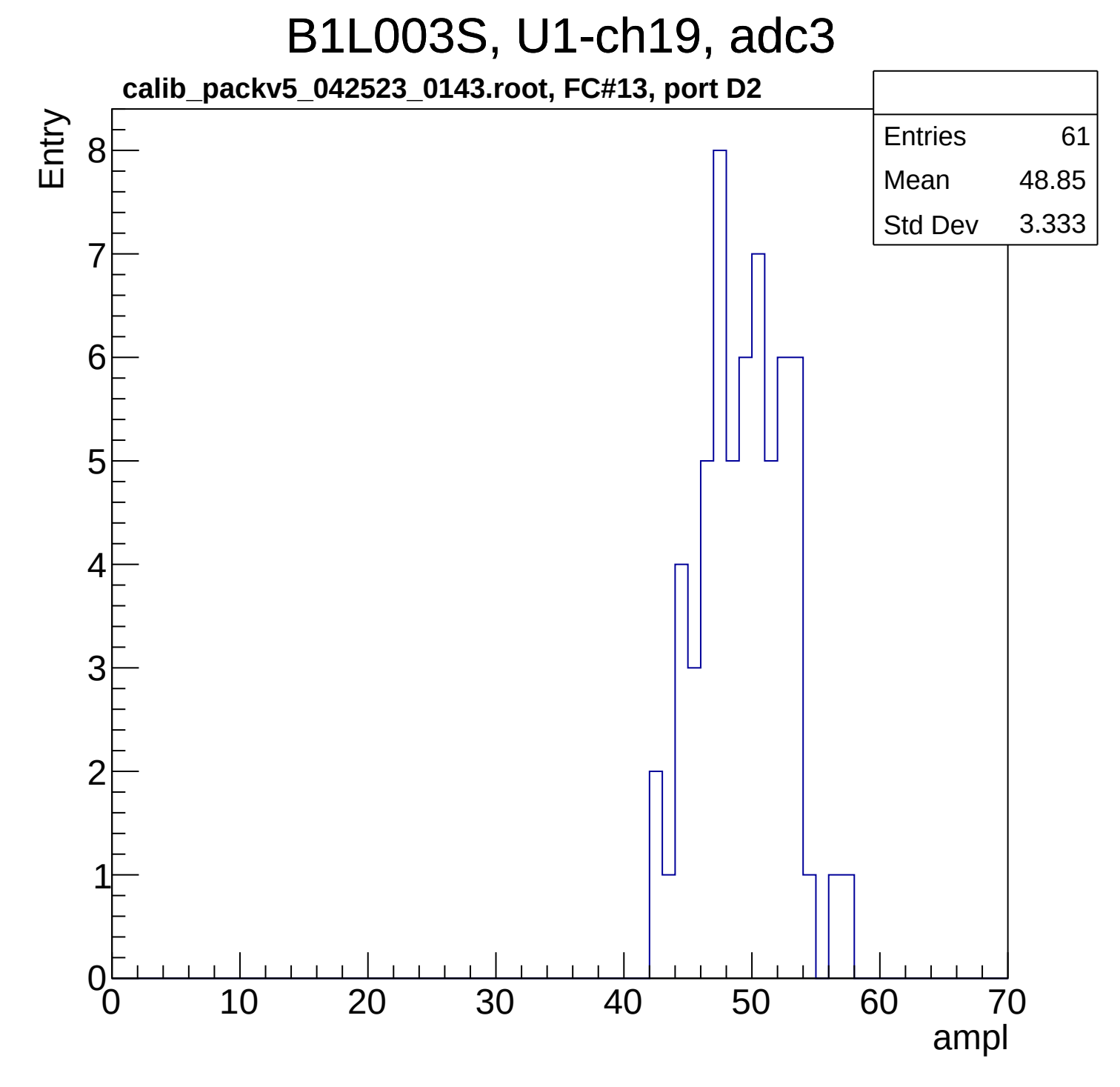

B1L003S, U1-ch18, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch18, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch19, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 28.5
Std Dev 4.759
Gaus mean : 29.1989
Gaus Width: 3.4551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.67
Std Dev 3.003
Gaus mean : 36.1682
Gaus Width: 3.2058
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.77
Std Dev 3.087
Gaus mean : 43.0124
Gaus Width: 3.6278
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 48.85
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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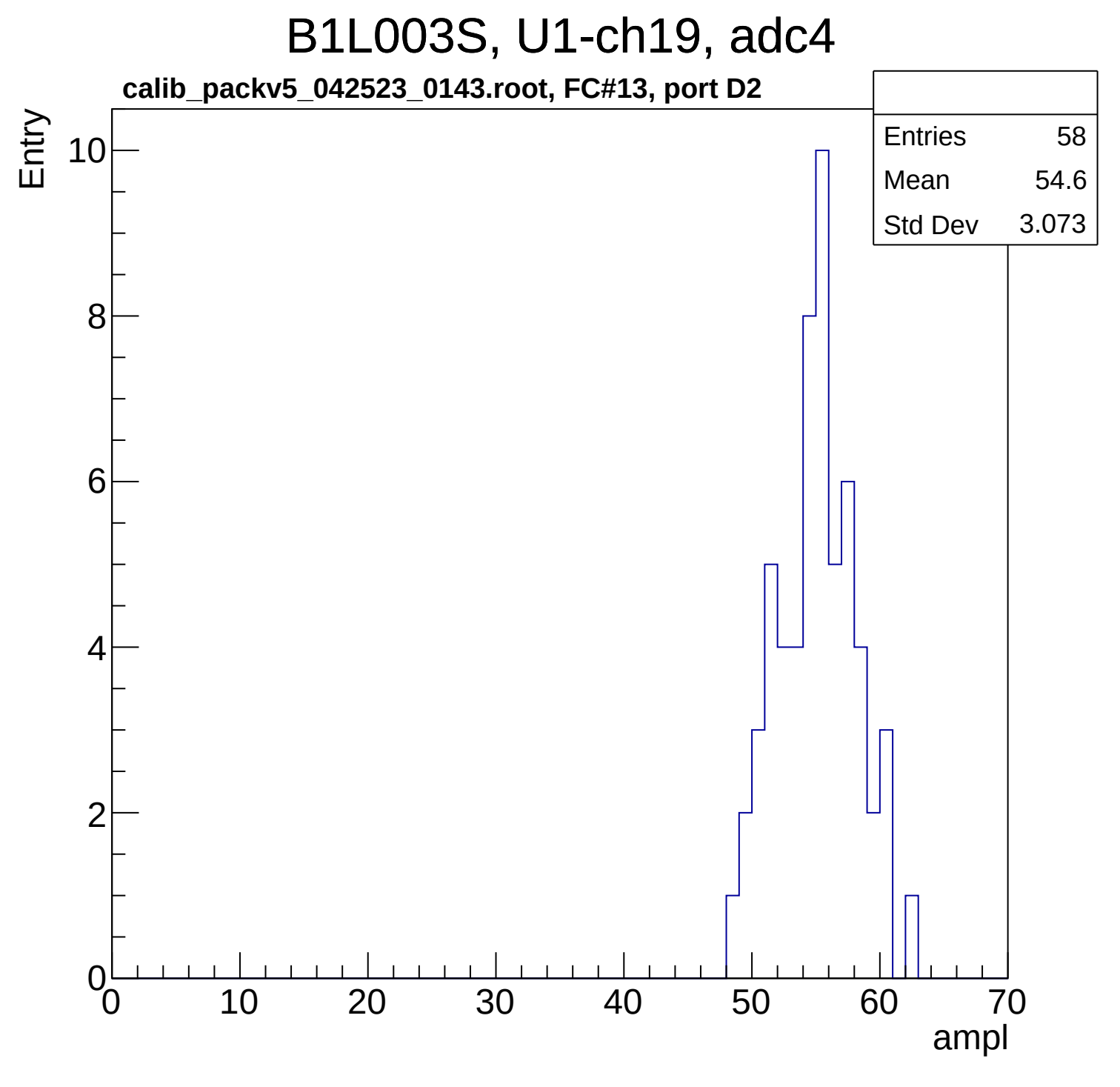
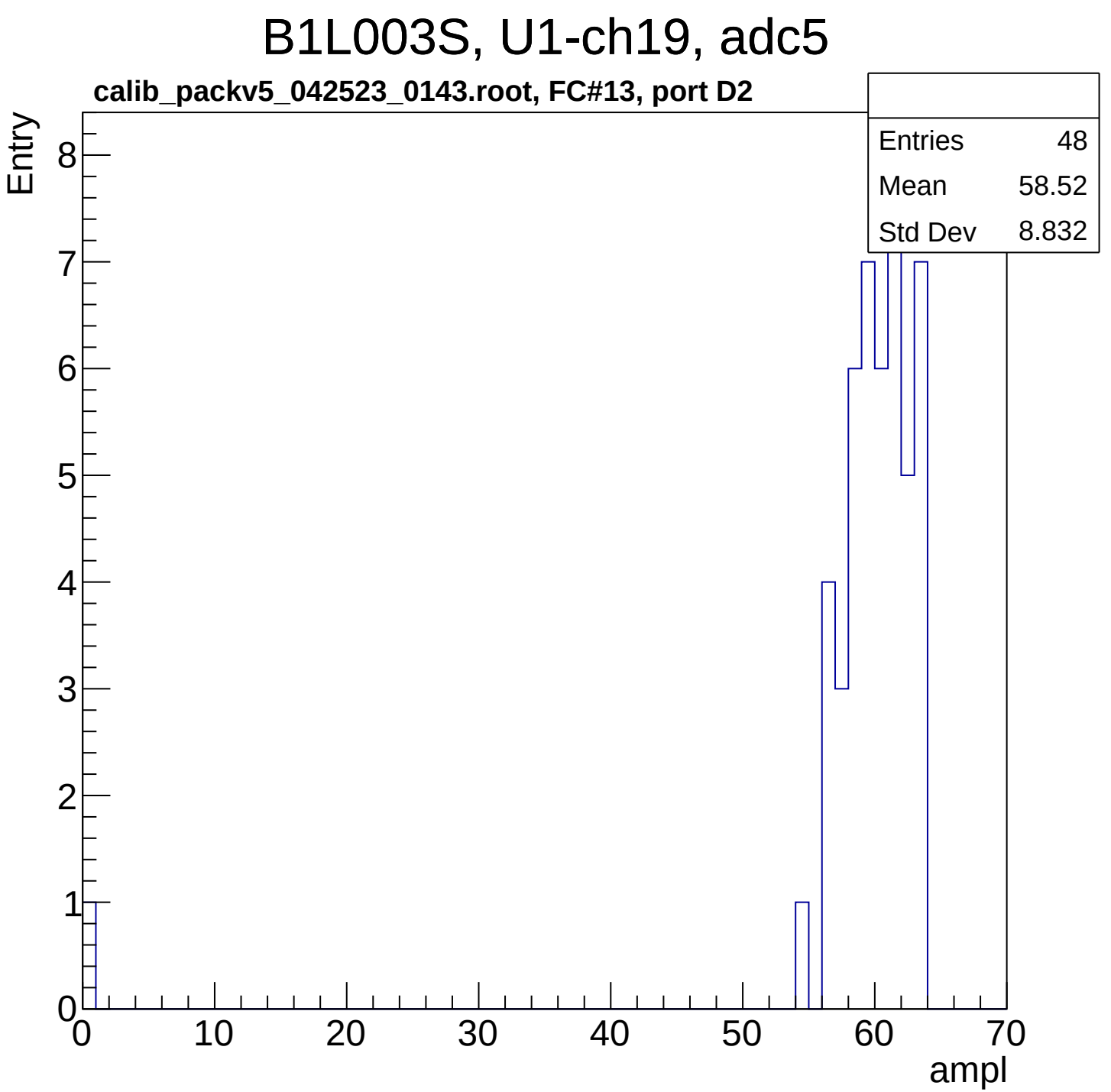
40
50
60
70


B1L003S, U1-ch19, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.6
Std Dev 3.073
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.52
Std Dev 8.832
Entry
ampl
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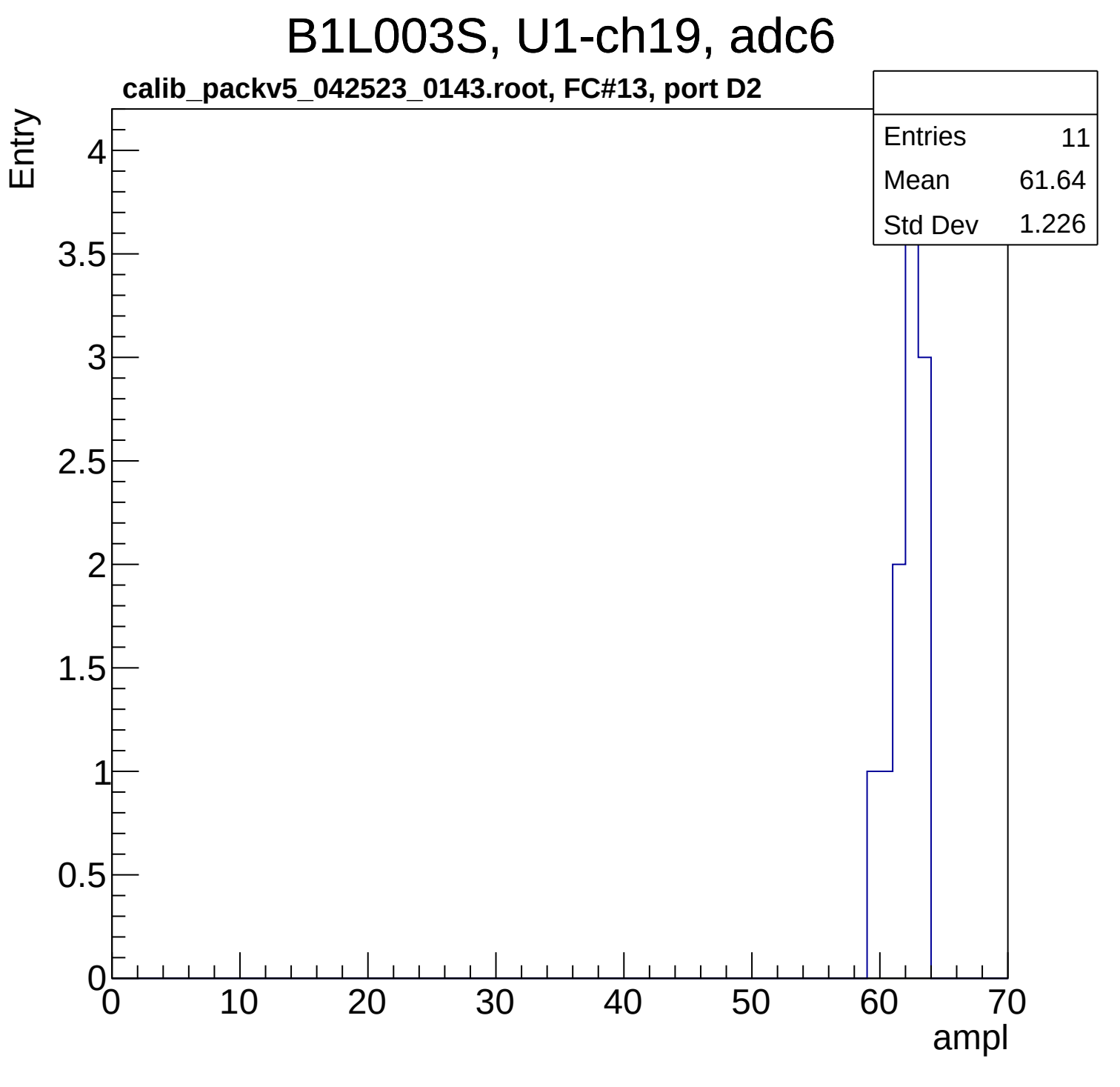
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.64
Std Dev 1.226
Entry
ampl
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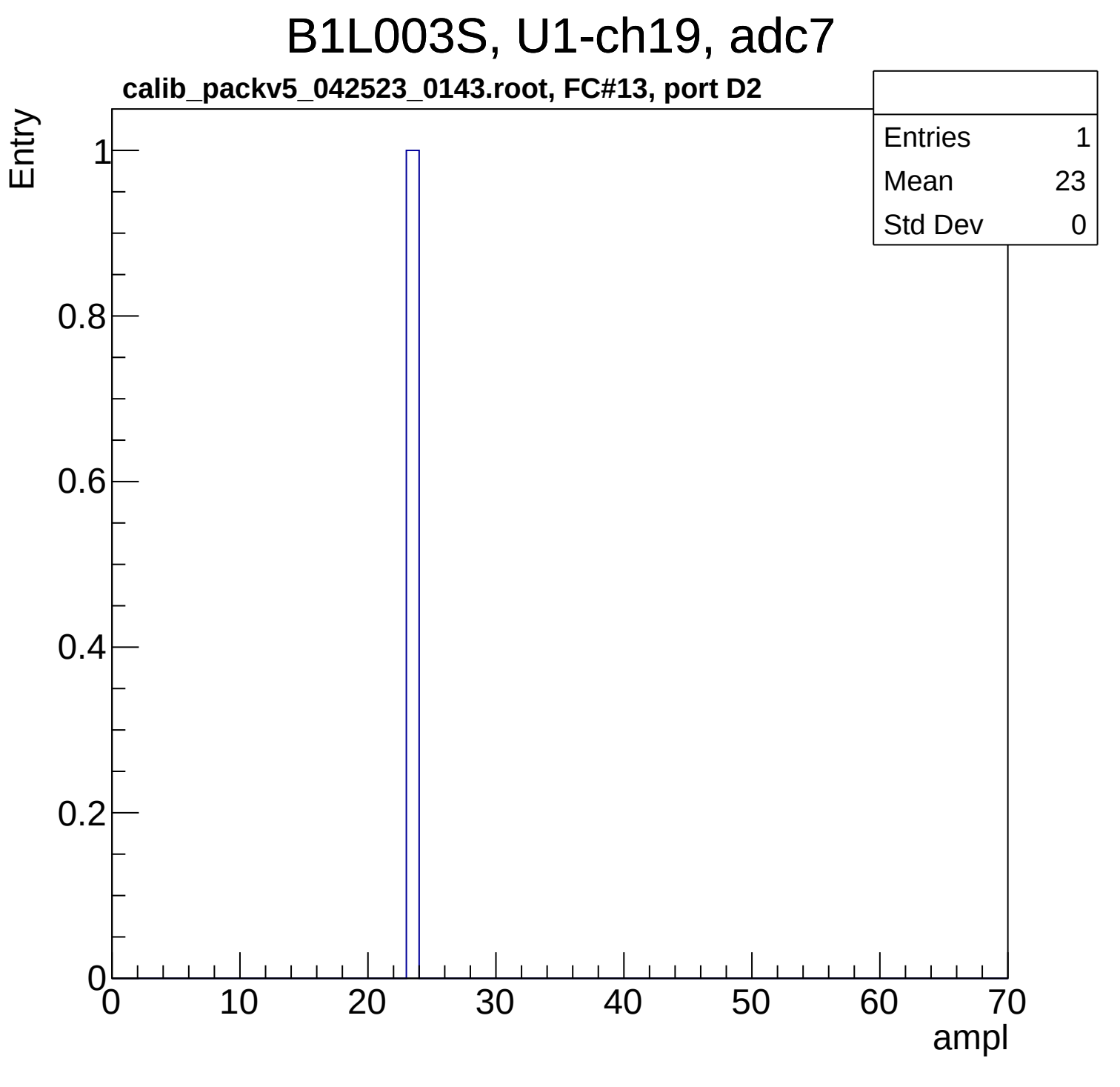
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch19, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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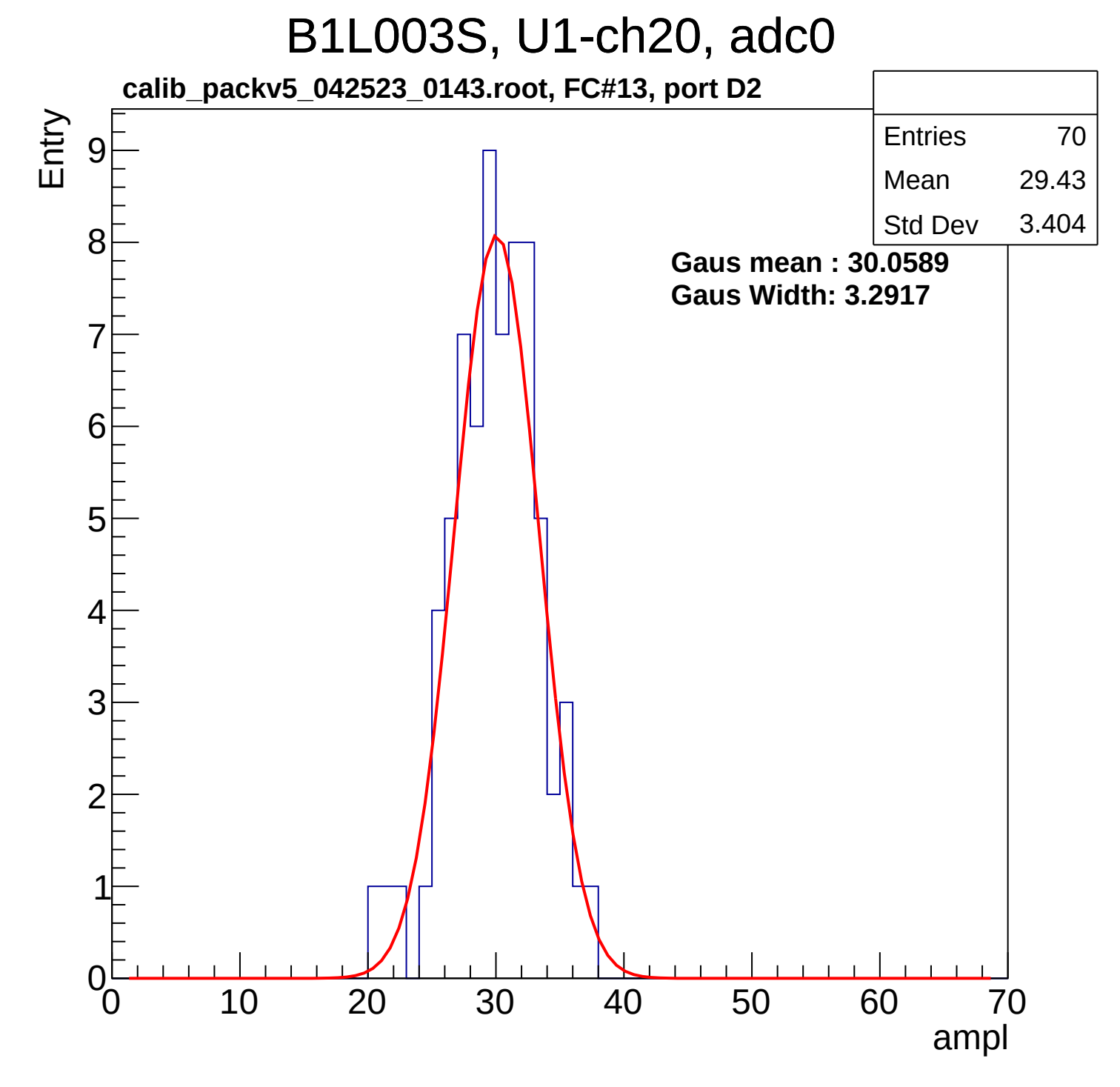
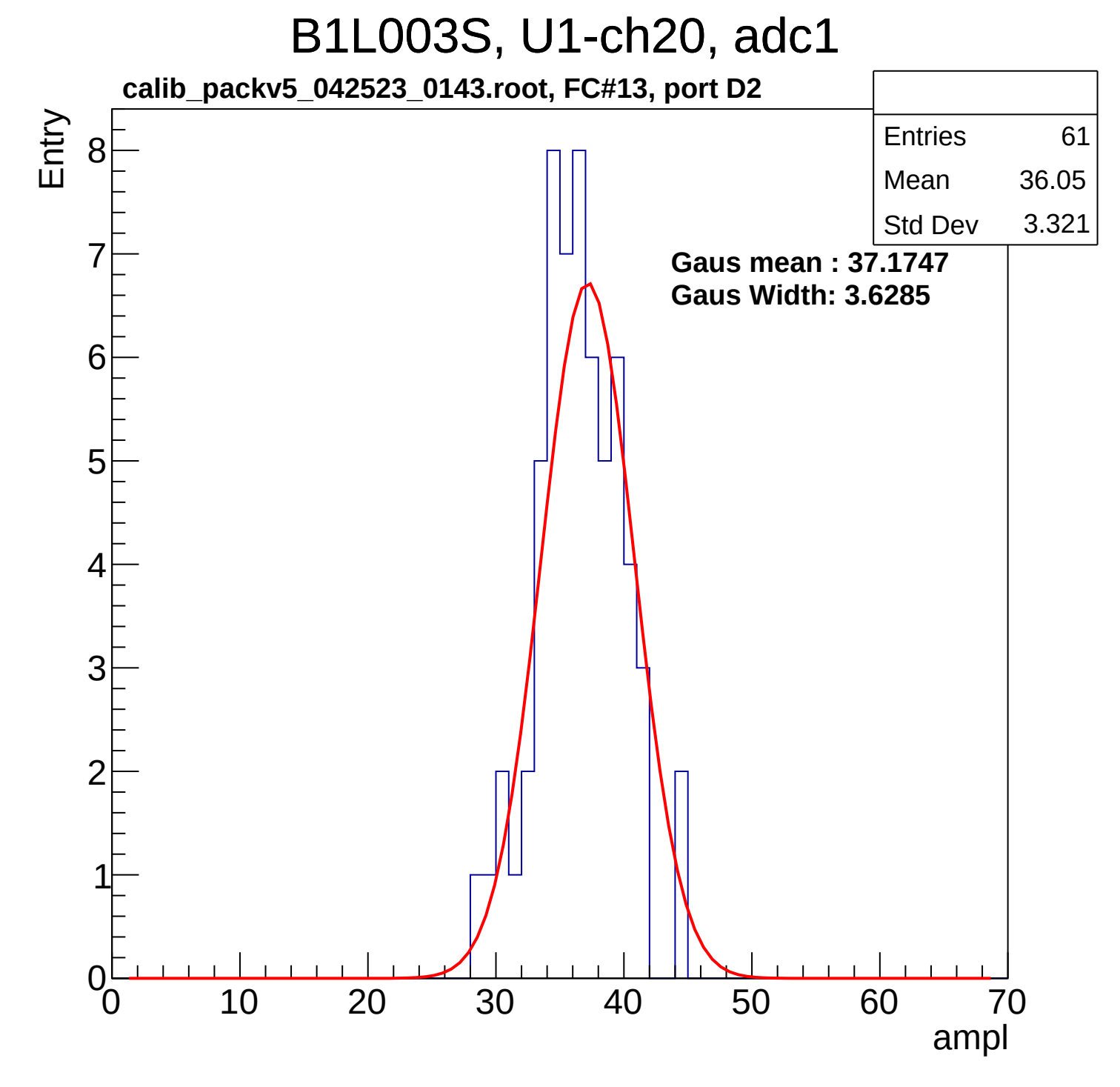
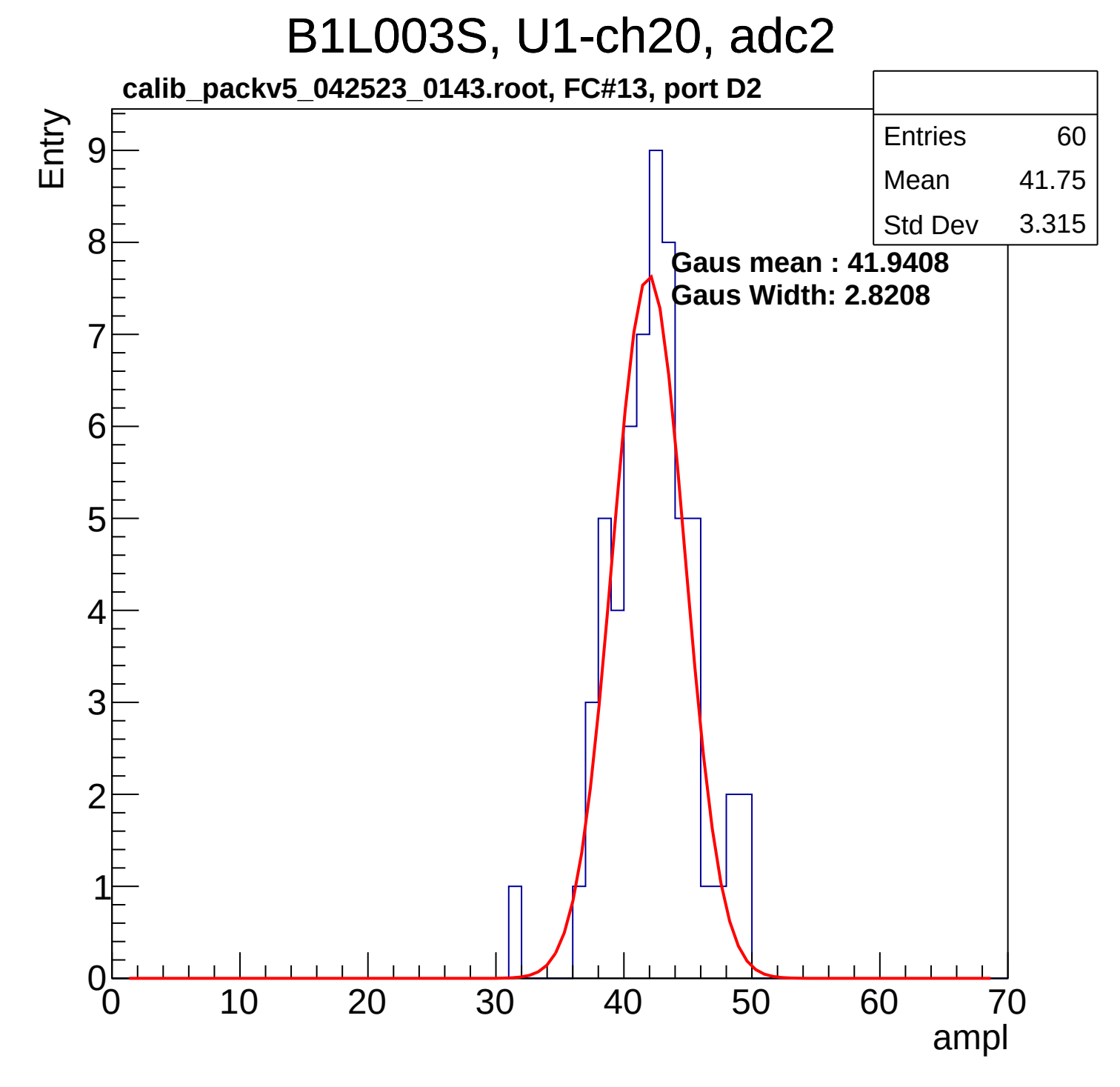
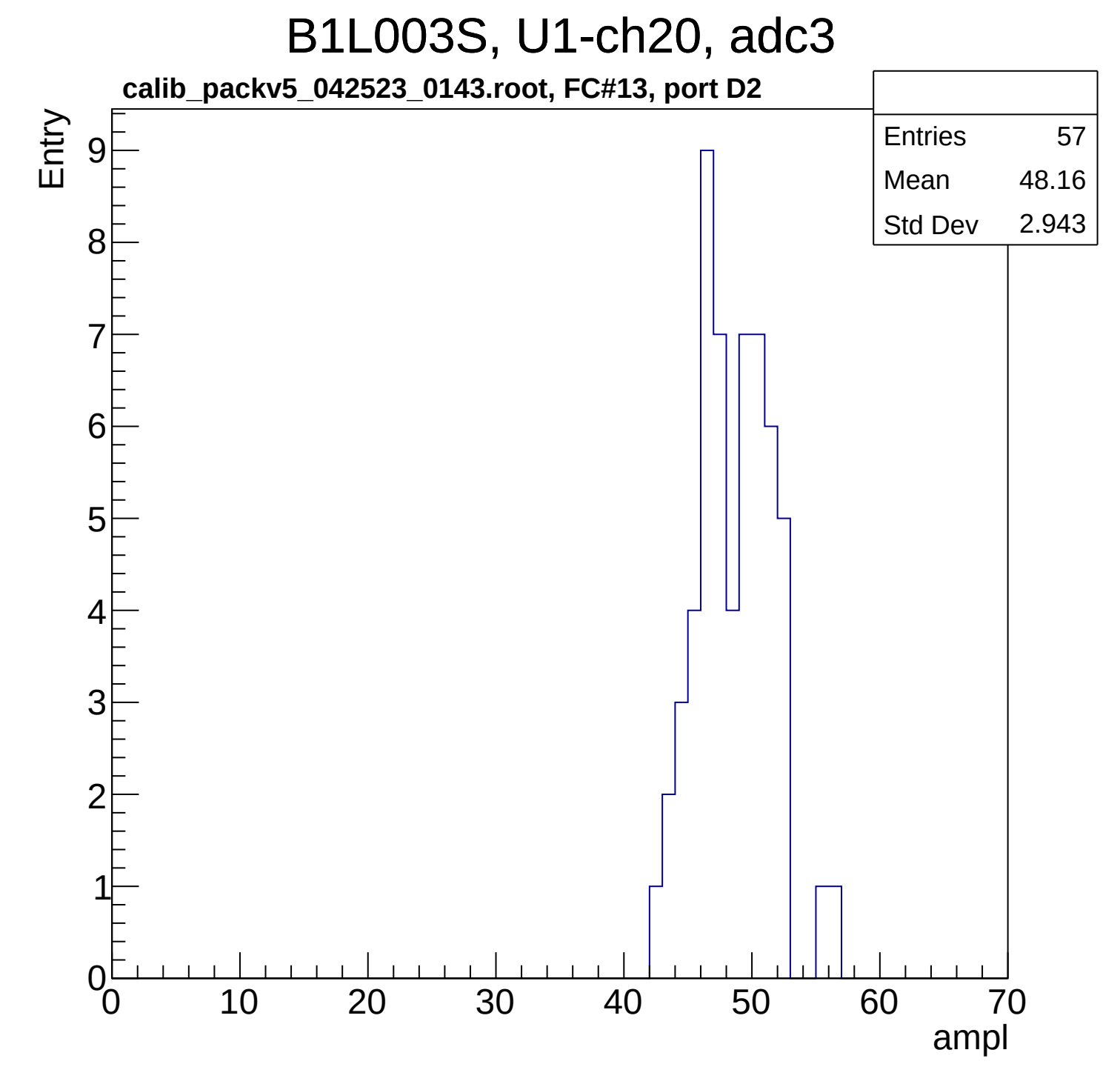
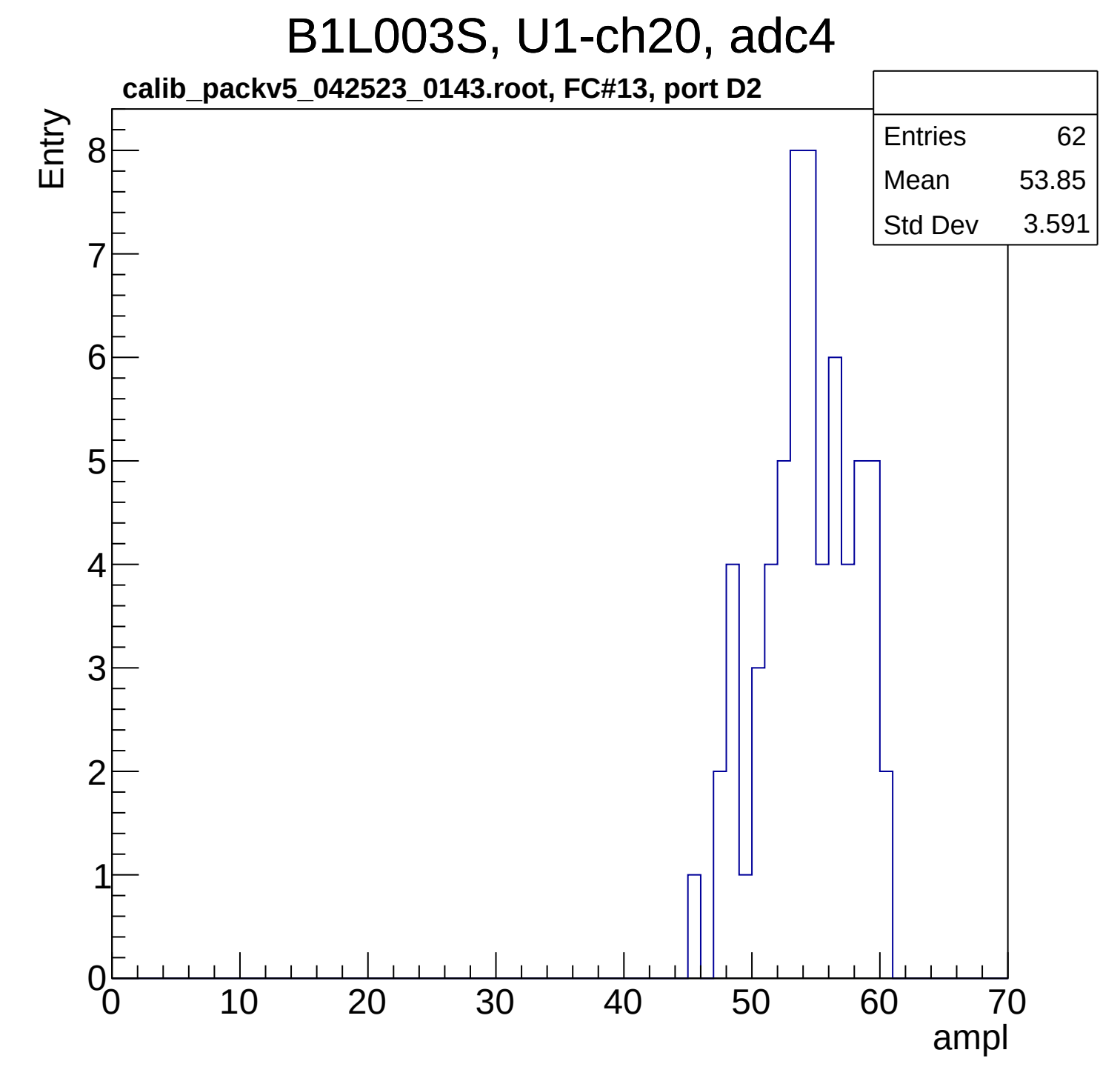
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

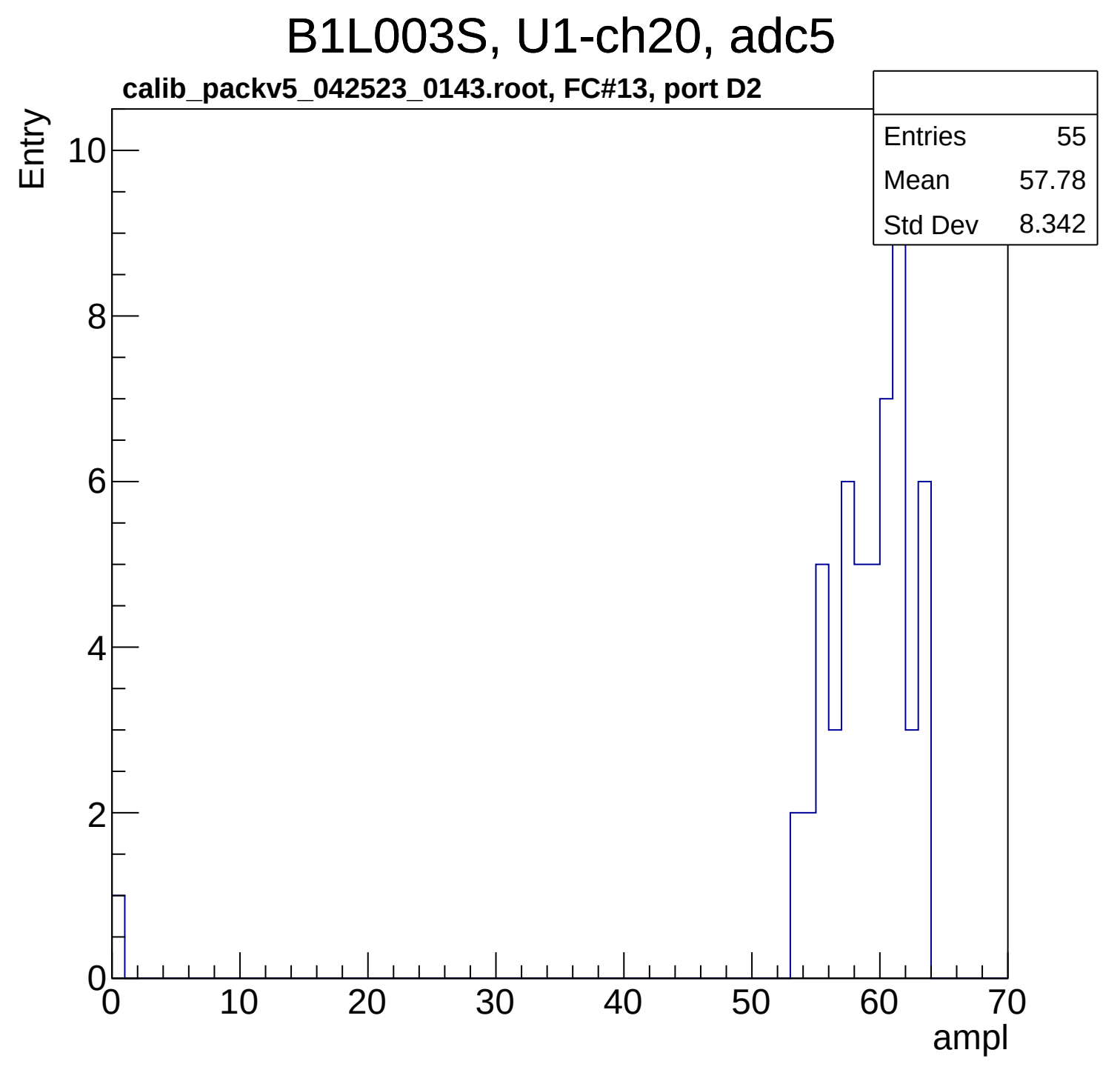

B1L003S, U1-ch20, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 29.43
Std Dev 3.404
Gaus mean : 30.0589
Gaus Width: 3.2917
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

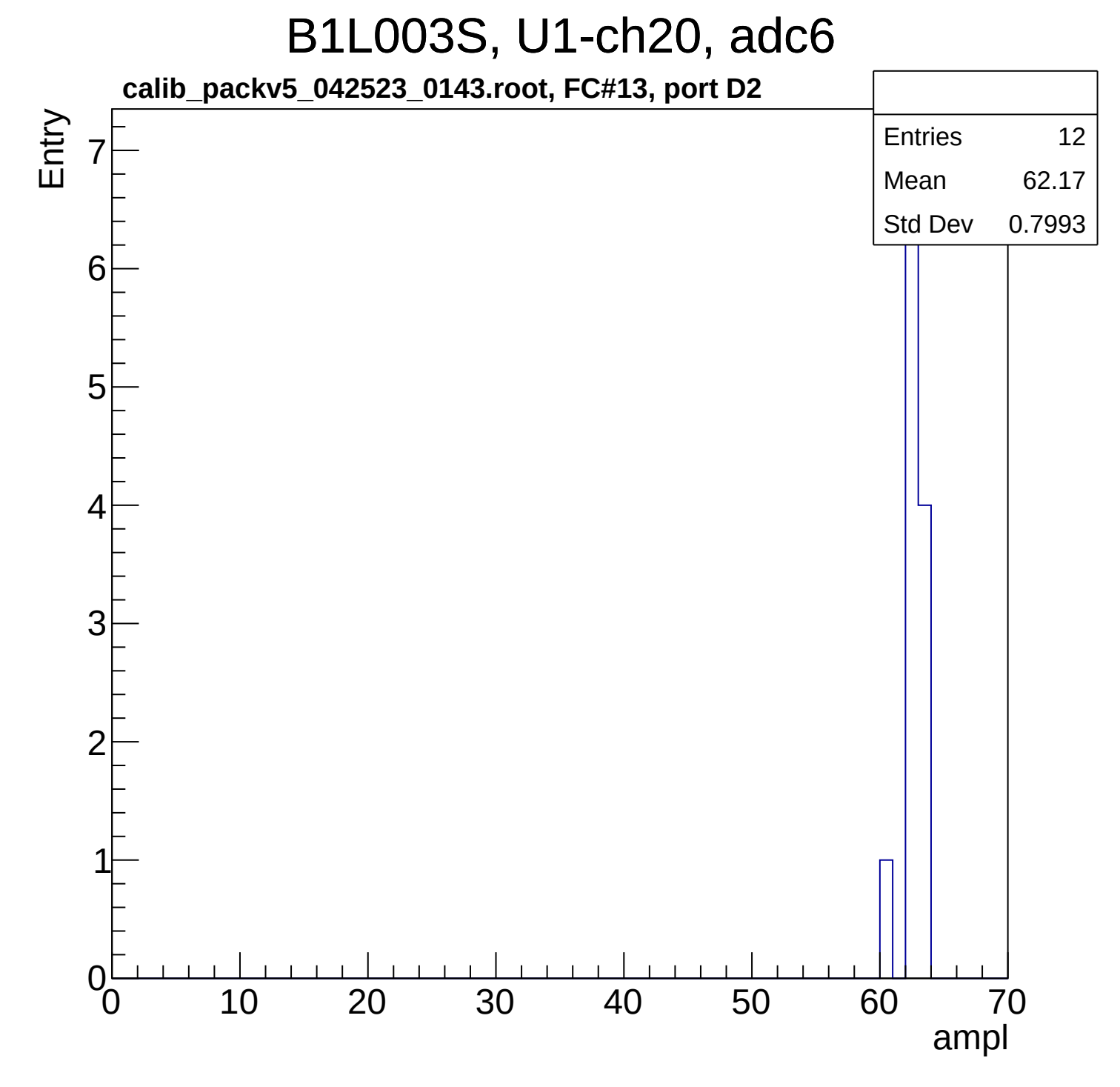

B1L003S, U1-ch20, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.05
Std Dev 3.321
Gaus mean : 37.1747
Gaus Width: 3.6285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

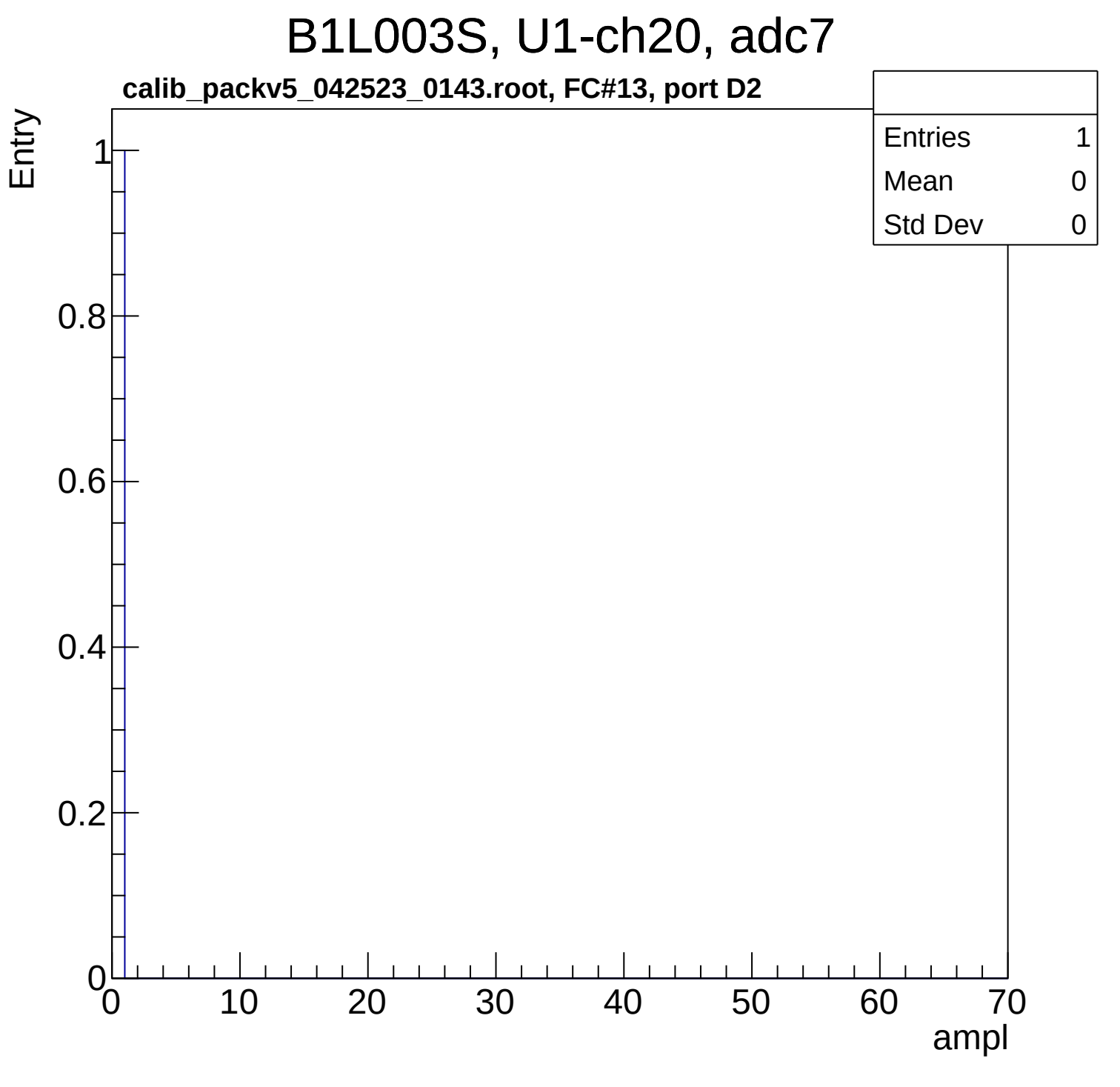

B1L003S, U1-ch20, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.75
Std Dev 3.315
Gaus mean : 41.9408
Gaus Width: 2.8208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch20, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.16
Std Dev 2.943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch20, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 53.85
Std Dev 3.591
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch20, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 57.78
Std Dev 8.342
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch20, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 62.17
Std Dev 0.7993
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch20, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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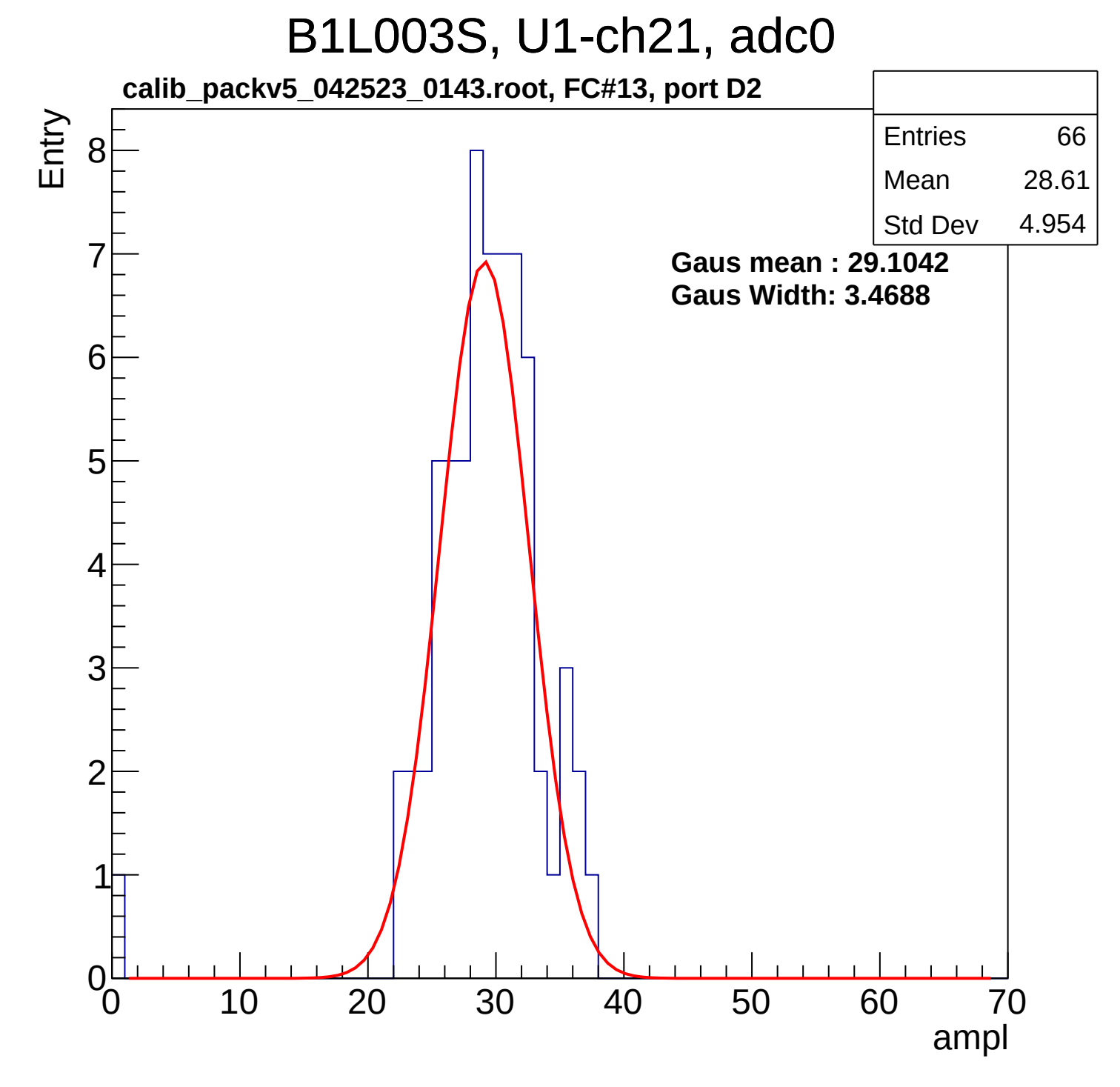
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch21, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.61
Std Dev 4.954
Gaus mean : 29.1042
Gaus Width: 3.4688
Entry
ampl
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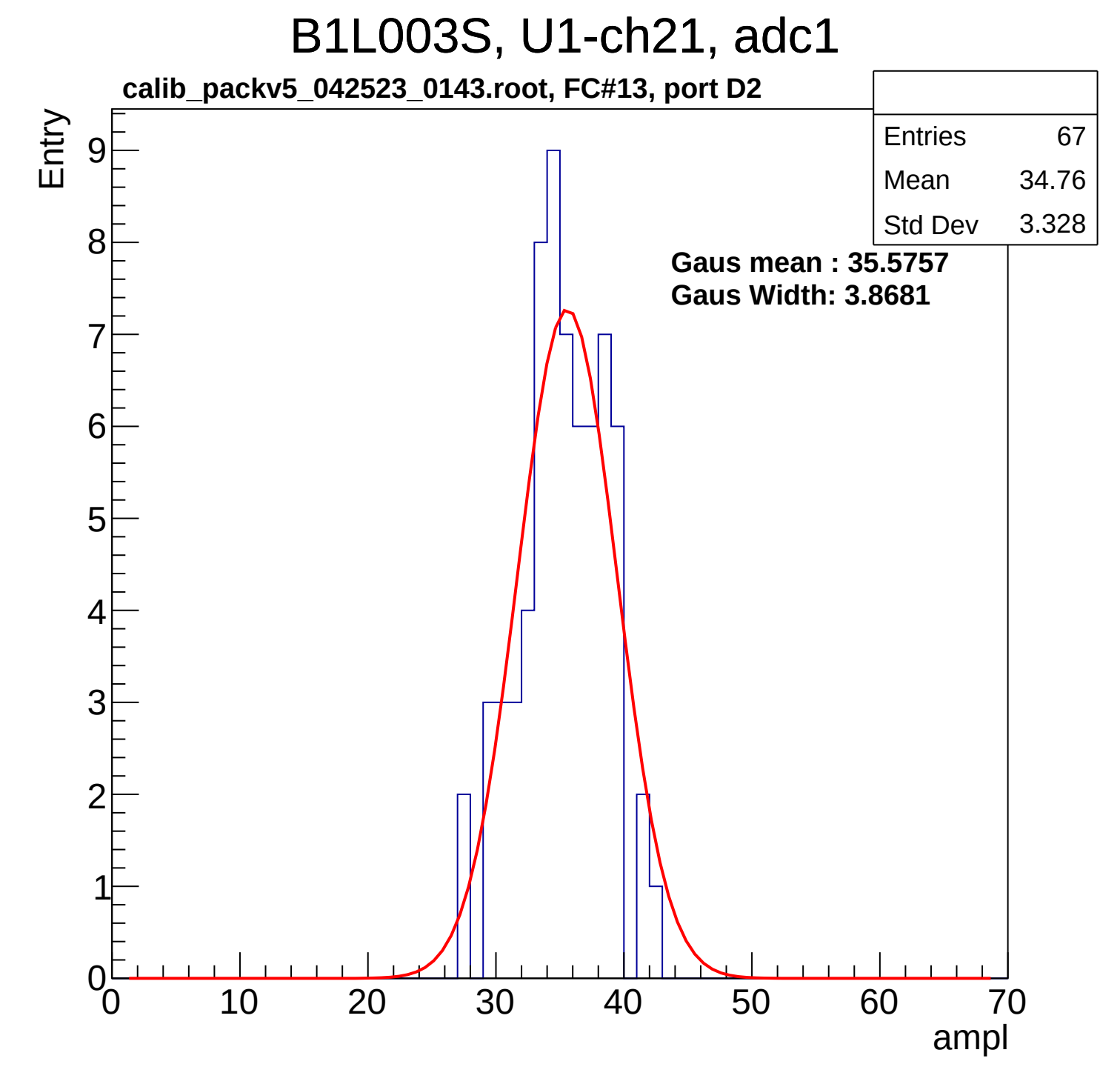
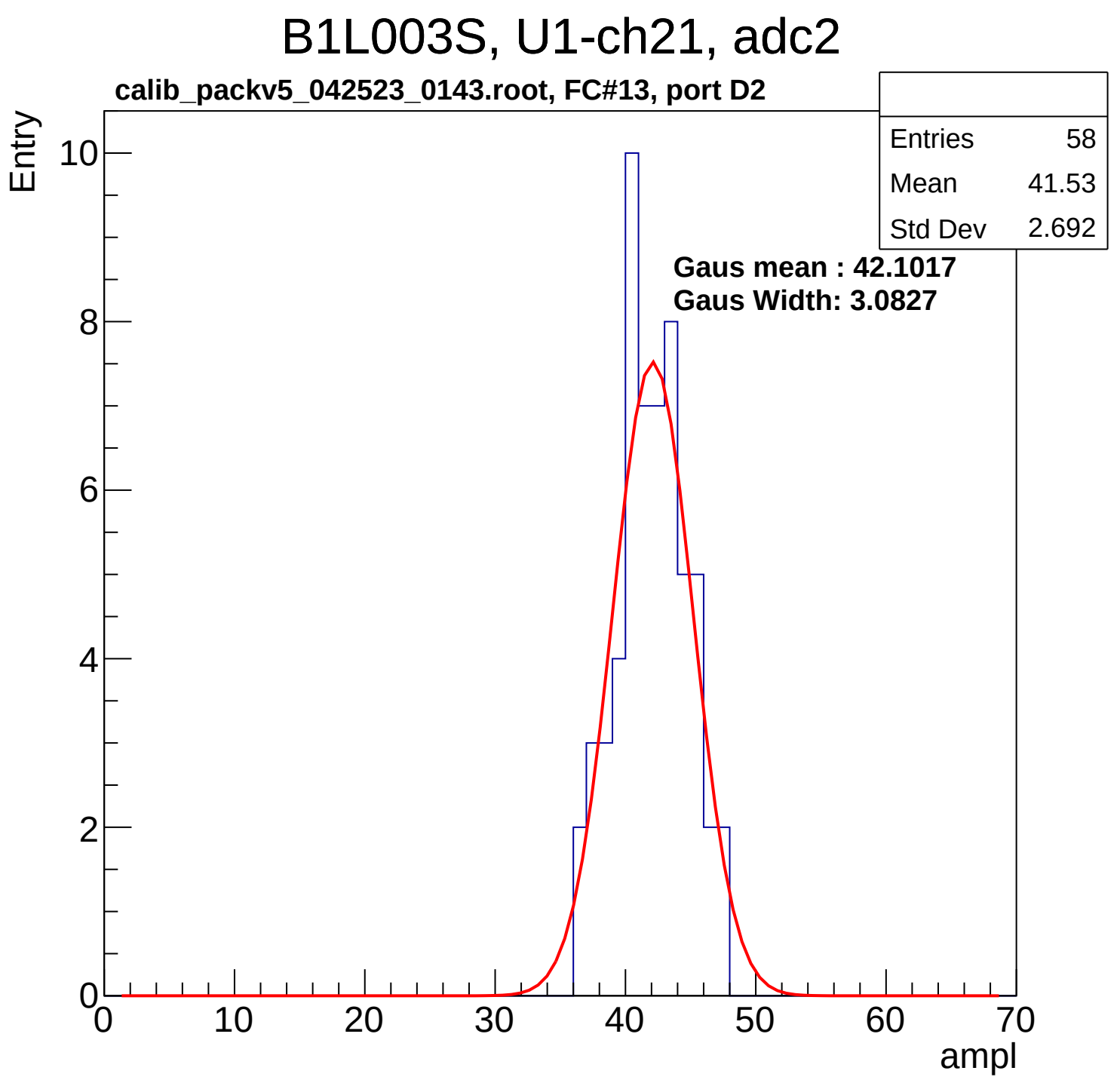
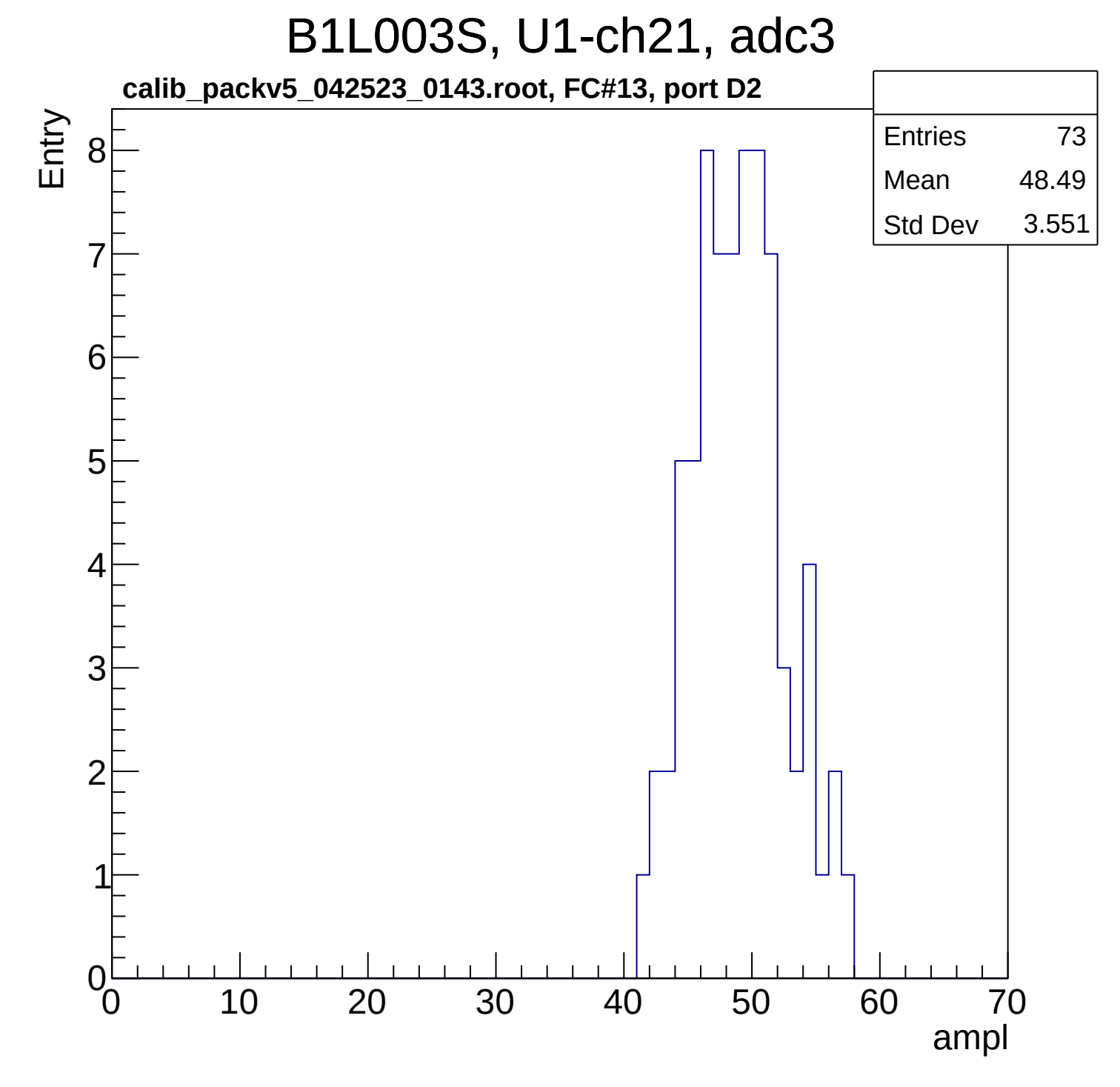
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 34.76
Std Dev 3.328
Gaus mean : 35.5757
Gaus Width: 3.8681
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 41.53
Std Dev 2.692
Gaus mean : 42.1017
Gaus Width: 3.0827
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 48.49
Std Dev 3.551
Entry
ampl
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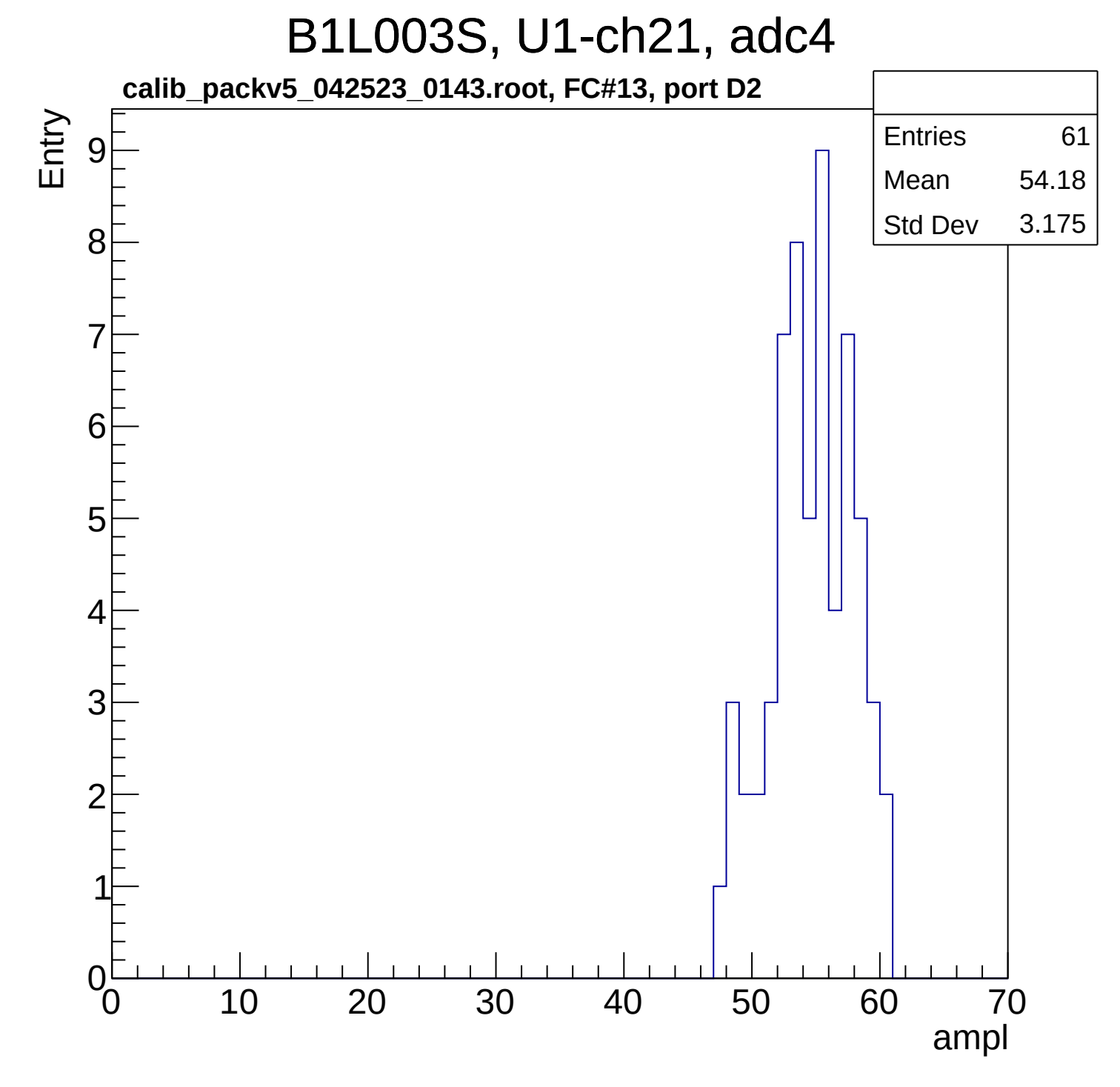
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.18
Std Dev 3.175
Entry
ampl
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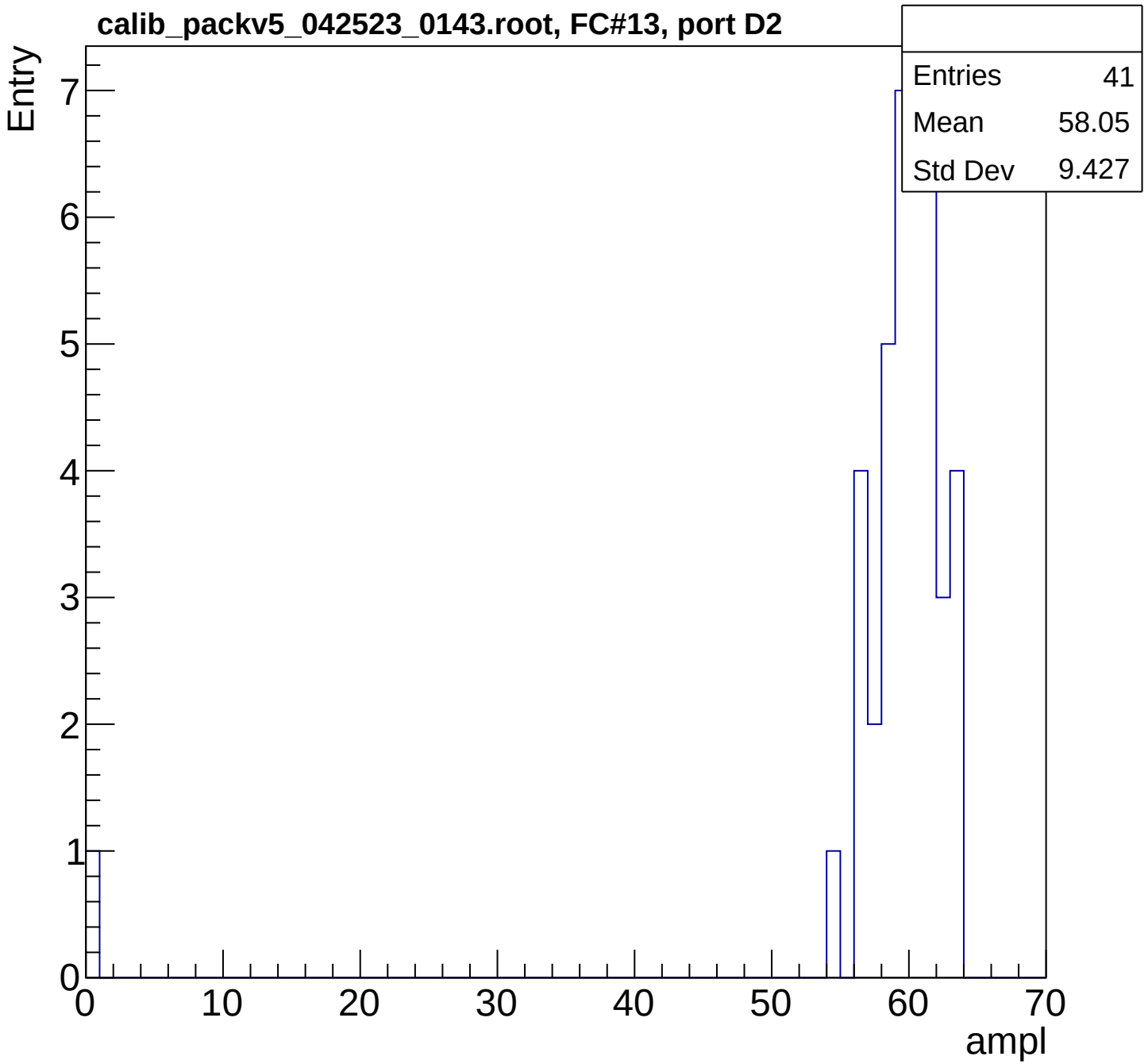
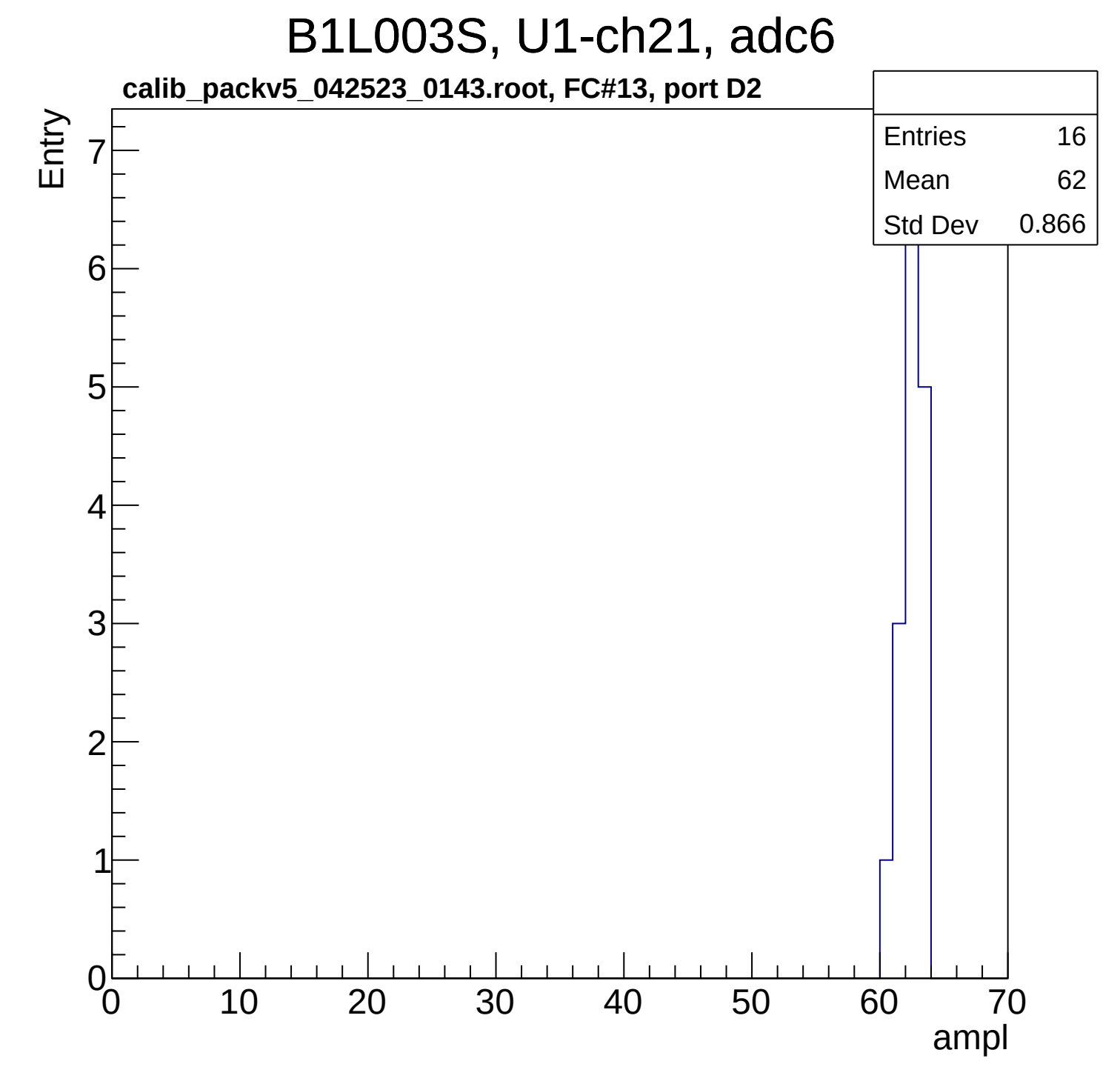
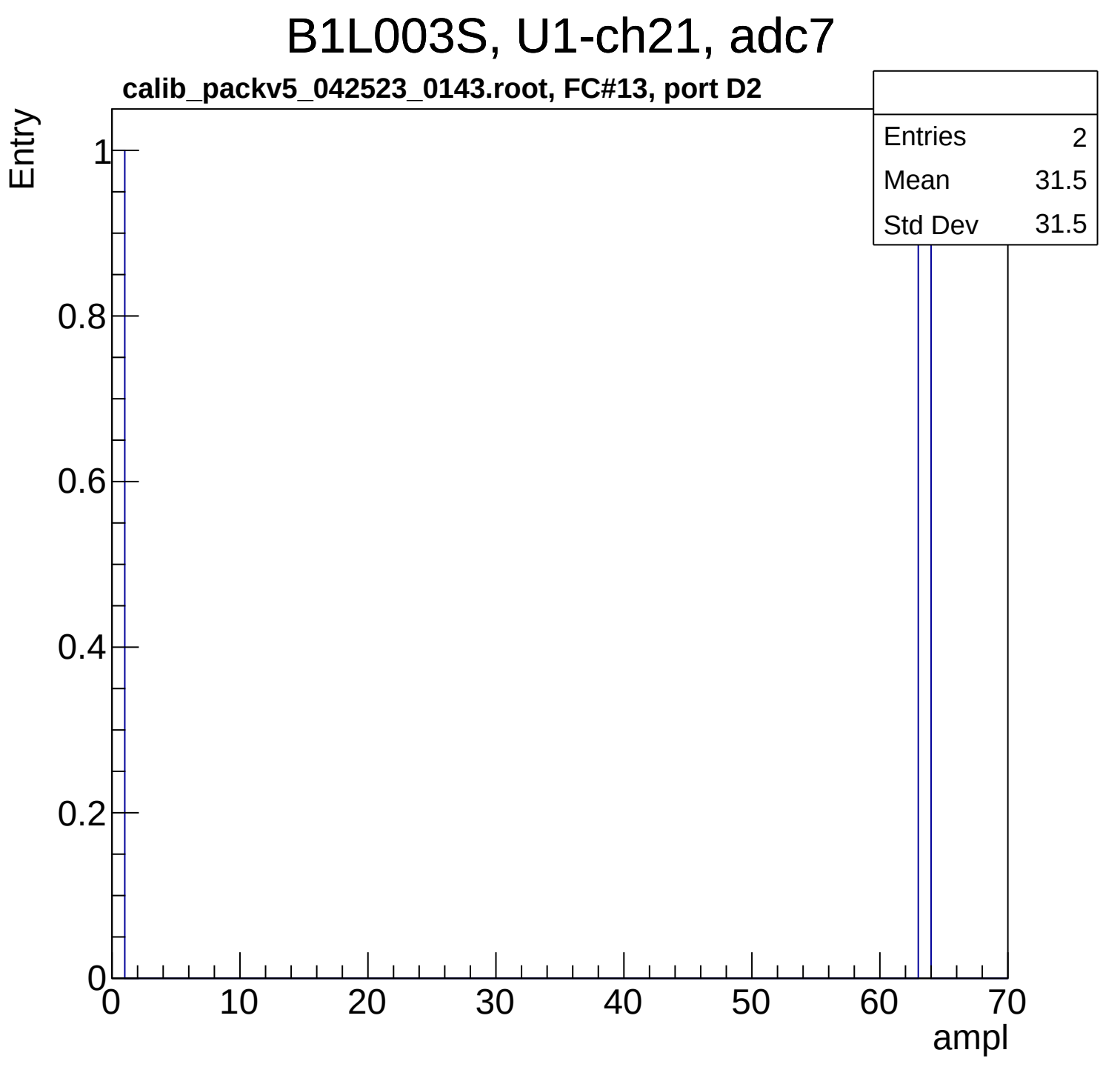
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.05
Std Dev 9.427
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 62
Std Dev 0.866
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch21, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
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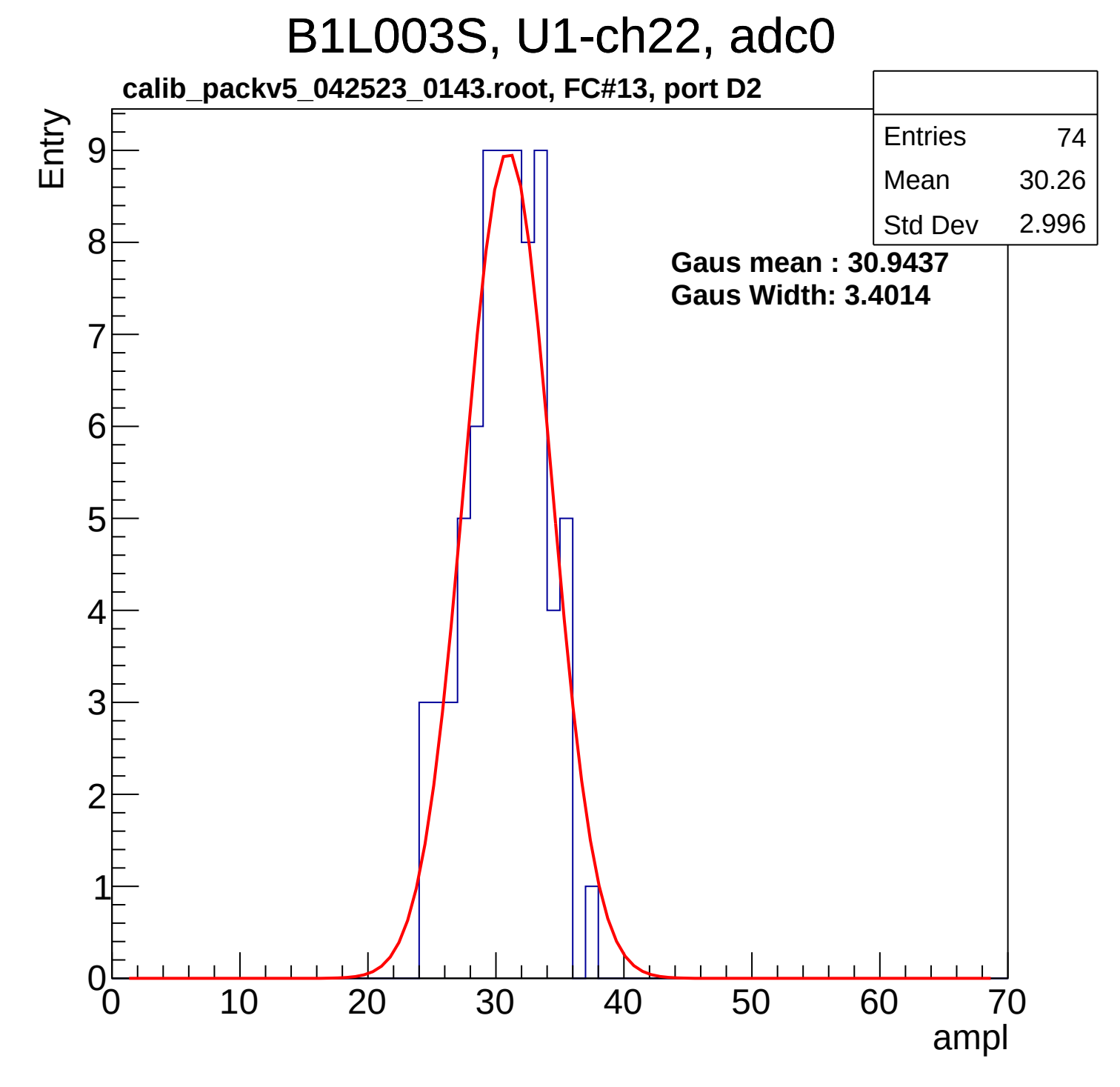
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch22, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 30.26
Std Dev 2.996
Gaus mean : 30.9437
Gaus Width: 3.4014
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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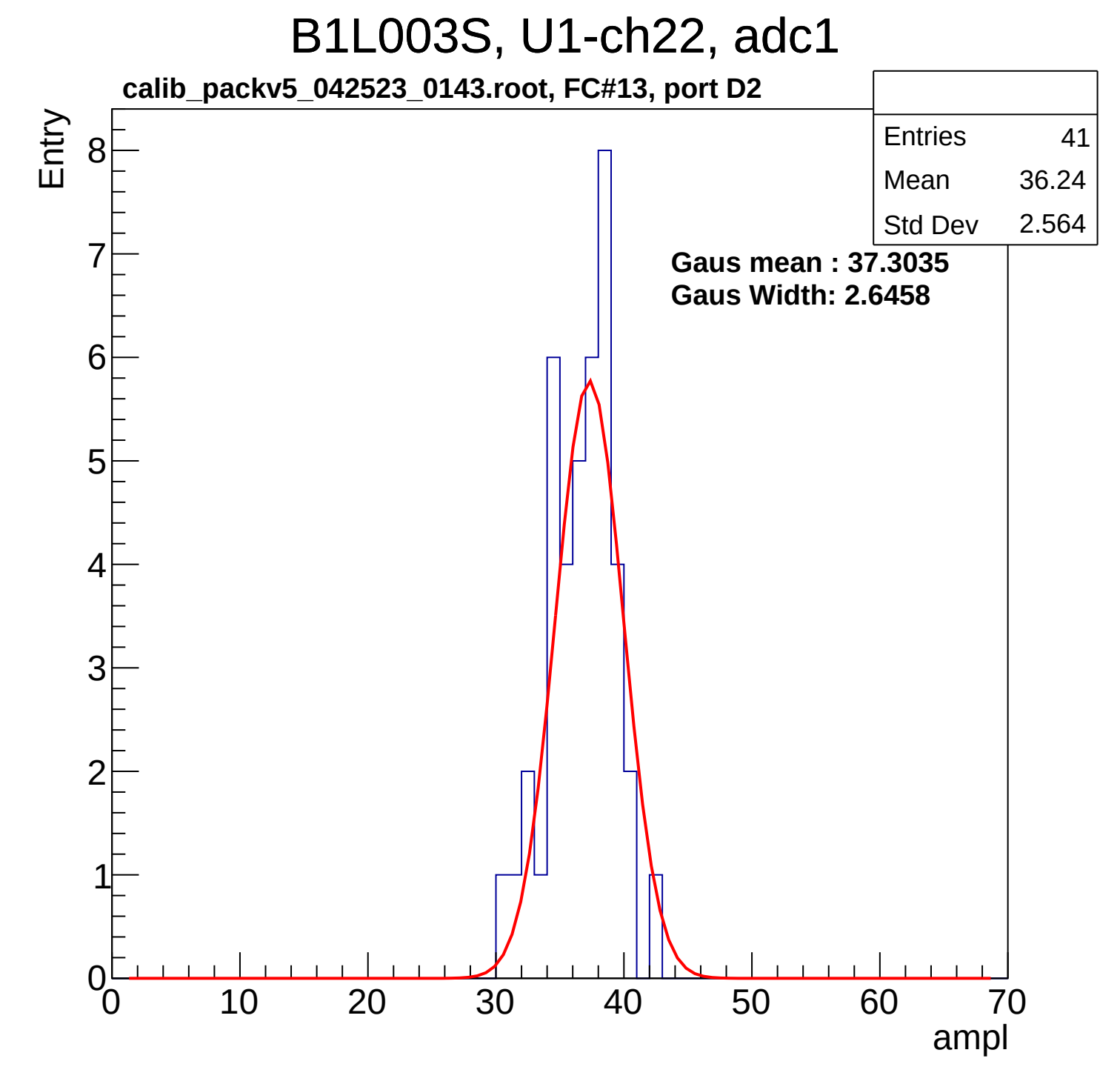
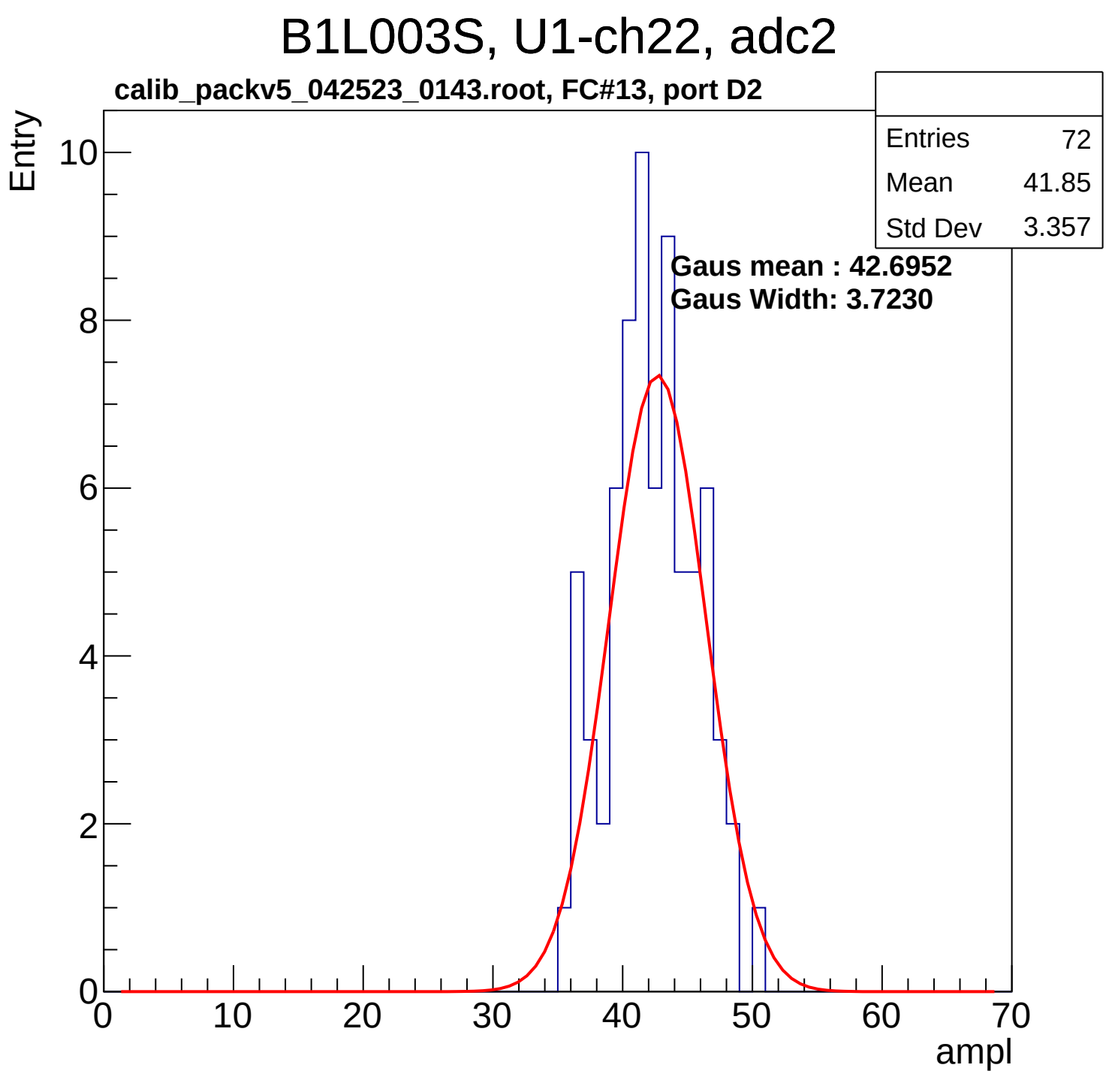

B1L003S, U1-ch22, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 36.24
Std Dev 2.564
Gaus mean : 37.3035
Gaus Width: 2.6458
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch22, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 41.85
Std Dev 3.357
Gaus mean : 42.6952
Gaus Width: 3.7230
Entry
ampl
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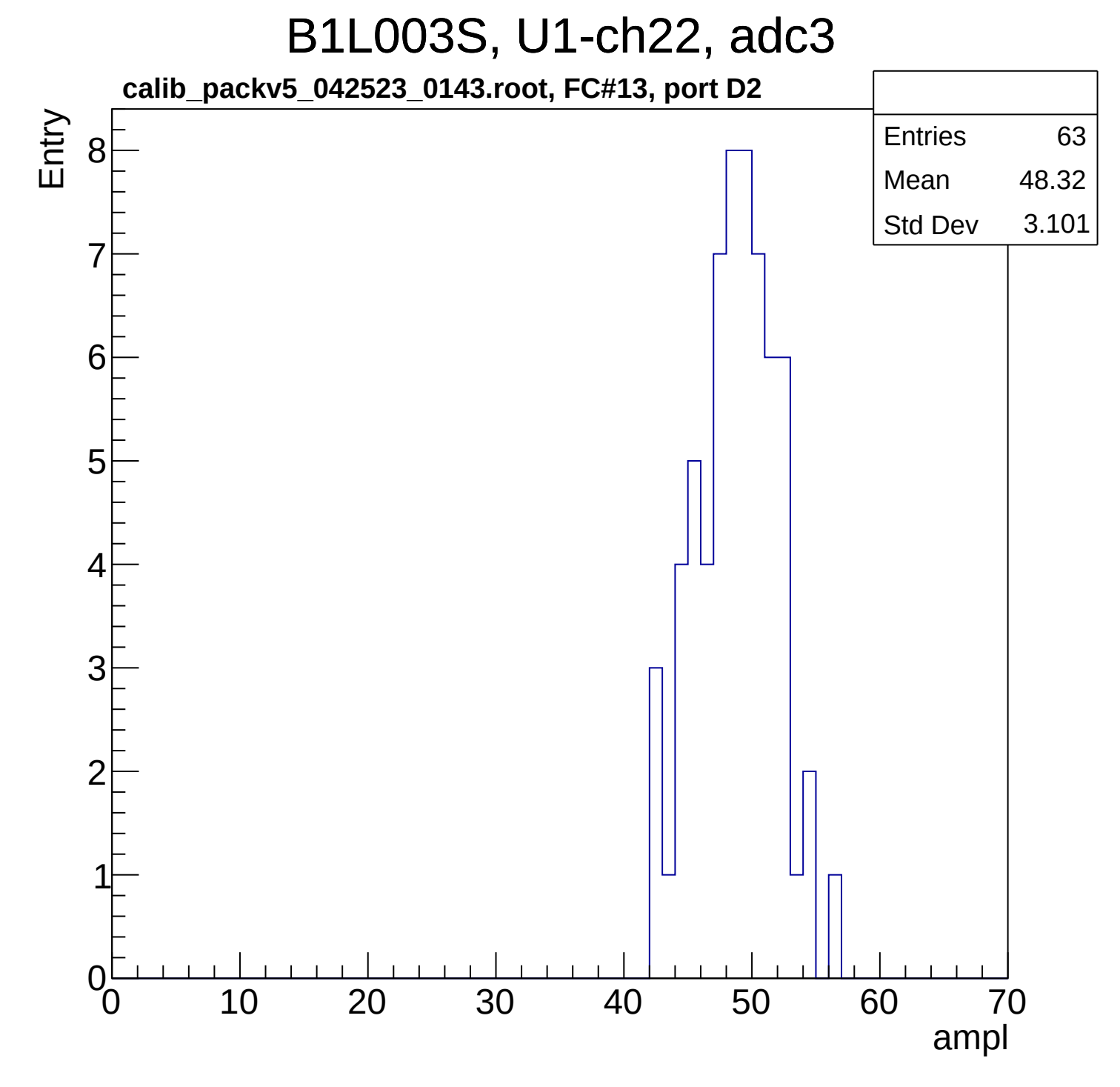
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch22, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.32
Std Dev 3.101
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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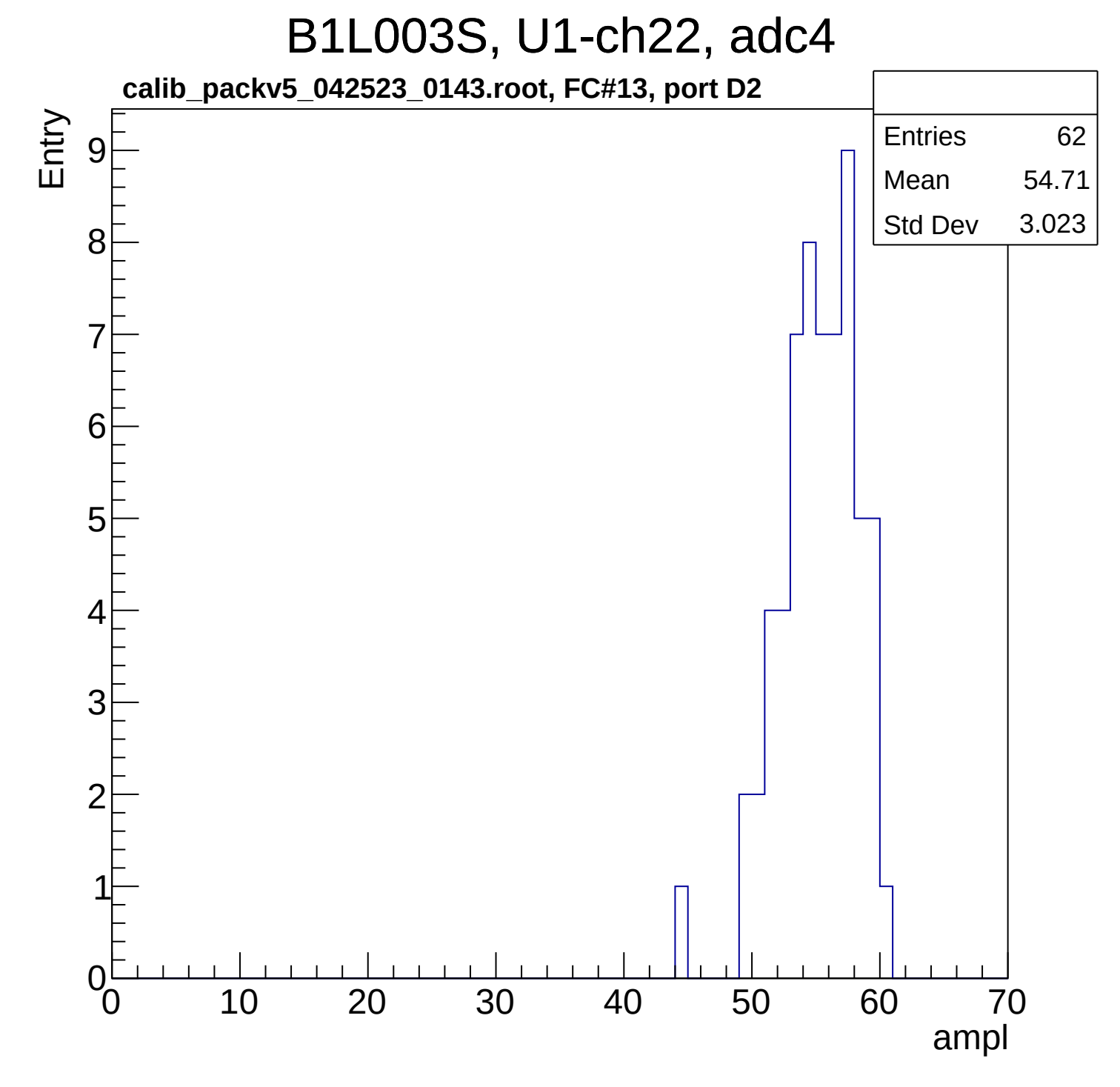
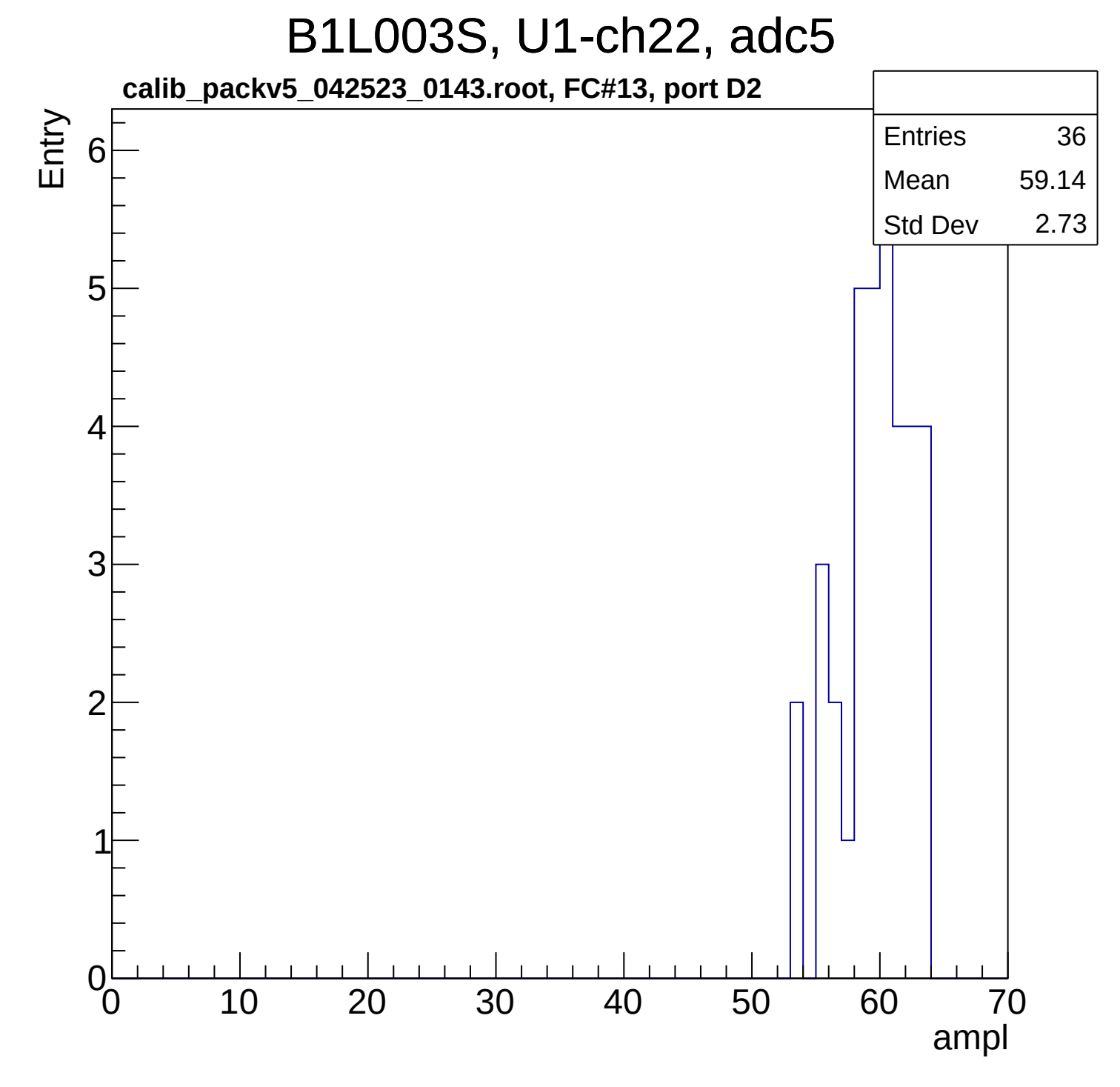

B1L003S, U1-ch22, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.71
Std Dev 3.023
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch22, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 59.14
Std Dev 2.73
Entry
0
1
2
3
4
5
6
0
10
20
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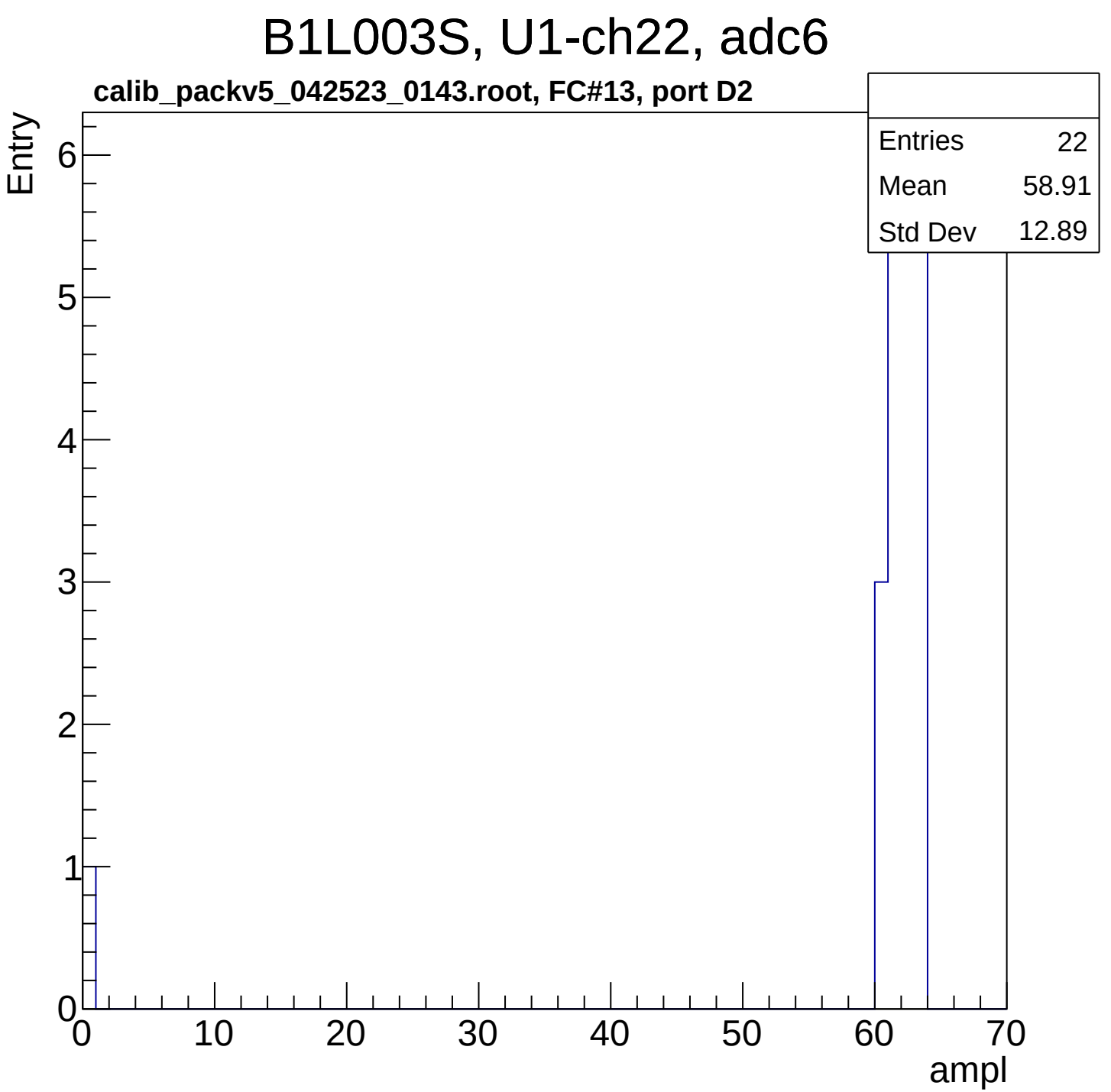
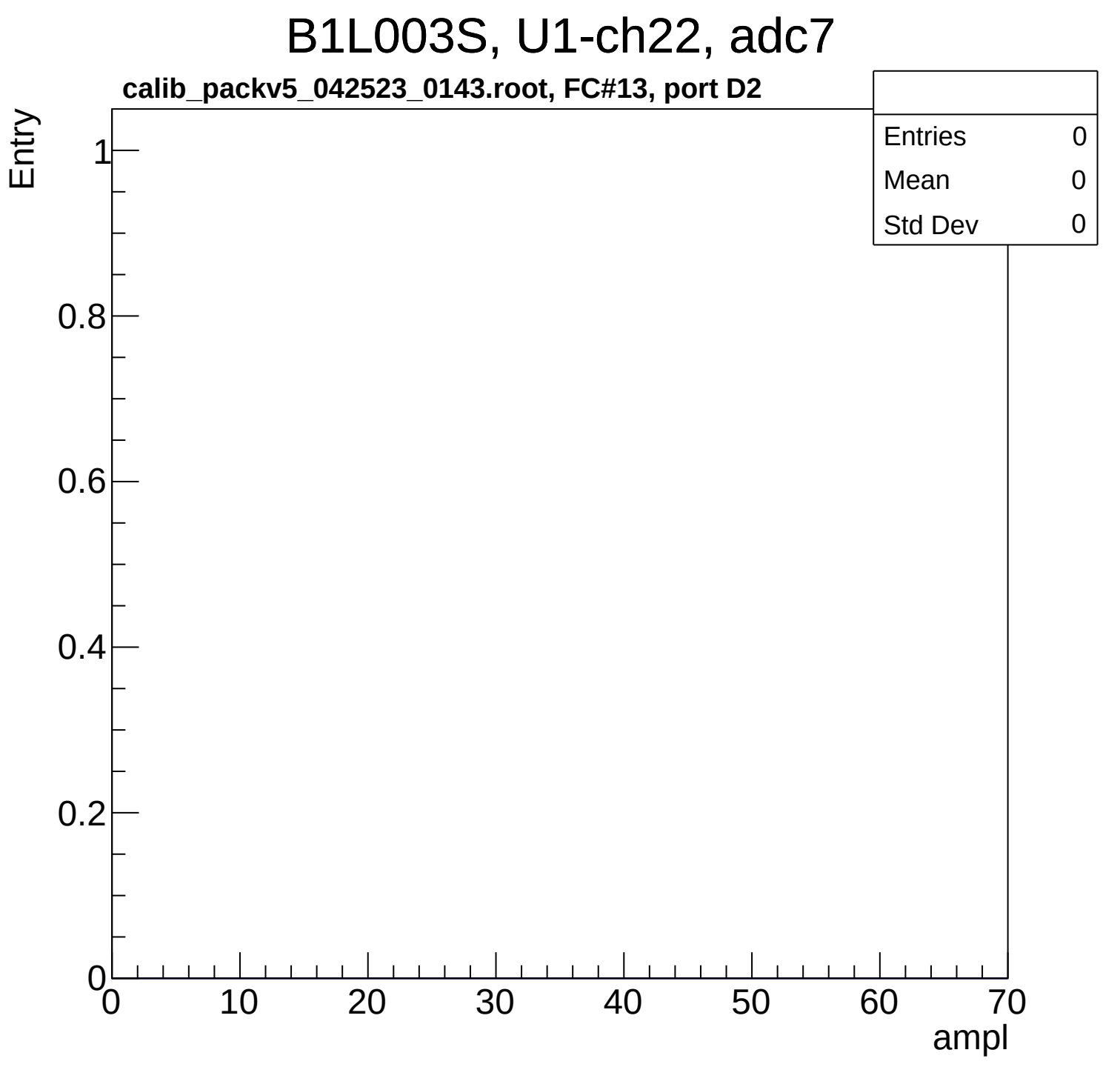
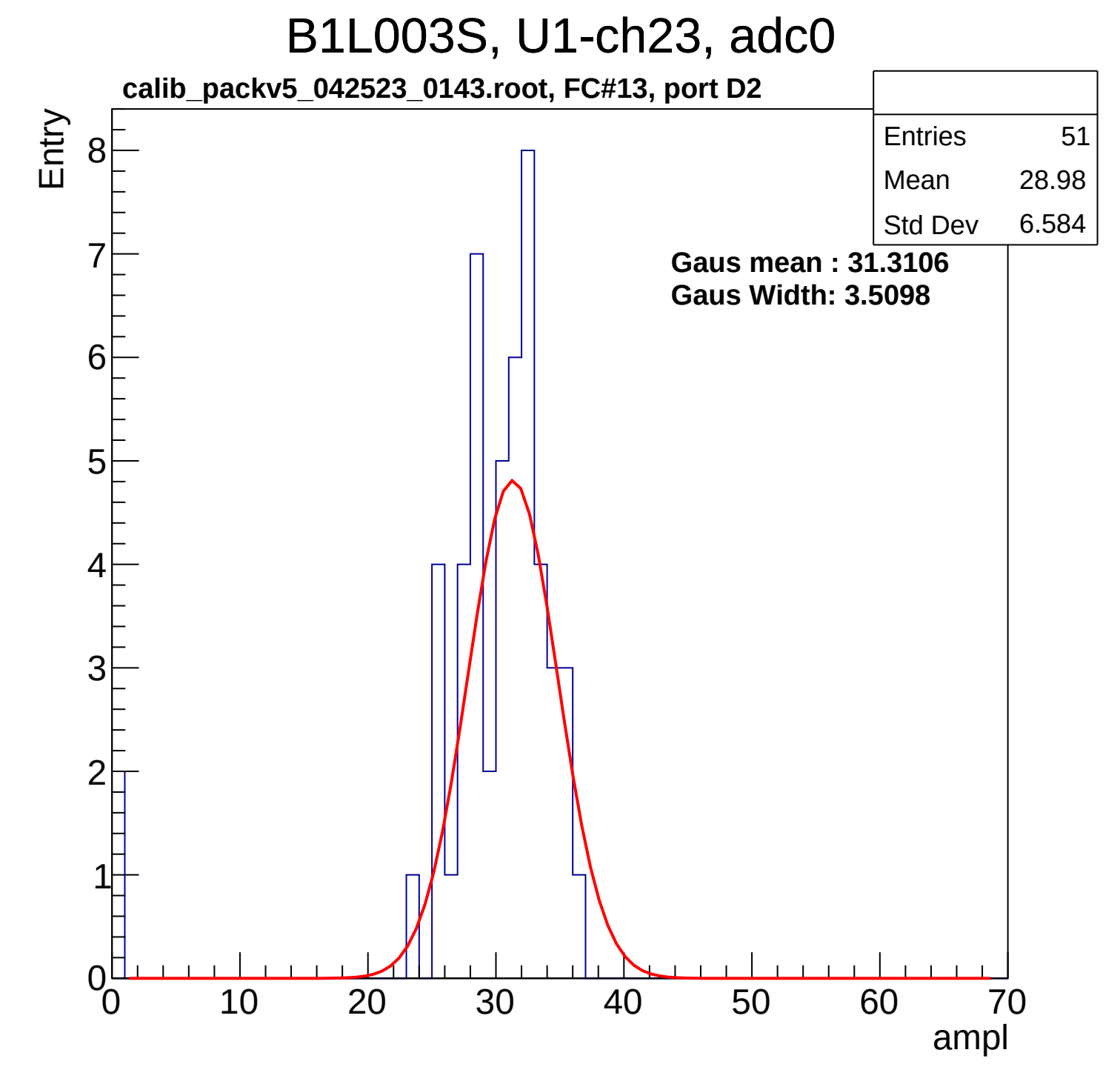
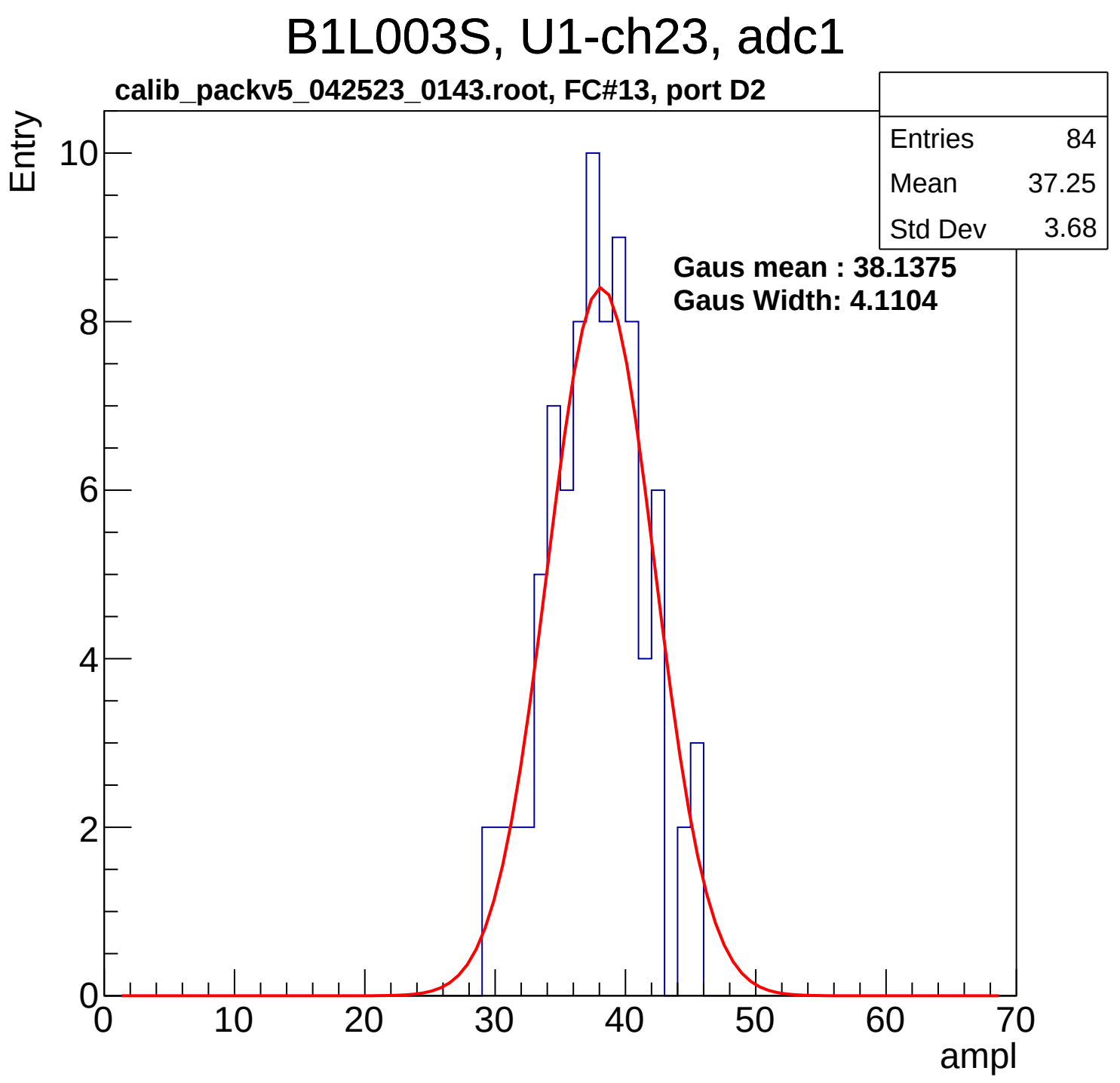
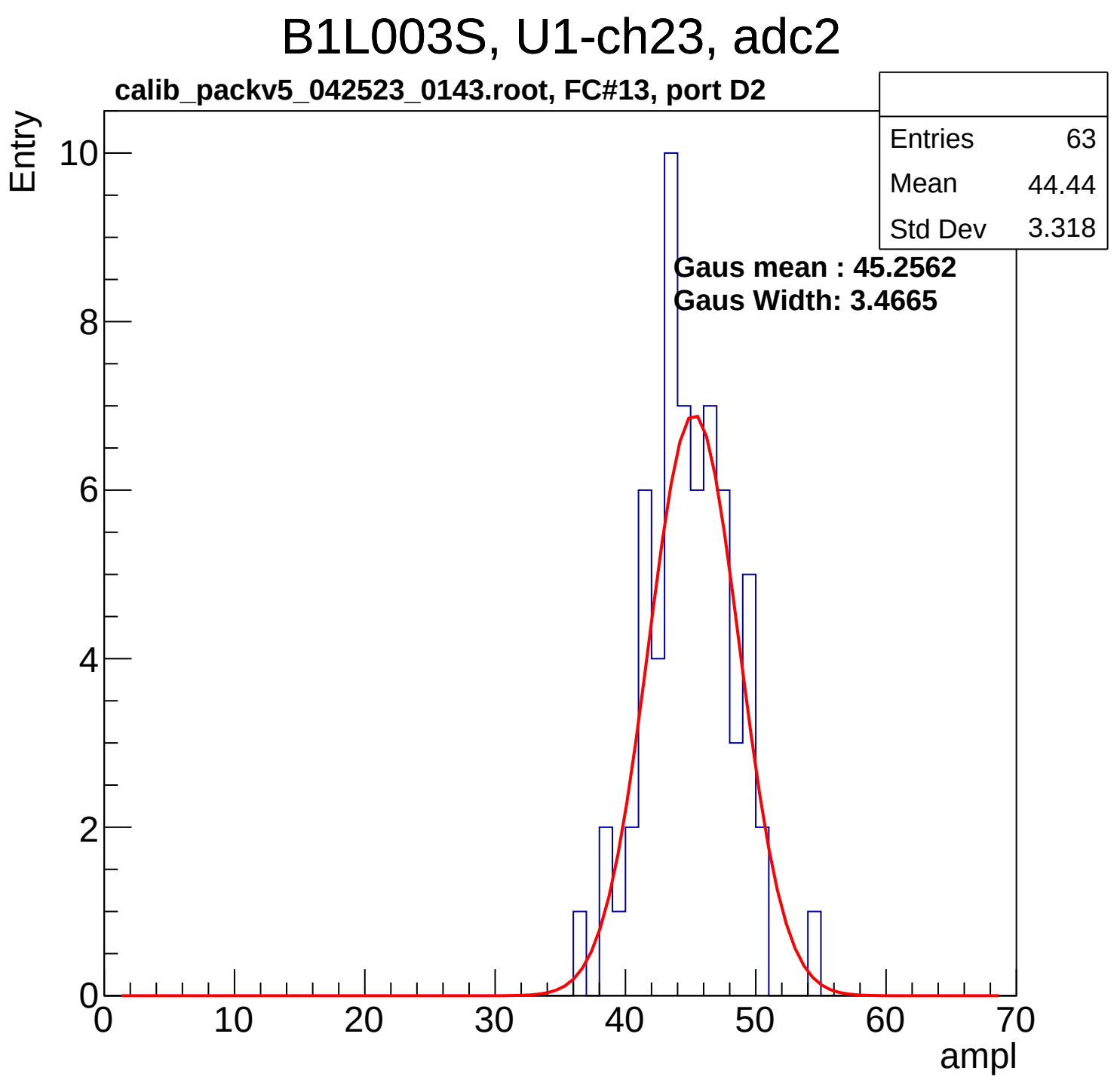
40
50
60
70
ampl

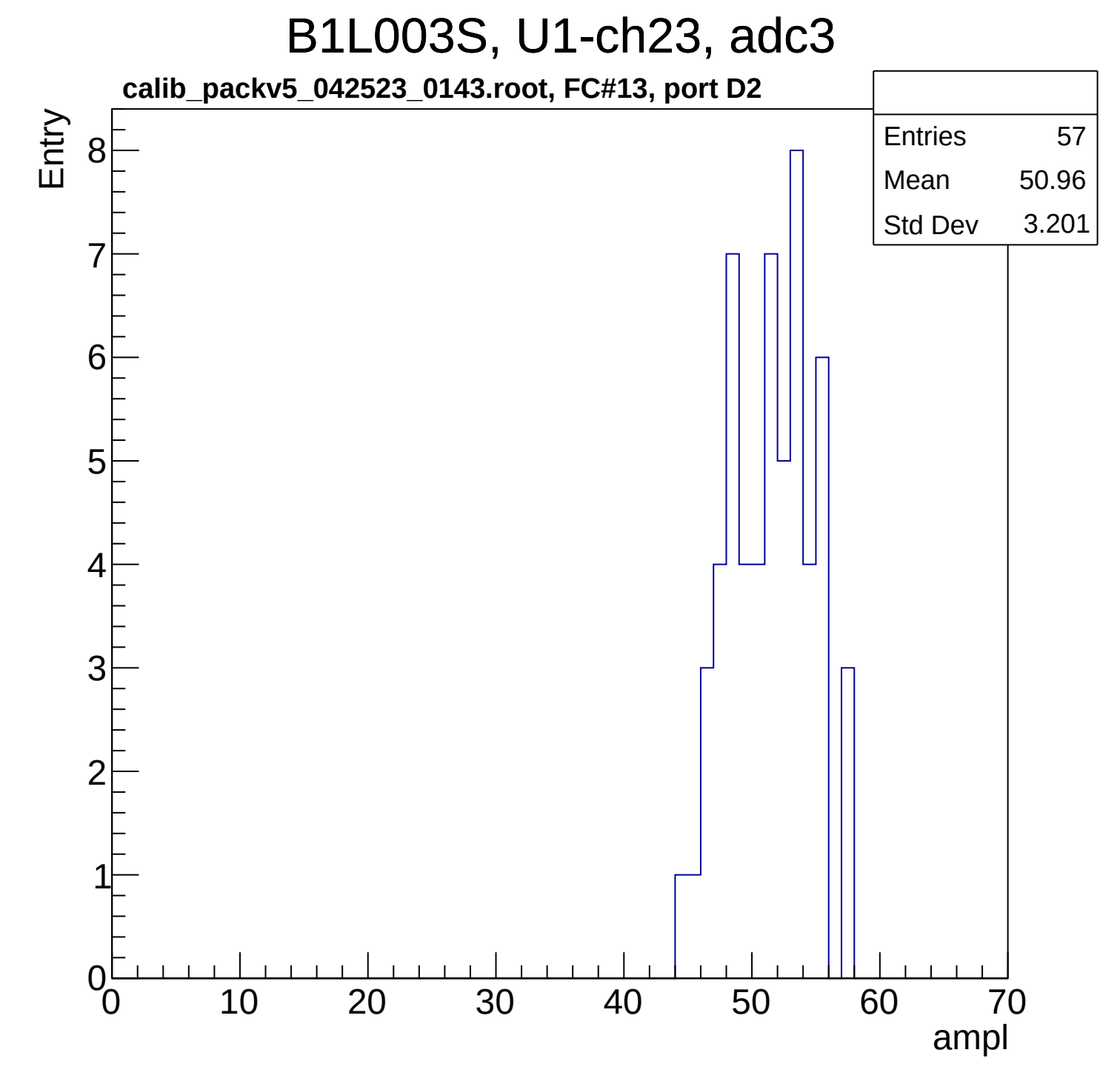

B1L003S, U1-ch22, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.91
Std Dev 12.89
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

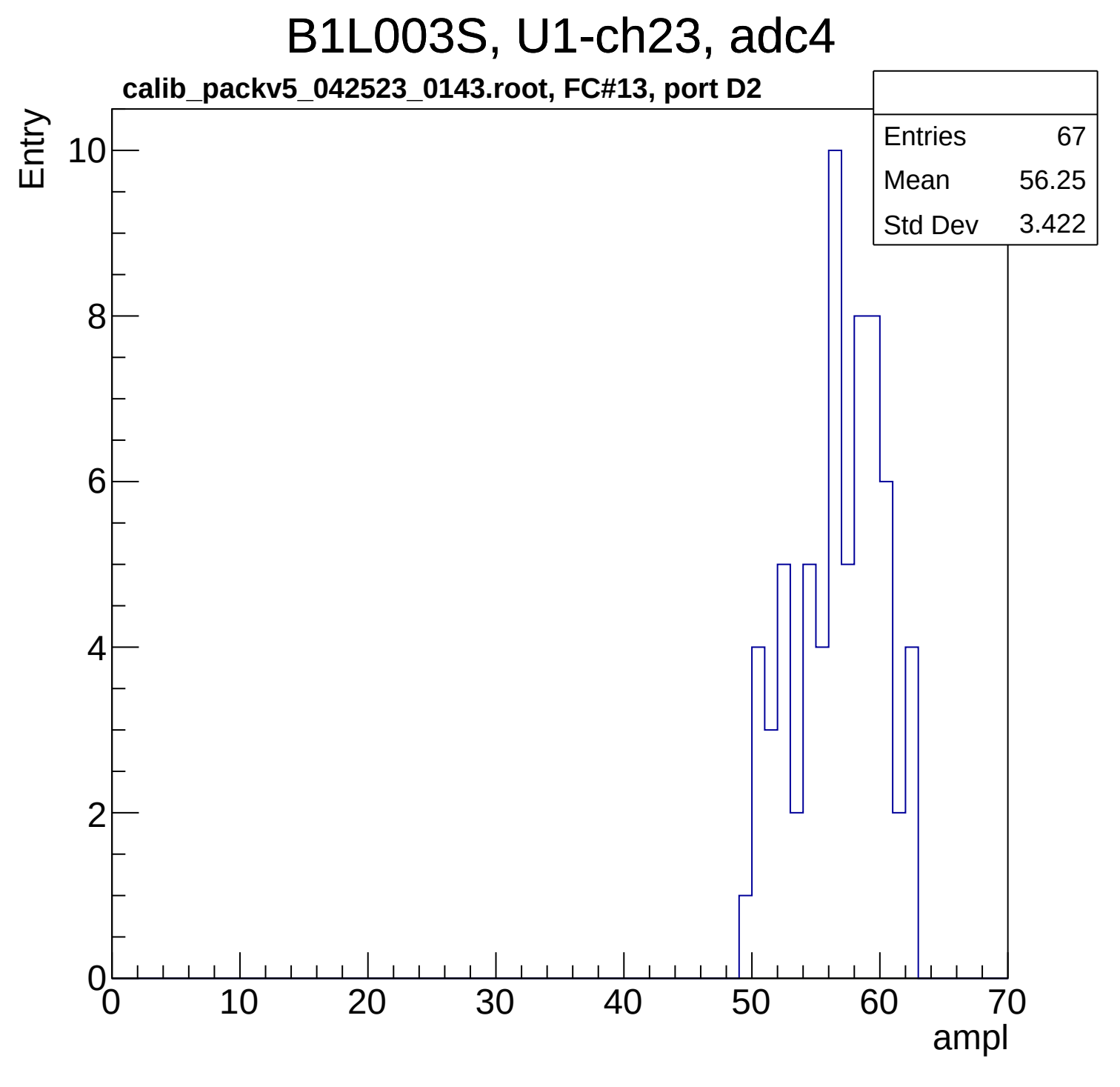

B1L003S, U1-ch22, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

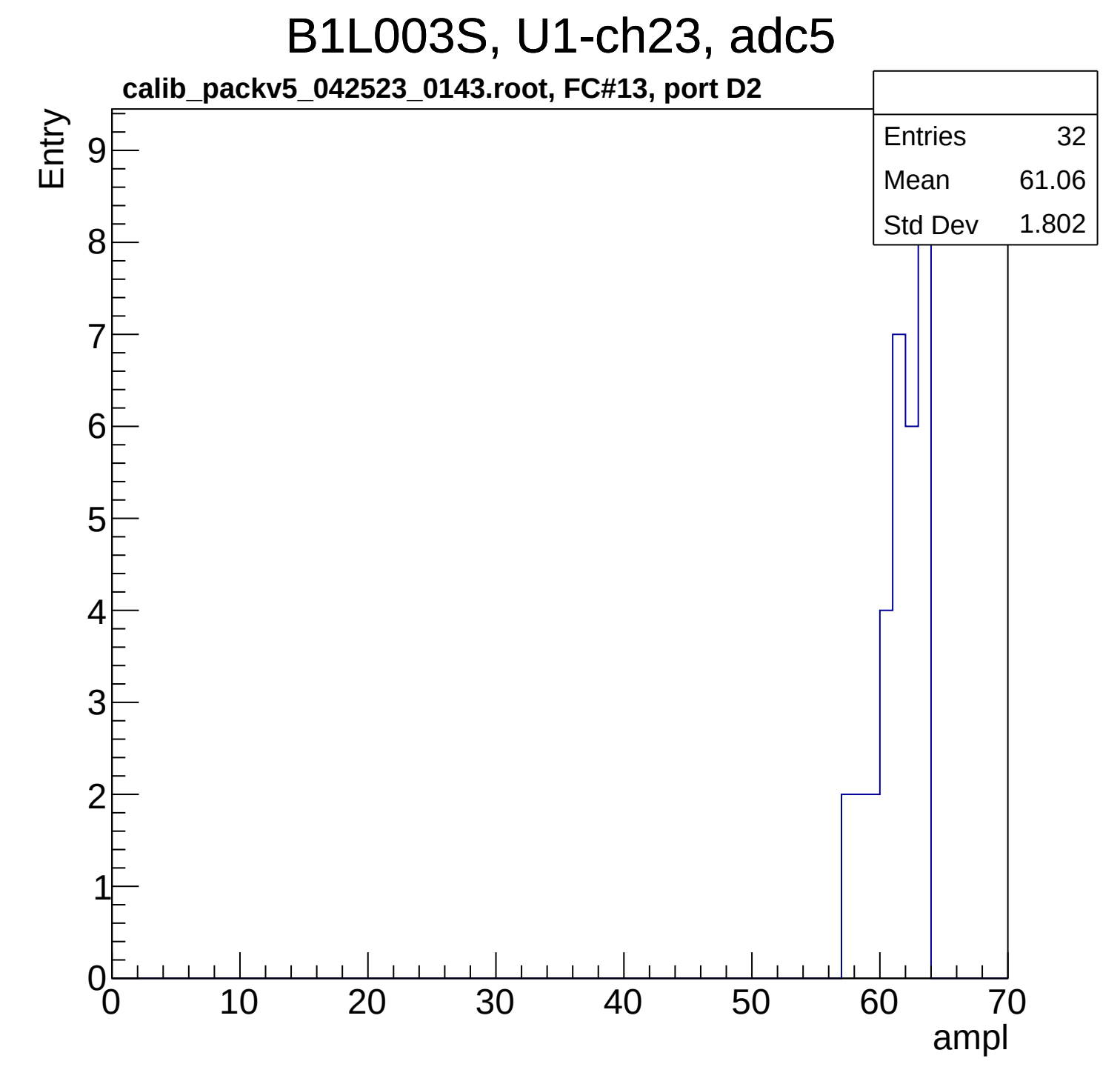

B1L003S, U1-ch23, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 28.98
Std Dev 6.584
Gaus mean : 31.3106
Gaus Width: 3.5098
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

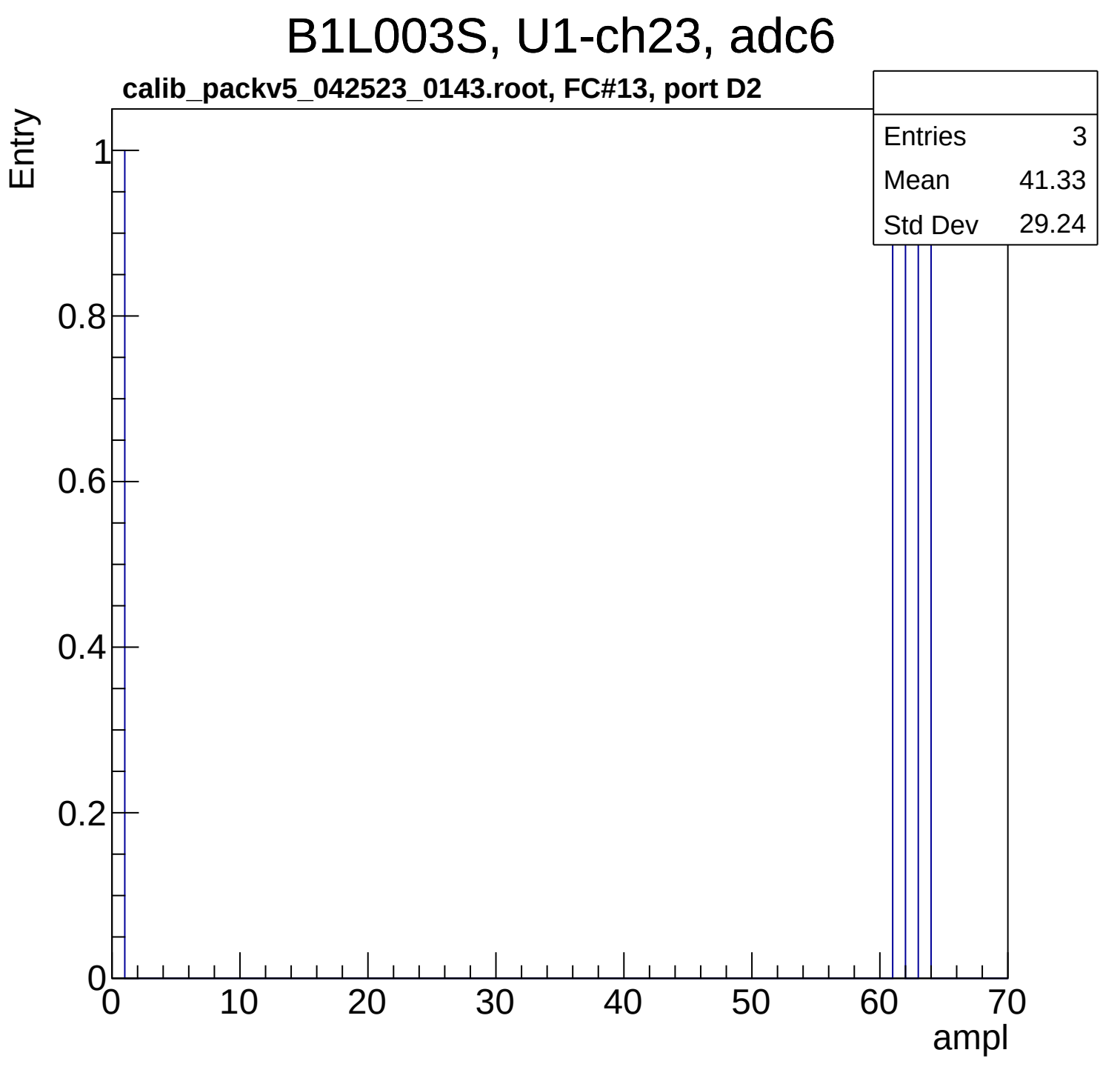

B1L003S, U1-ch23, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 37.25
Std Dev 3.68
Gaus mean : 38.1375
Gaus Width: 4.1104
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

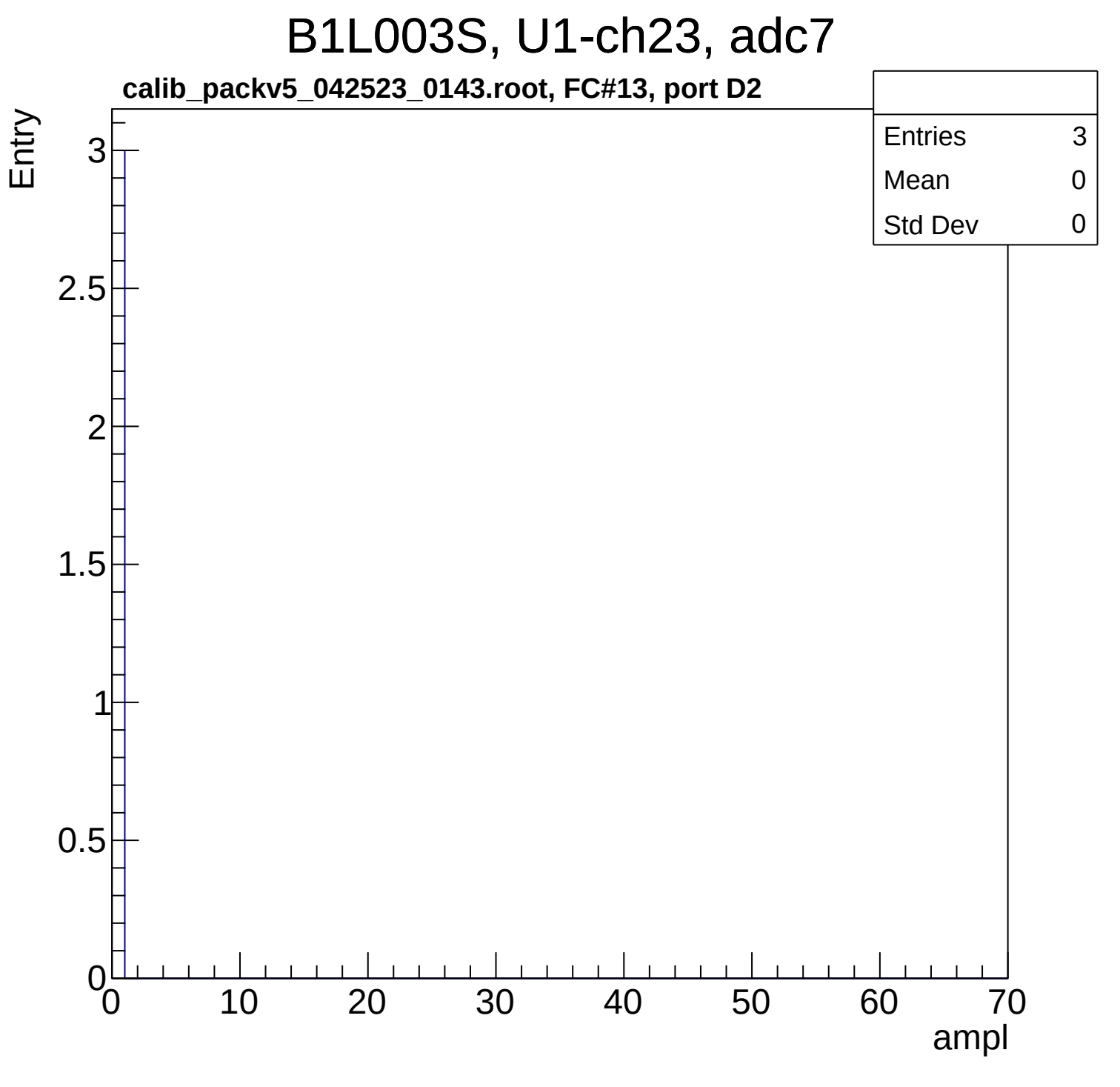

B1L003S, U1-ch23, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 44.44
Std Dev 3.318
Gaus mean : 45.2562
Gaus Width: 3.4665
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

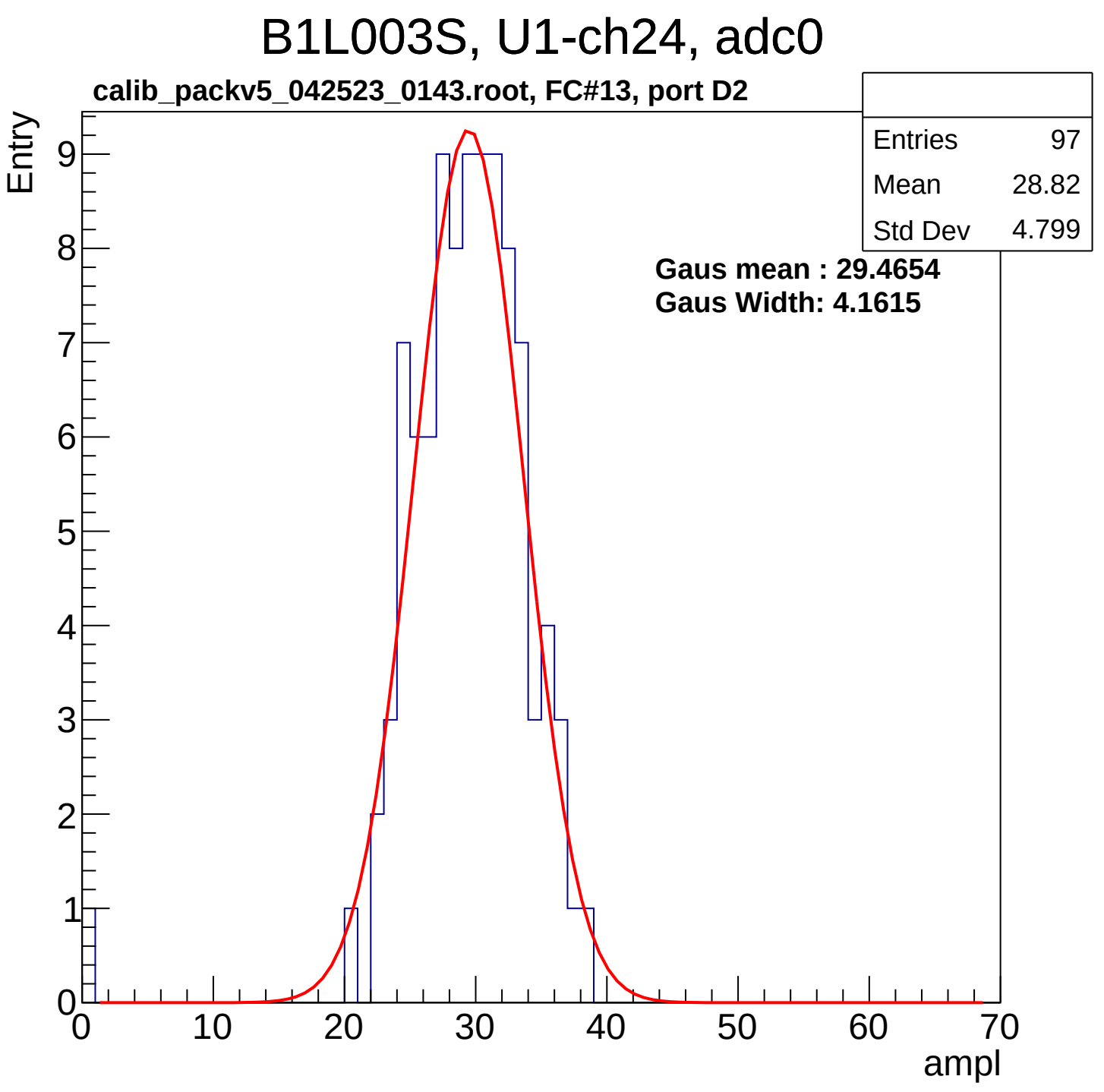

B1L003S, U1-ch23, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 50.96
Std Dev 3.201
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

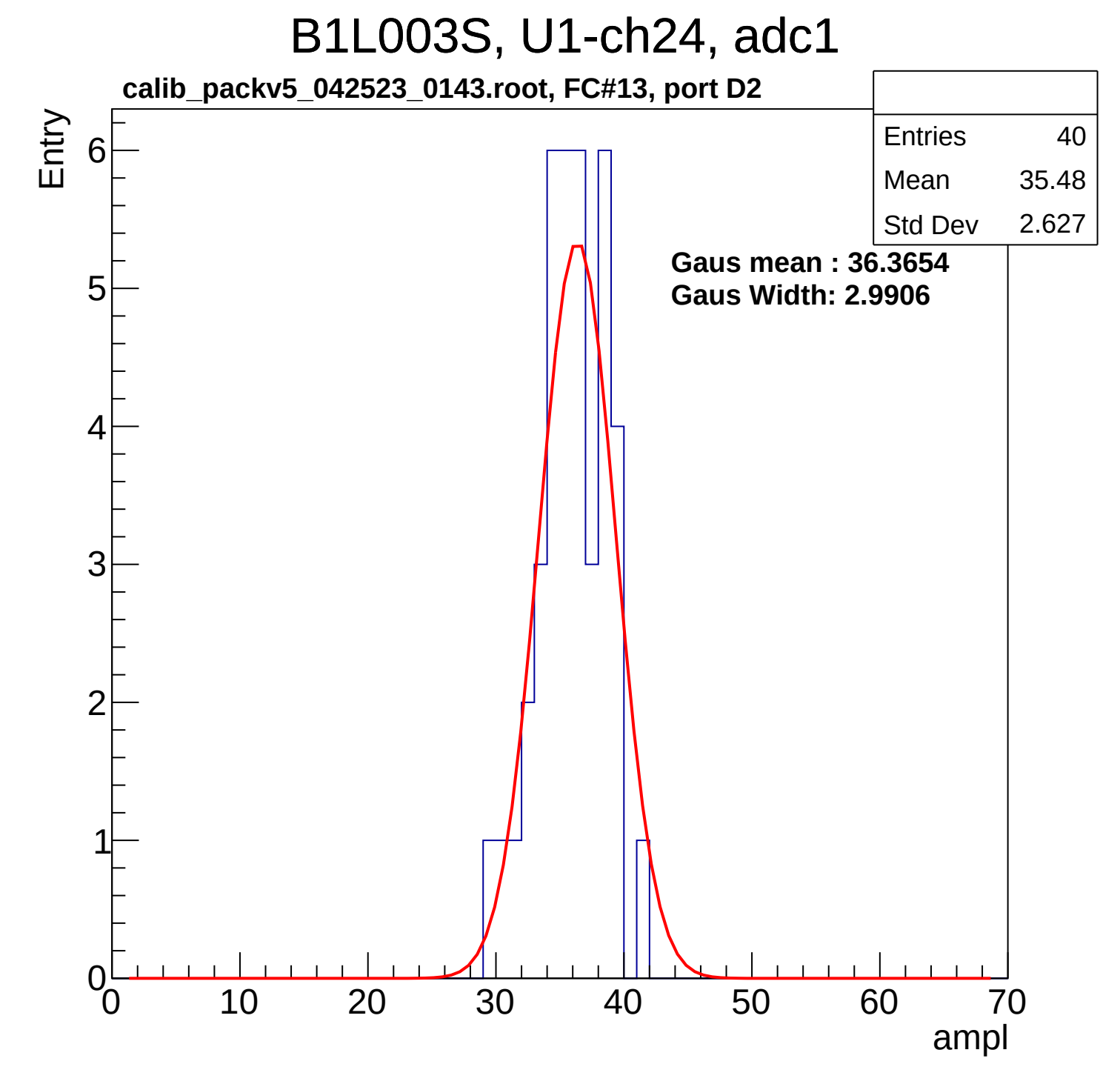

B1L003S, U1-ch23, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 56.25
Std Dev 3.422
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

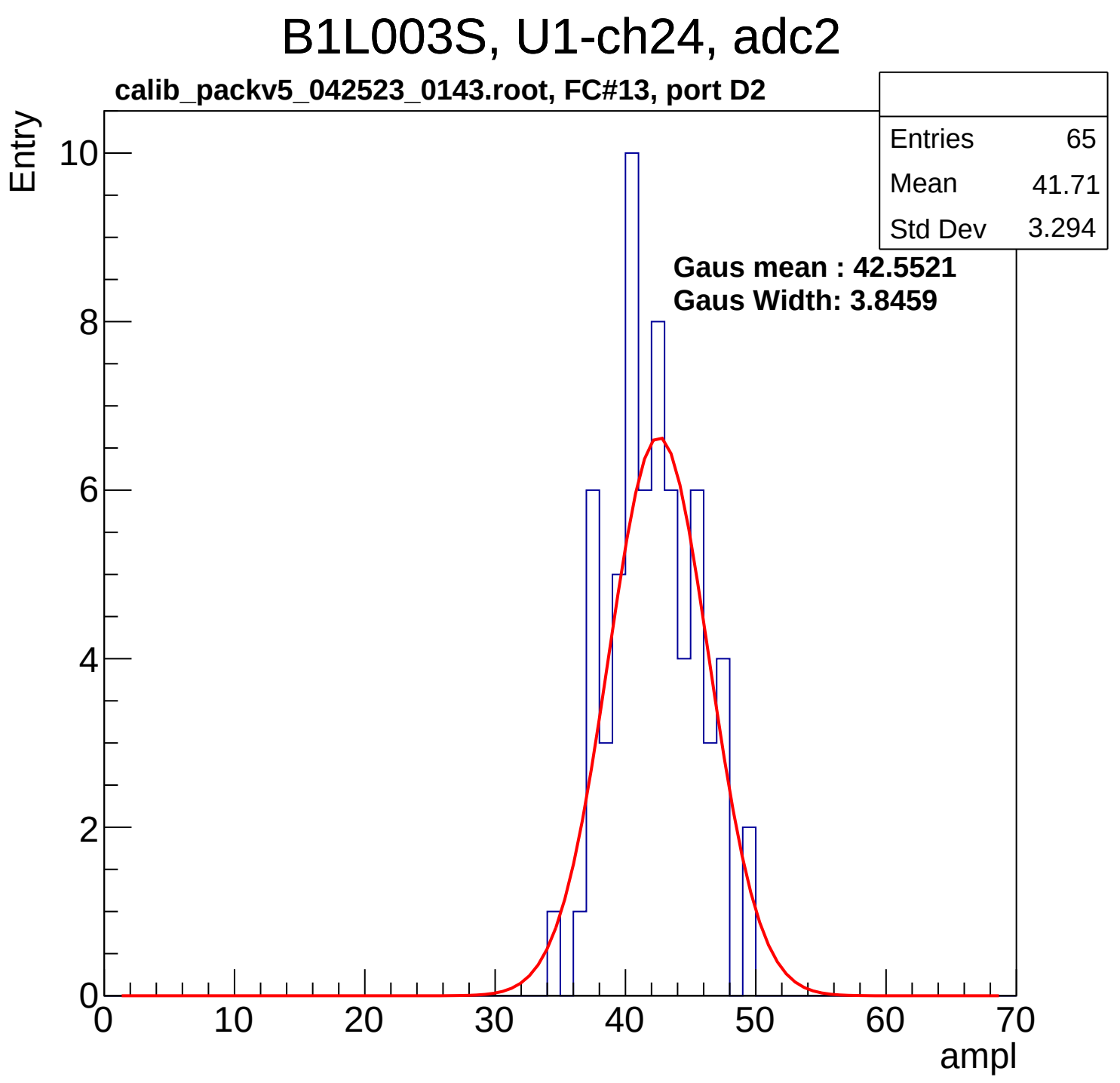

B1L003S, U1-ch23, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 61.06
Std Dev 1.802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

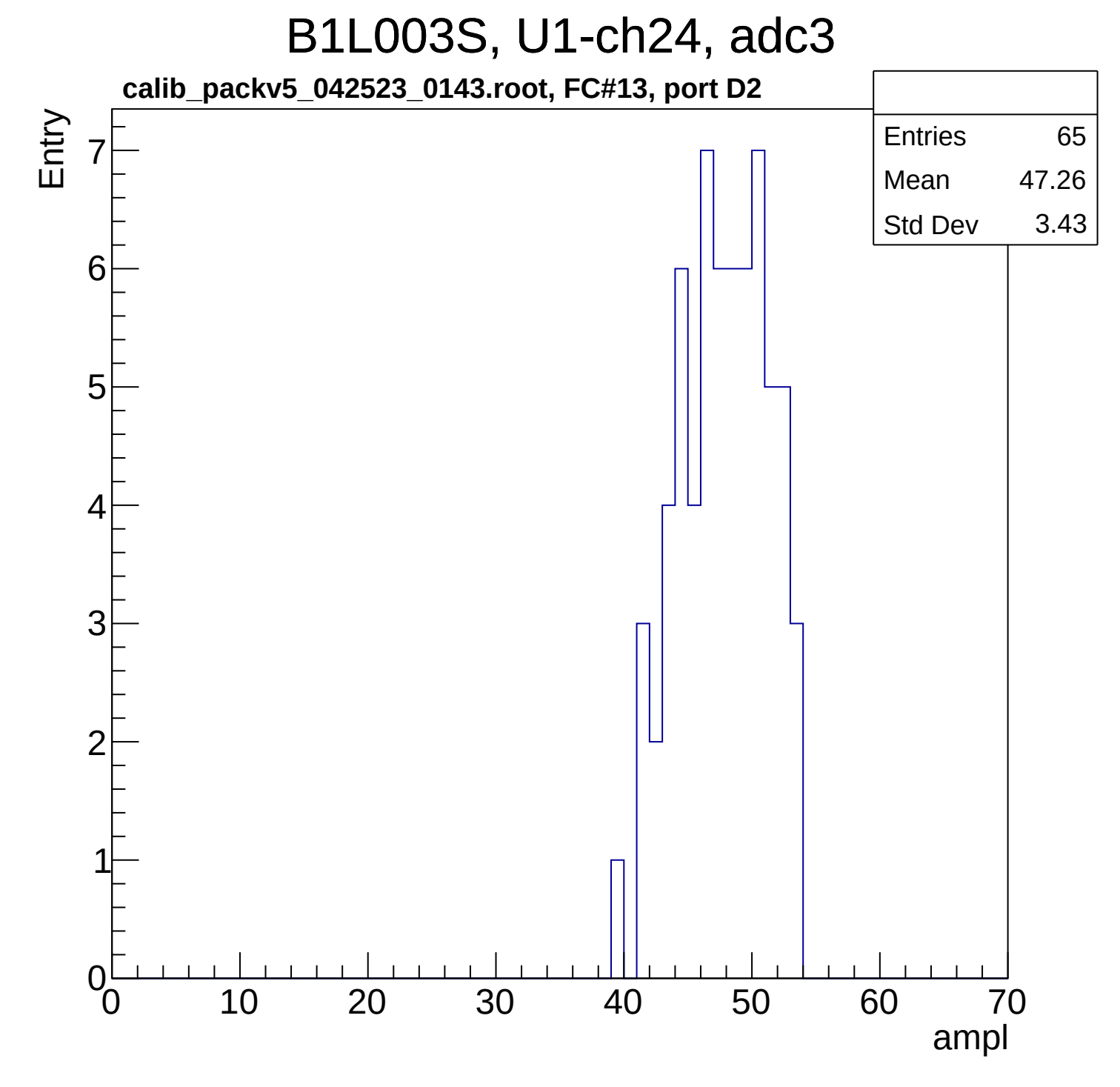

B1L003S, U1-ch23, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.33
Std Dev 29.24
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

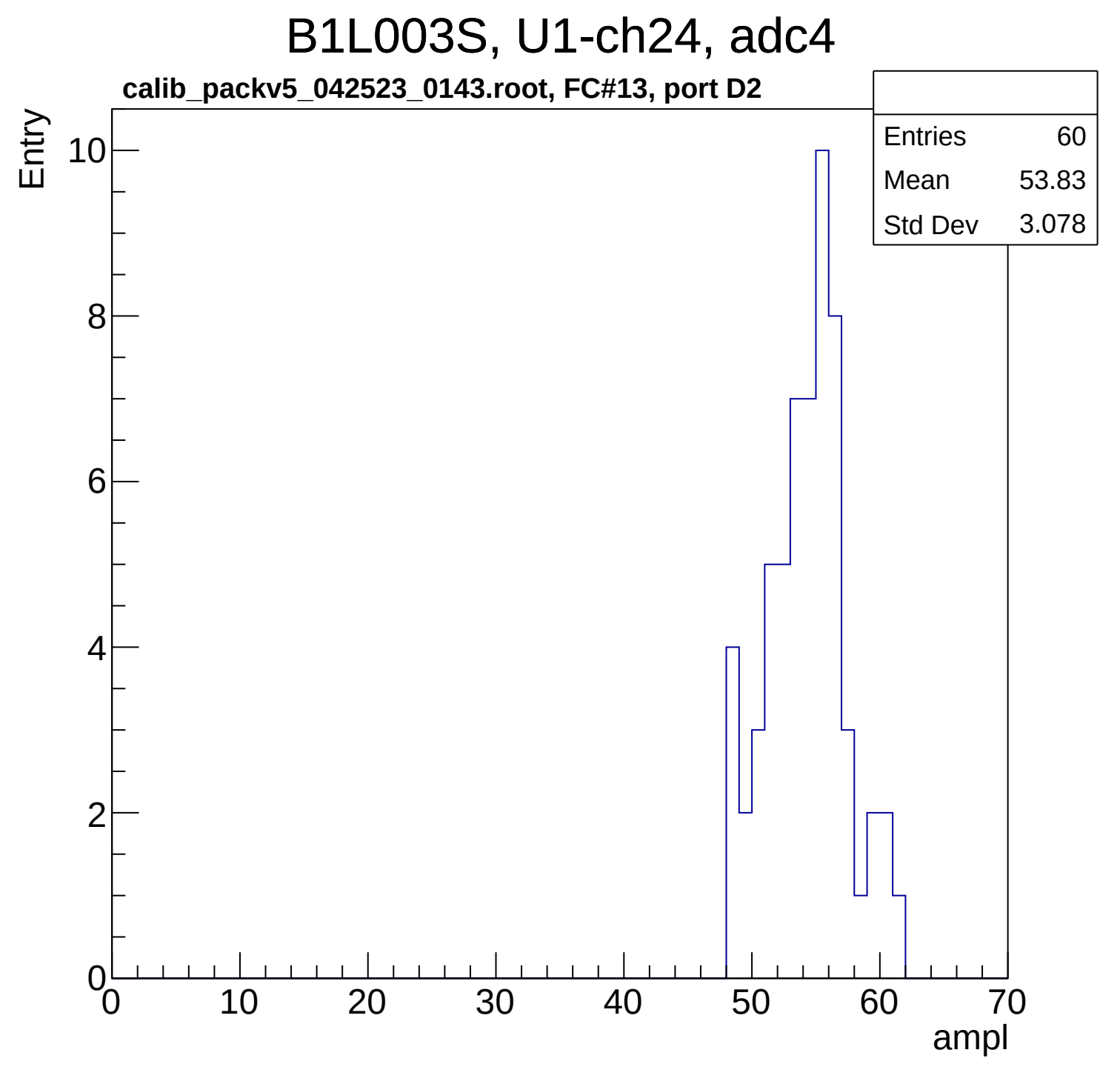

B1L003S, U1-ch23, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

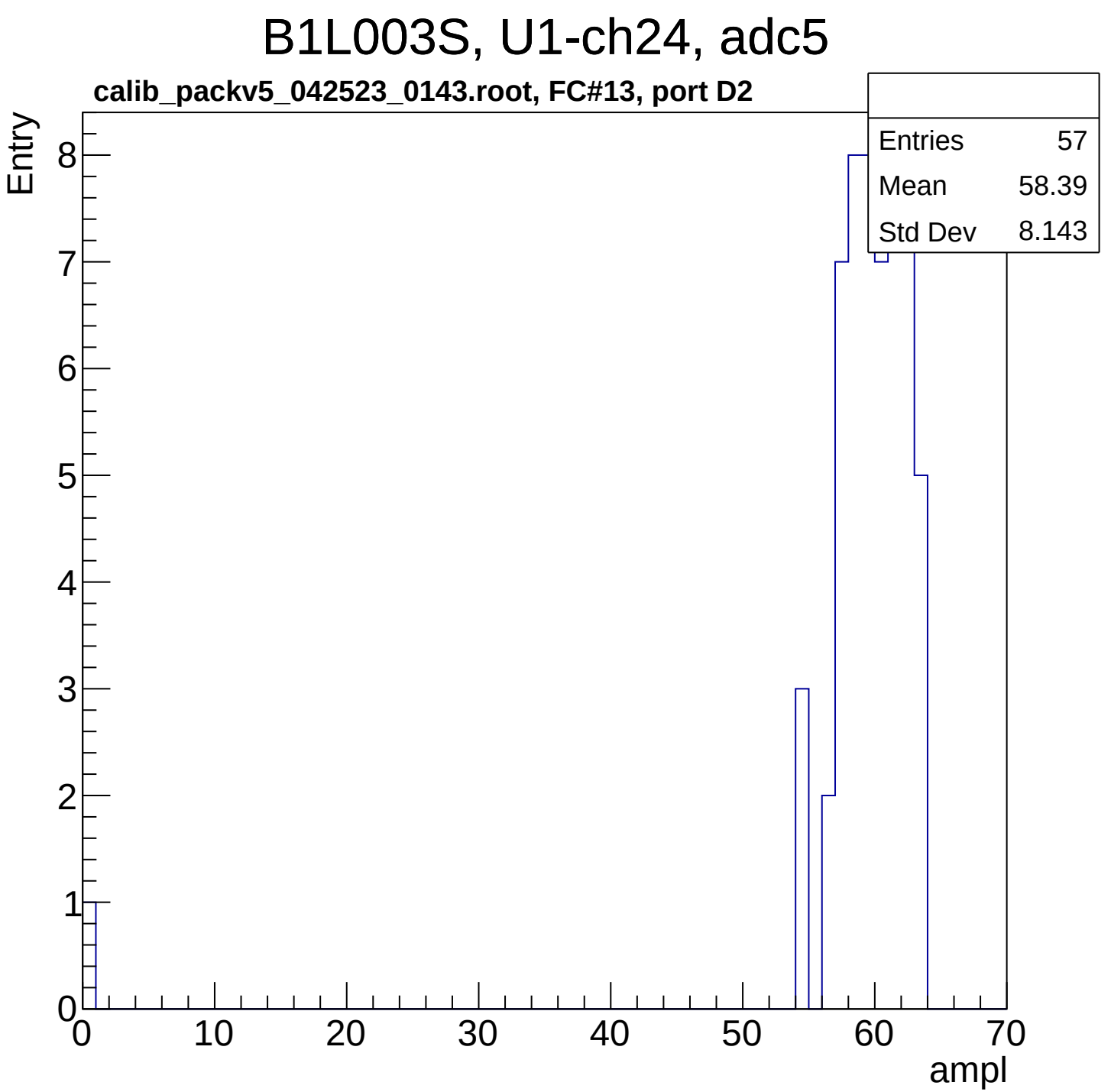

B1L003S, U1-ch24, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 97
Mean 28.82
Std Dev 4.799
Gaus mean : 29.4654
Gaus Width: 4.1615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

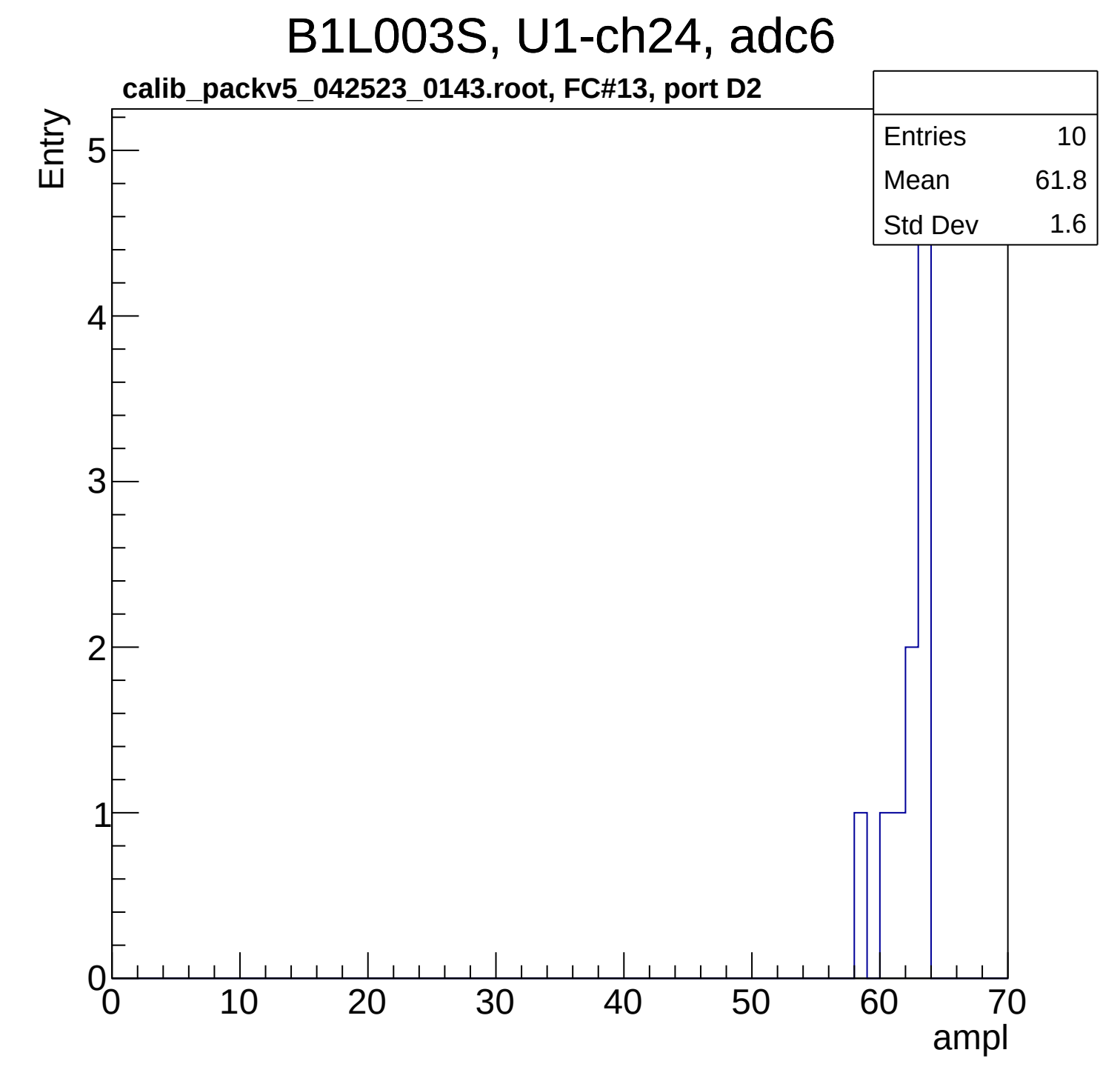

B1L003S, U1-ch24, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 35.48
Std Dev 2.627
Gaus mean : 36.3654
Gaus Width: 2.9906
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

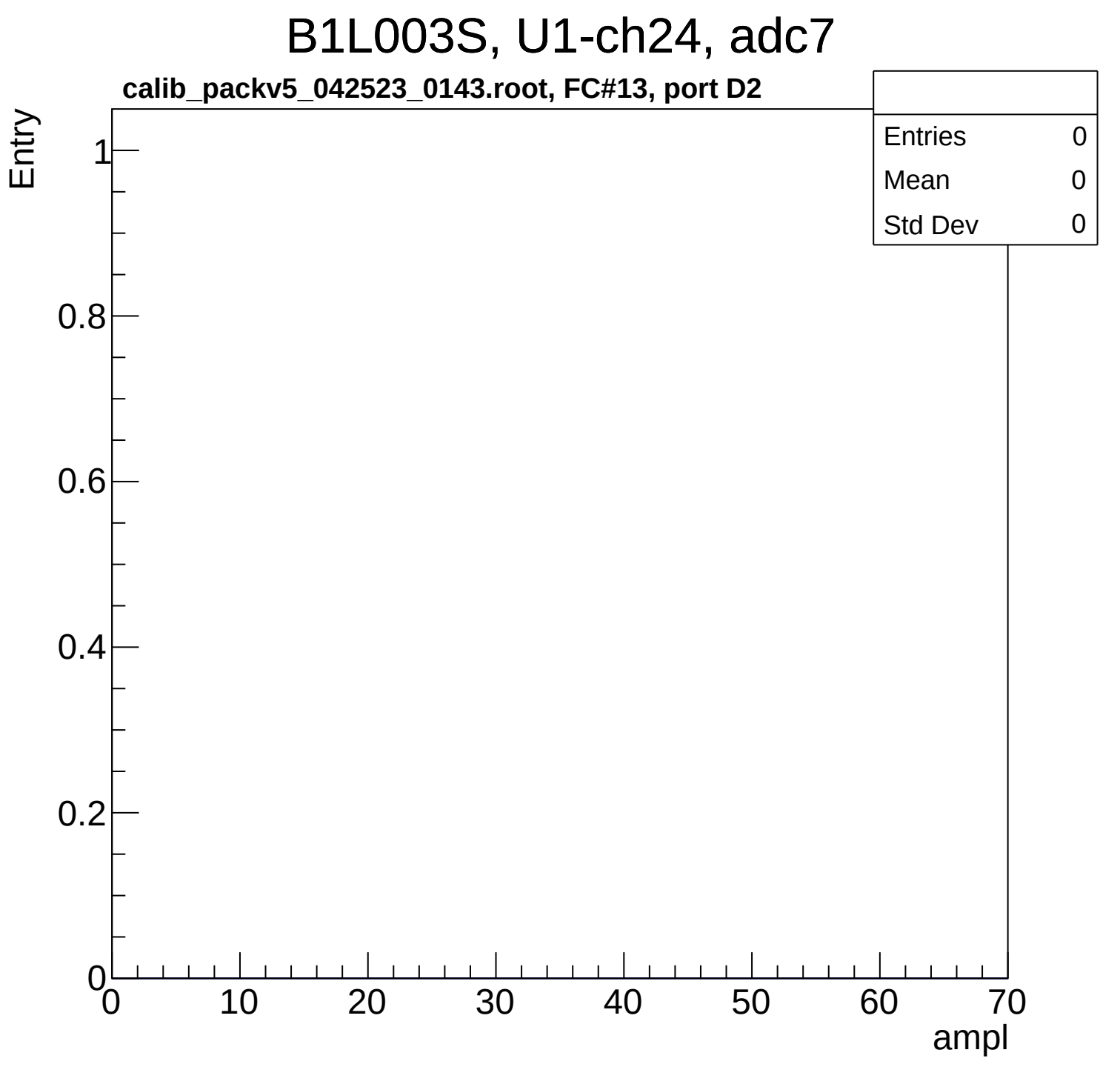

B1L003S, U1-ch24, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.71
Std Dev 3.294
Gaus mean : 42.5521
Gaus Width: 3.8459
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

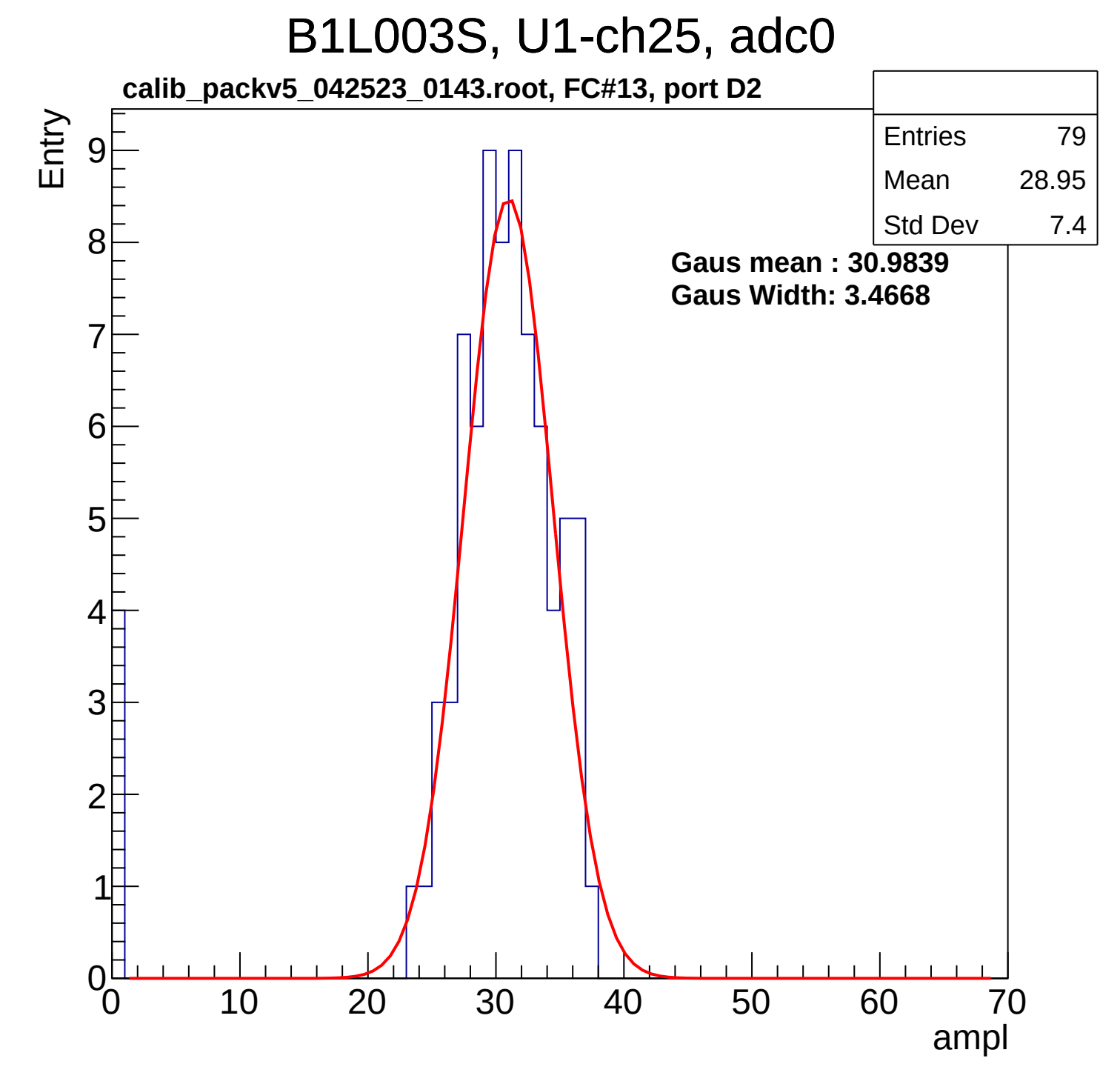

B1L003S, U1-ch24, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.26
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

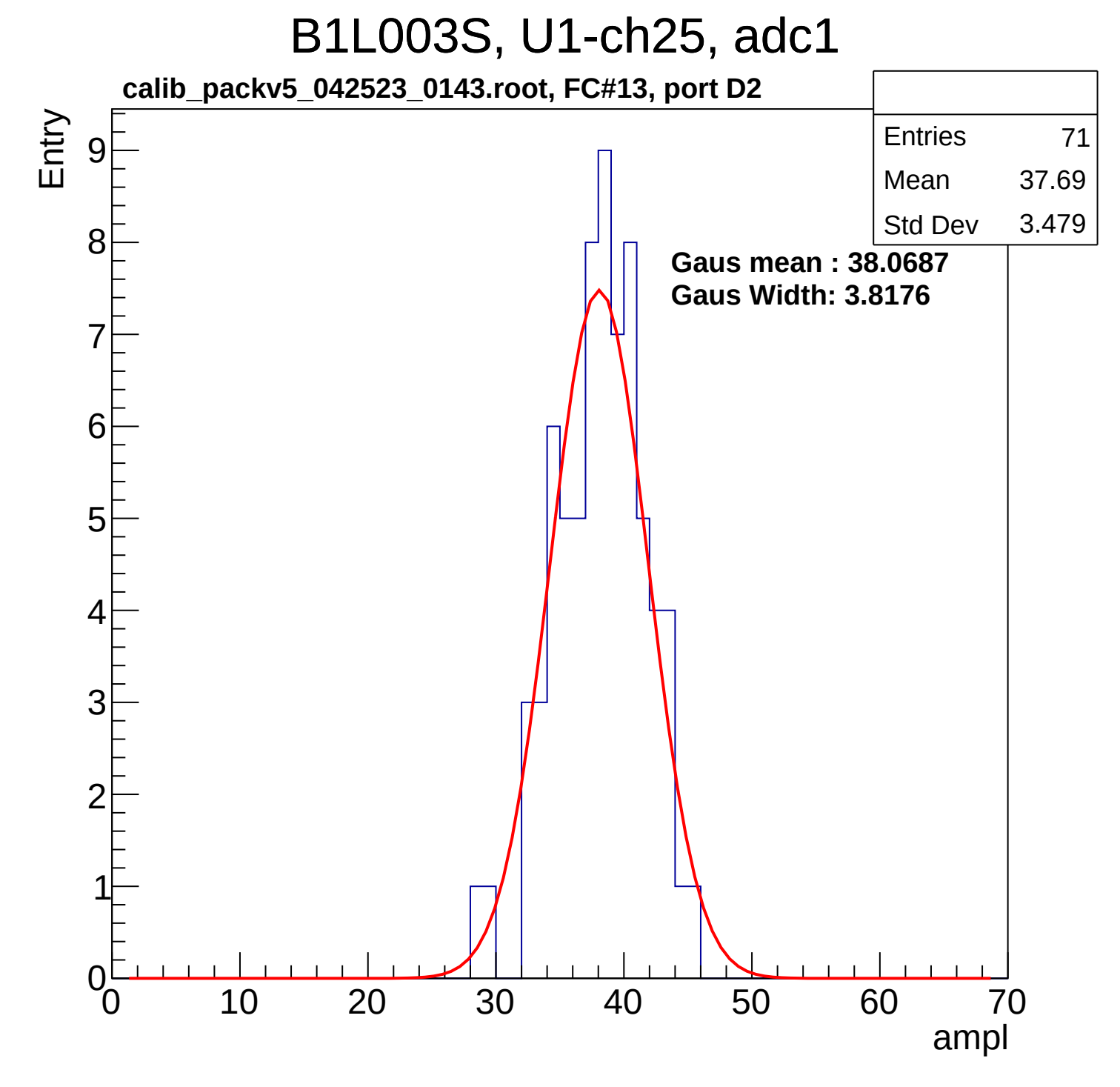

B1L003S, U1-ch24, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 53.83
Std Dev 3.078
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

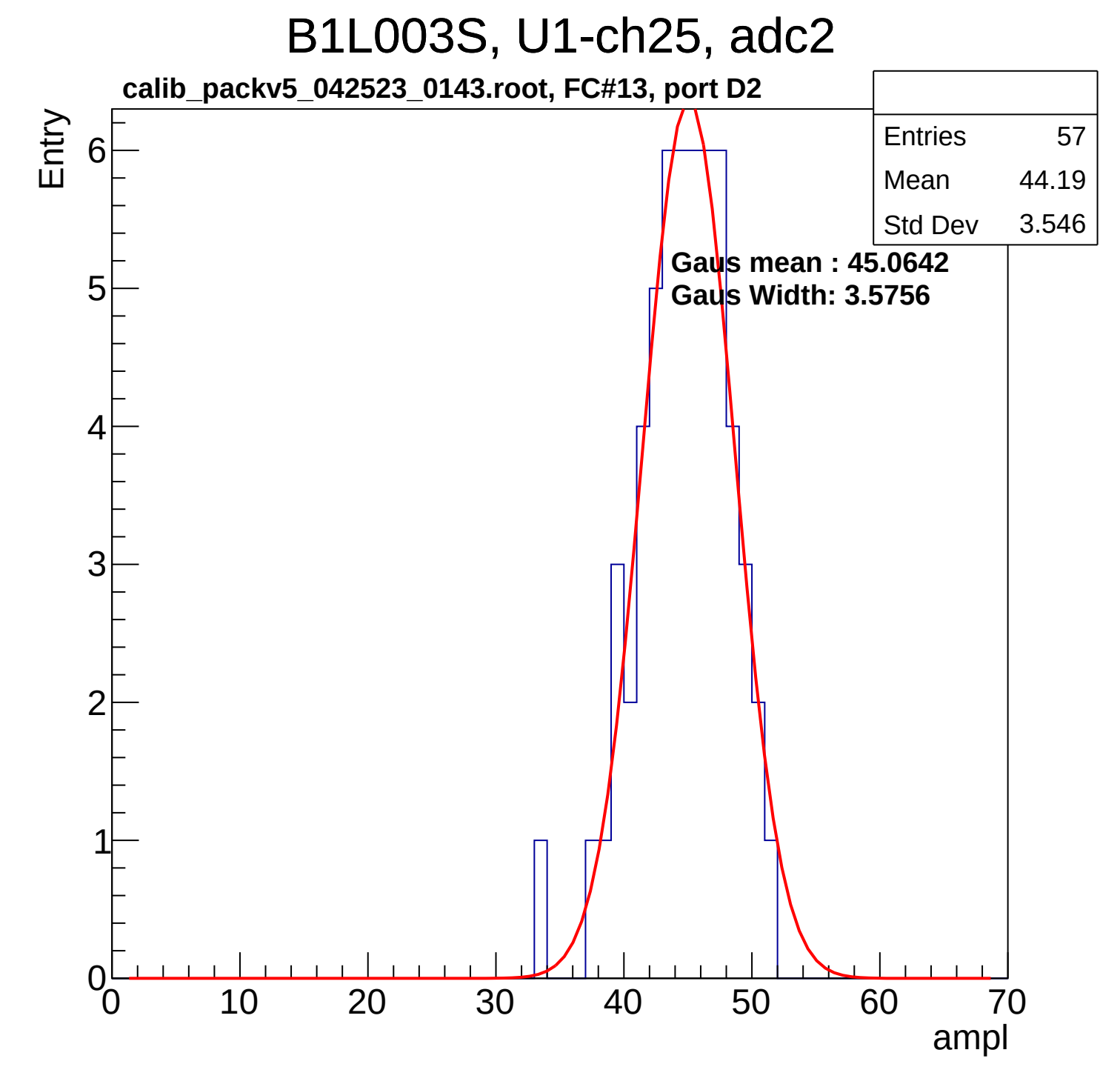

B1L003S, U1-ch24, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.39
Std Dev 8.143
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

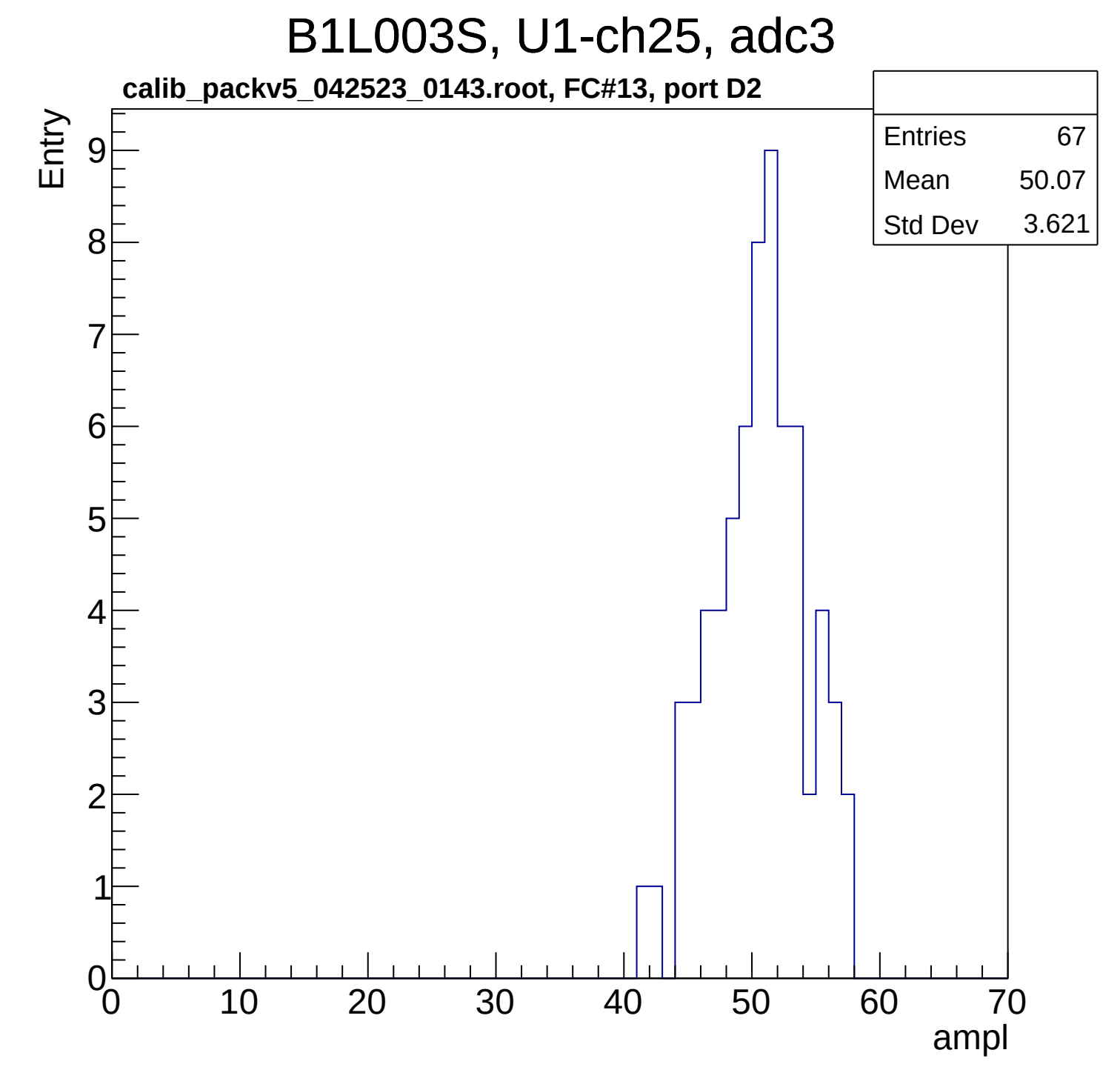

B1L003S, U1-ch24, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.8
Std Dev 1.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch24, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch25, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.95
Std Dev 7.4
Gaus mean : 30.9839
Gaus Width: 3.4668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch25, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 37.69
Std Dev 3.479
Gaus mean : 38.0687
Gaus Width: 3.8176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch25, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 44.19
Std Dev 3.546
Gaus mean : 45.0642
Gaus Width: 3.5756
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch25, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 50.07
Std Dev 3.621
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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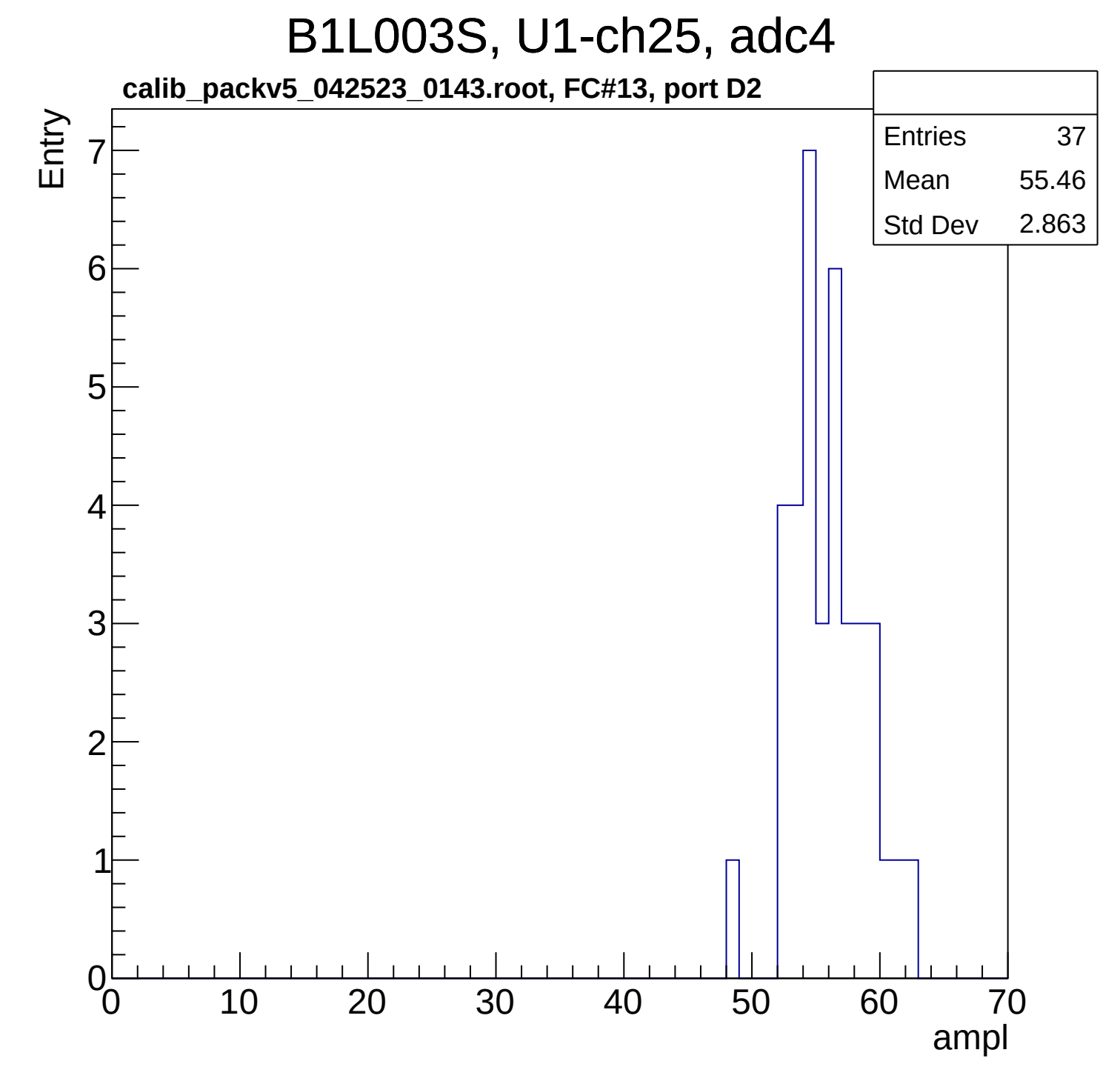
40
50
60
70


B1L003S, U1-ch25, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 55.46
Std Dev 2.863
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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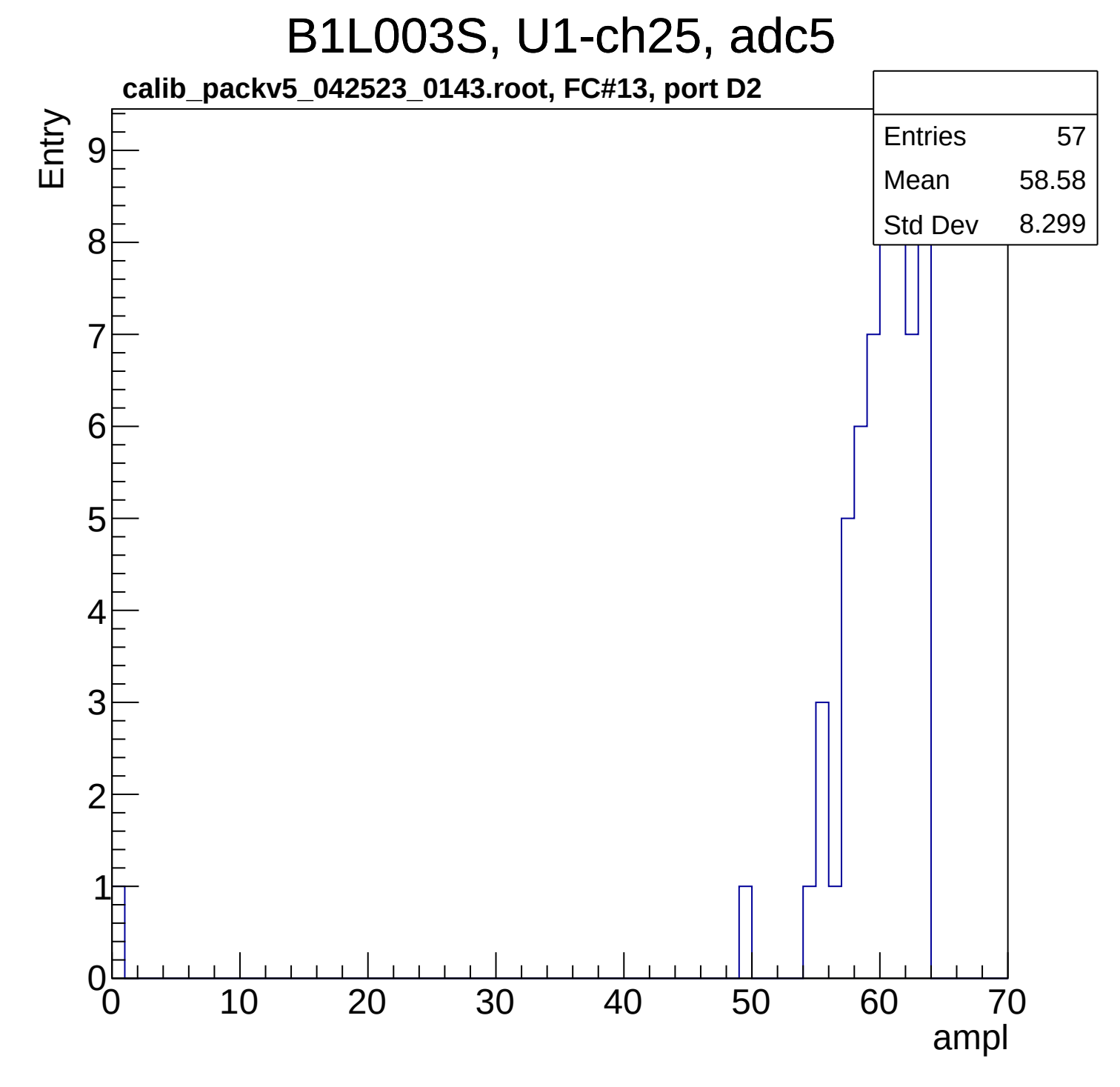
40
50
60
70


B1L003S, U1-ch25, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.58
Std Dev 8.299
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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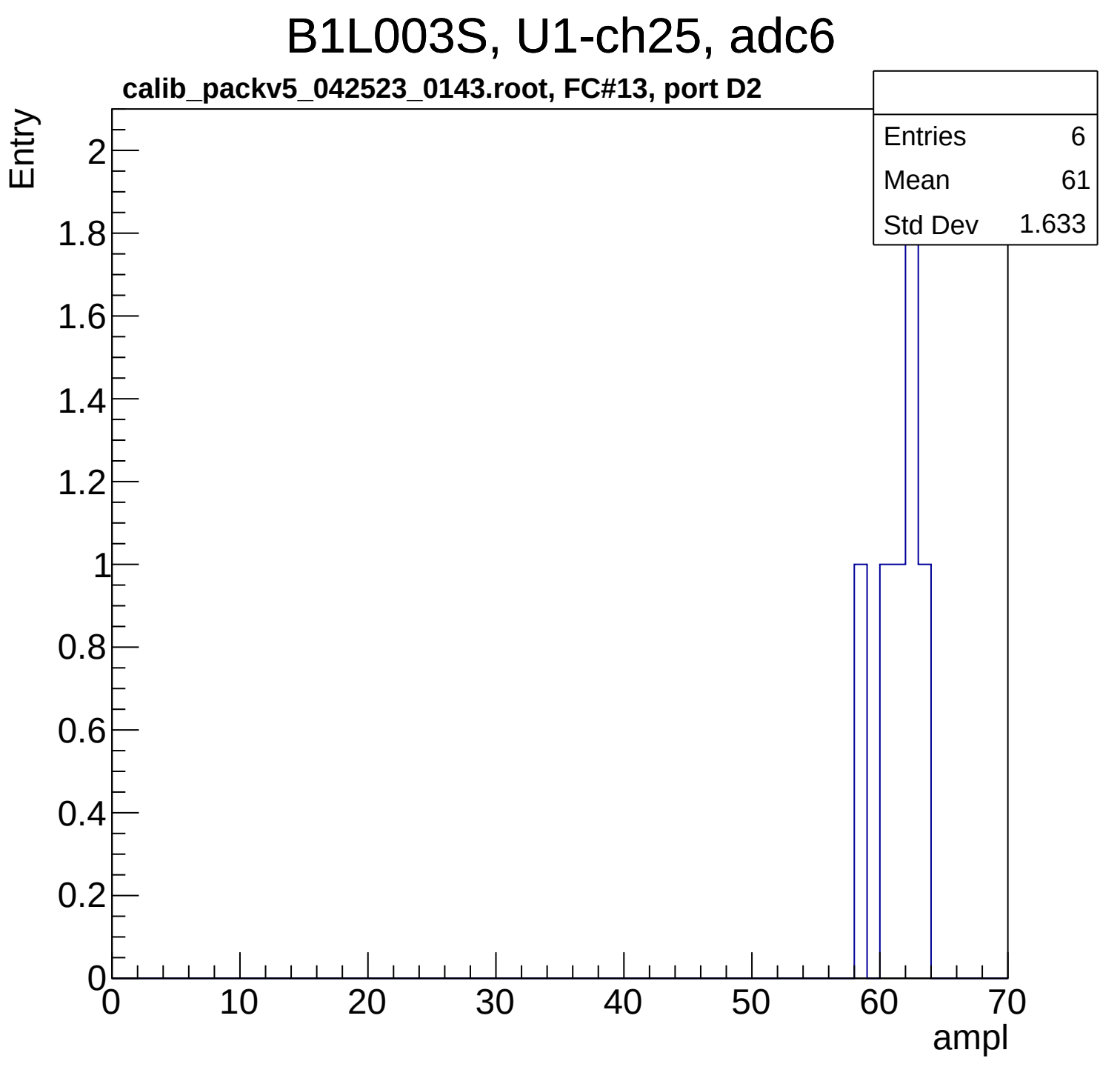
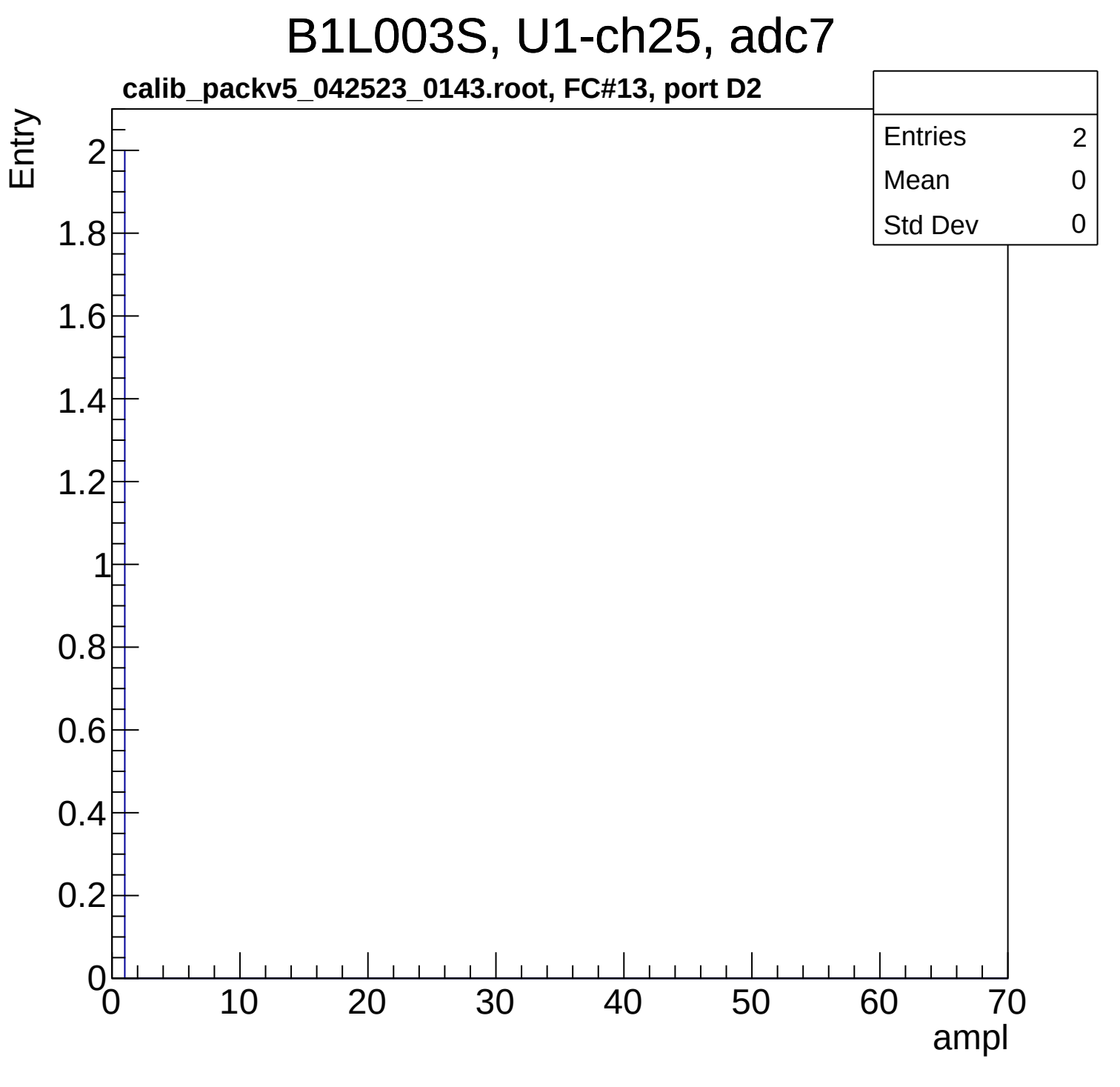
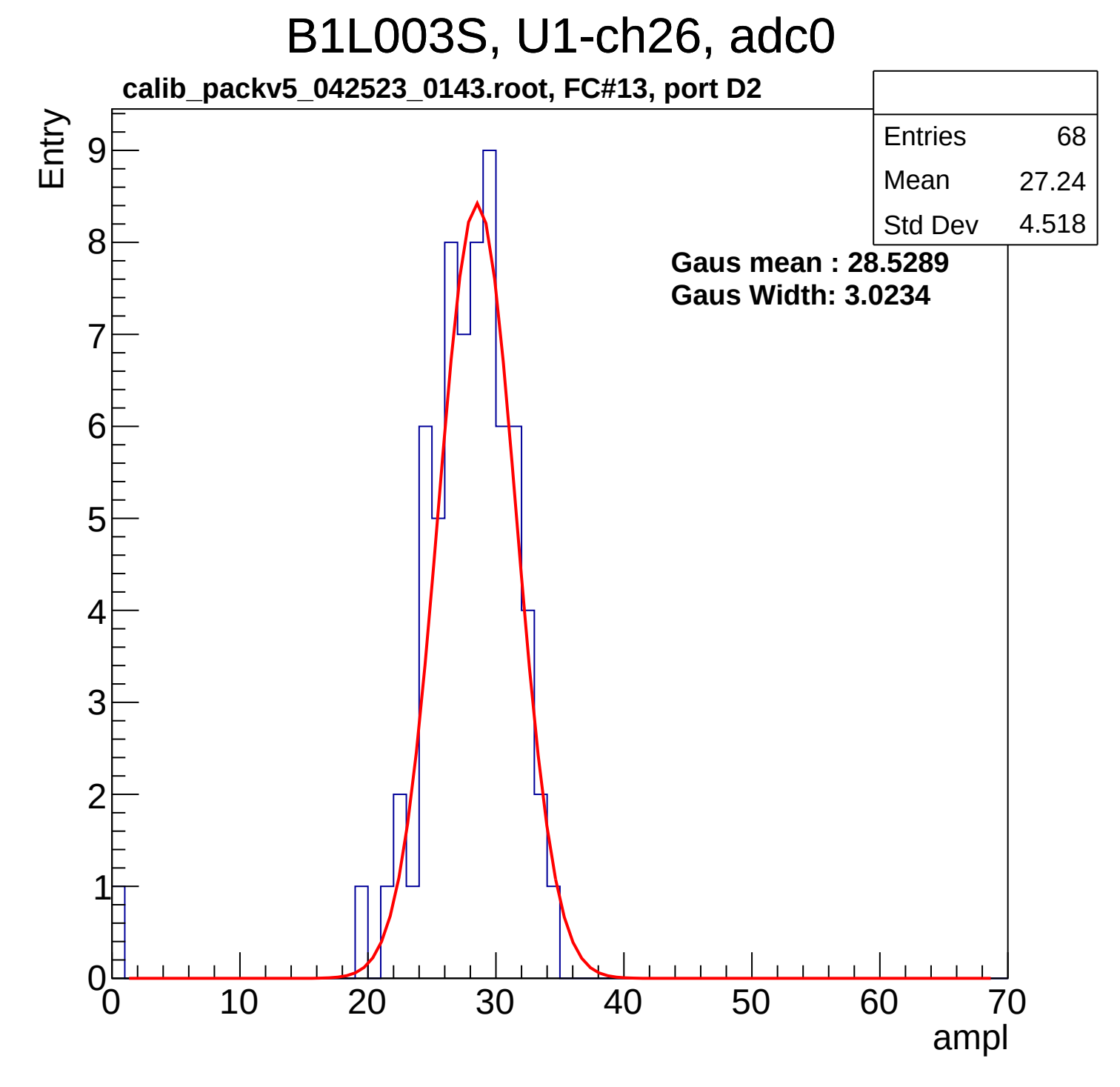
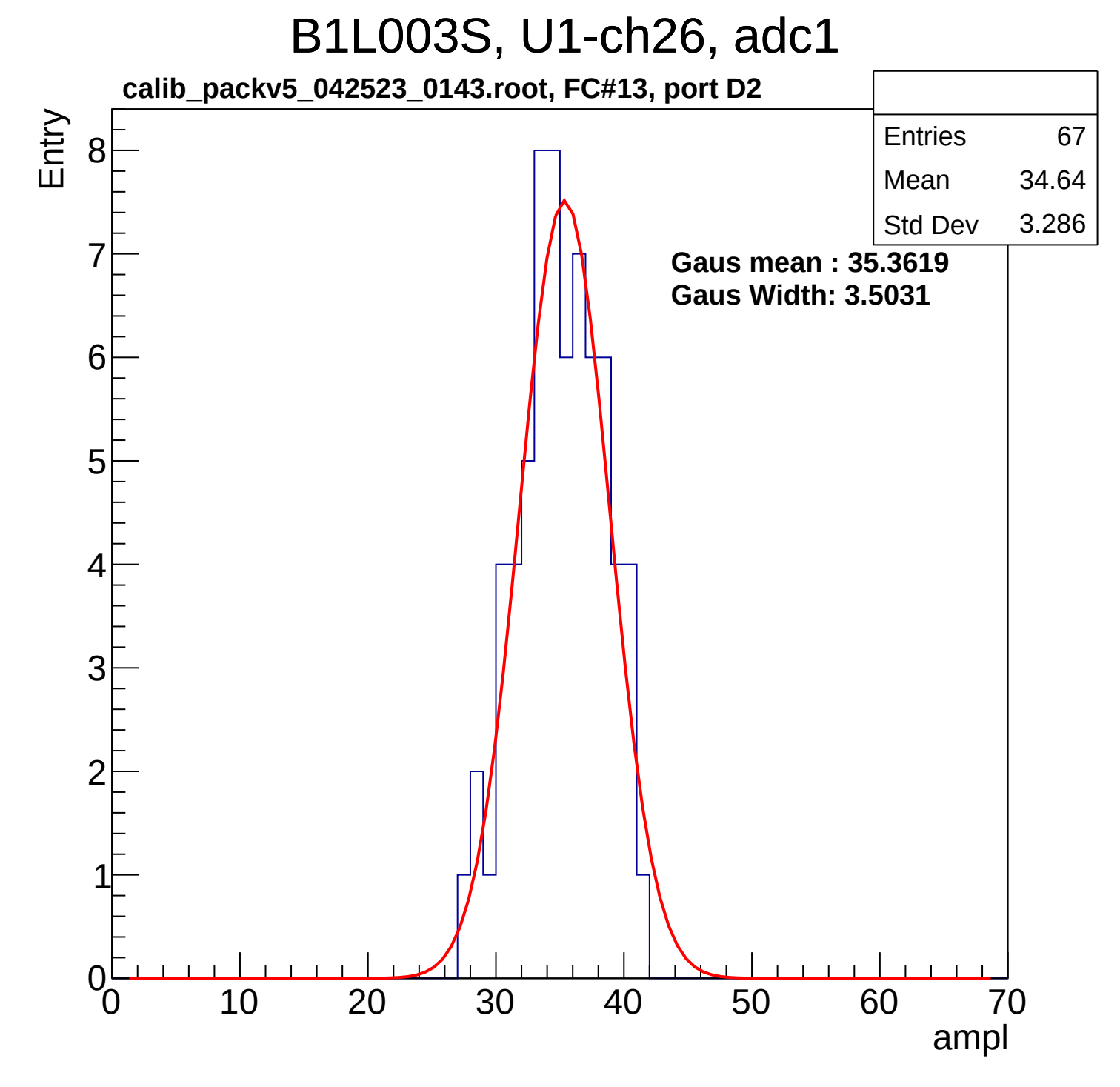
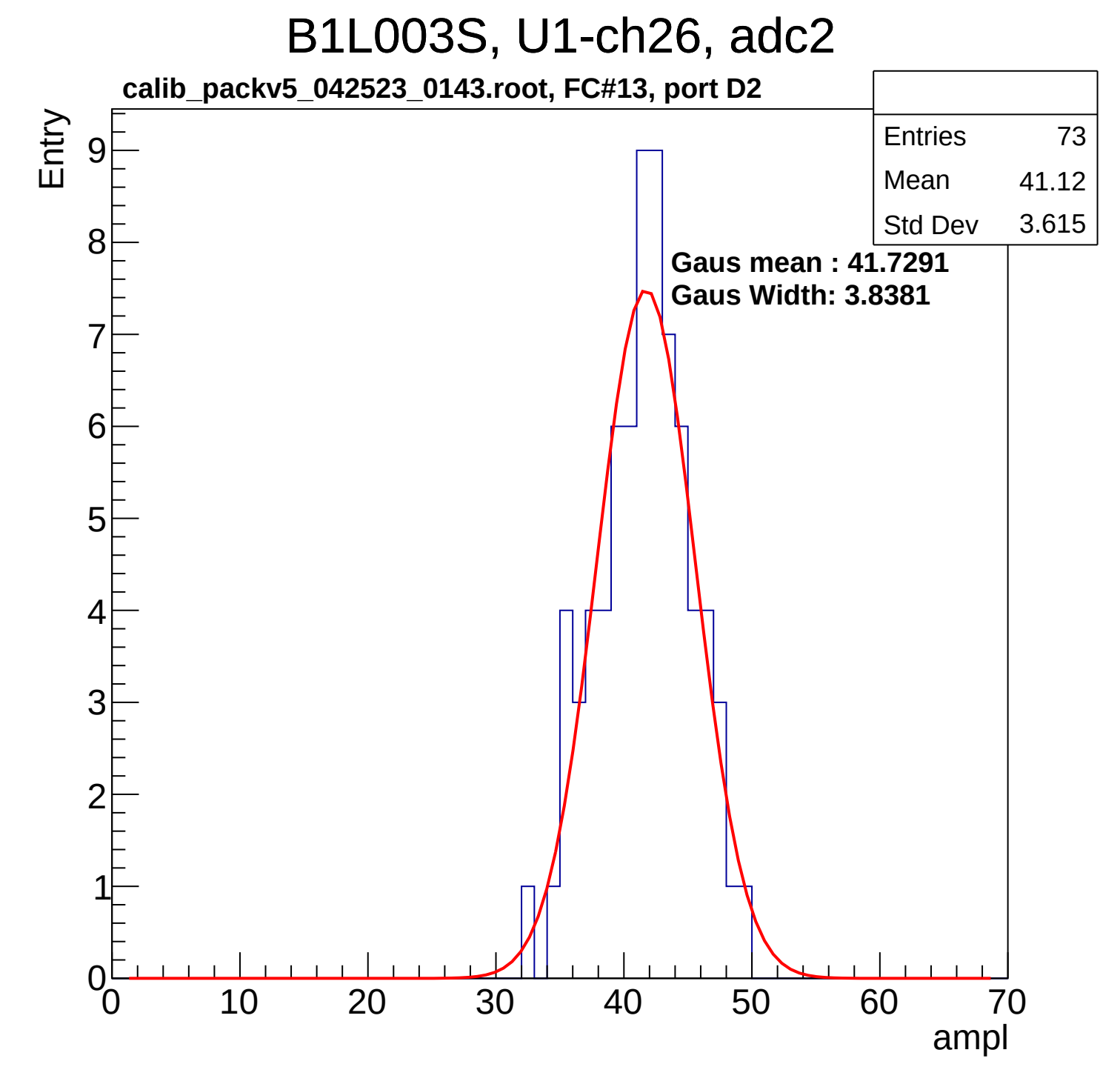

B1L003S, U1-ch25, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61
Std Dev 1.633
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch25, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch26, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 27.24
Std Dev 4.518
Gaus mean : 28.5289
Gaus Width: 3.0234
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch26, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 34.64
Std Dev 3.286
Gaus mean : 35.3619
Gaus Width: 3.5031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch26, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.12
Std Dev 3.615
Gaus mean : 41.7291
Gaus Width: 3.8381
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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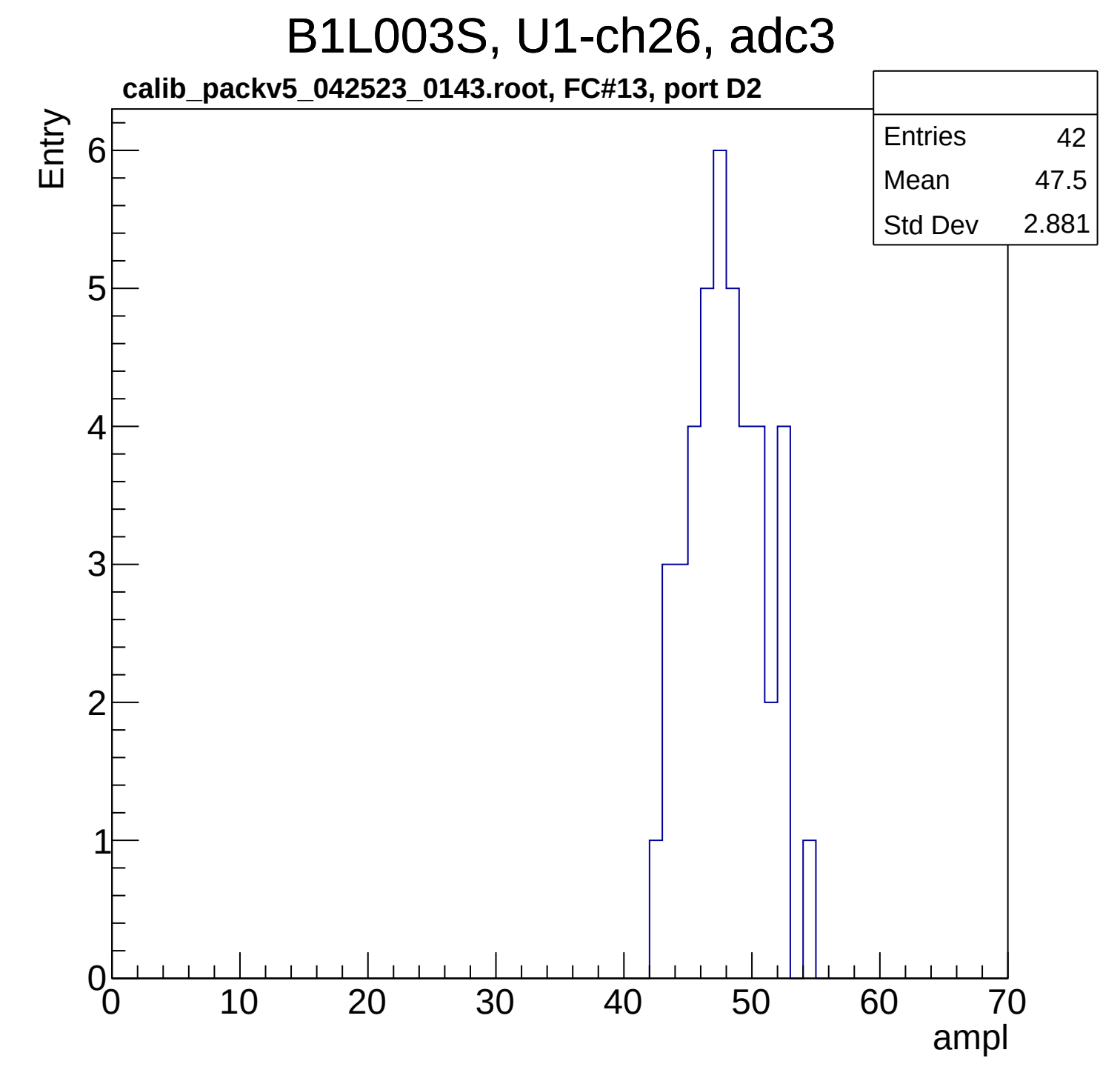
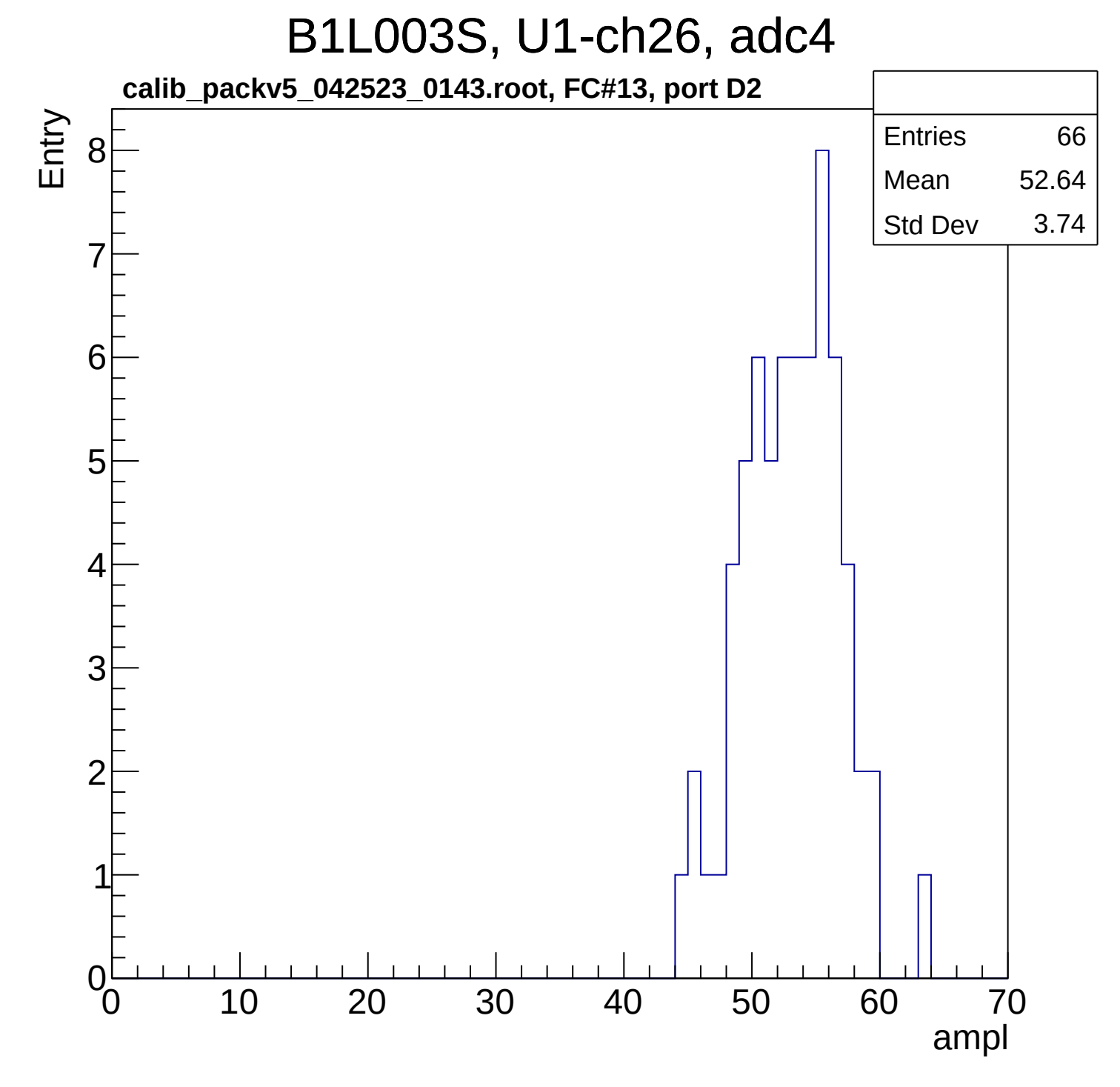
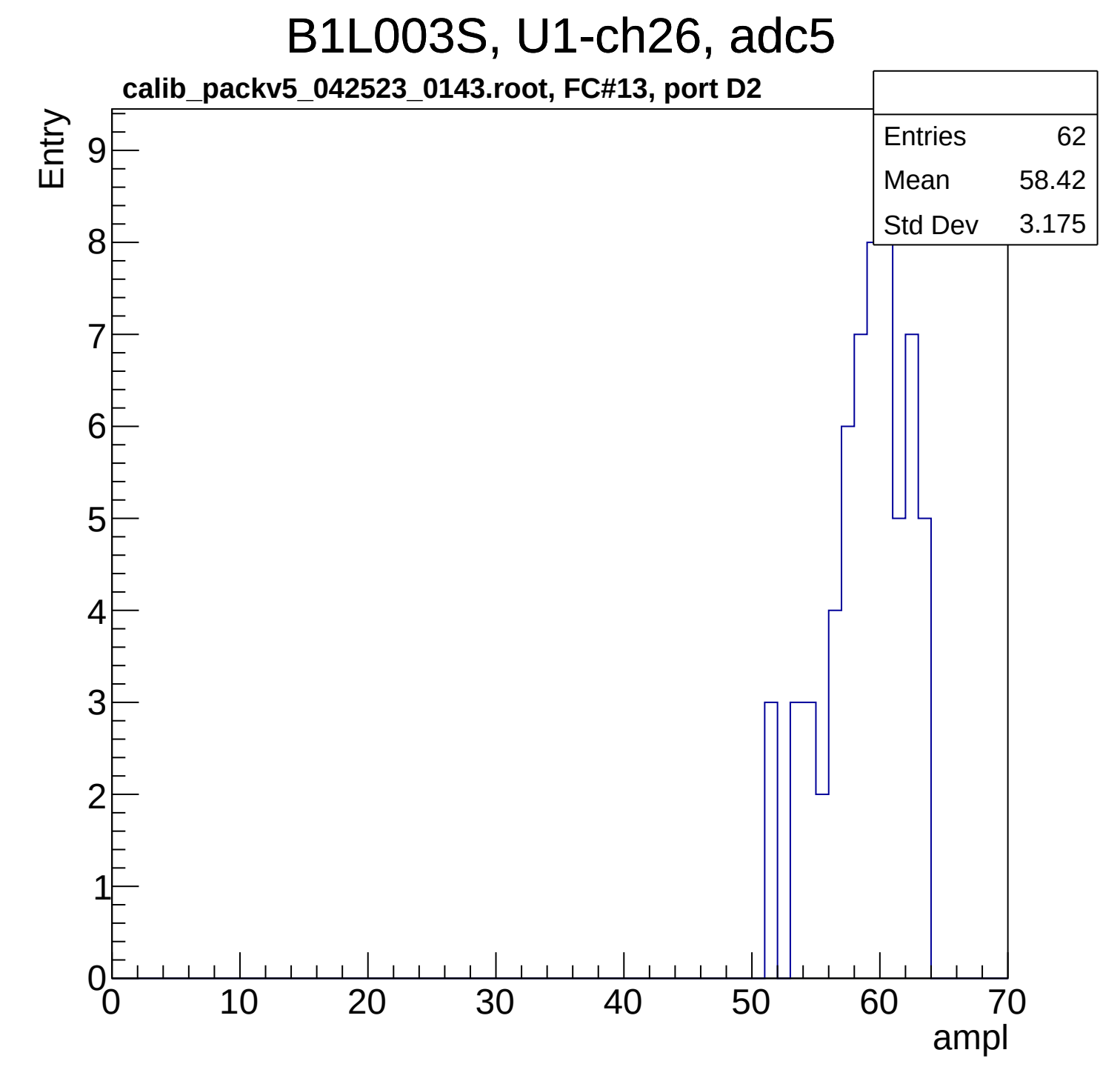
40
50
60
70


B1L003S, U1-ch26, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 47.5
Std Dev 2.881
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch26, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 52.64
Std Dev 3.74
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch26, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.42
Std Dev 3.175
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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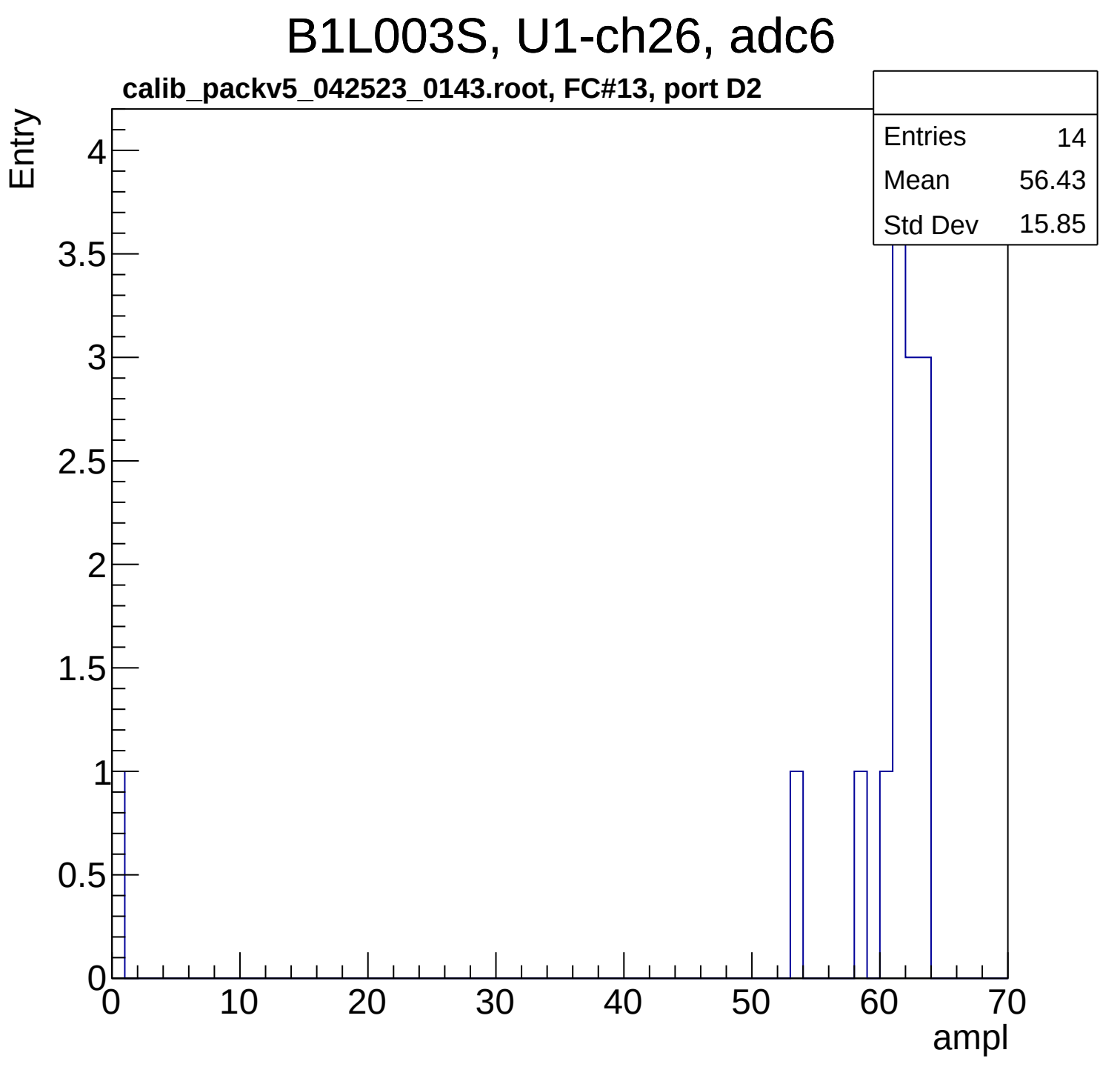
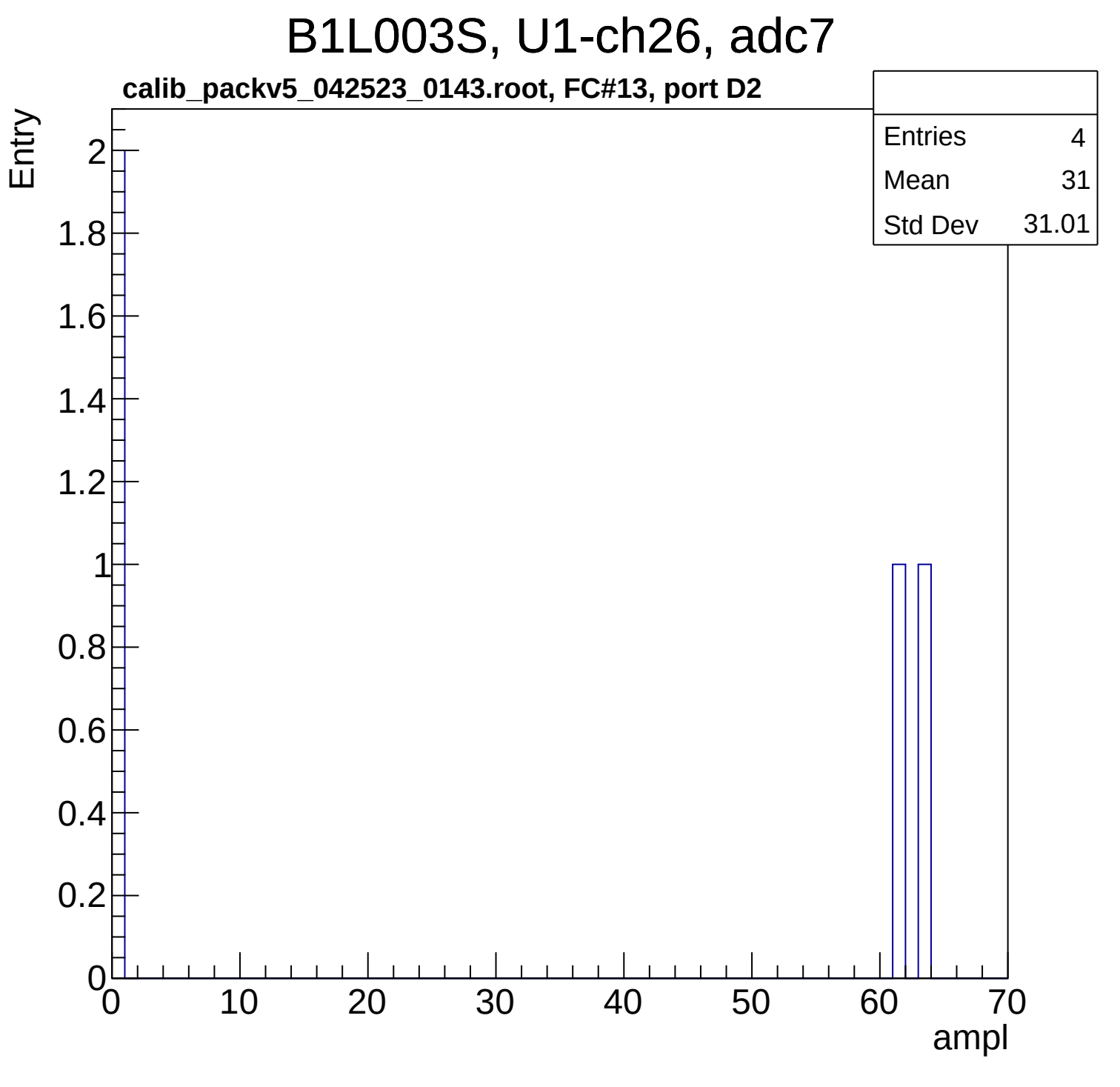
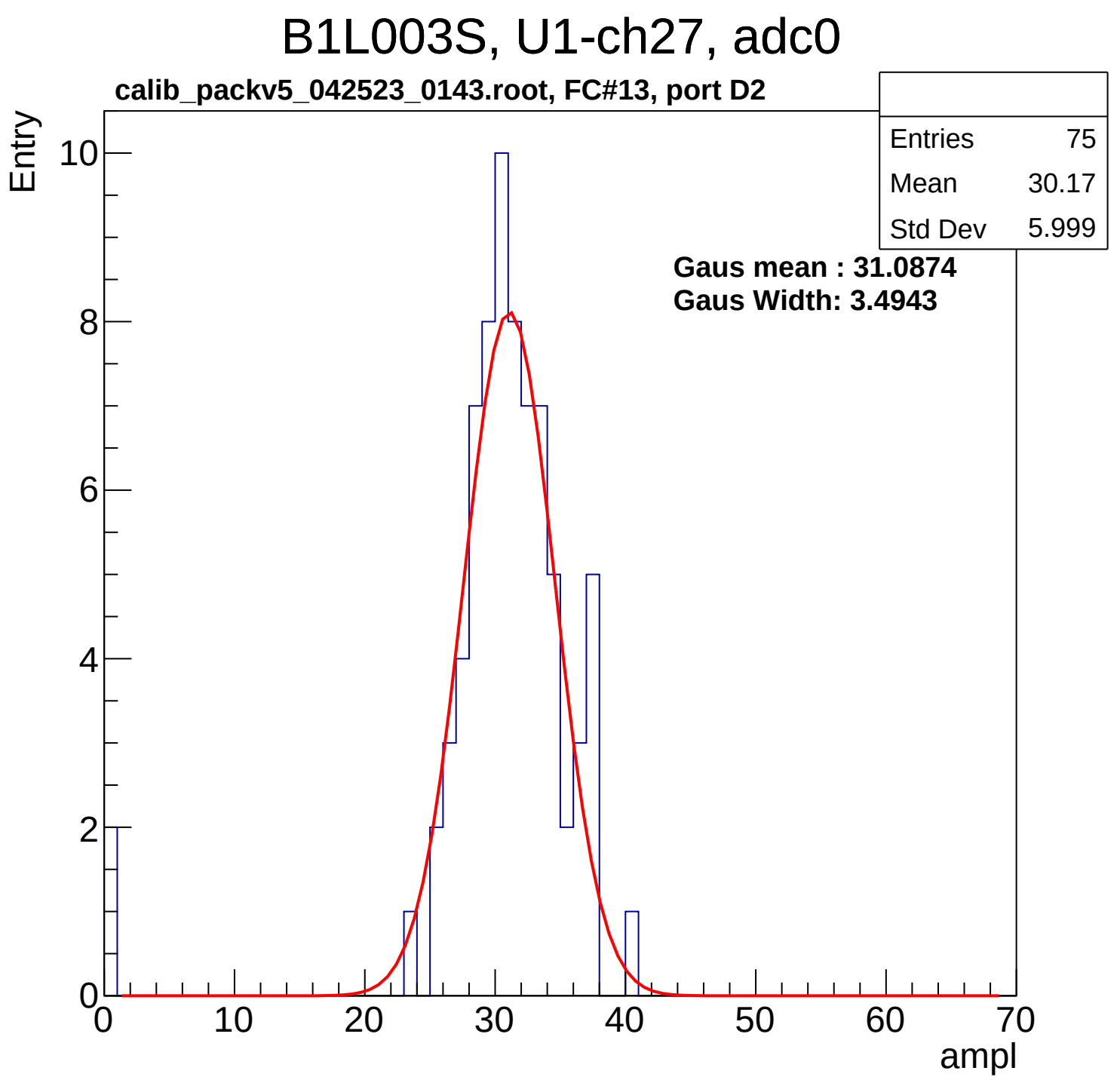
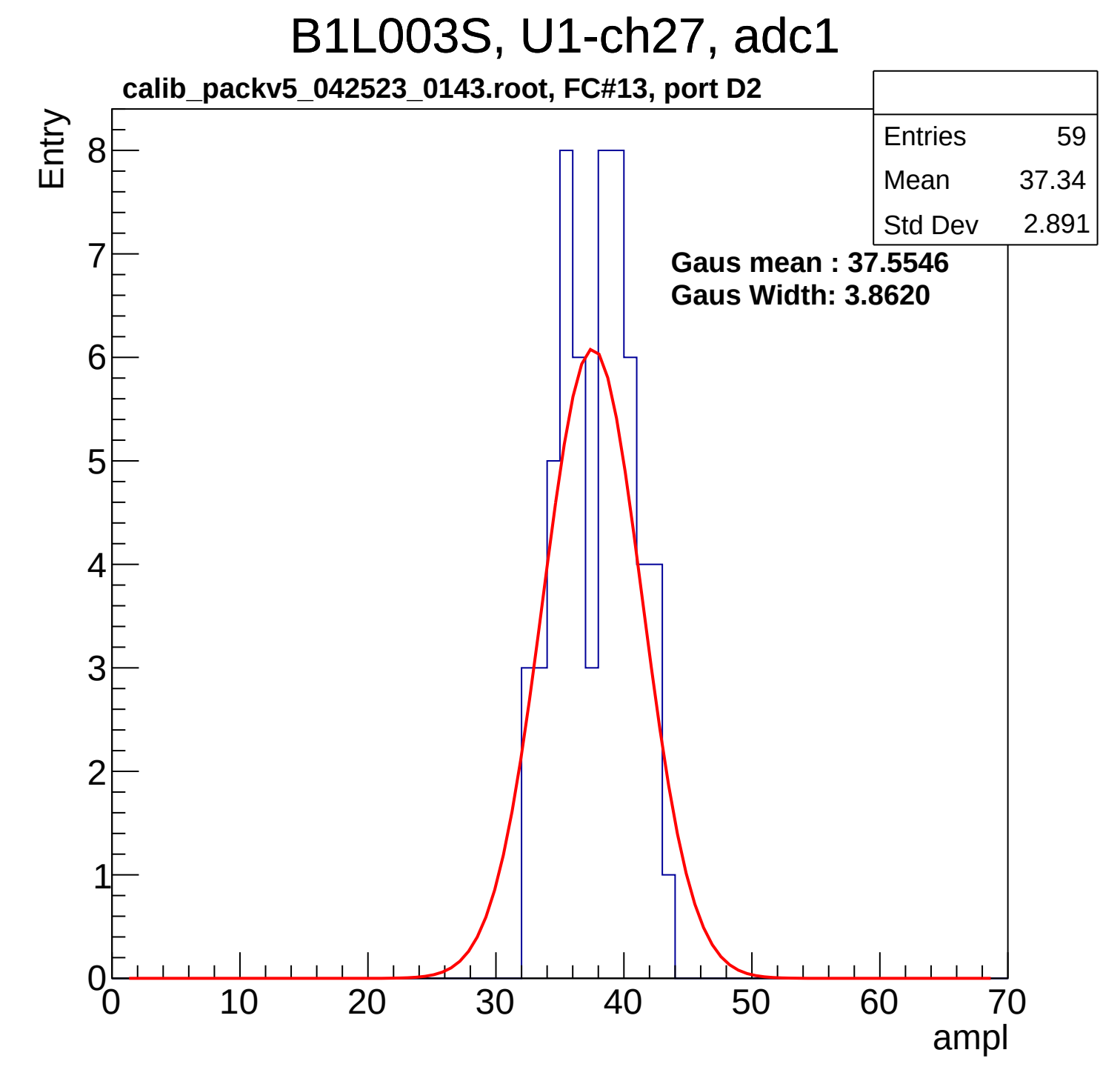
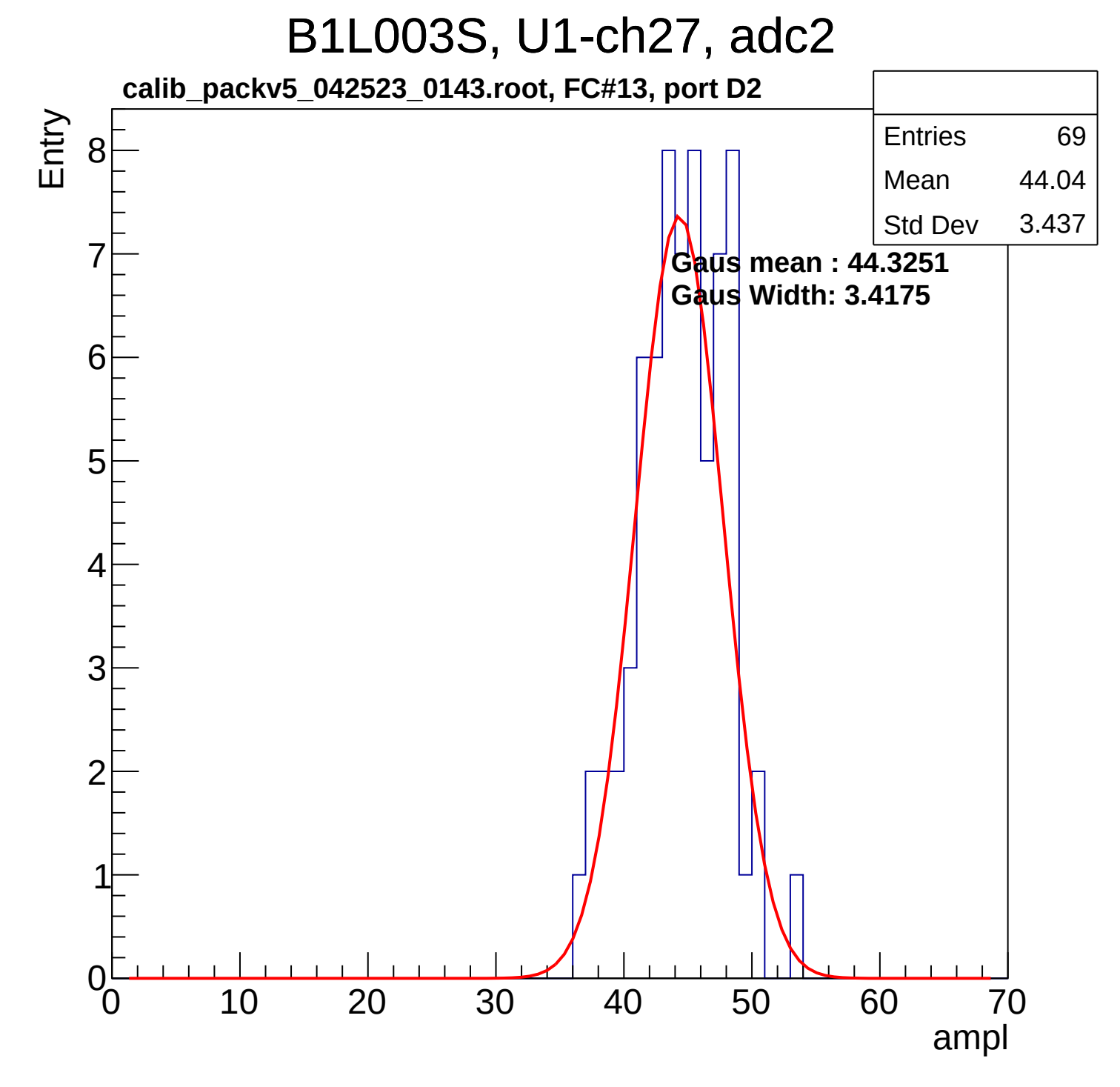
30
40
50
60
70

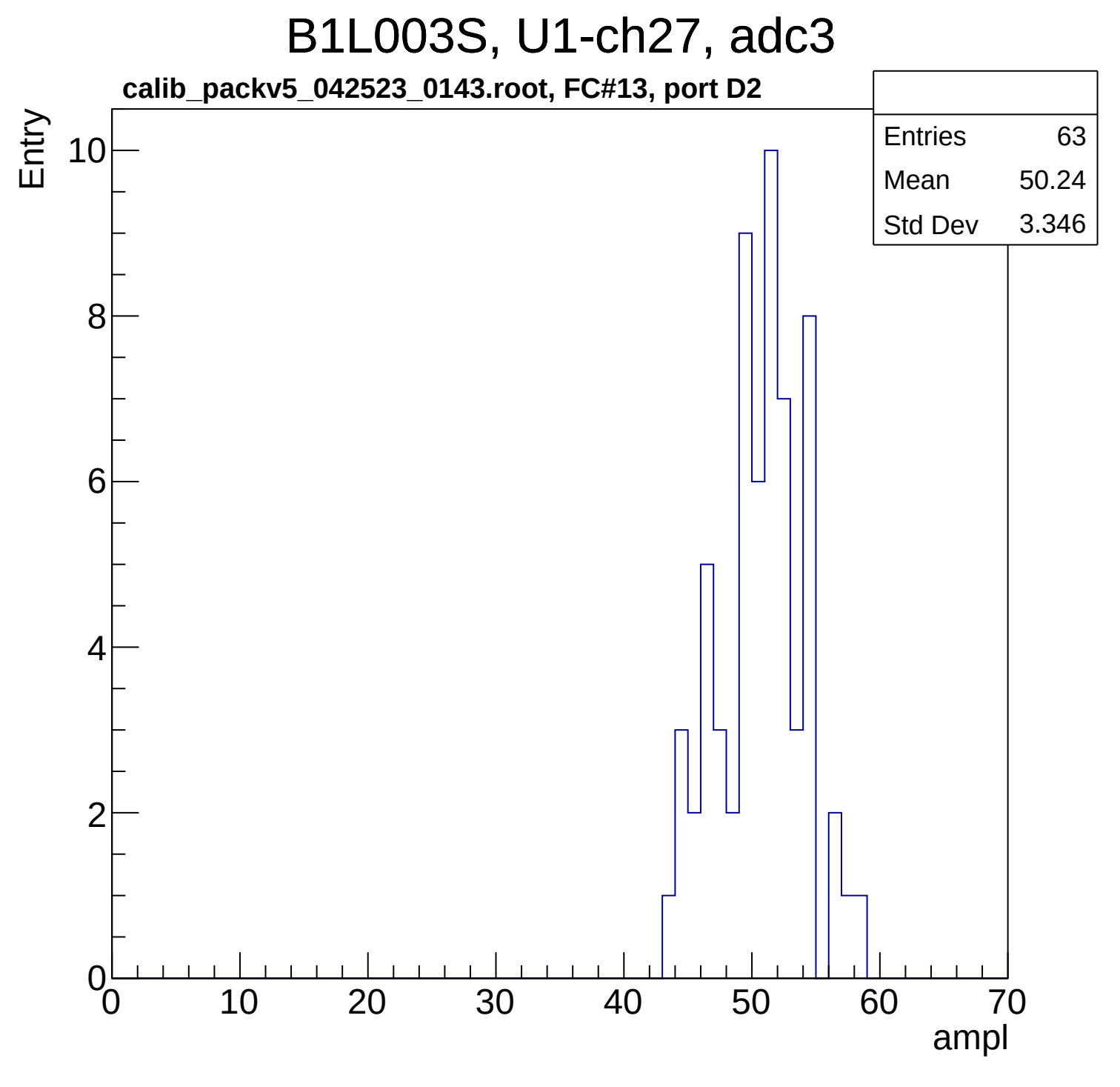

B1L003S, U1-ch26, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 56.43
Std Dev 15.85
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

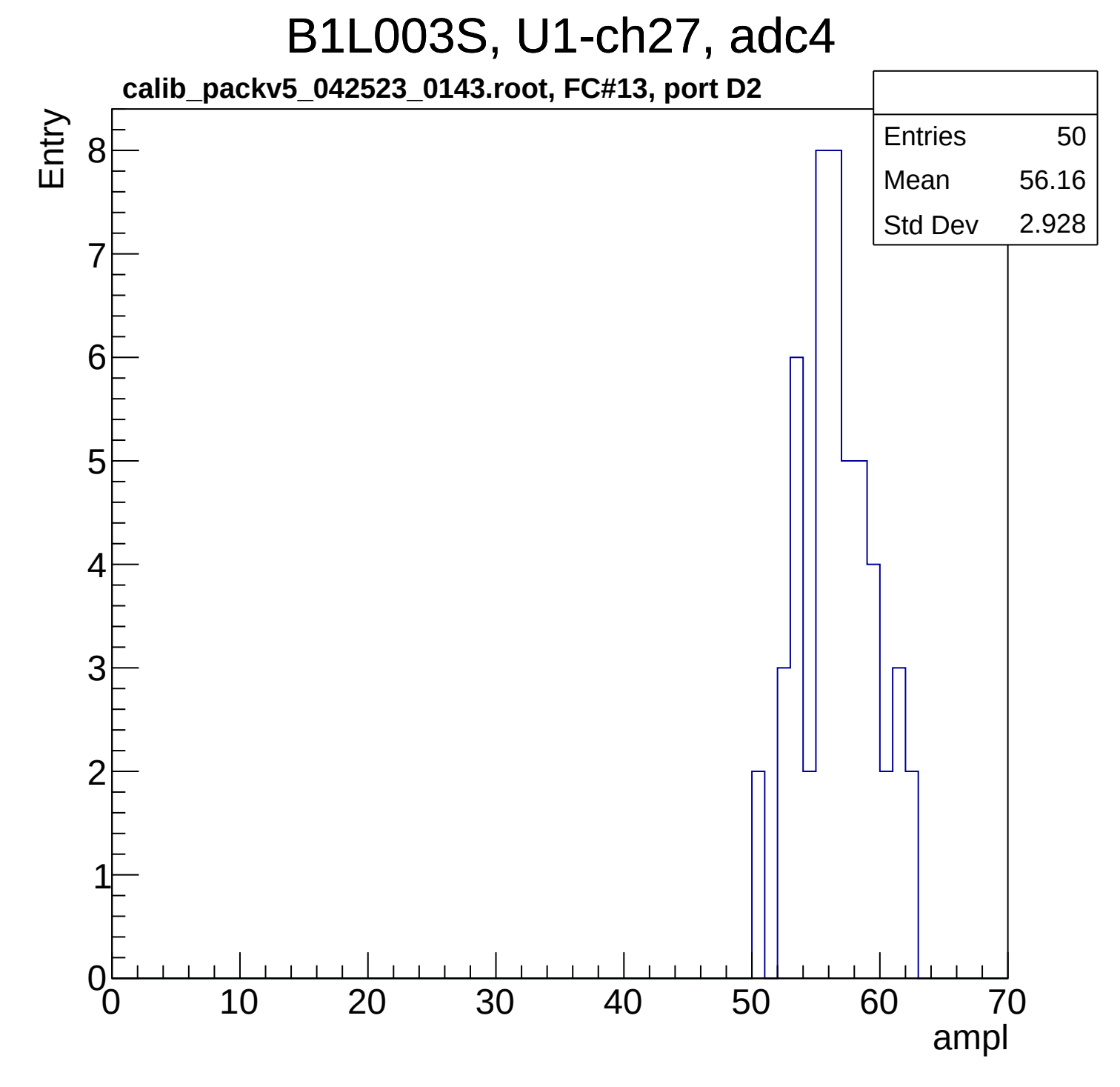

B1L003S, U1-ch26, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31
Std Dev 31.01
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

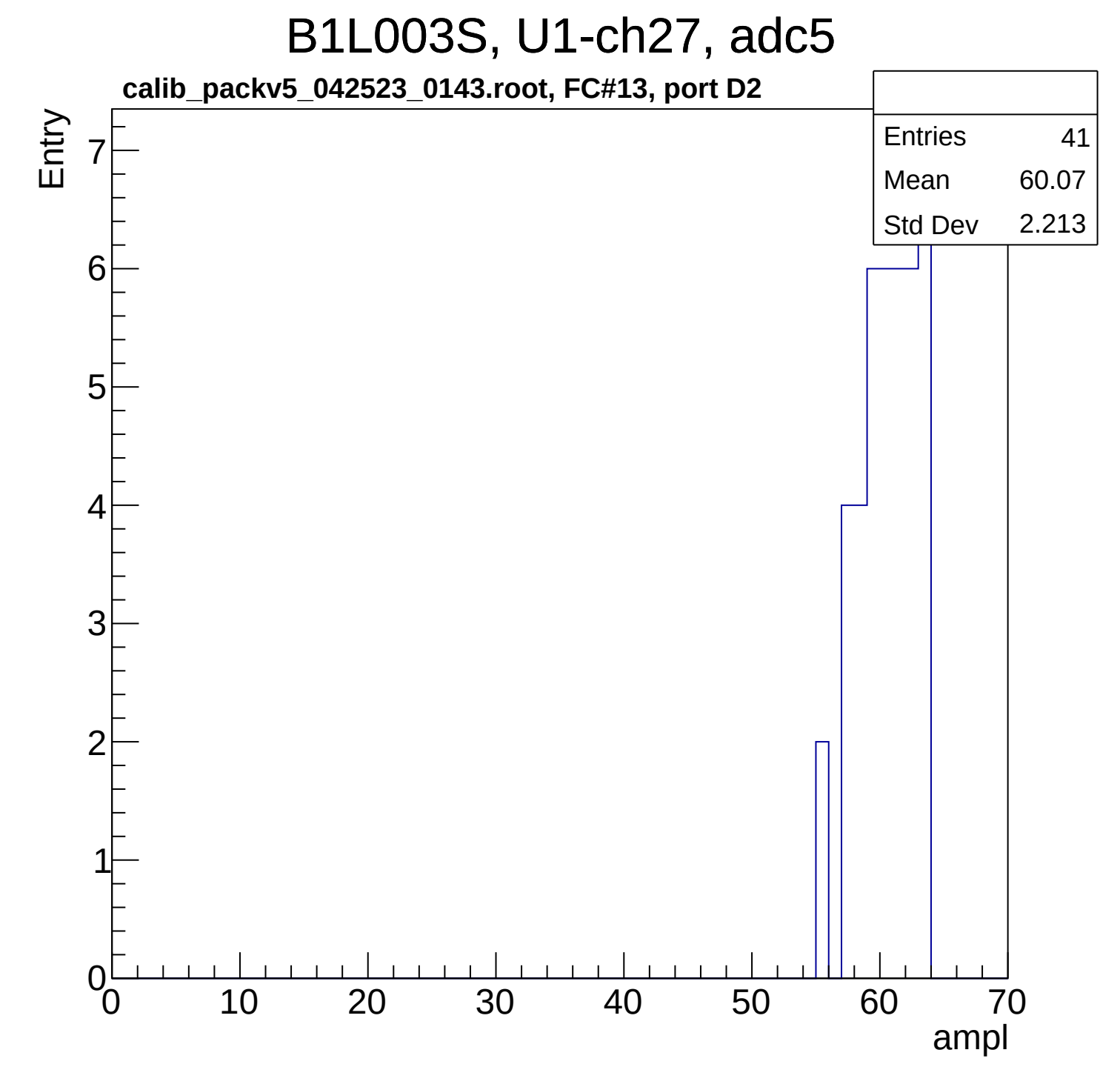

B1L003S, U1-ch27, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 30.17
Std Dev 5.999
Gaus mean : 31.0874
Gaus Width: 3.4943
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

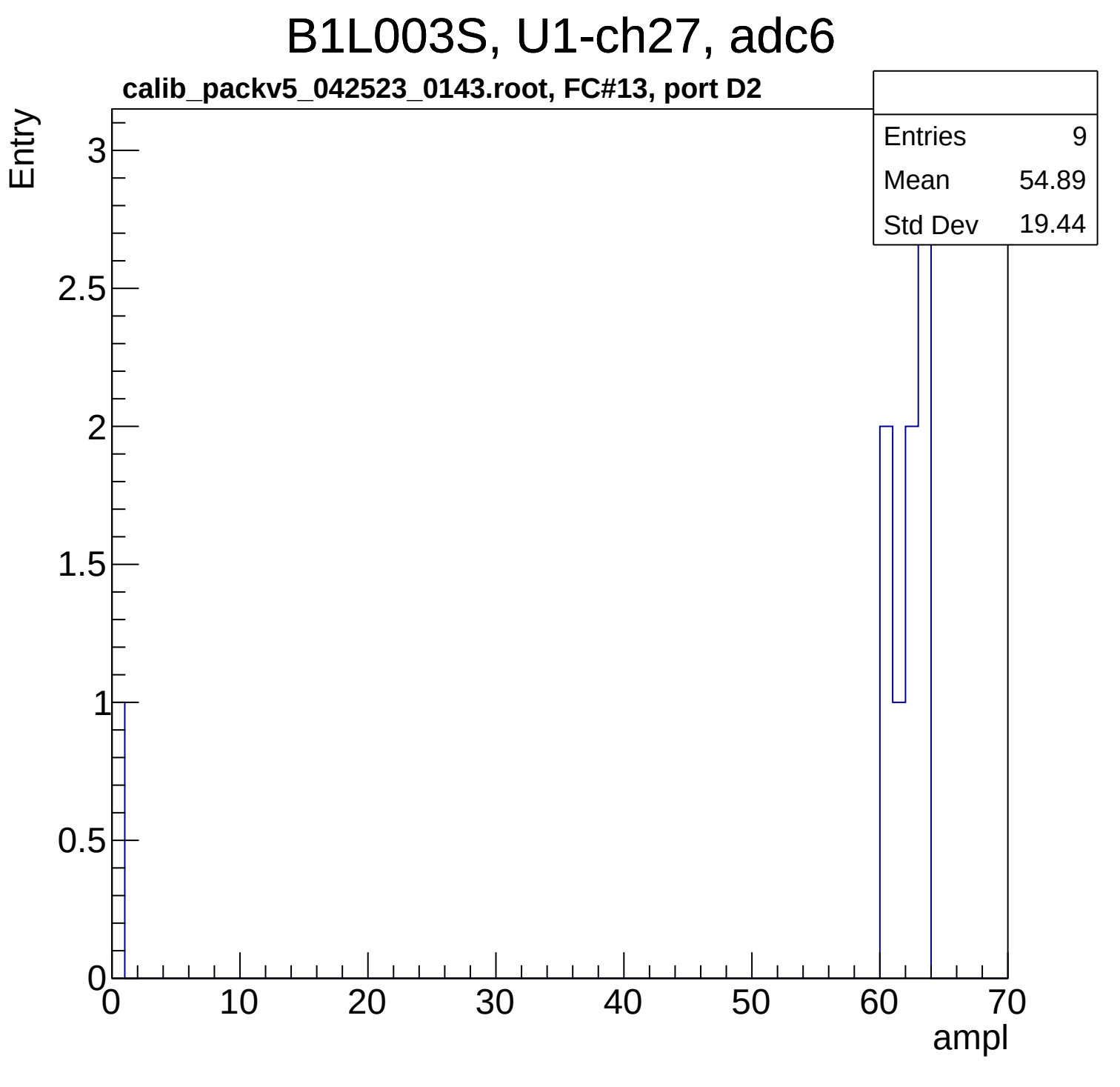

B1L003S, U1-ch27, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.34
Std Dev 2.891
Gaus mean : 37.5546
Gaus Width: 3.8620
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

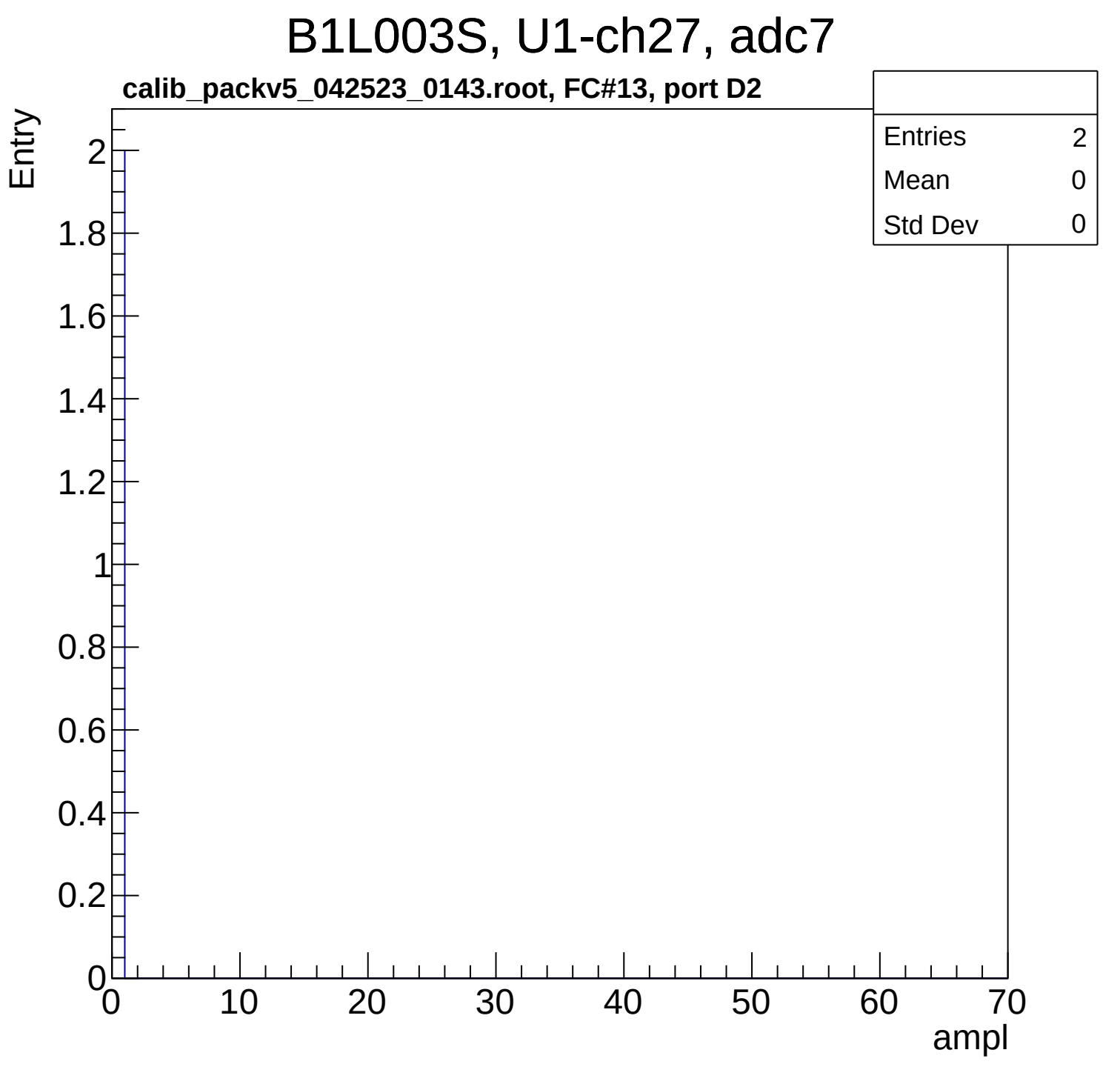

B1L003S, U1-ch27, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 44.04
Std Dev 3.437
Gaus mean : 44.3251
Gaus Width: 3.4175
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

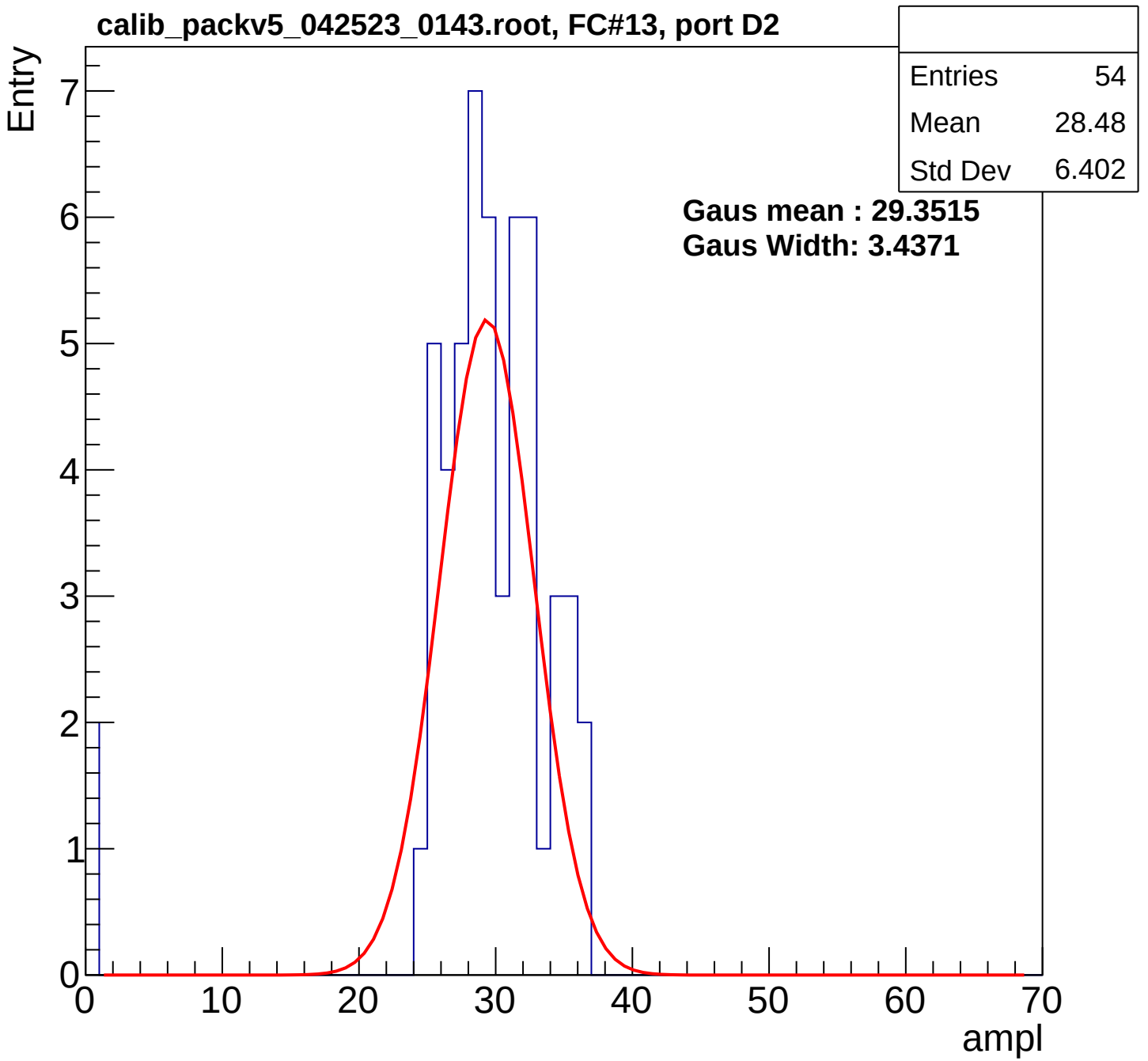

B1L003S, U1-ch27, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 50.24
Std Dev 3.346
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

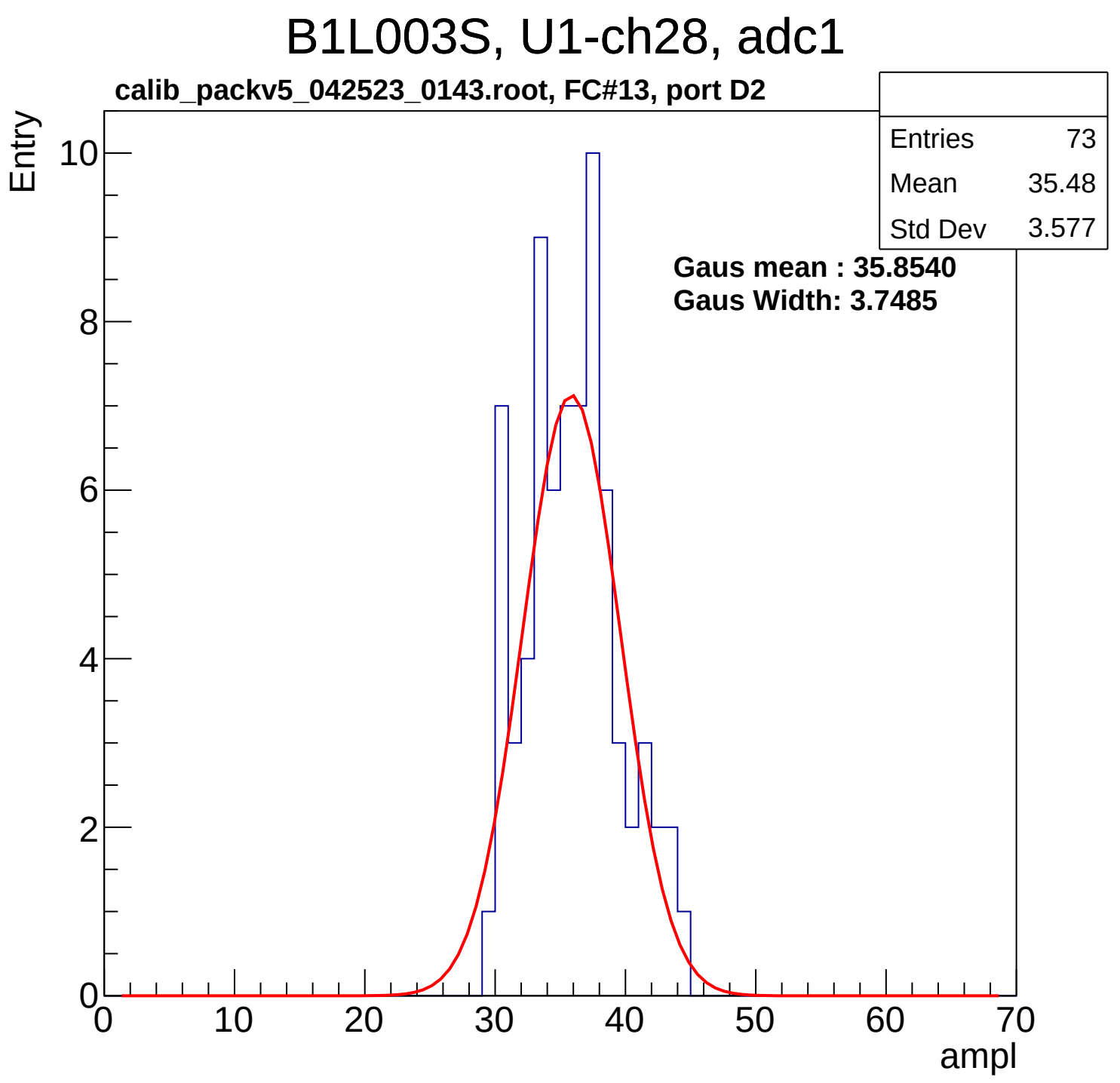

B1L003S, U1-ch27, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 56.16
Std Dev 2.928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

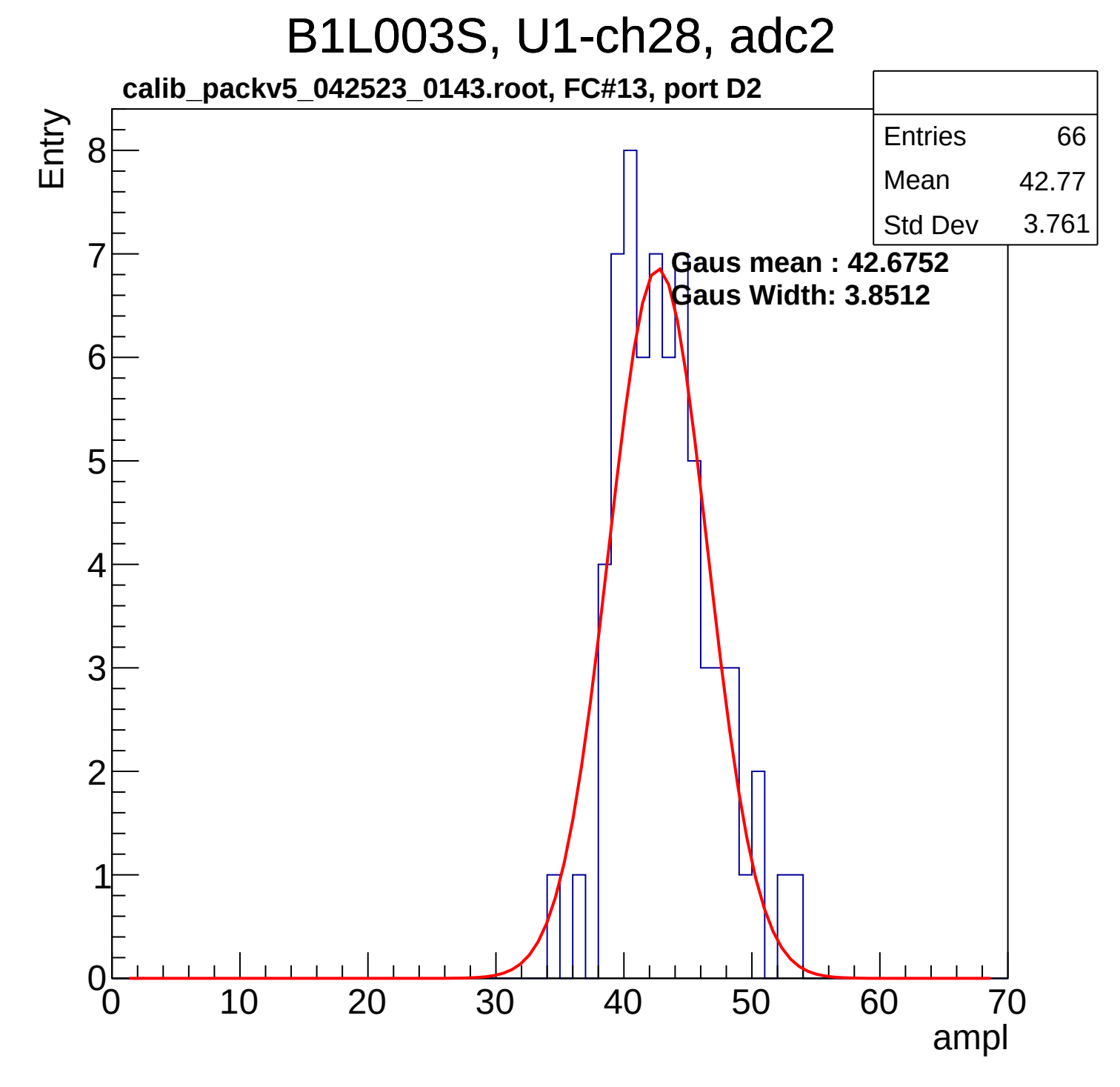

B1L003S, U1-ch27, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 60.07
Std Dev 2.213
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

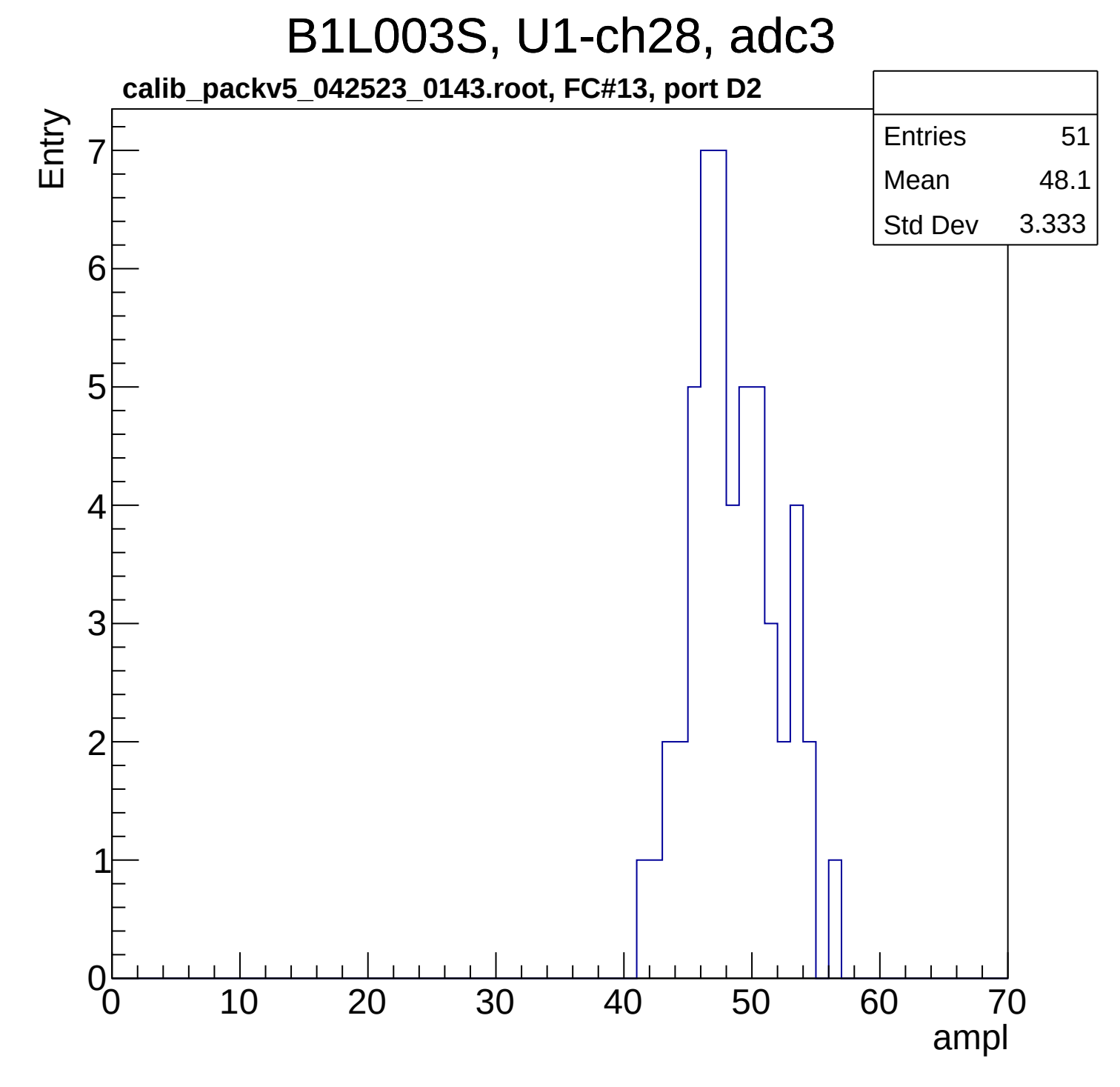

B1L003S, U1-ch27, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

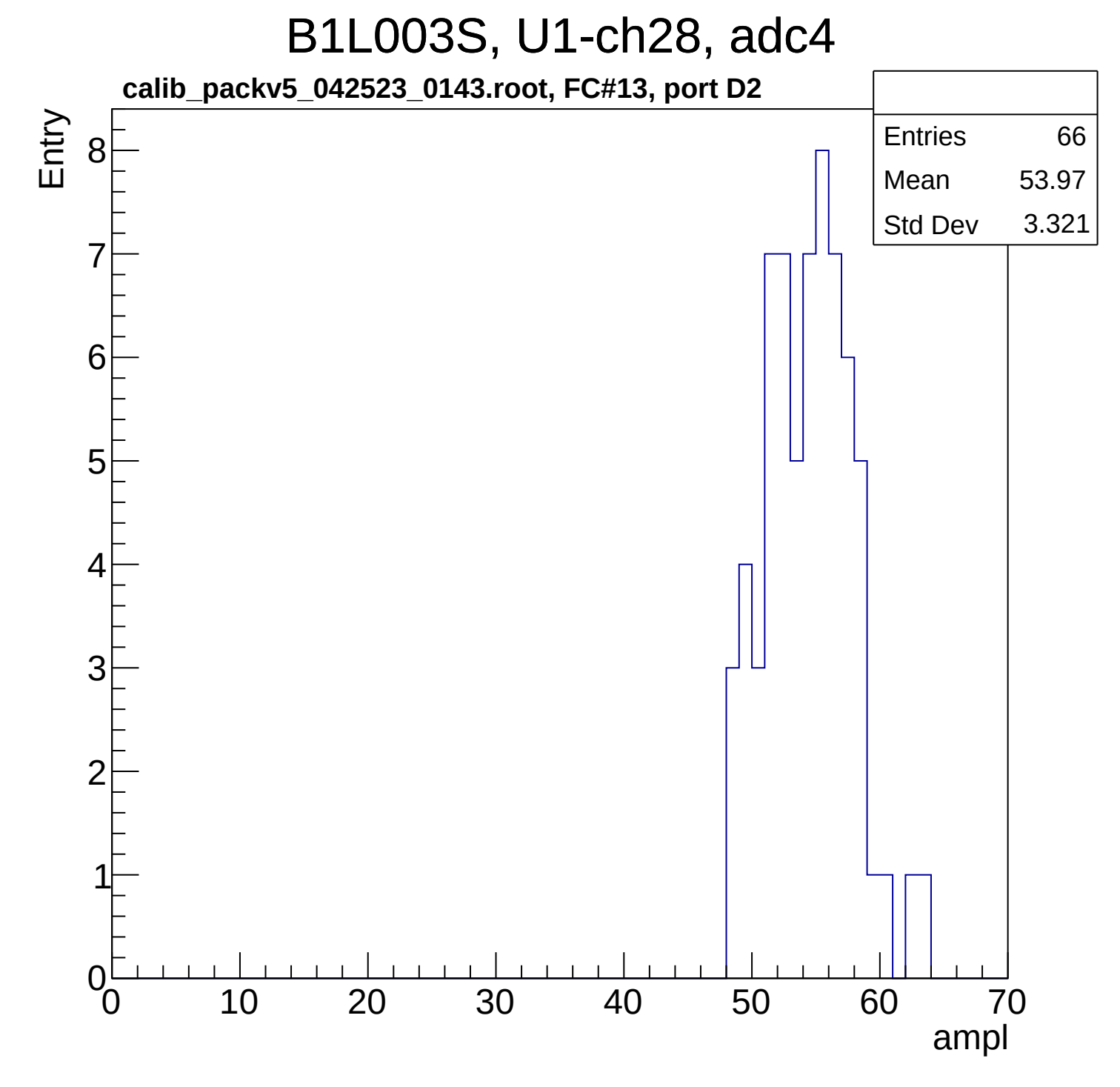

B1L003S, U1-ch27, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

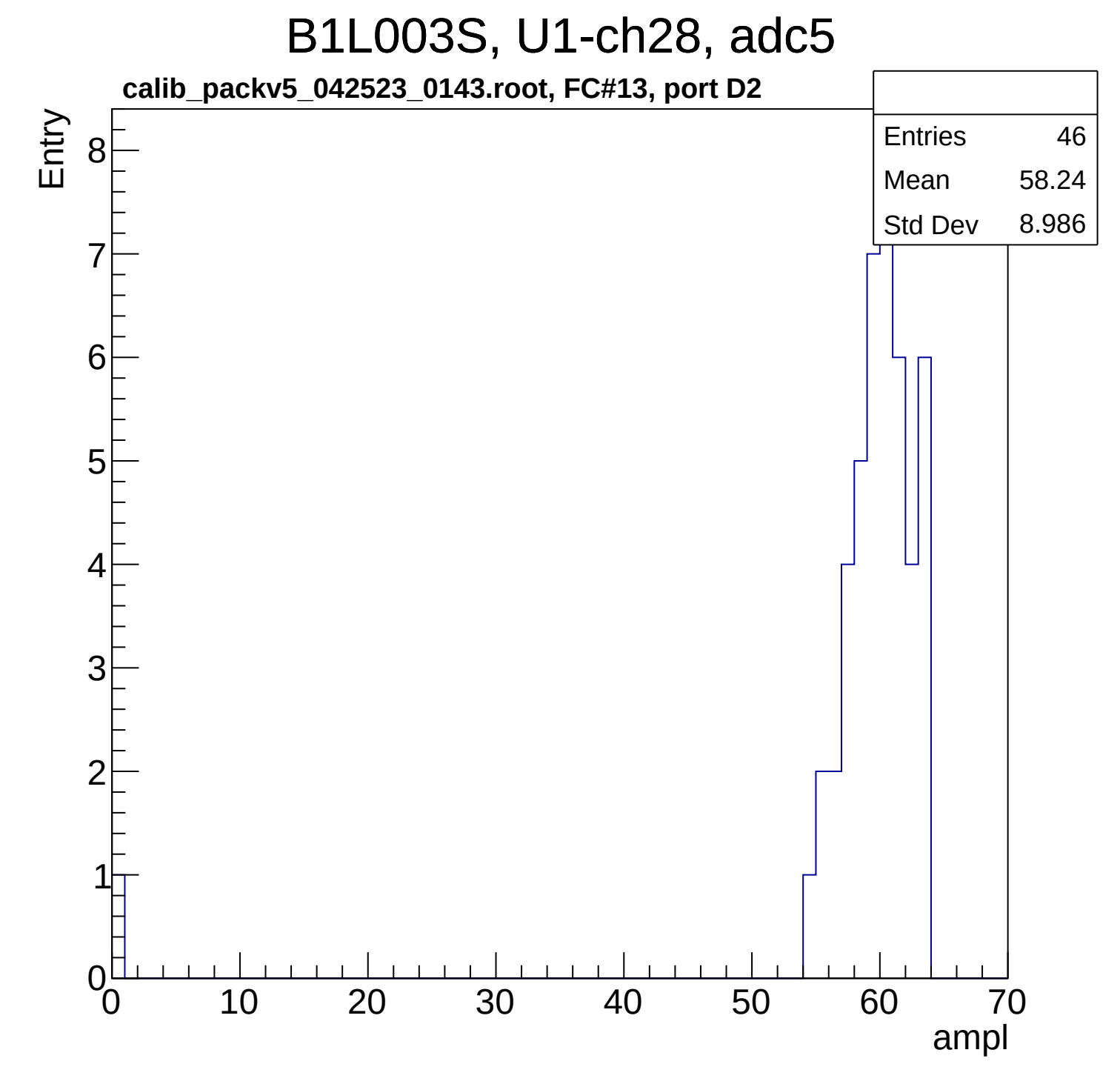

B1L003S, U1-ch28, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 28.48
Std Dev 6.402
Gaus mean : 29.3515
Gaus Width: 3.4371
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch28, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.48
Std Dev 3.577
Gaus mean : 35.8540
Gaus Width: 3.7485
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch28, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.77
Std Dev 3.761
Gaus mean : 42.6752
Gaus Width: 3.8512
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch28, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 48.1
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch28, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.97
Std Dev 3.321
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch28, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.24
Std Dev 8.986
Entry
0
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2
3
4
5
6
7
8
0
10
20
30
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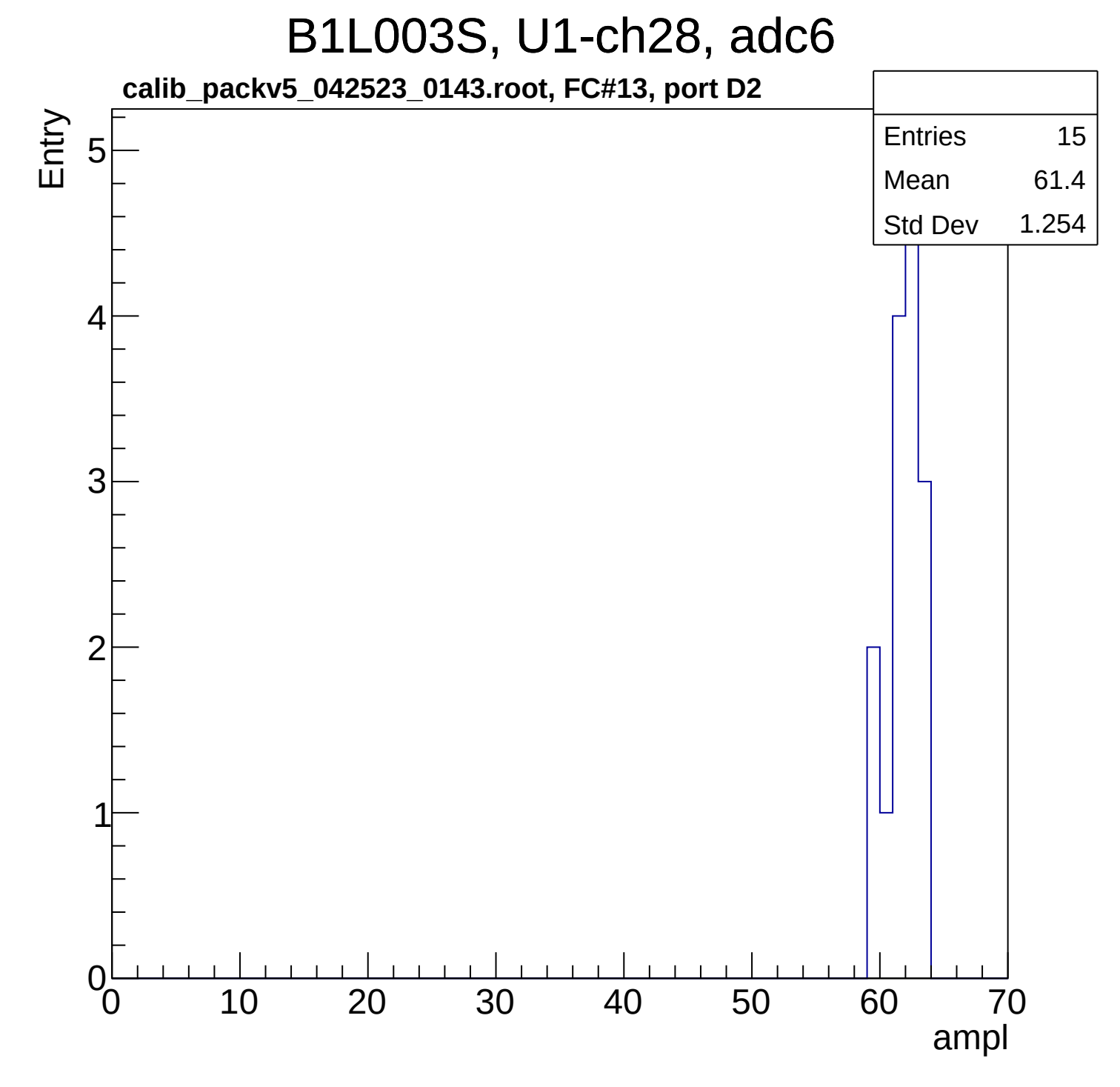
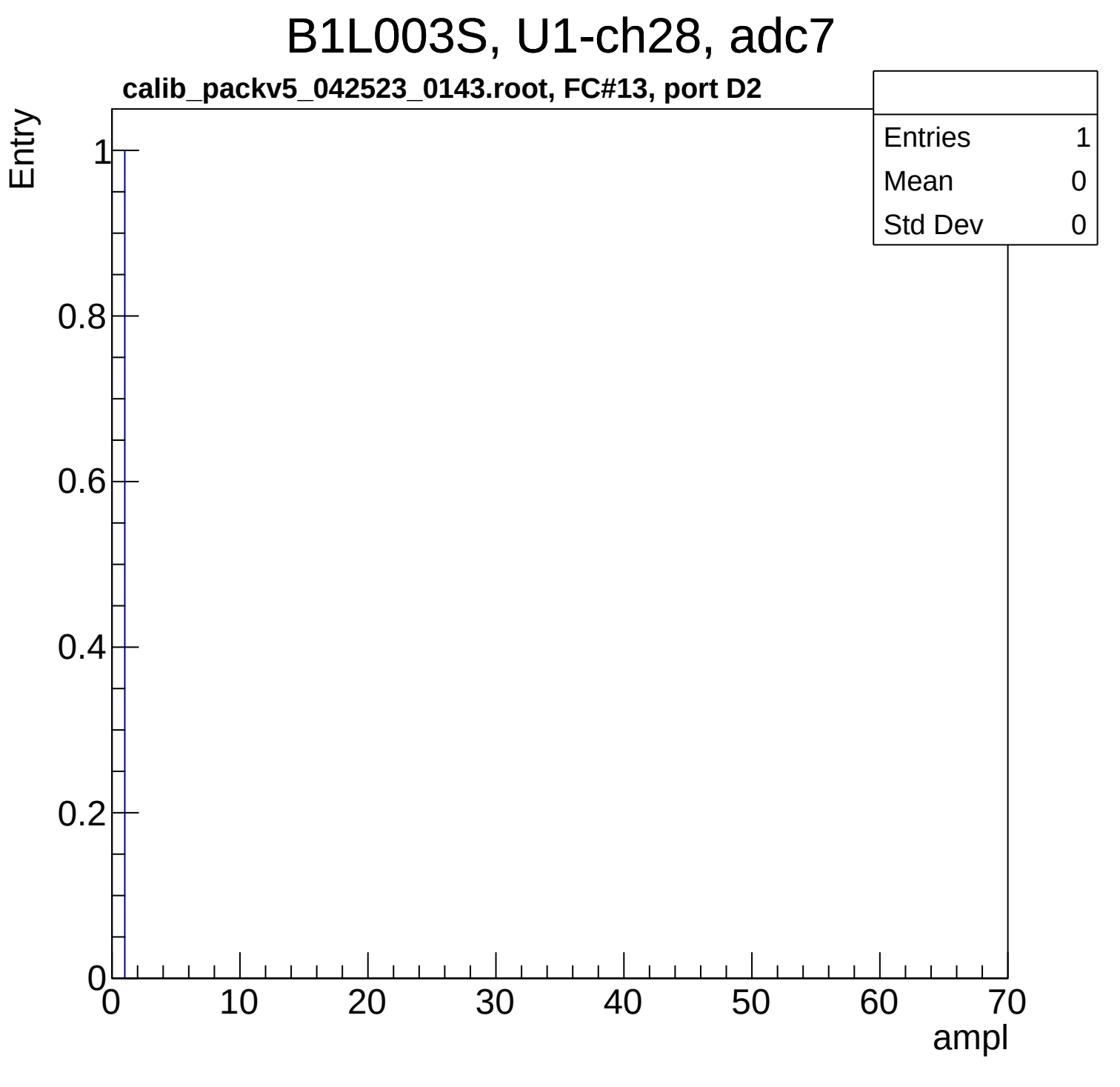
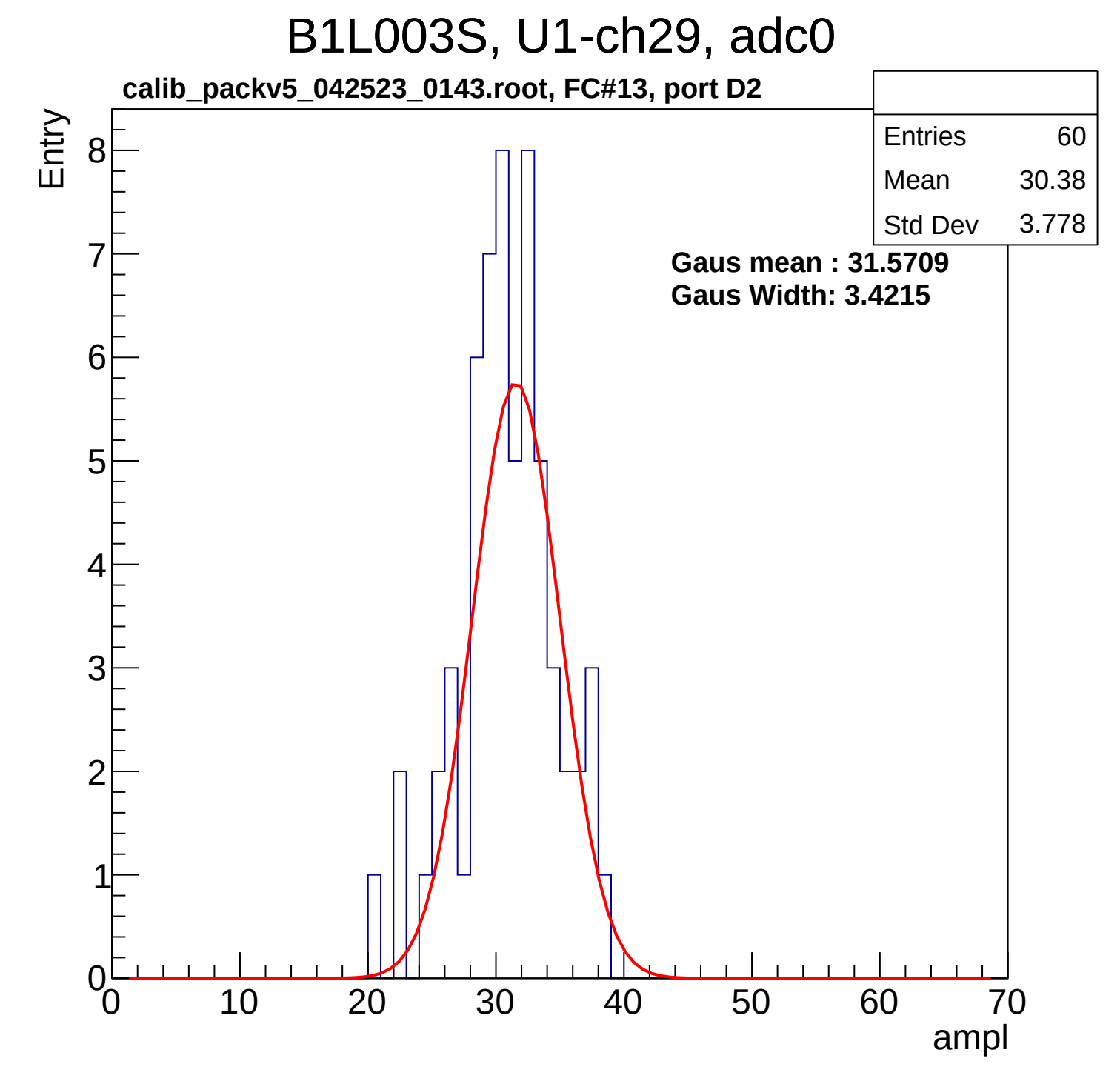
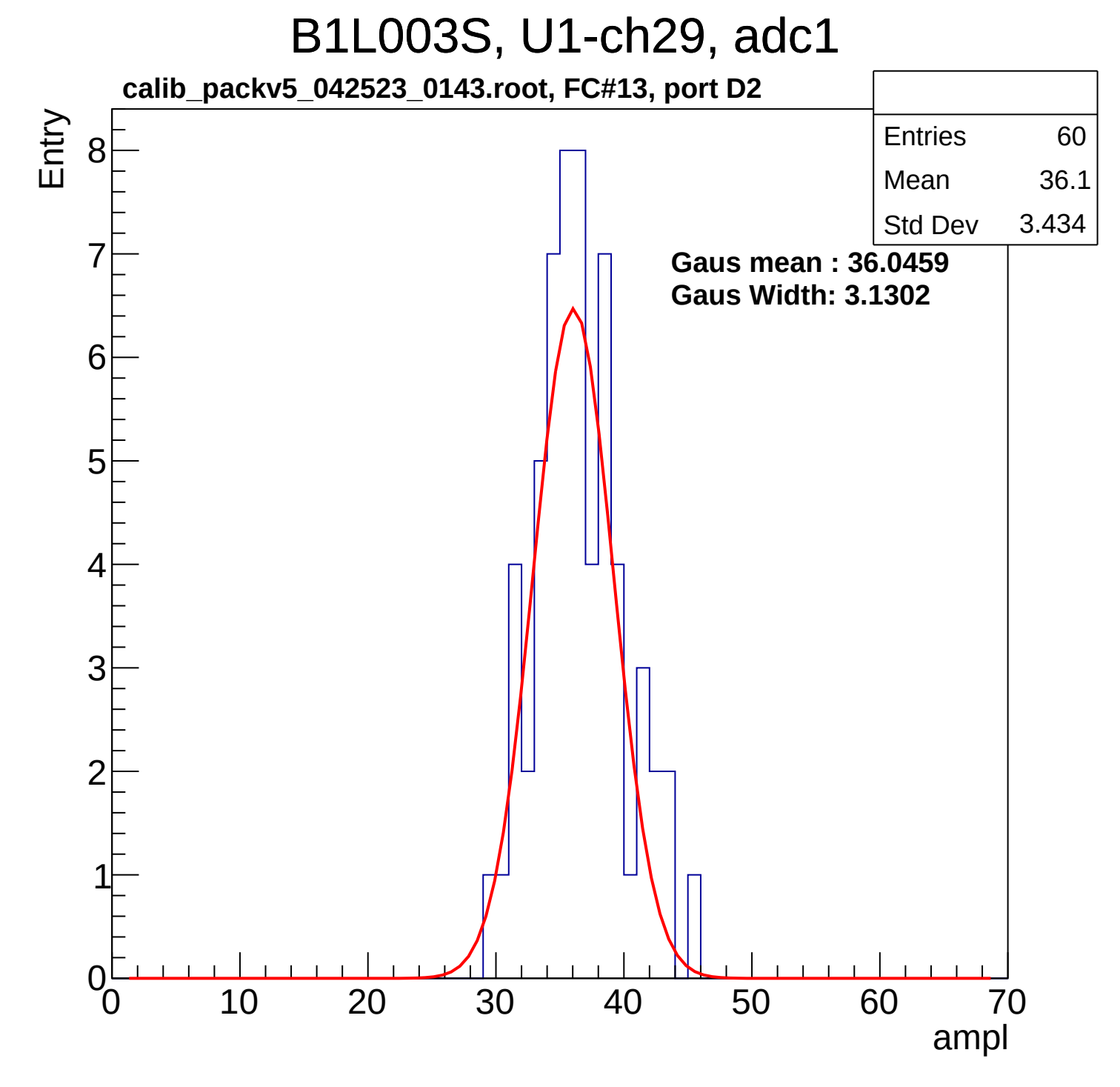
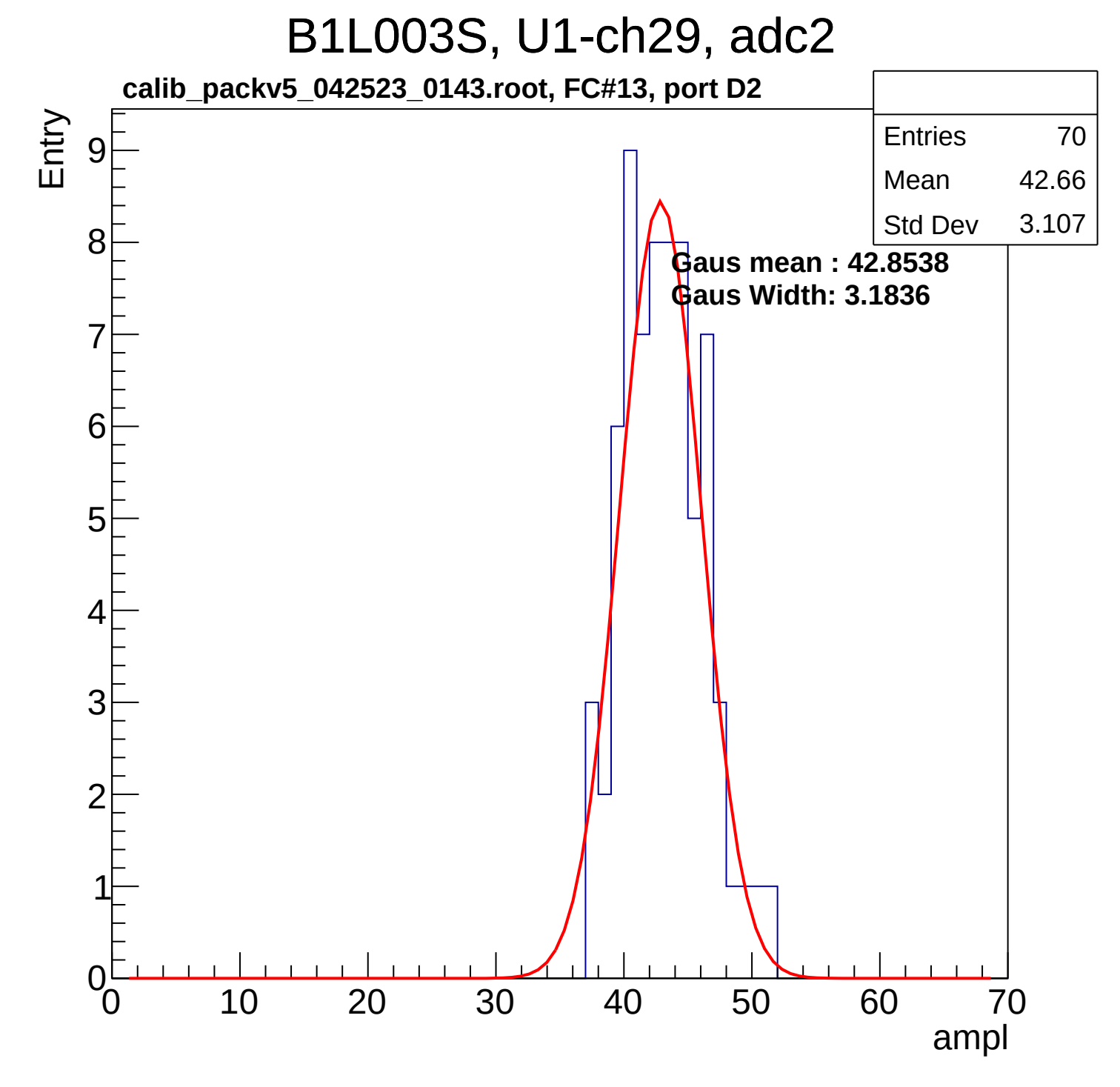
50
60
70
ampl

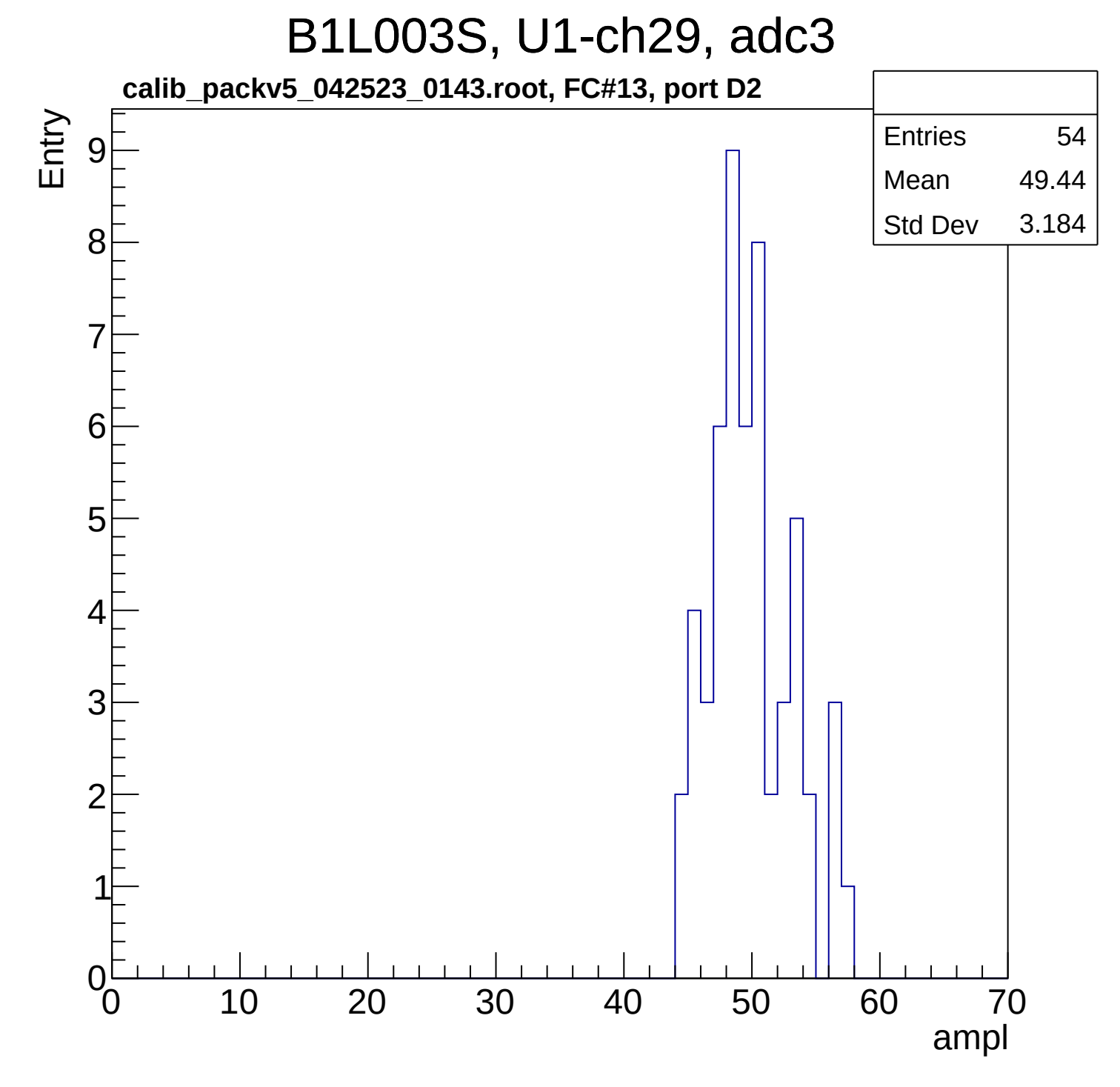

B1L003S, U1-ch28, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.4
Std Dev 1.254
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

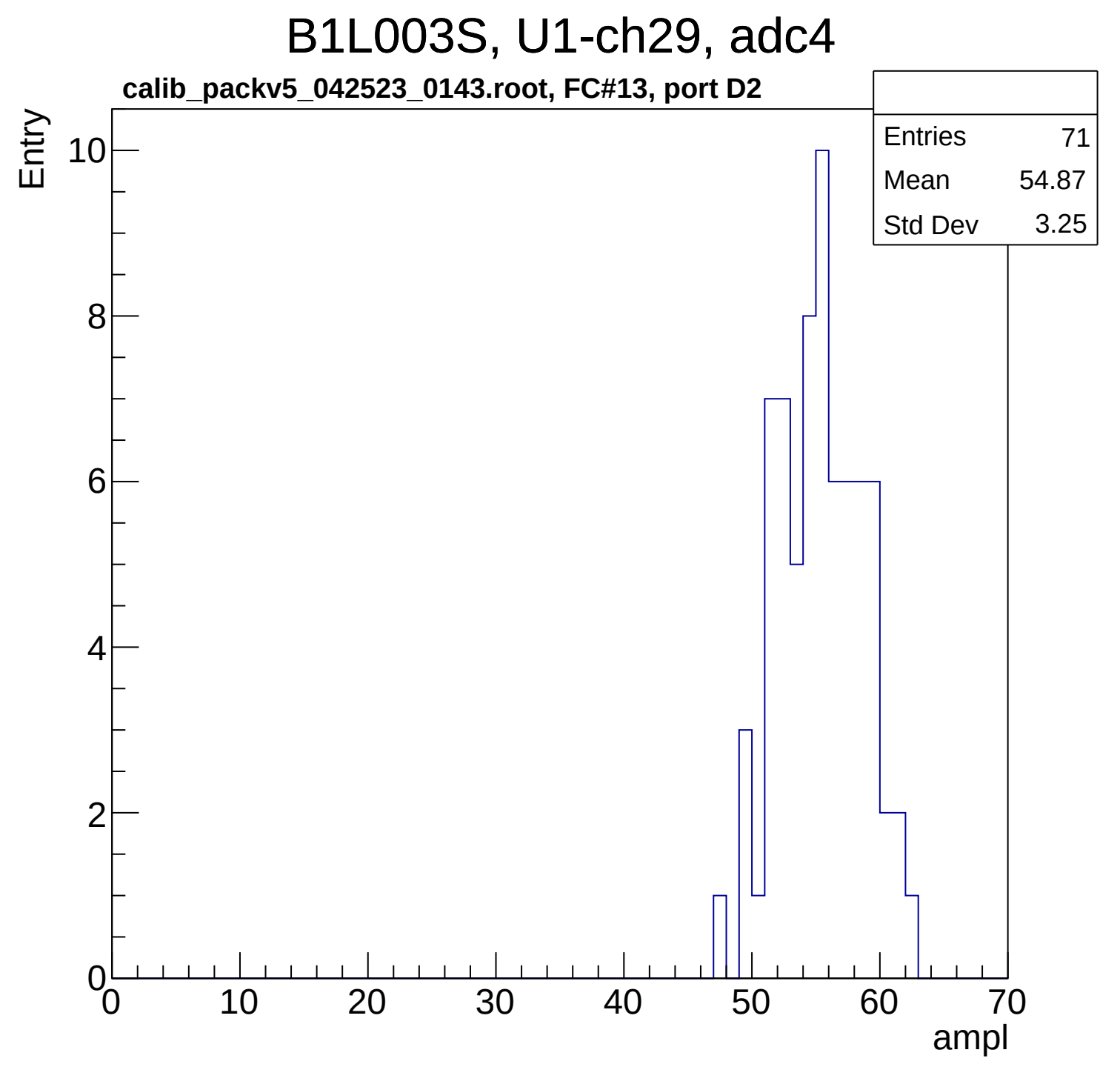

B1L003S, U1-ch28, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

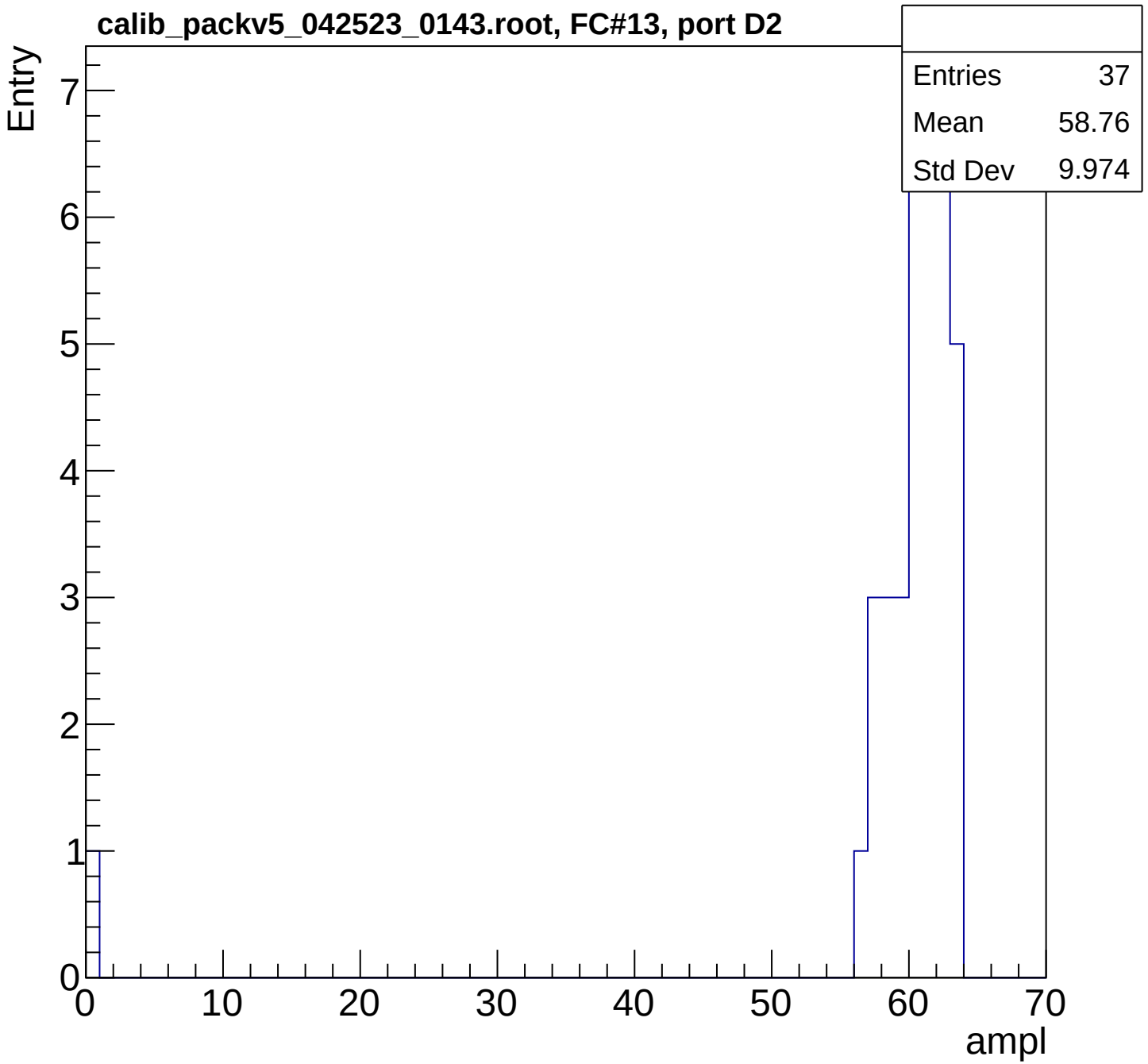

B1L003S, U1-ch29, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.38
Std Dev 3.778
Gaus mean : 31.5709
Gaus Width: 3.4215
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

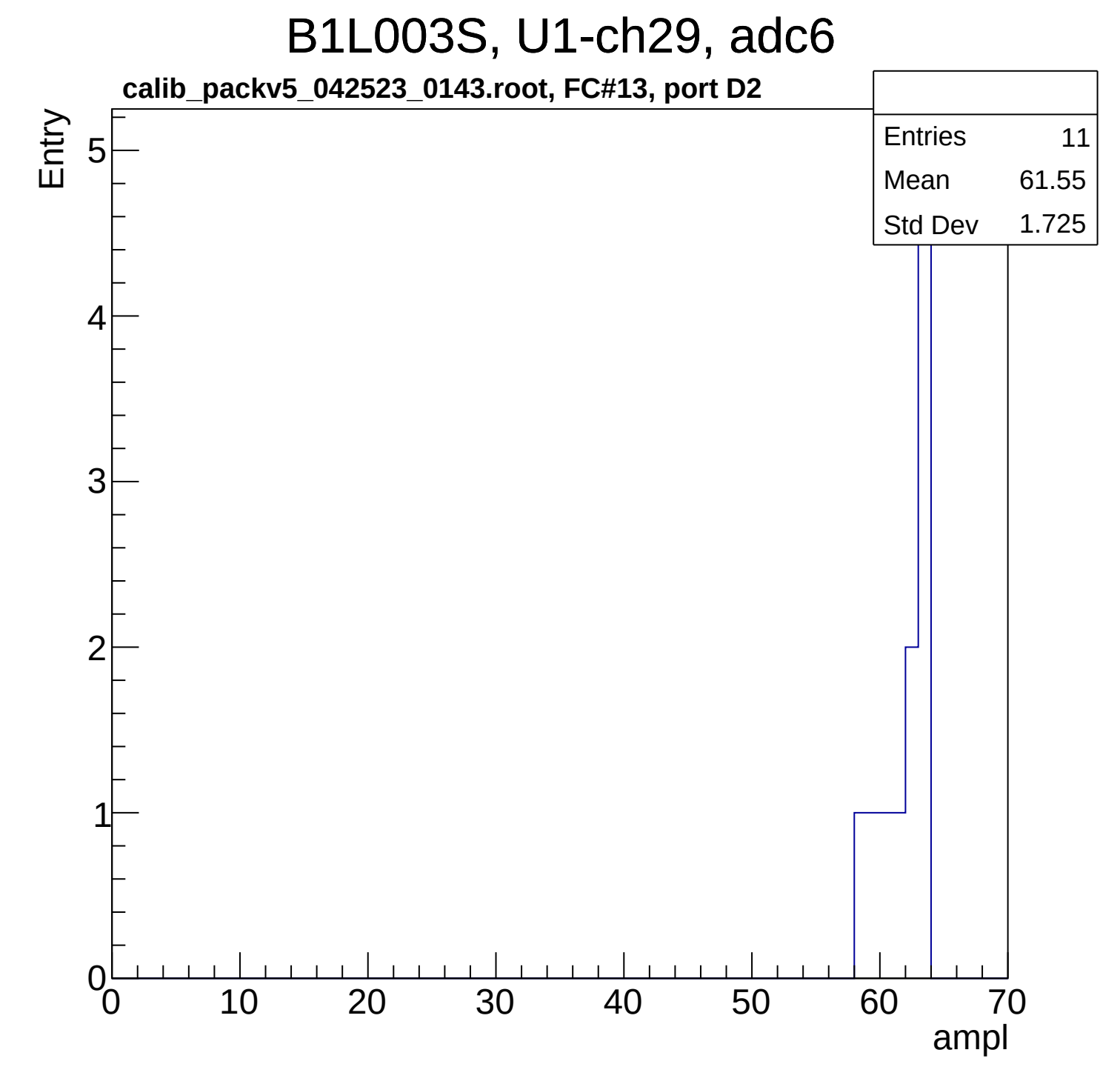

B1L003S, U1-ch29, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.1
Std Dev 3.434
Gaus mean : 36.0459
Gaus Width: 3.1302
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

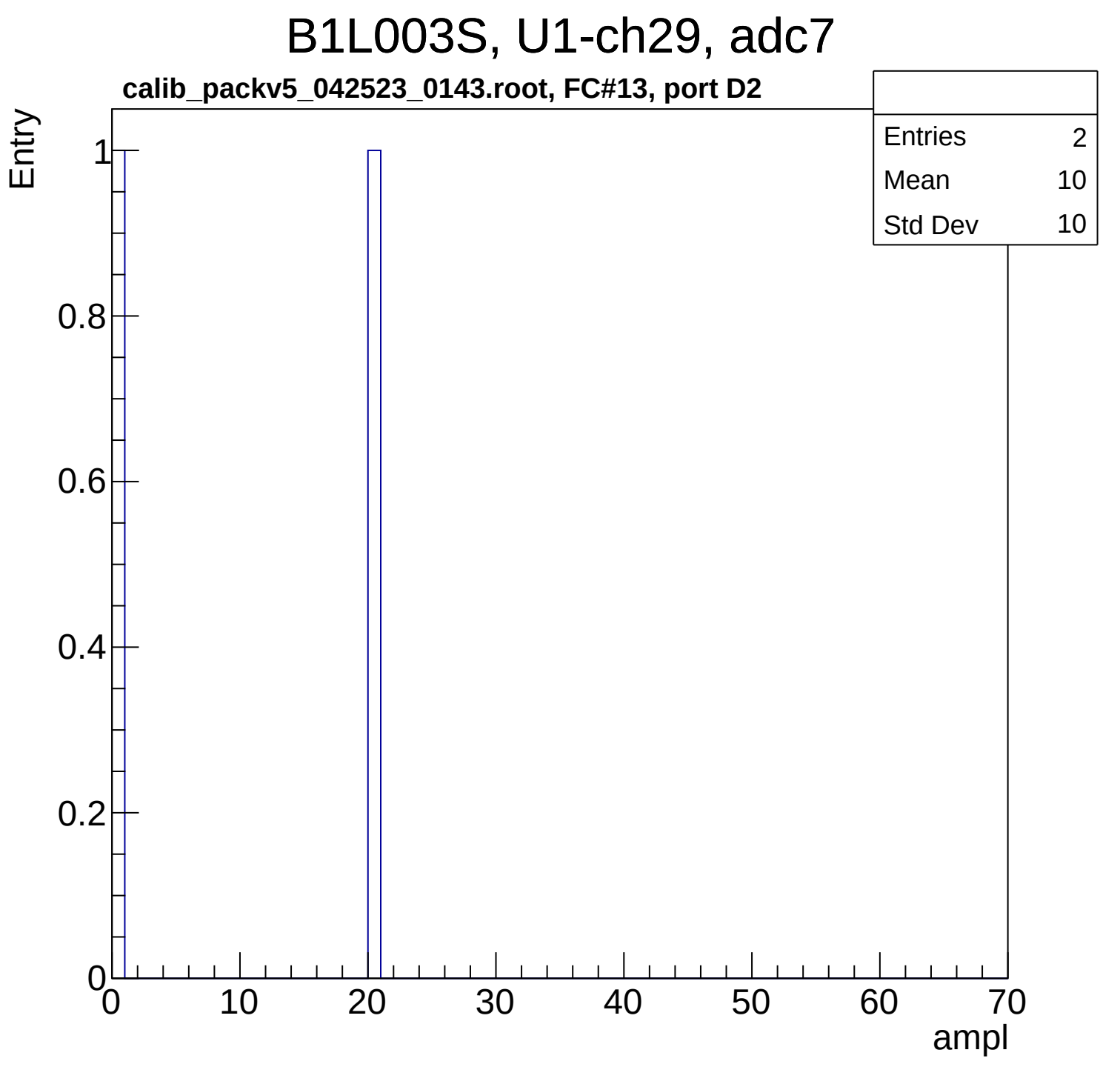

B1L003S, U1-ch29, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.66
Std Dev 3.107
Gaus mean : 42.8538
Gaus Width: 3.1836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

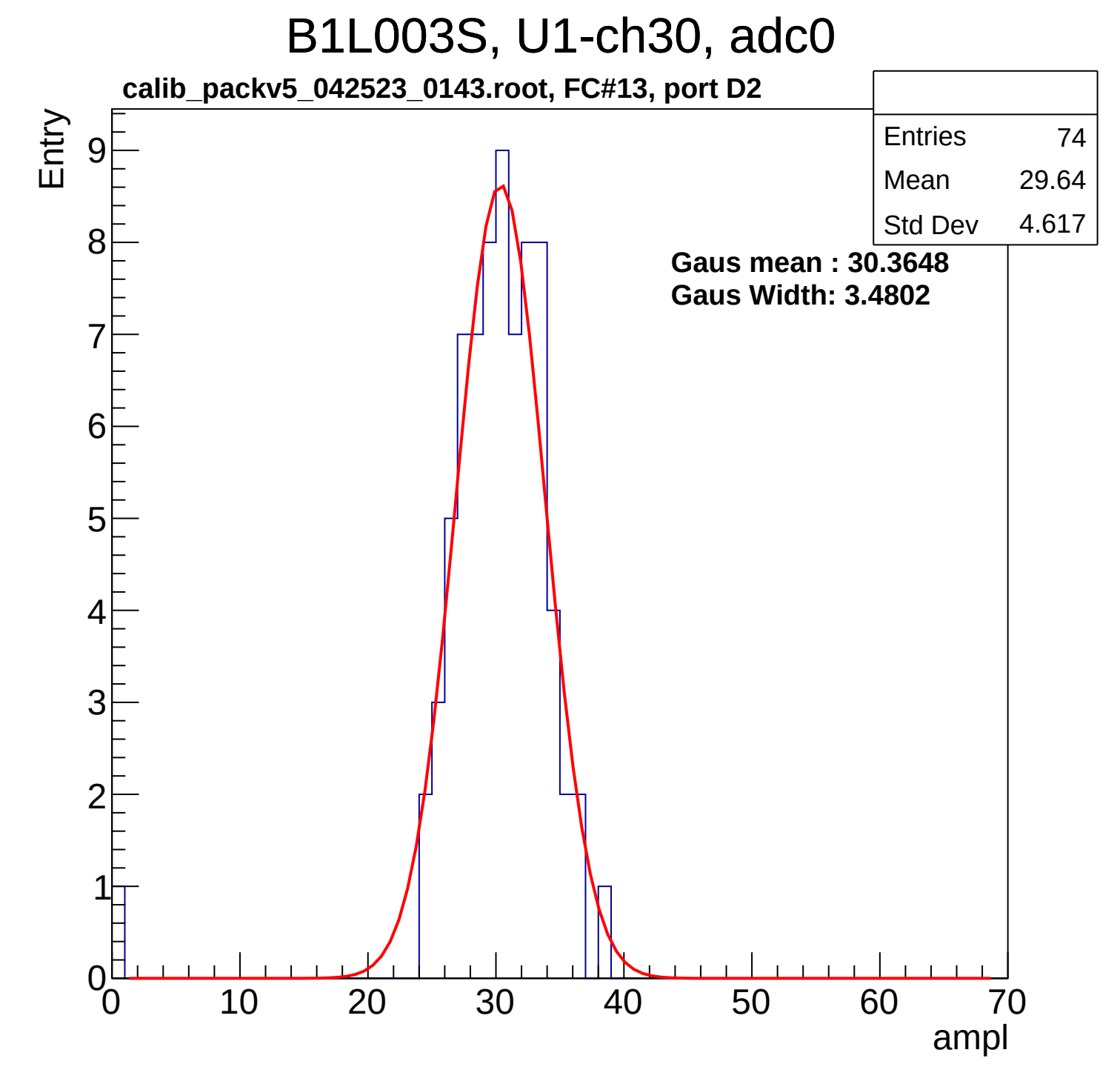

B1L003S, U1-ch29, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.44
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

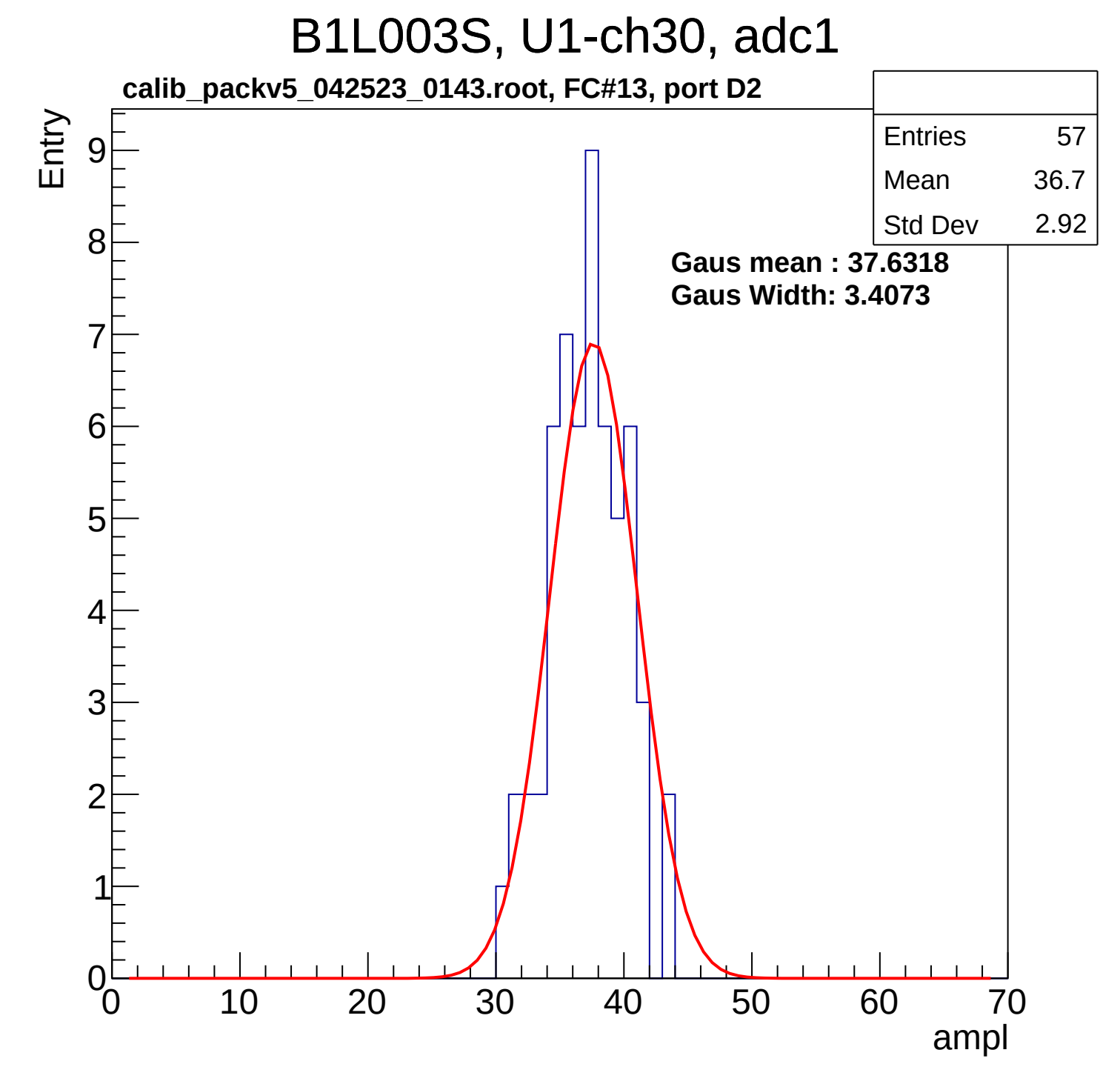

B1L003S, U1-ch29, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 54.87
Std Dev 3.25
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch29, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 58.76
Std Dev 9.974
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch29, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.55
Std Dev 1.725
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch29, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch30, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.64
Std Dev 4.617
Gaus mean : 30.3648
Gaus Width: 3.4802
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch30, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 36.7
Std Dev 2.92
Gaus mean : 37.6318
Gaus Width: 3.4073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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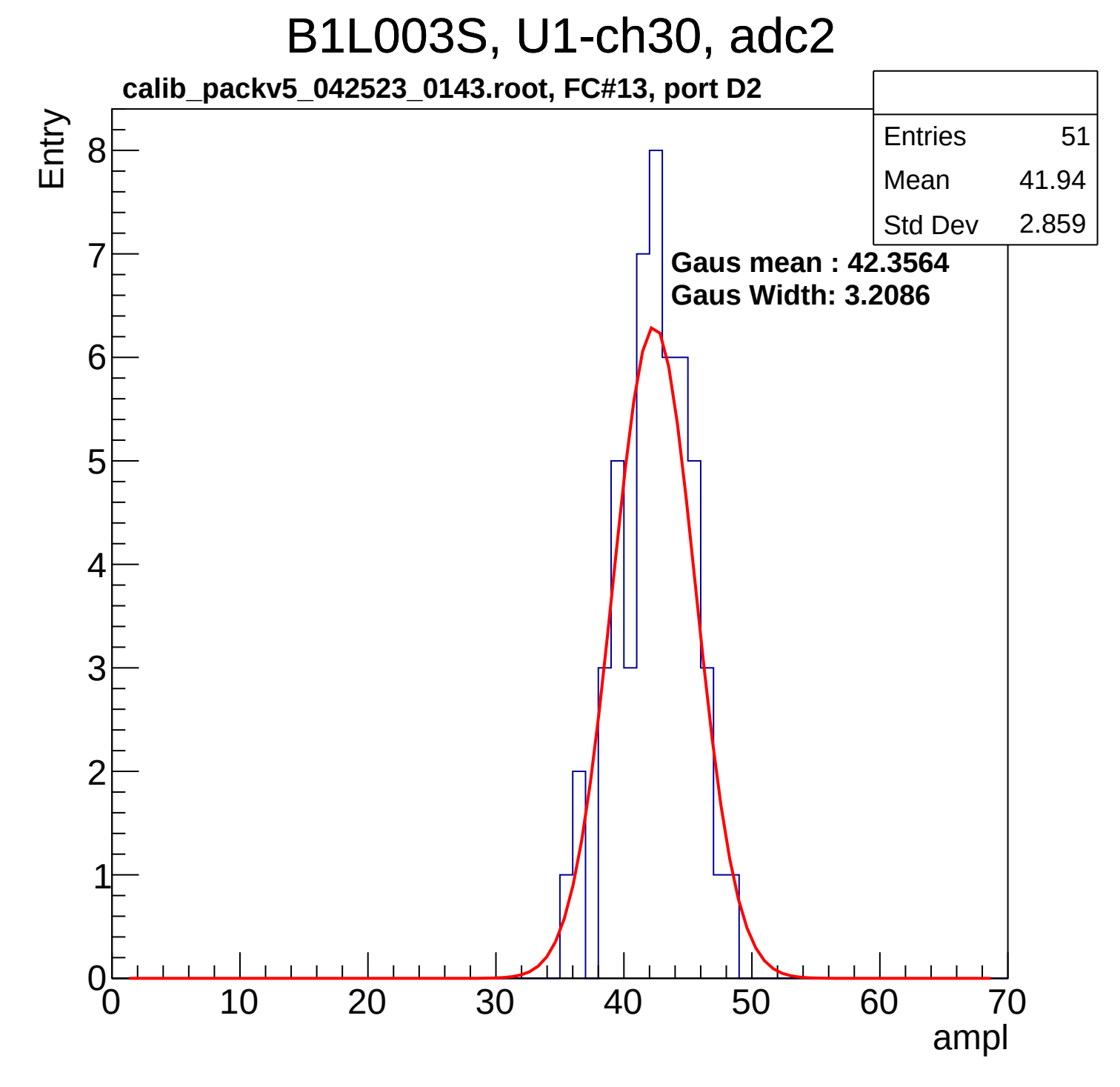
B1L003S, U1-ch30, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 41.94
Std Dev 2.859
Gaus mean : 42.3564
Gaus Width: 3.2086
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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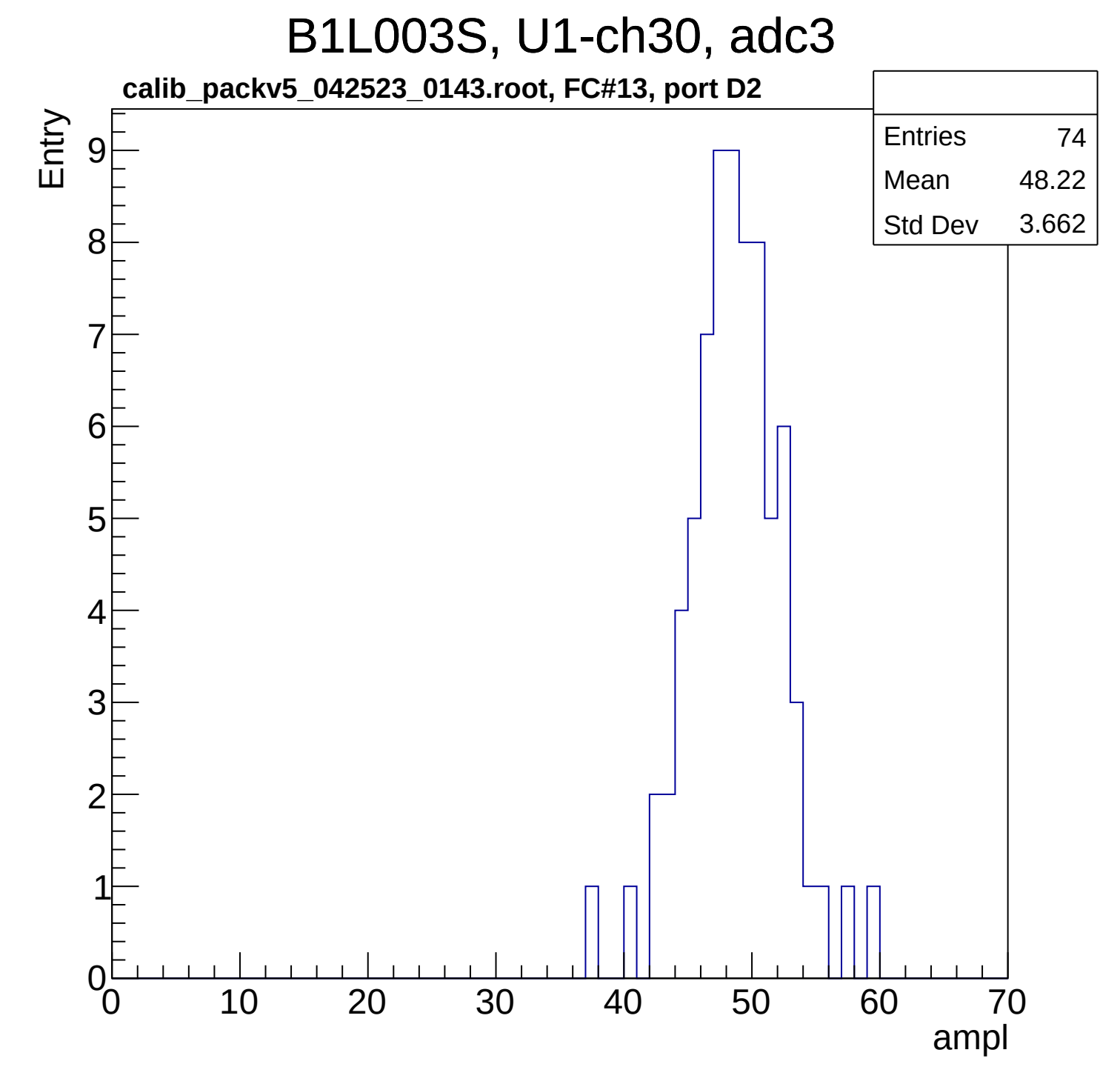
B1L003S, U1-ch30, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 48.22
Std Dev 3.662
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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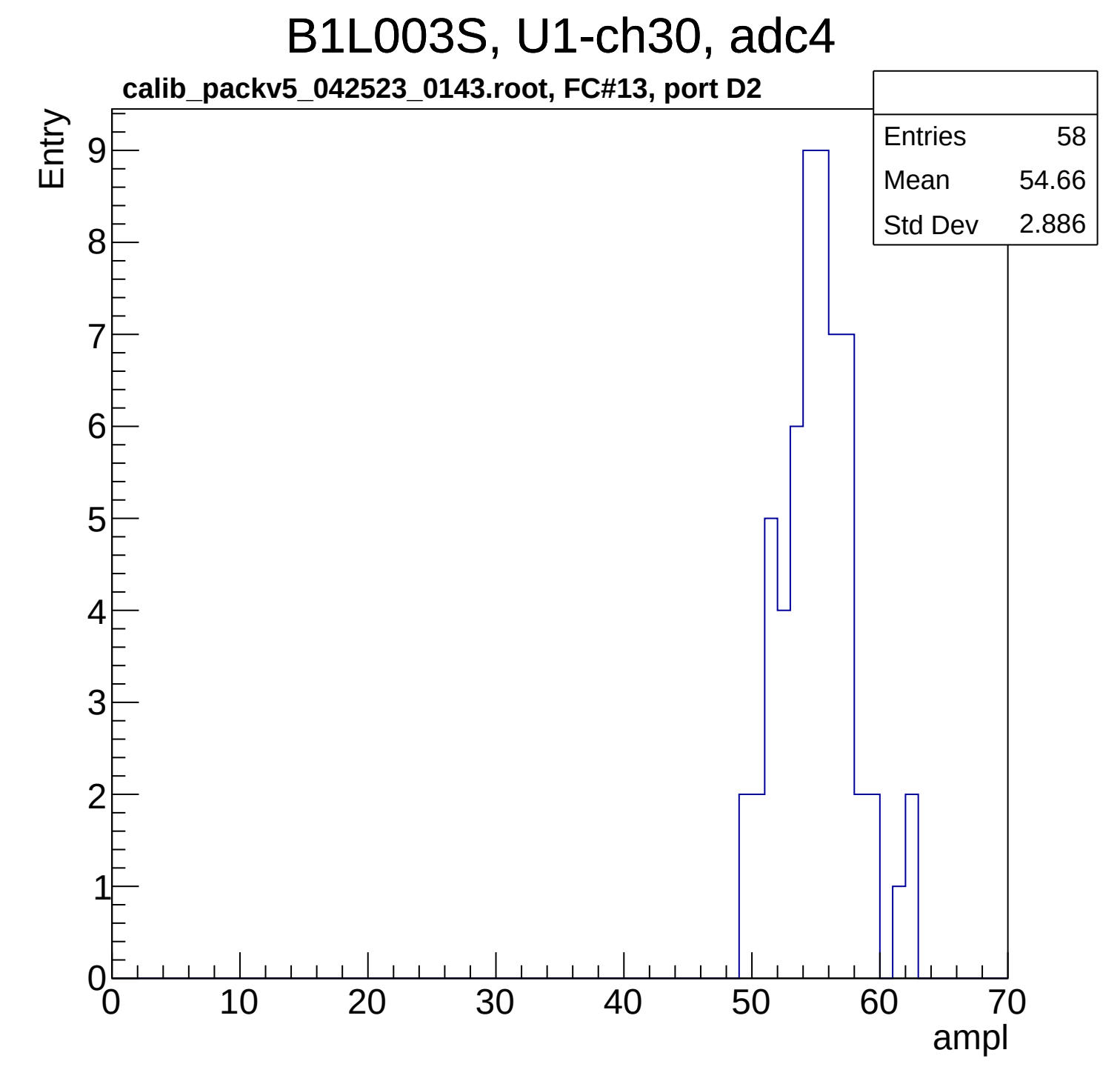
B1L003S, U1-ch30, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.66
Std Dev 2.886
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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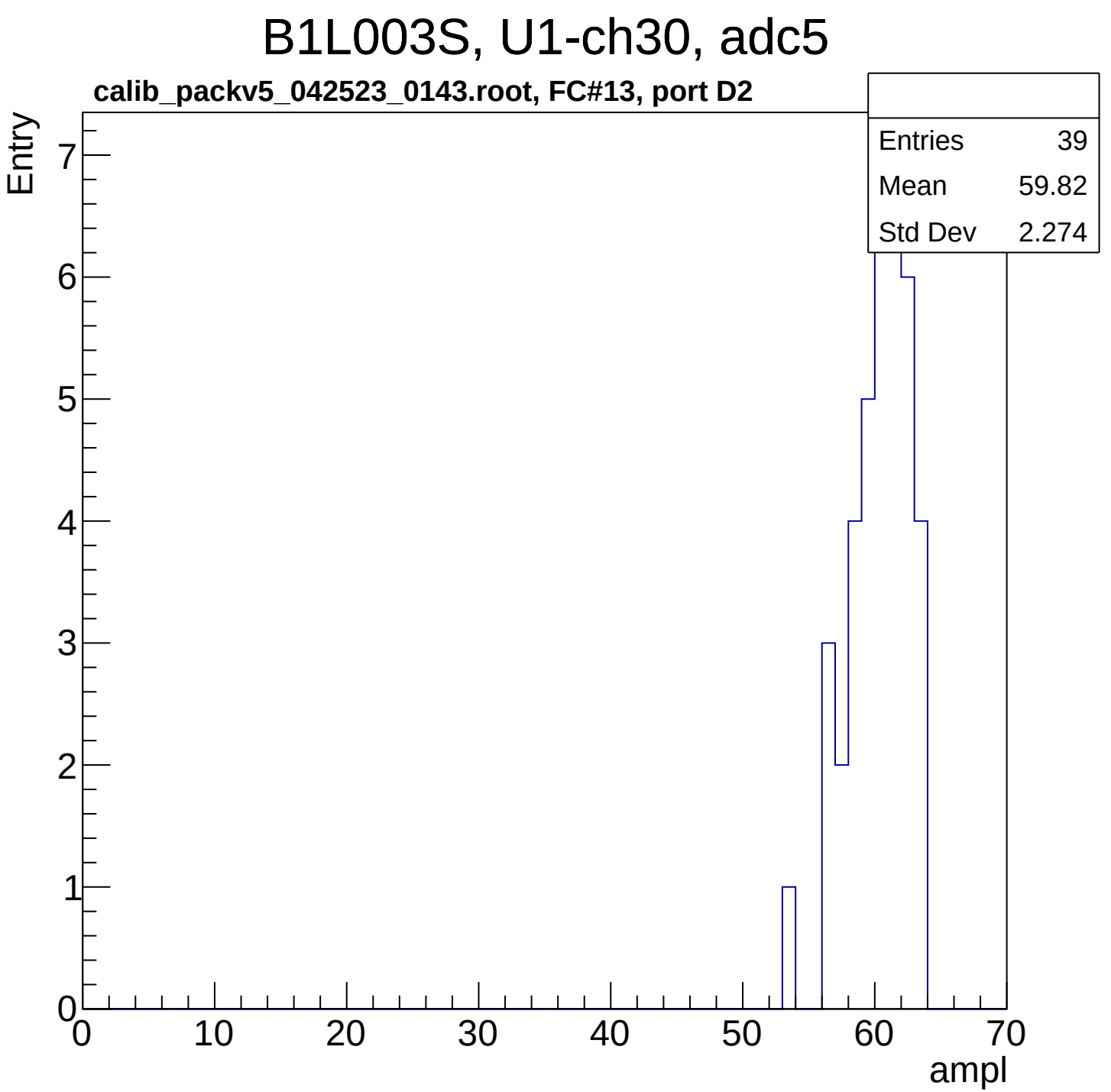
B1L003S, U1-ch30, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.82
Std Dev 2.274
Entry
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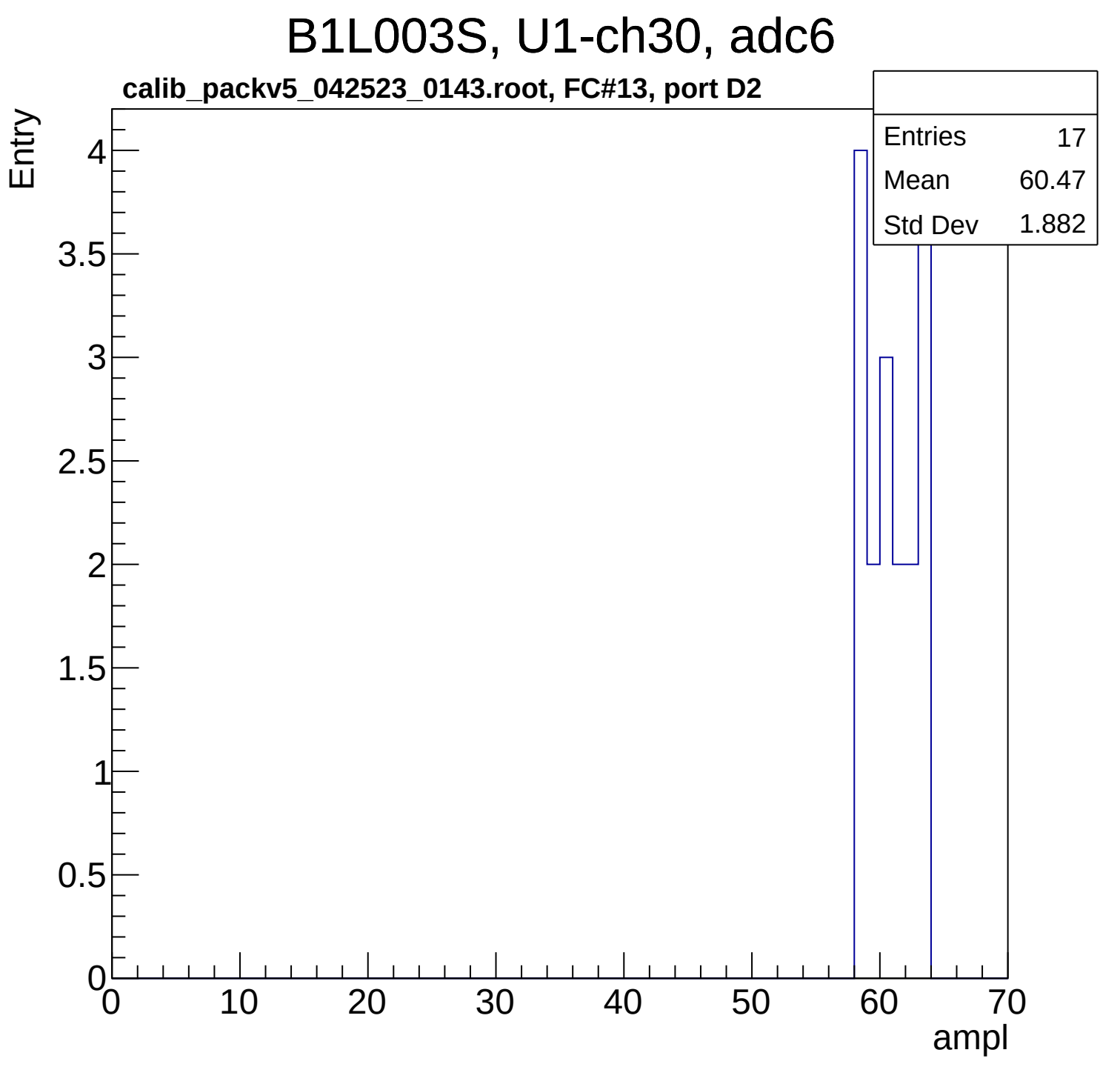
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch30, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 60.47
Std Dev 1.882
Entry
ampl
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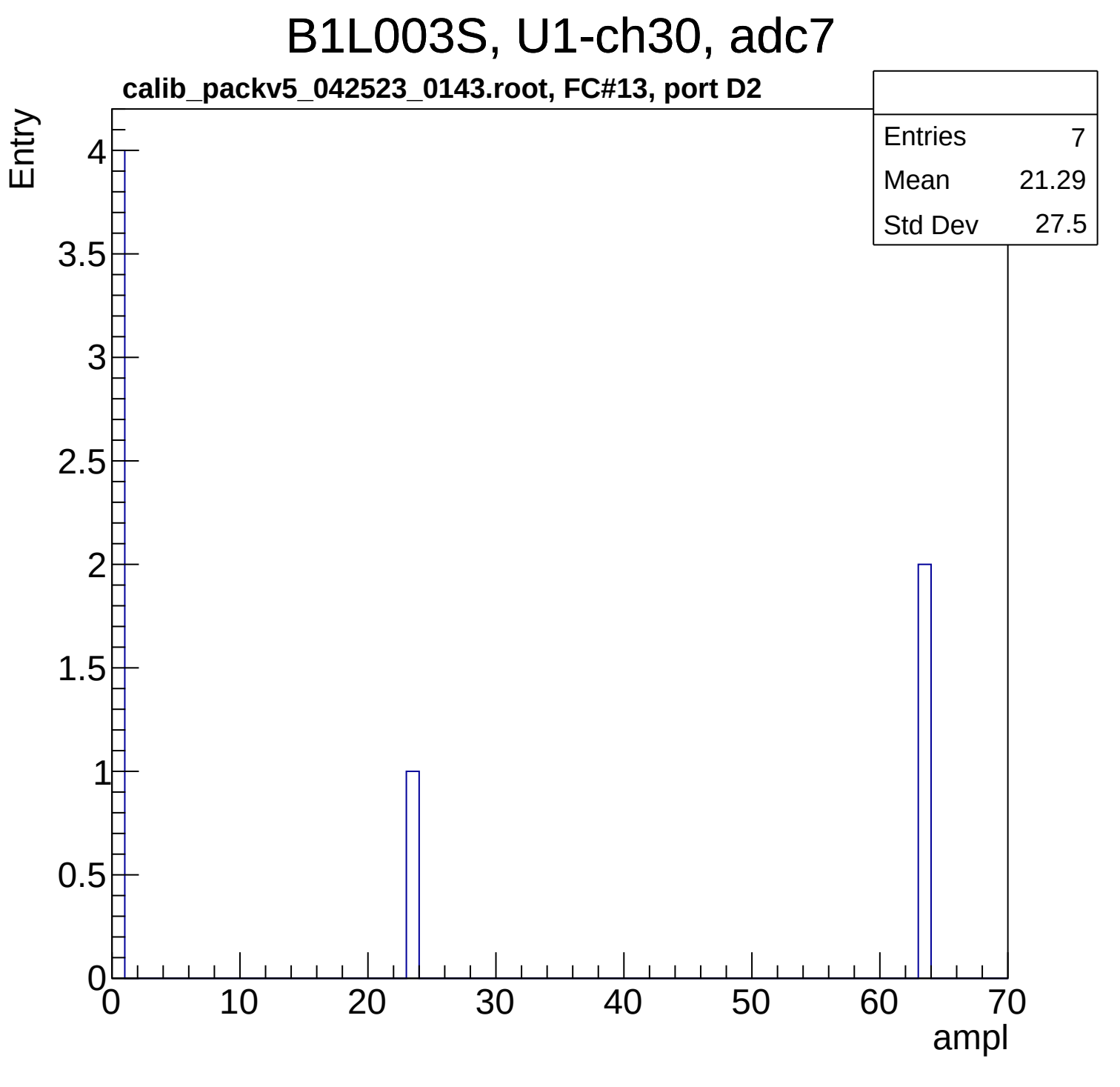
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch30, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 21.29
Std Dev 27.5
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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40
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60
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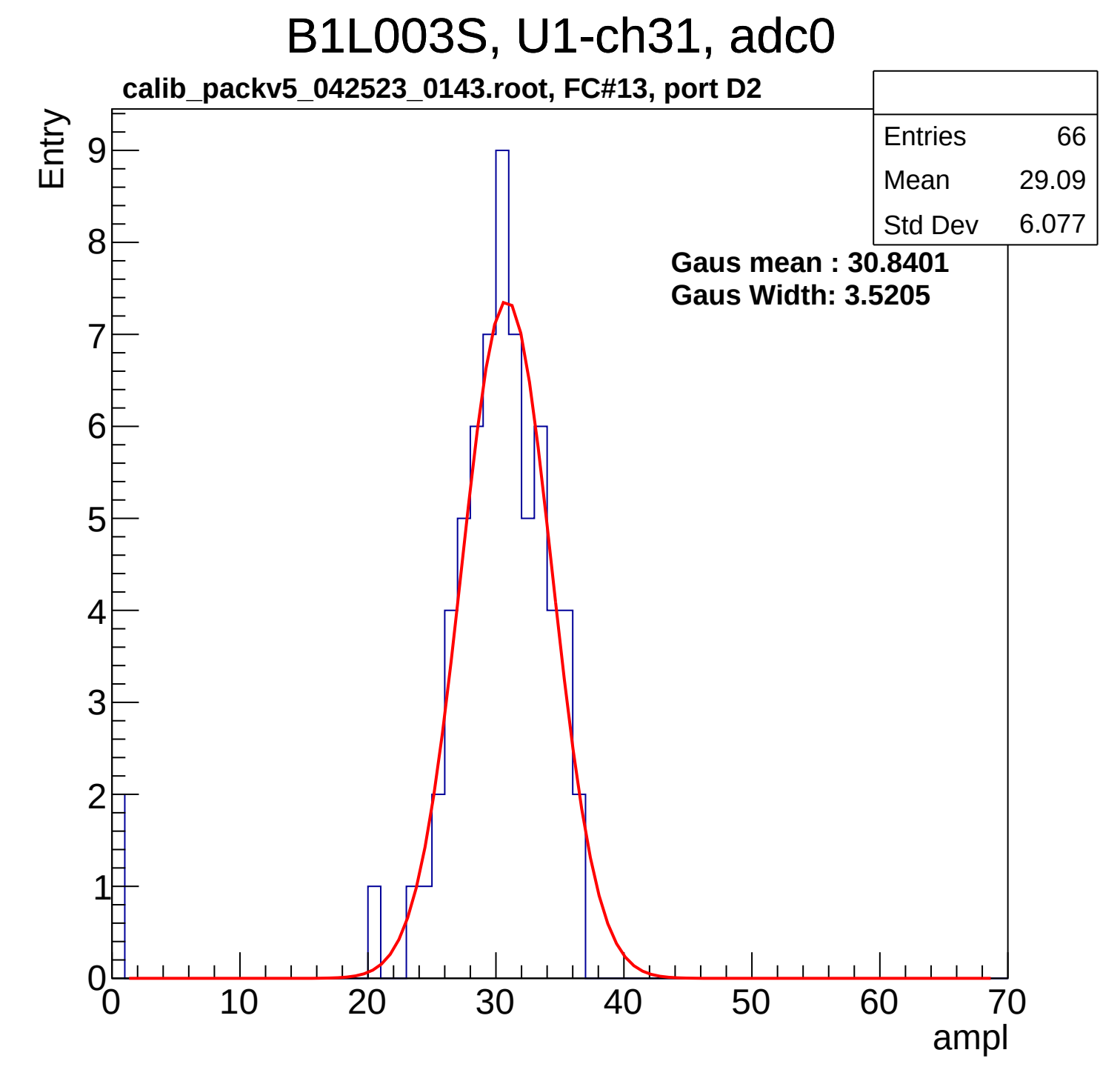
B1L003S, U1-ch31, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.09
Std Dev 6.077
Gaus mean : 30.8401
Gaus Width: 3.5205
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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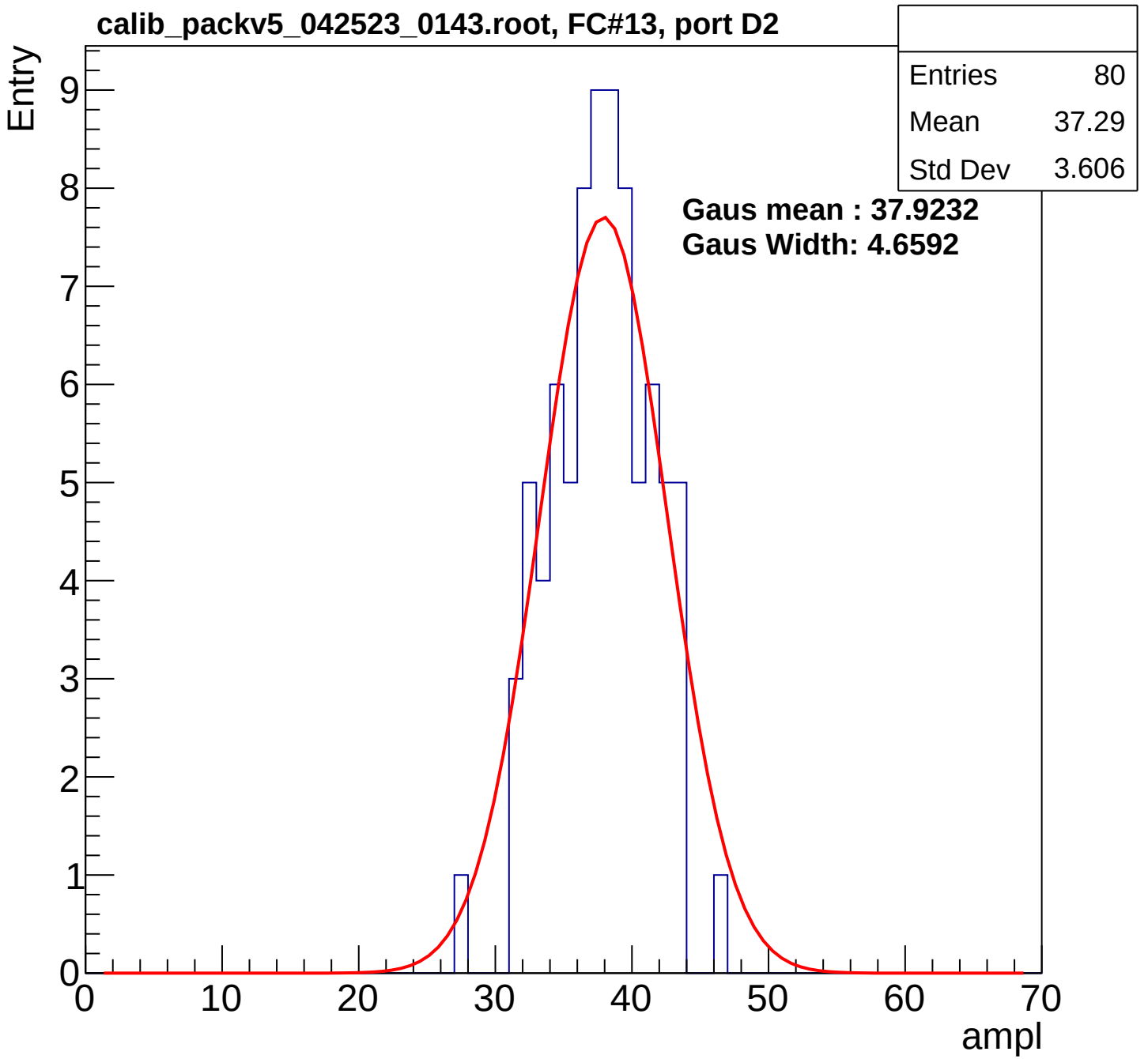
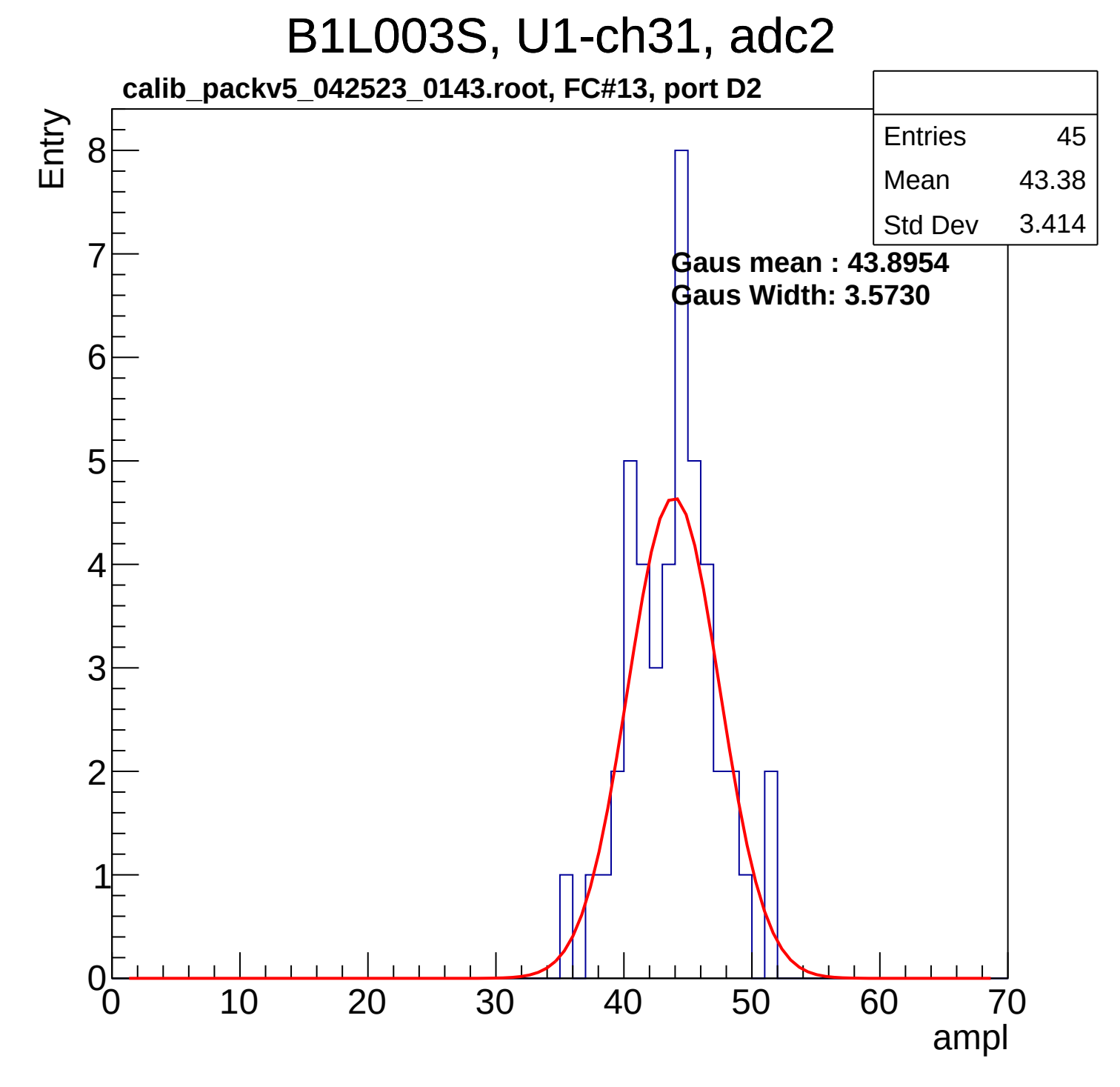

B1L003S, U1-ch31, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 37.29
Std Dev 3.606
Gaus mean : 37.9232
Gaus Width: 4.6592
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch31, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 43.38
Std Dev 3.414
Gaus mean : 43.8954
Gaus Width: 3.5730
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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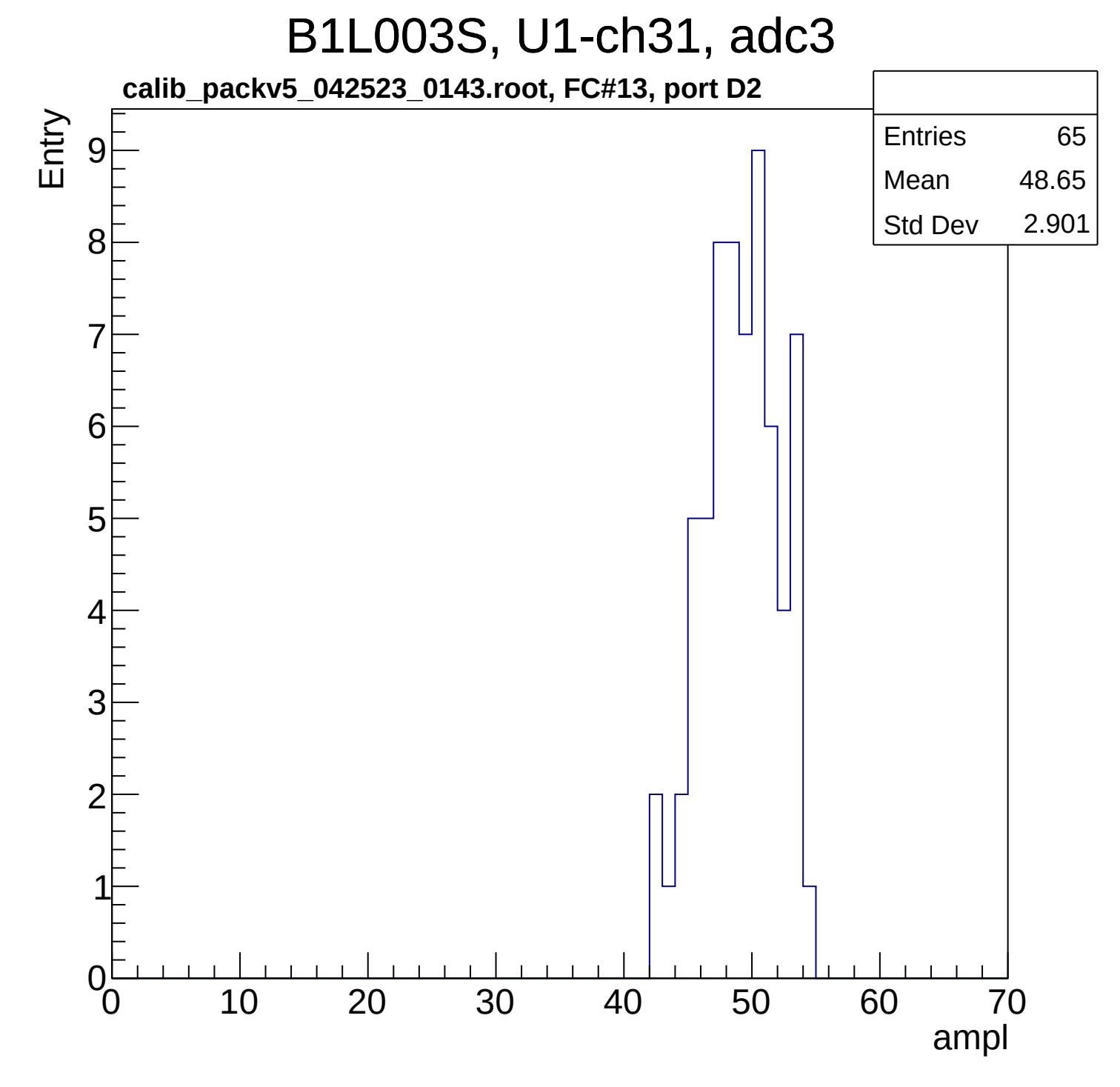
B1L003S, U1-ch31, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.65
Std Dev 2.901
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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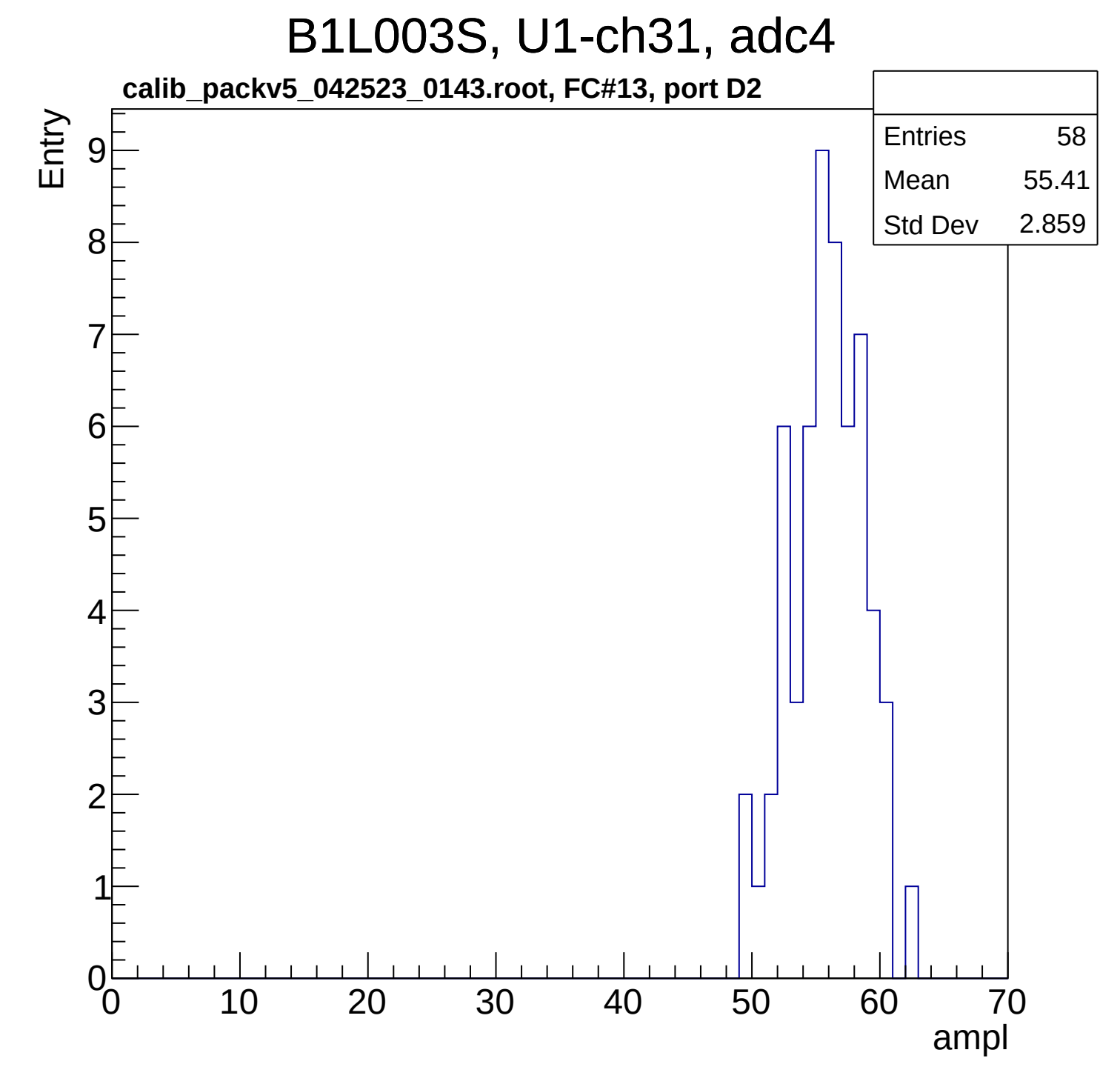
B1L003S, U1-ch31, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.41
Std Dev 2.859
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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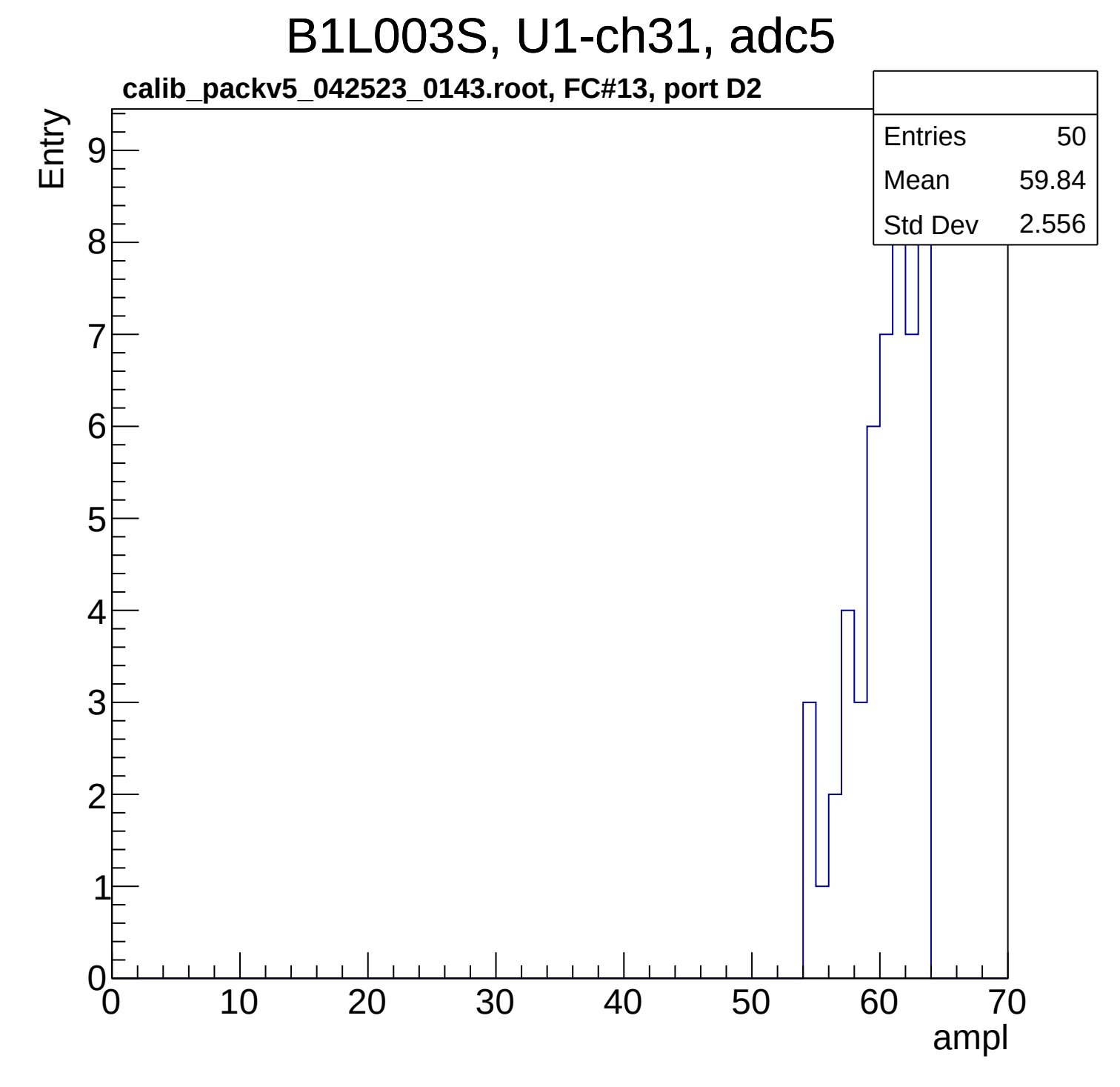
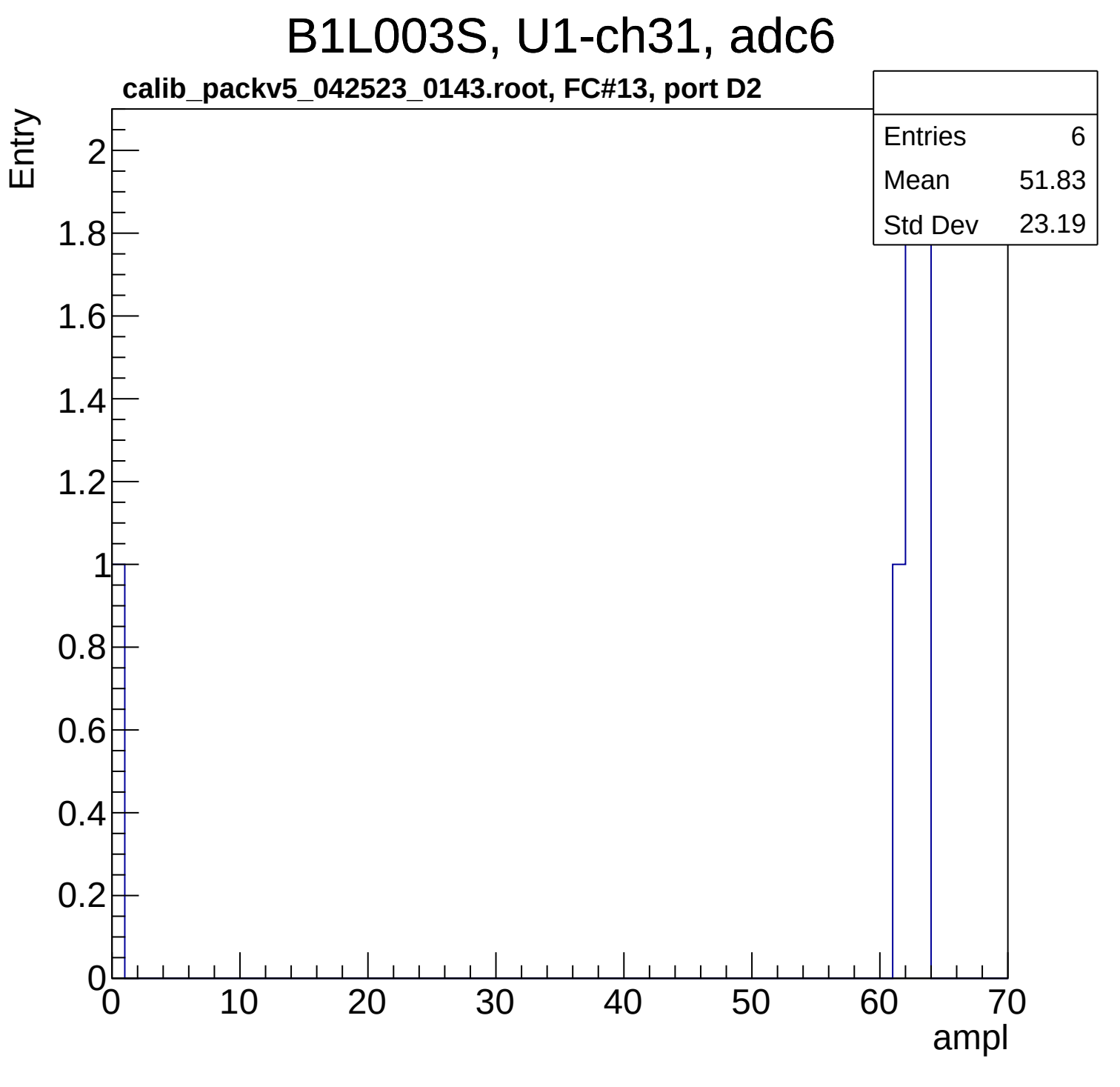

B1L003S, U1-ch31, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.84
Std Dev 2.556
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch31, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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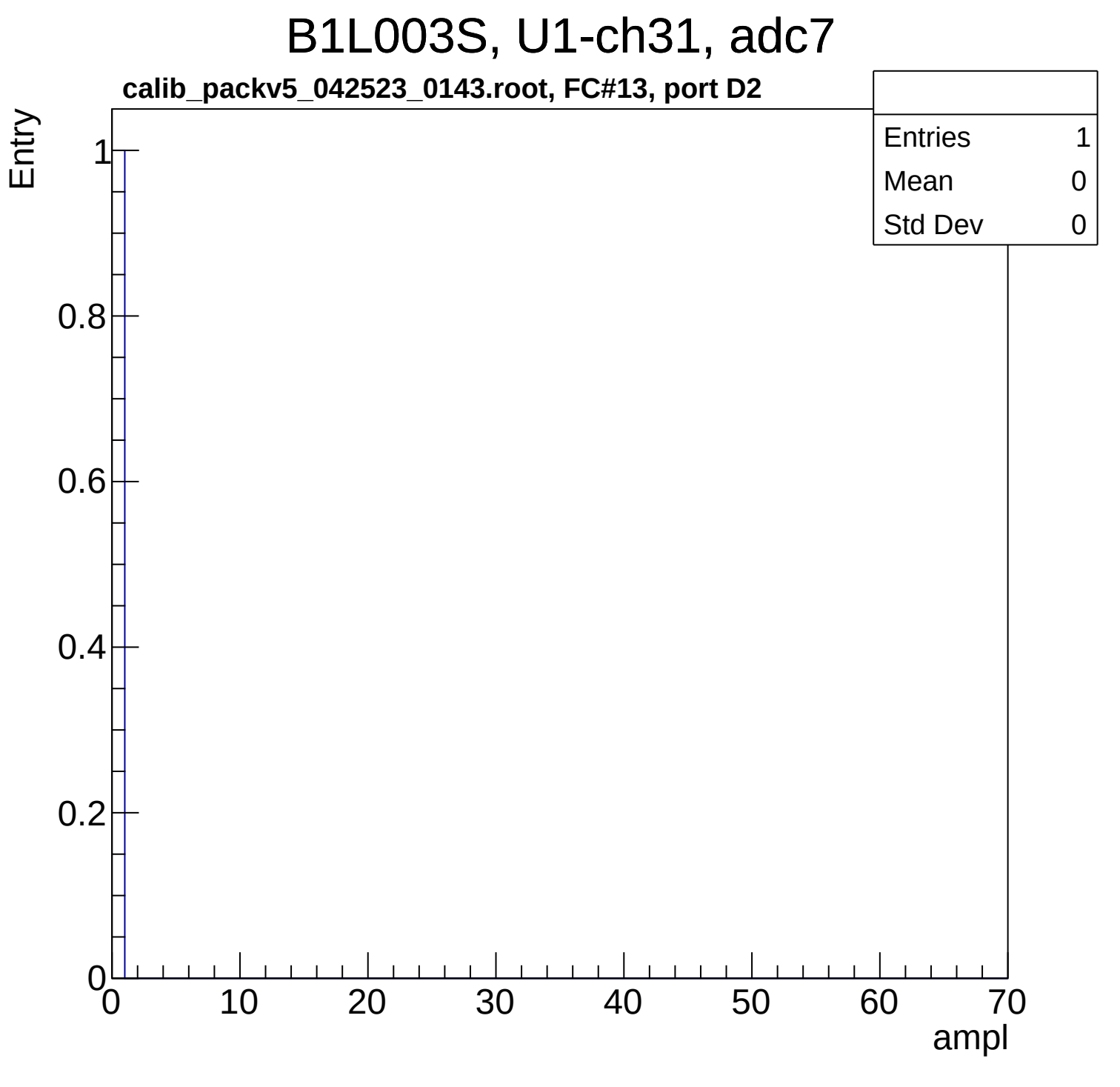
B1L003S, U1-ch31, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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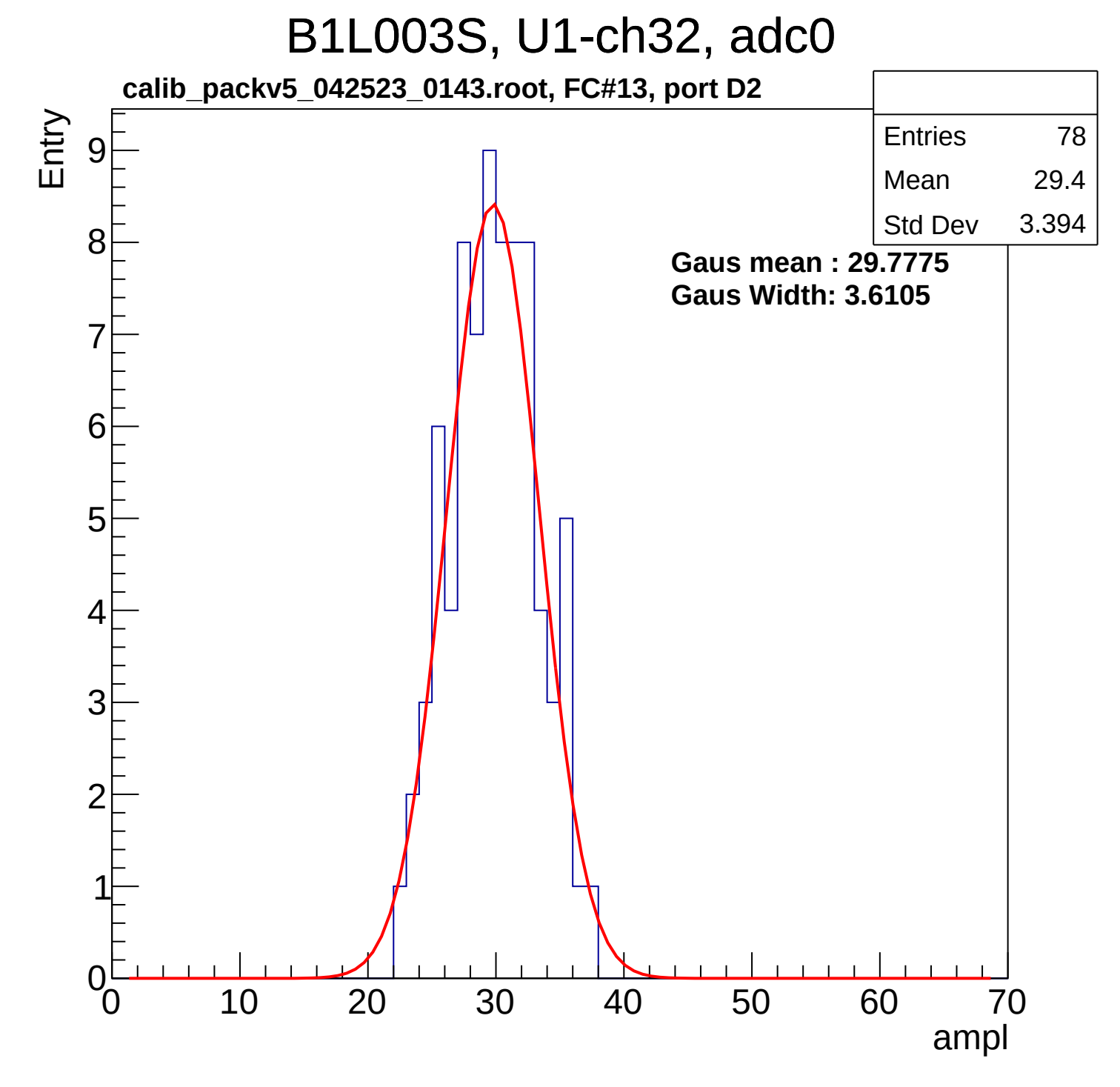
50
60
70
ampl


B1L003S, U1-ch32, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.4
Std Dev 3.394
Gaus mean : 29.7775
Gaus Width: 3.6105
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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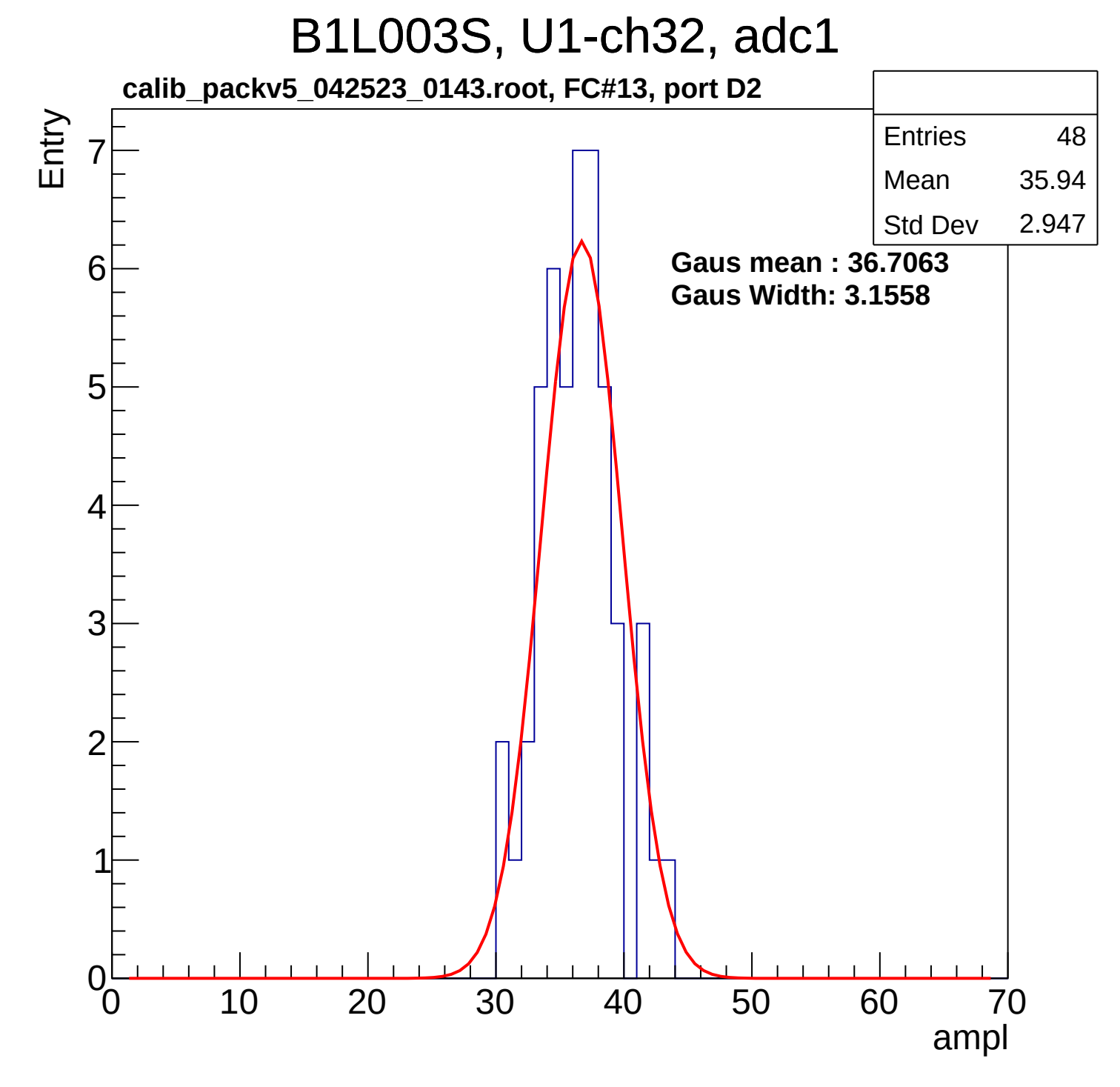
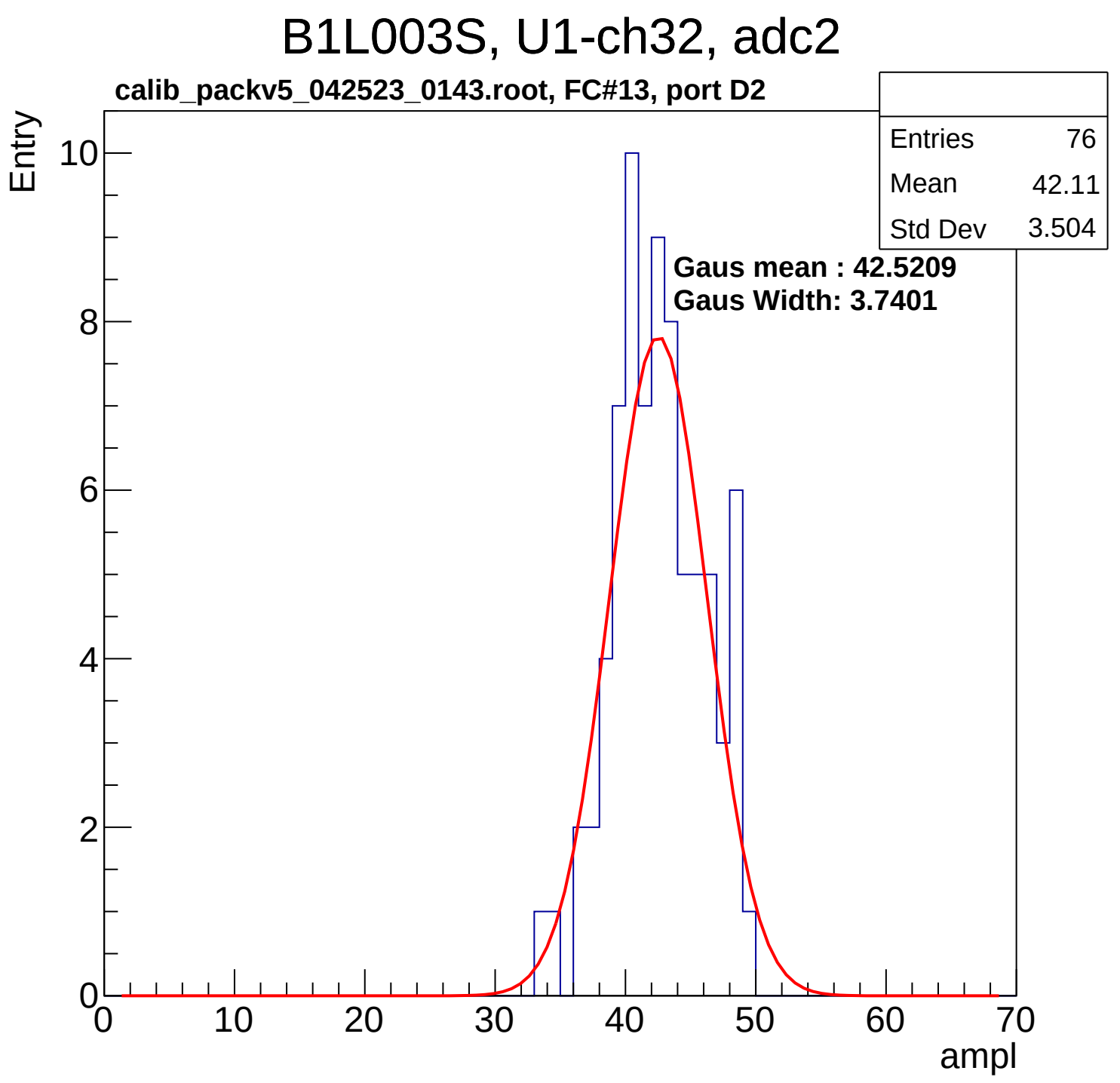
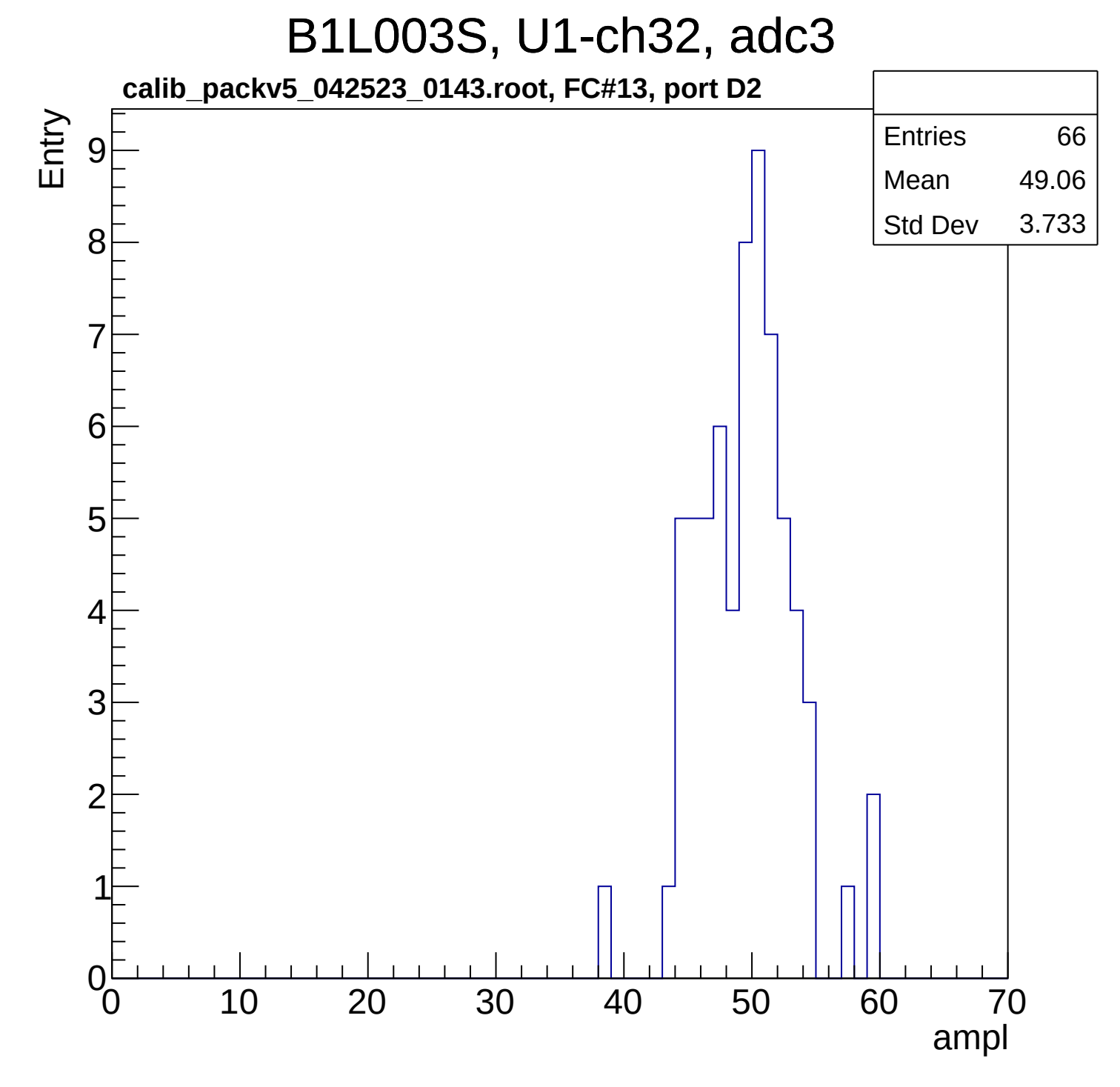

B1L003S, U1-ch32, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 35.94
Std Dev 2.947
Gaus mean : 36.7063
Gaus Width: 3.1558
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch32, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 42.11
Std Dev 3.504
Gaus mean : 42.5209
Gaus Width: 3.7401
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch32, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 49.06
Std Dev 3.733
Entry
ampl
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2
3
4
5
6
7
8
9
0
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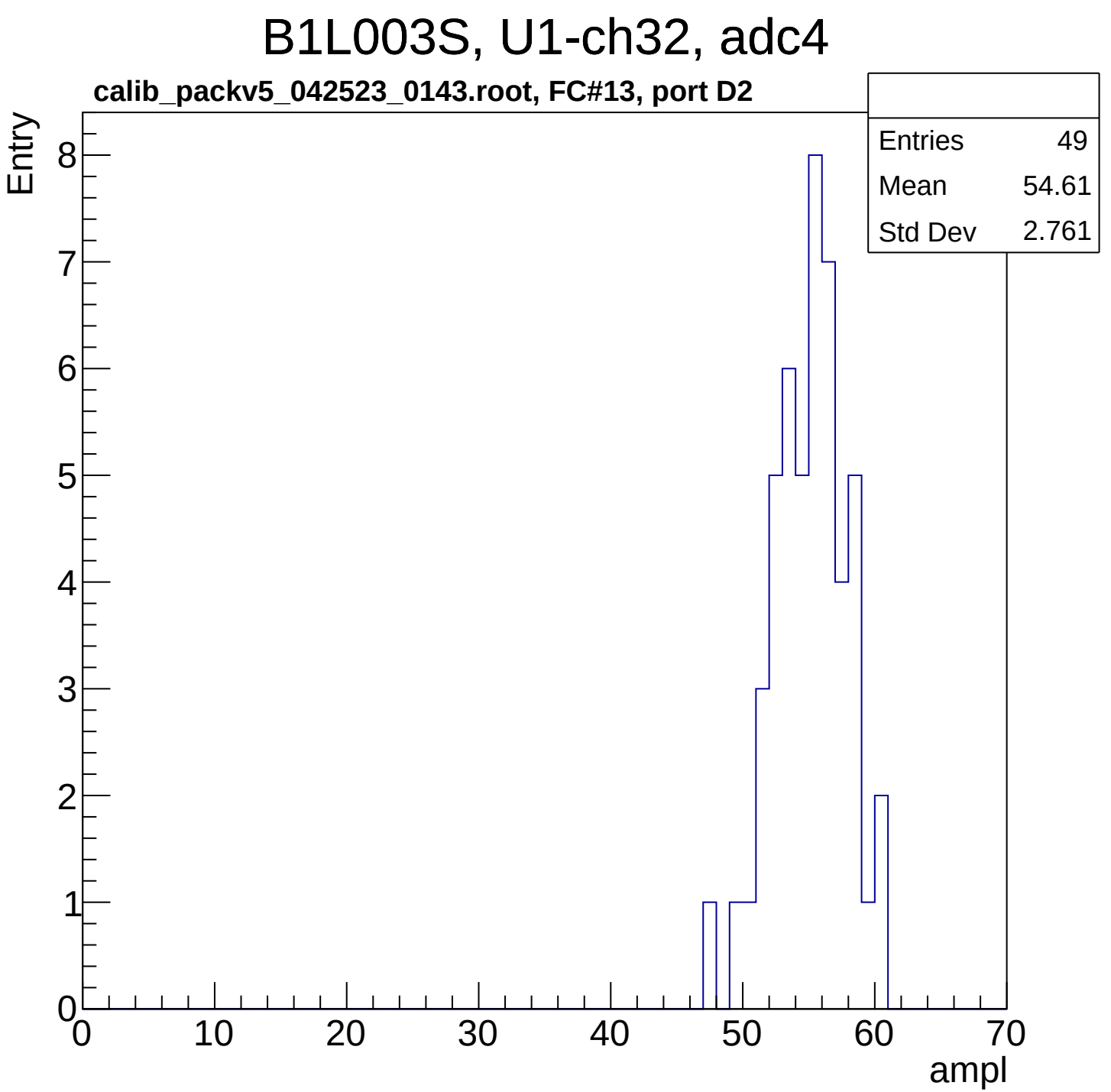
B1L003S, U1-ch32, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.61
Std Dev 2.761
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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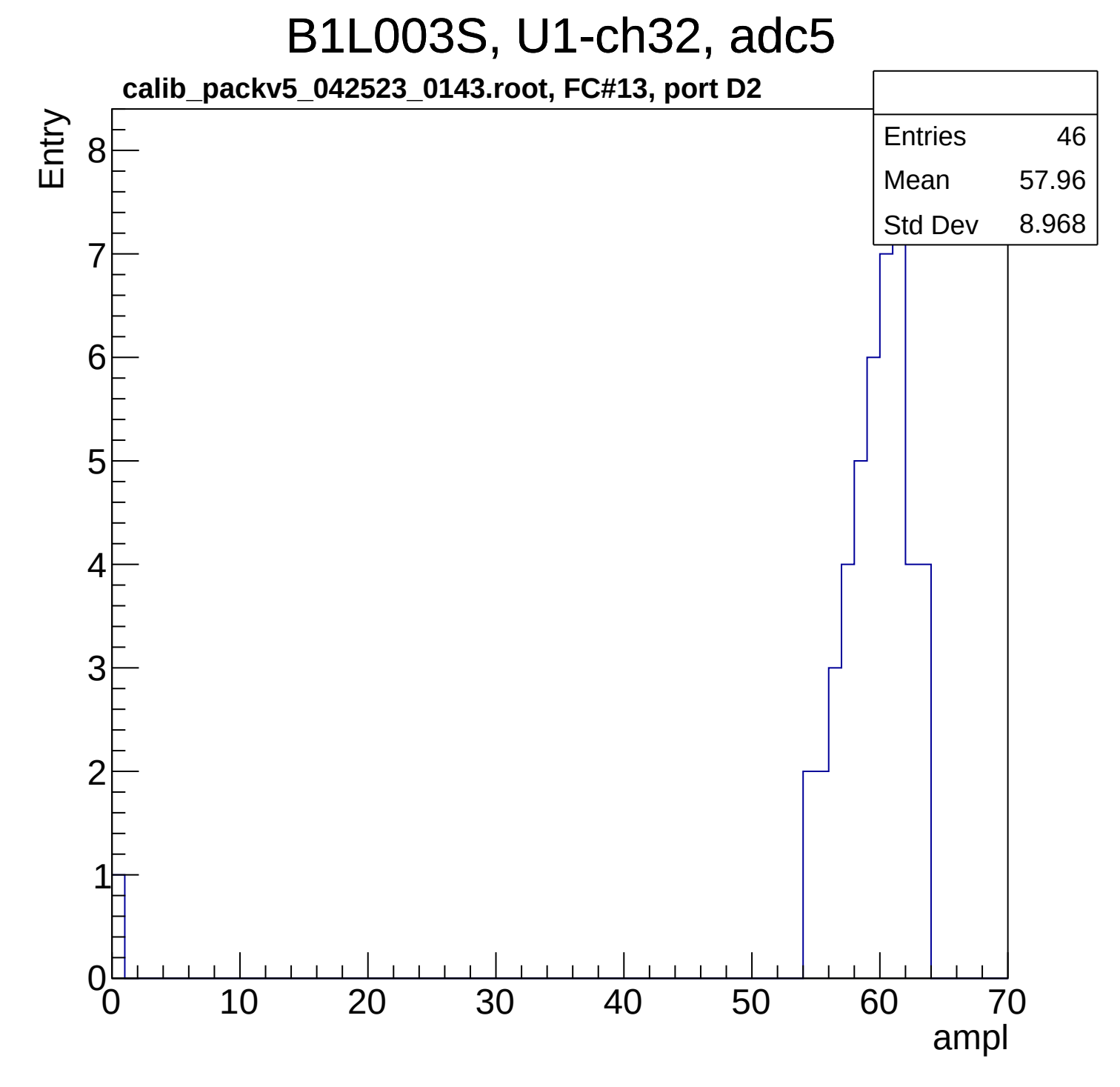
B1L003S, U1-ch32, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 57.96
Std Dev 8.968
Entry
ampl
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1
2
3
4
5
6
7
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0
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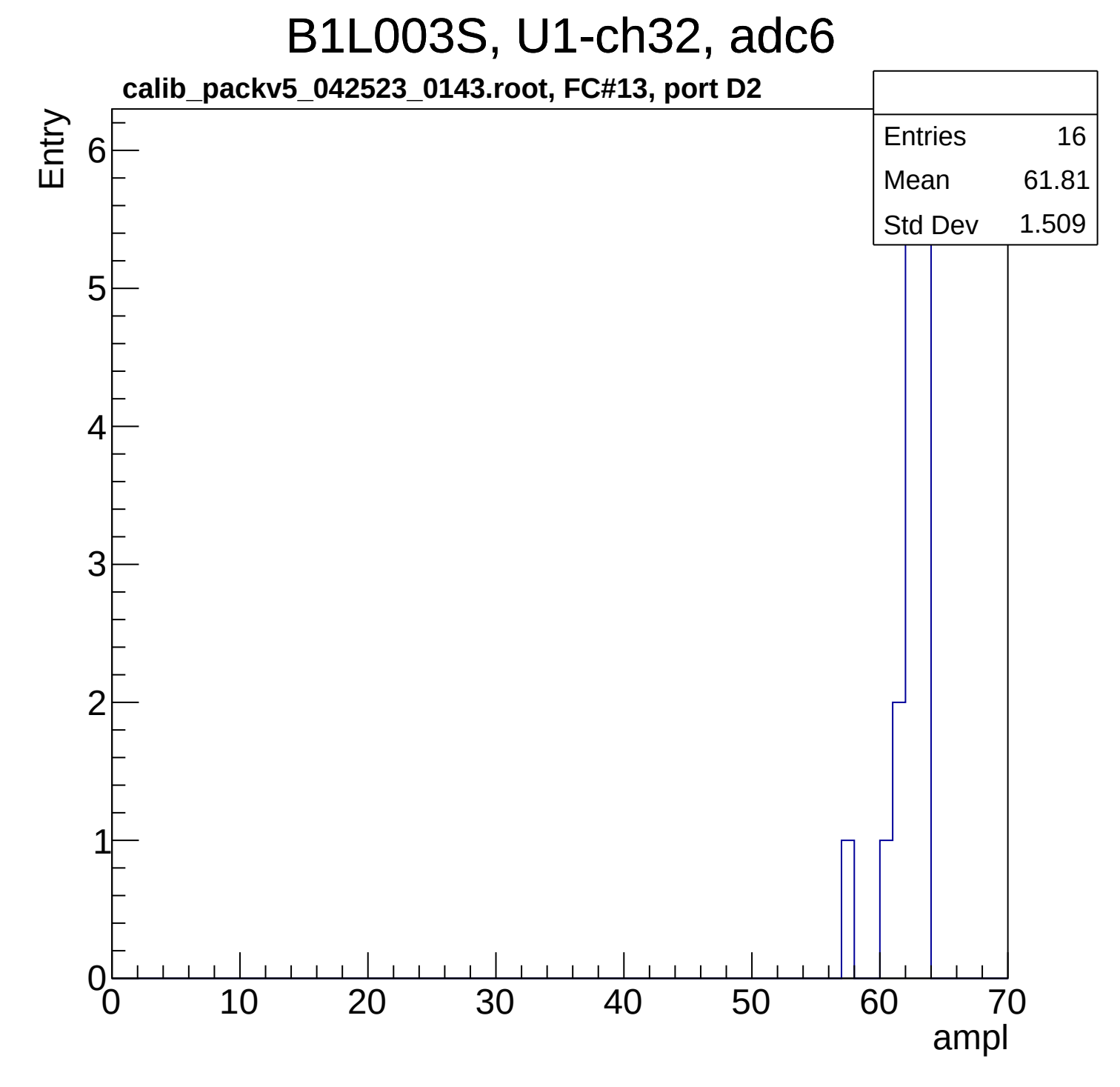
B1L003S, U1-ch32, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.81
Std Dev 1.509
Entry
ampl
0
1
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3
4
5
6
0
10
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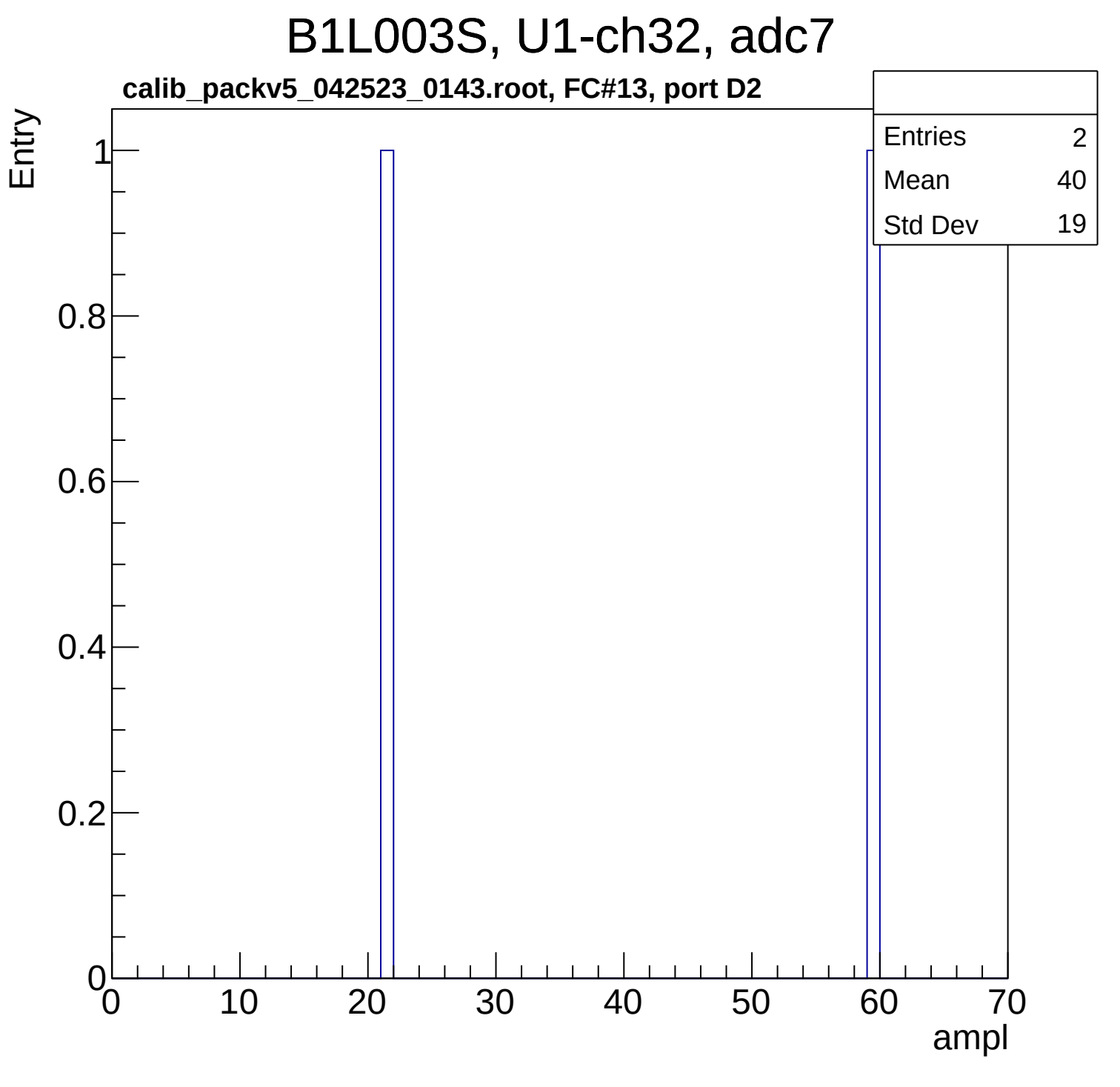
B1L003S, U1-ch32, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 40
Std Dev 19
Entry
ampl
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0.2
0.4
0.6
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1
0
10
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60
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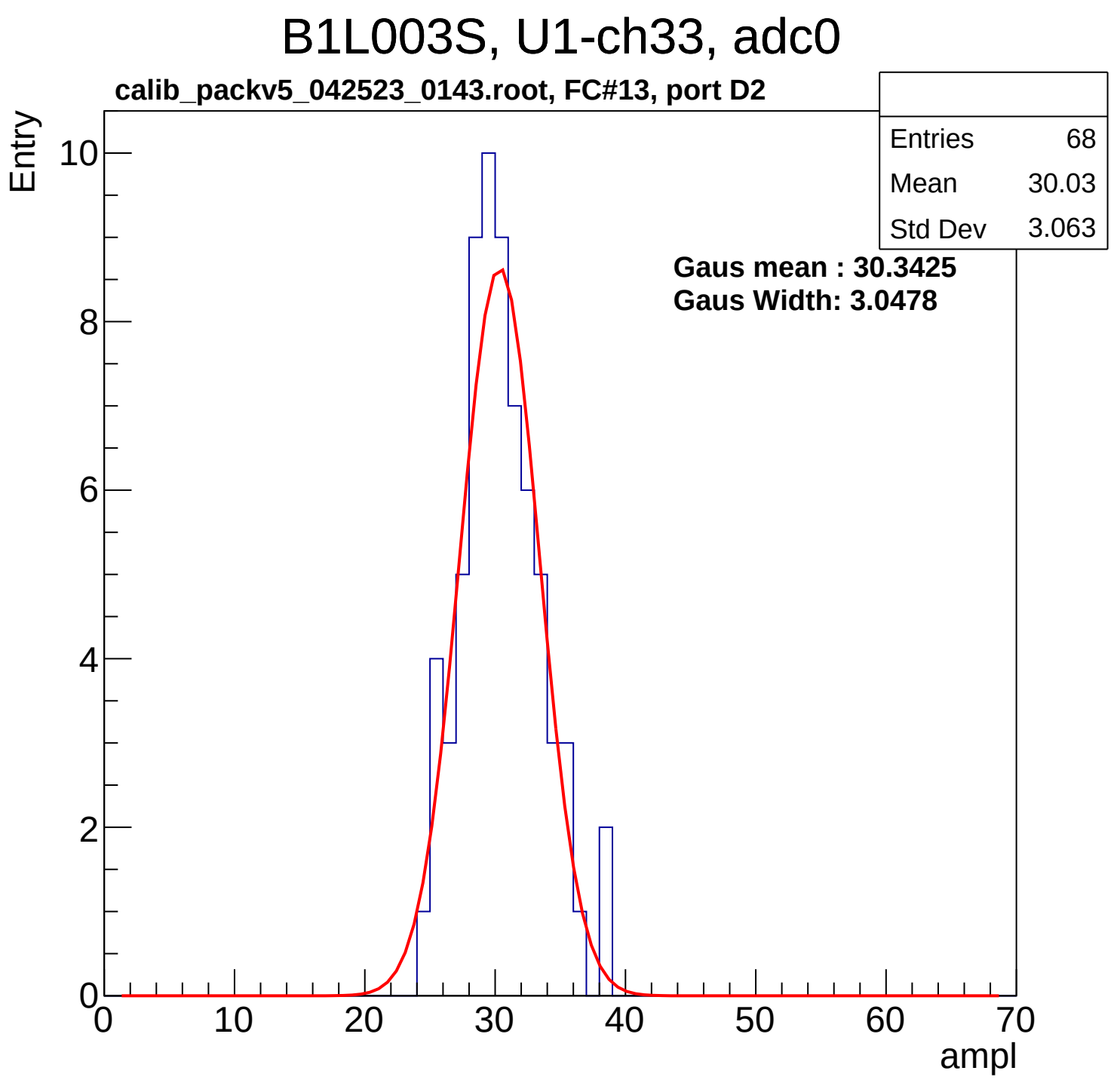
B1L003S, U1-ch33, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 30.03
Std Dev 3.063
Gaus mean : 30.3425
Gaus Width: 3.0478
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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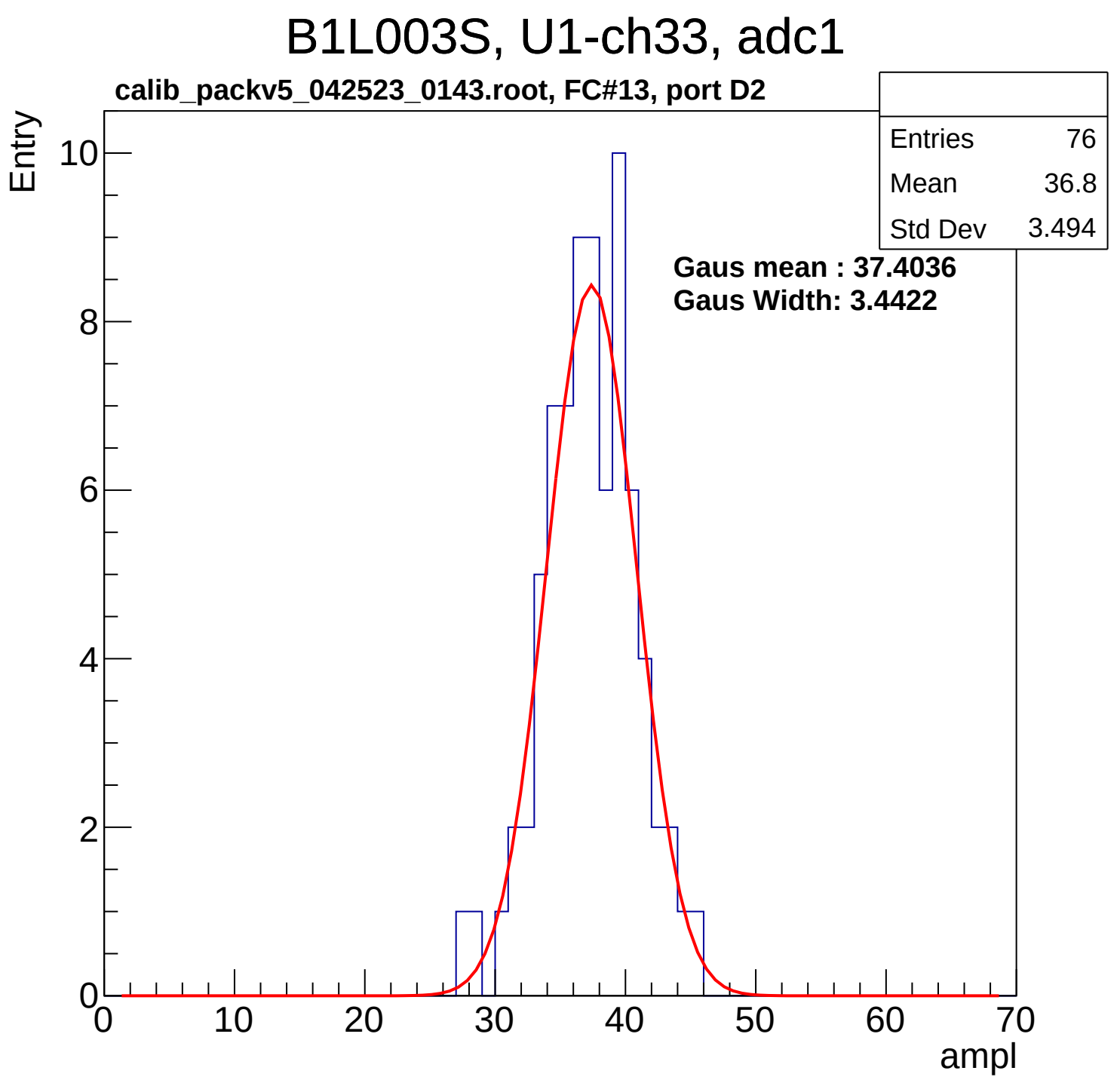
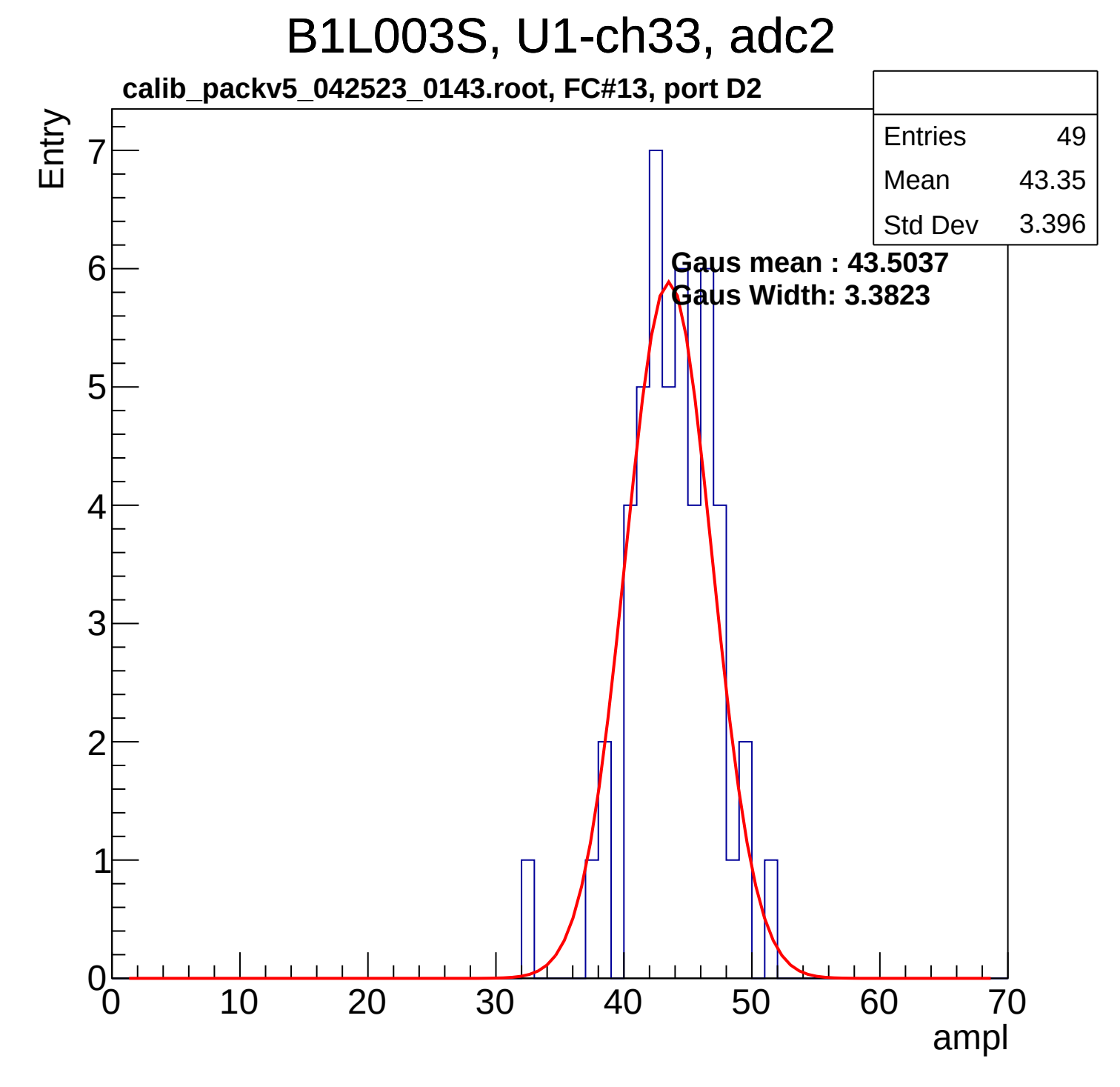

B1L003S, U1-ch33, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.8
Std Dev 3.494
Gaus mean : 37.4036
Gaus Width: 3.4422
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch33, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 43.35
Std Dev 3.396
Gaus mean : 43.5037
Gaus Width: 3.3823
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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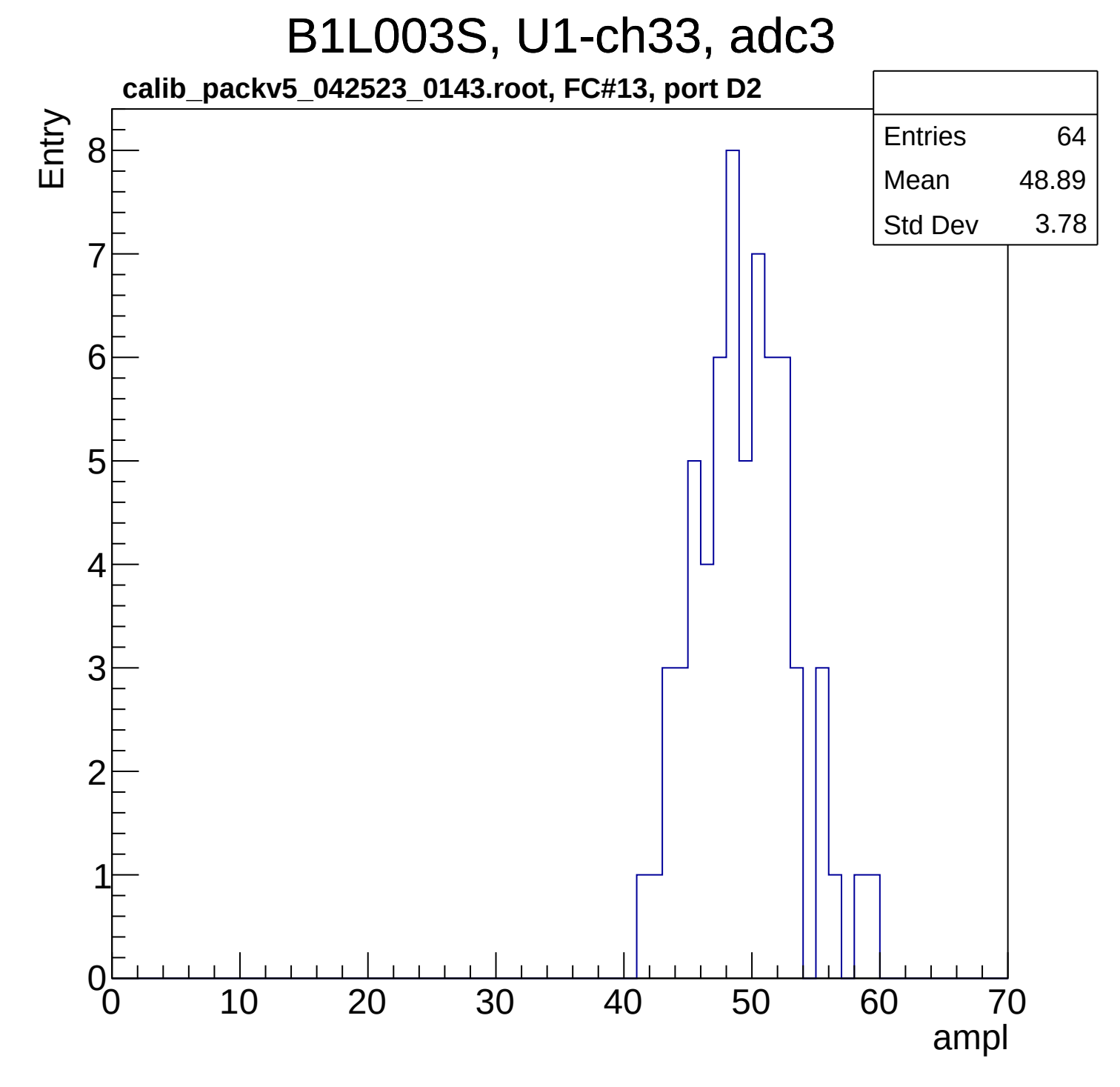
B1L003S, U1-ch33, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 48.89
Std Dev 3.78
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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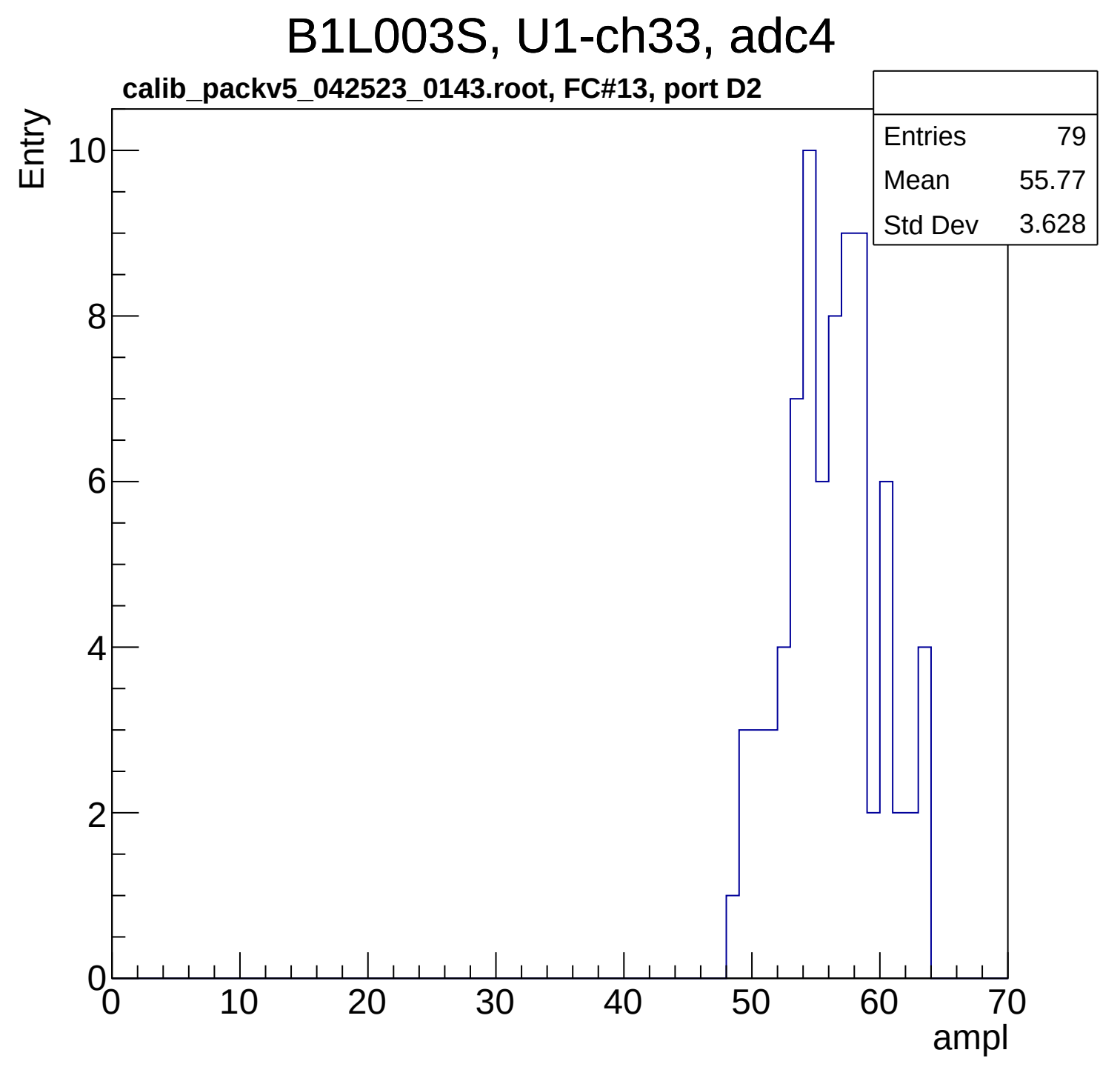
B1L003S, U1-ch33, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 55.77
Std Dev 3.628
Entry
ampl
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6
8
10
0
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40
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60
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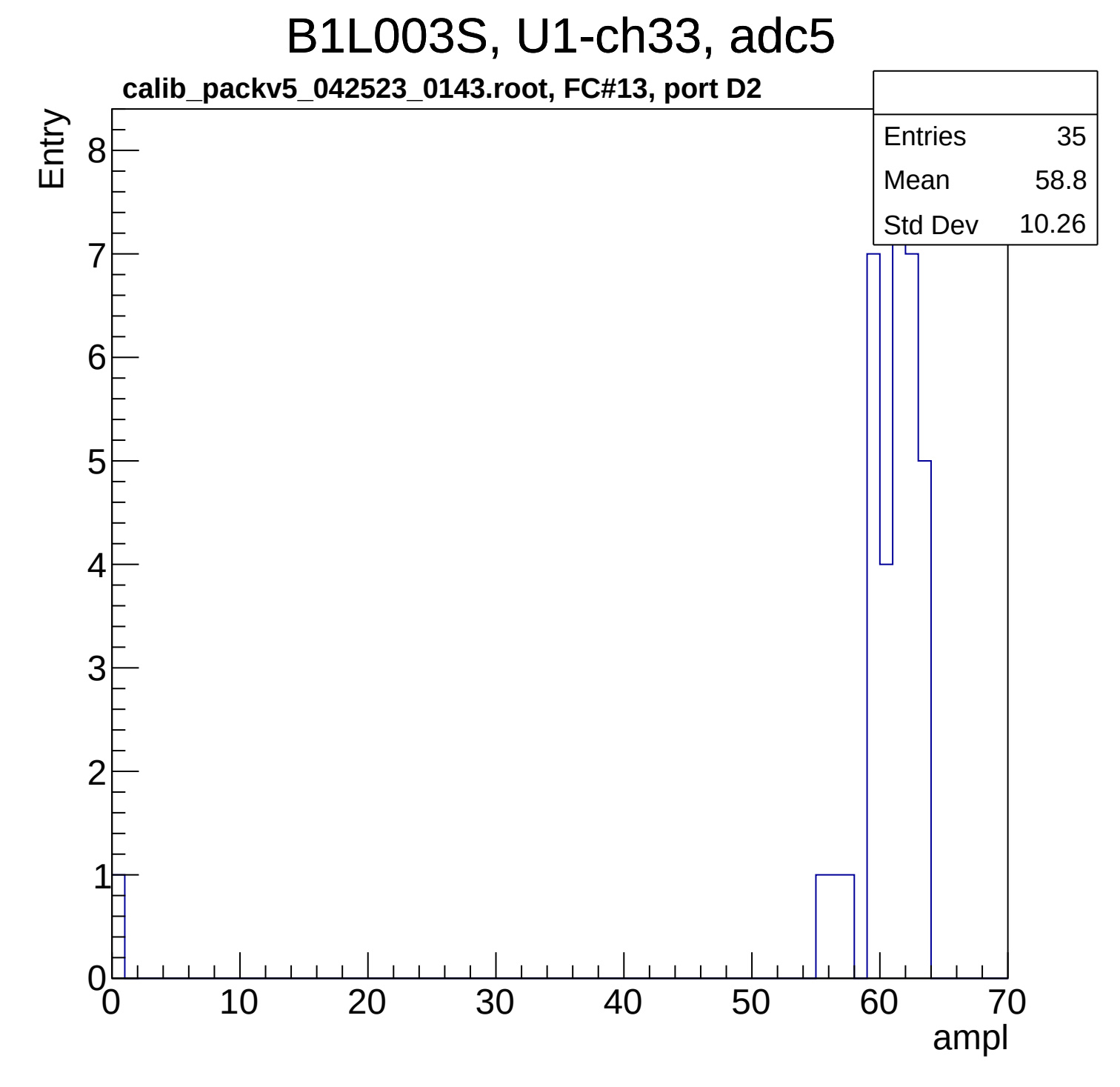
B1L003S, U1-ch33, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 35
Mean 58.8
Std Dev 10.26
Entry
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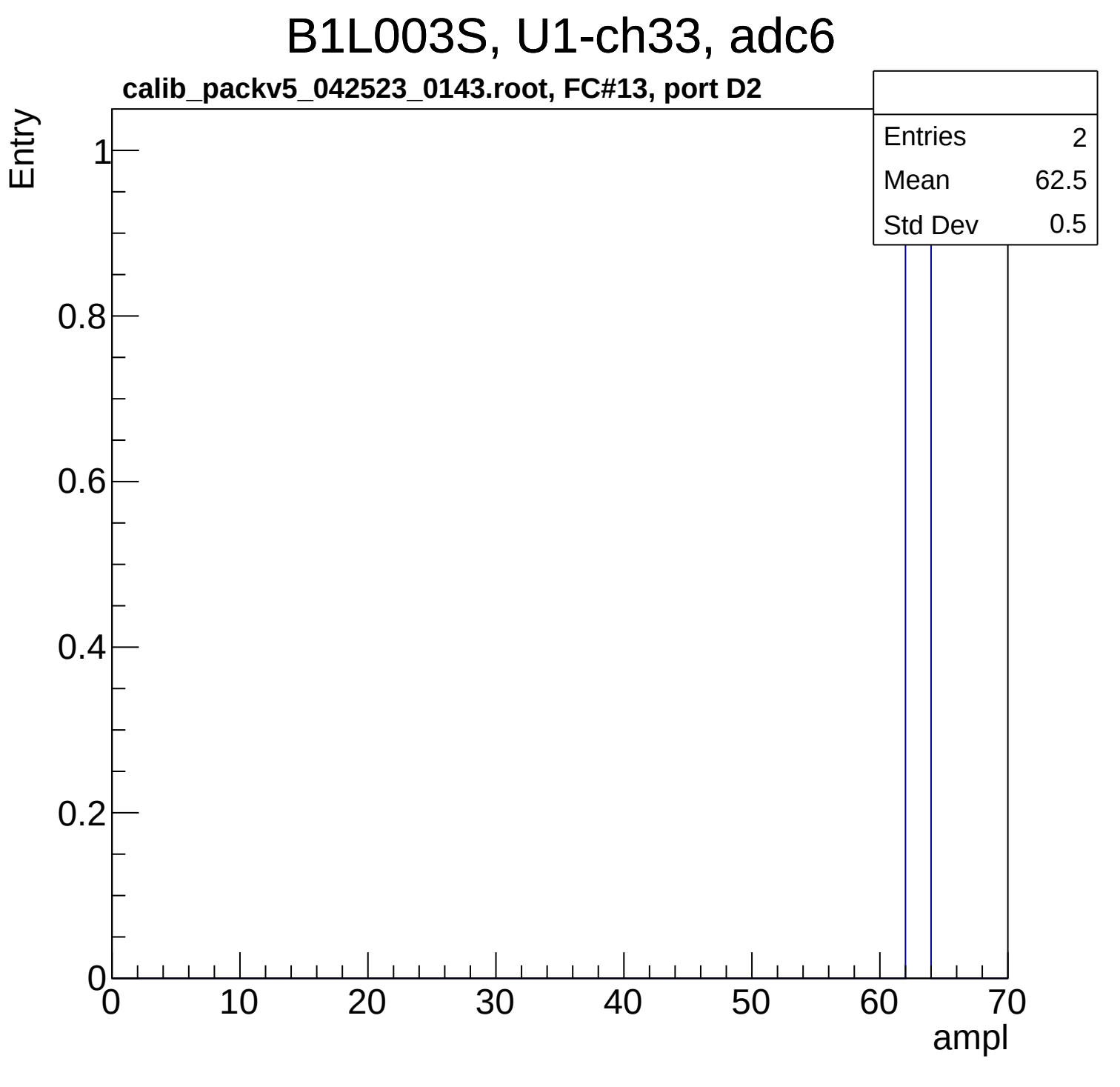
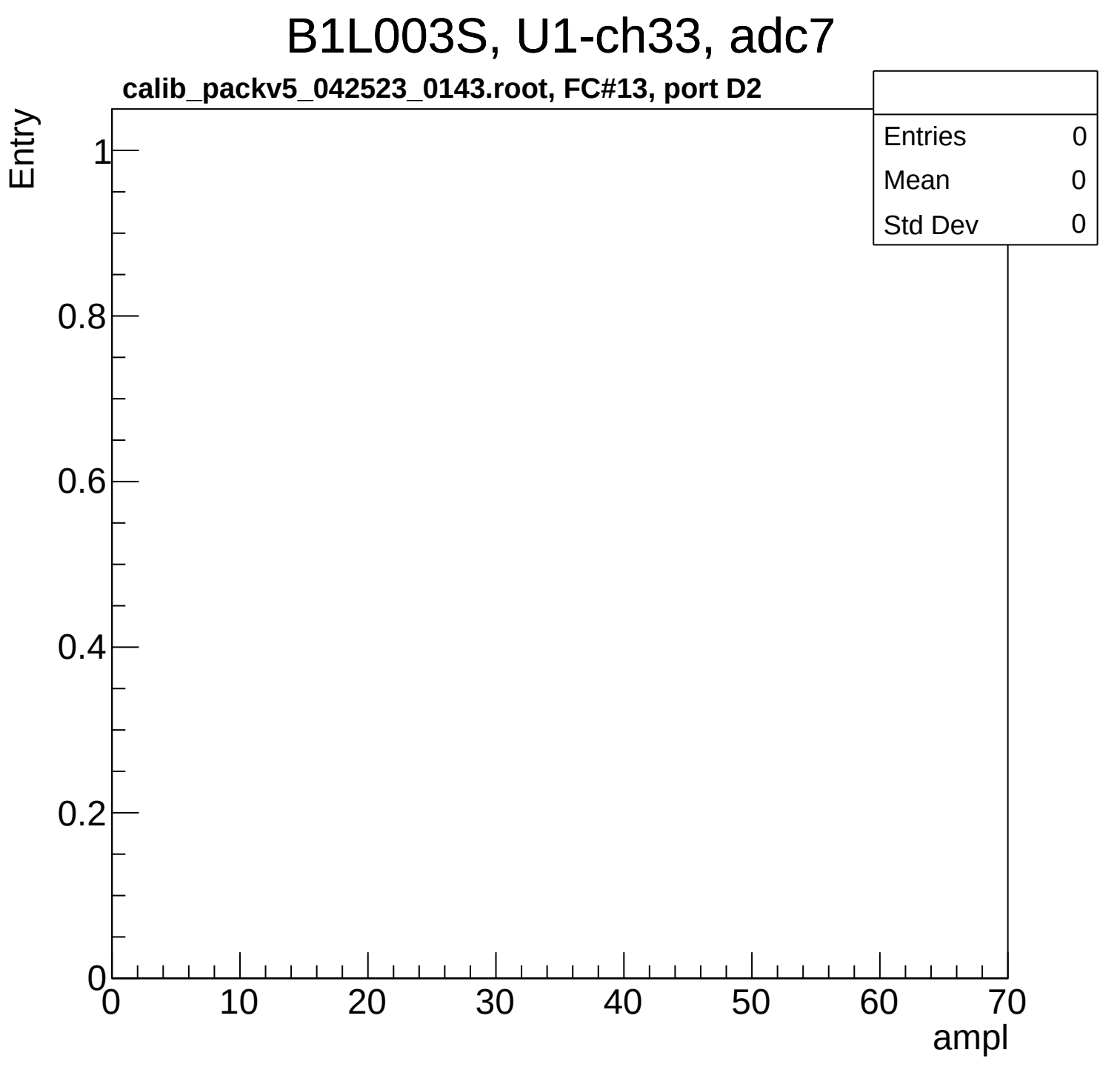
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch33, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch33, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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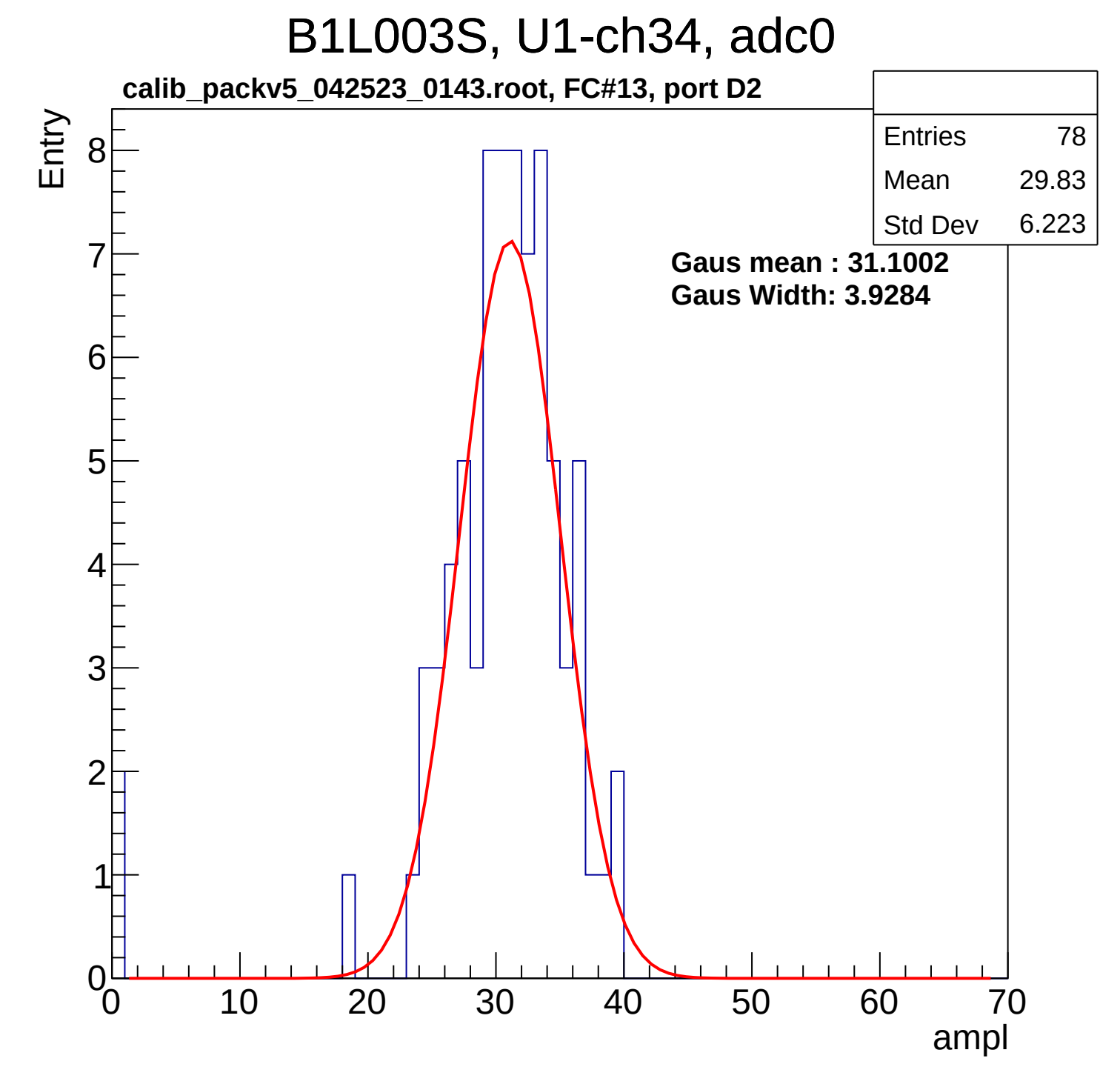
40
50
60
70
ampl


B1L003S, U1-ch34, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 29.83
Std Dev 6.223
Gaus mean : 31.1002
Gaus Width: 3.9284
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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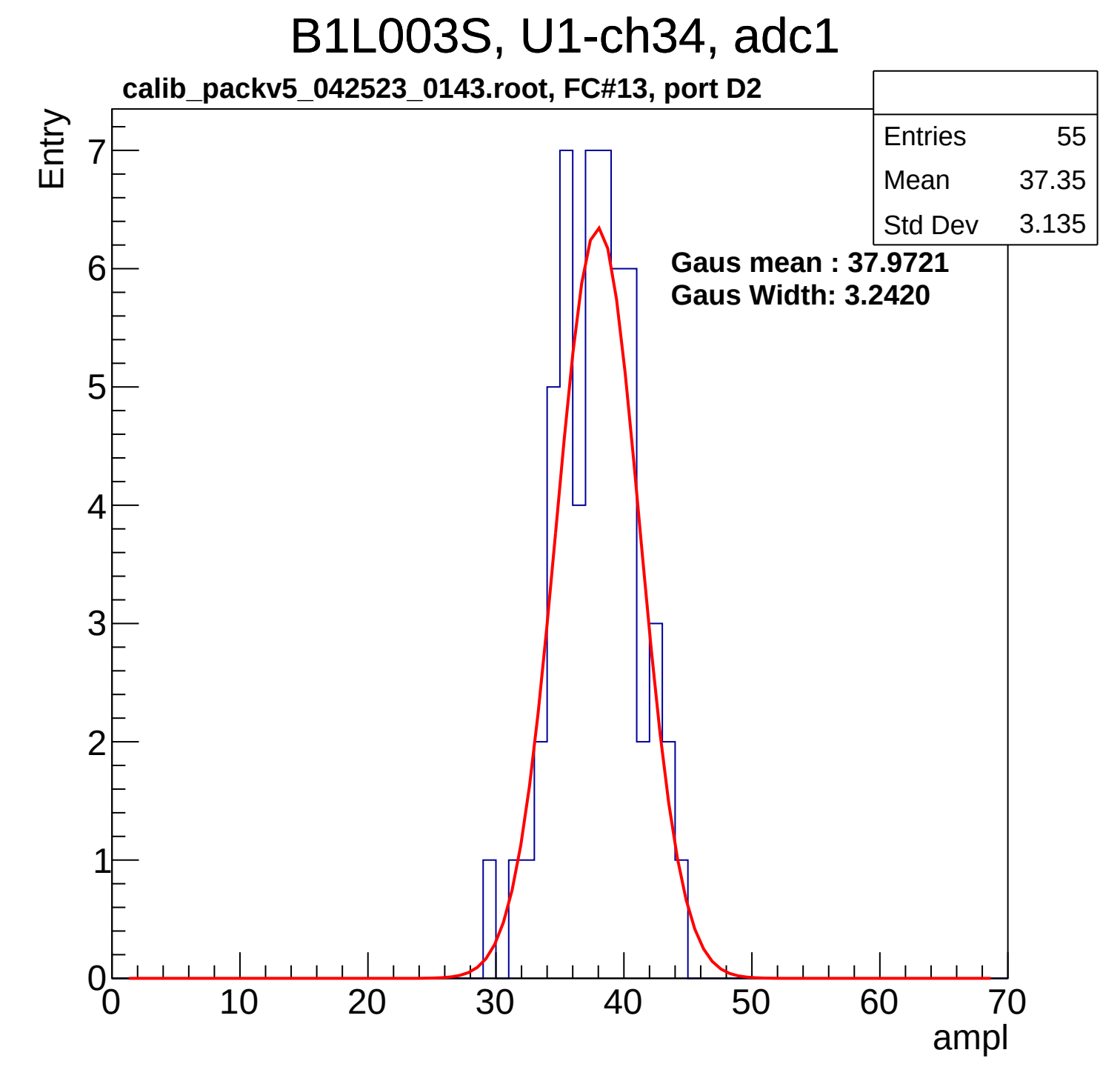
B1L003S, U1-ch34, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37.35
Std Dev 3.135
Gaus mean : 37.9721
Gaus Width: 3.2420
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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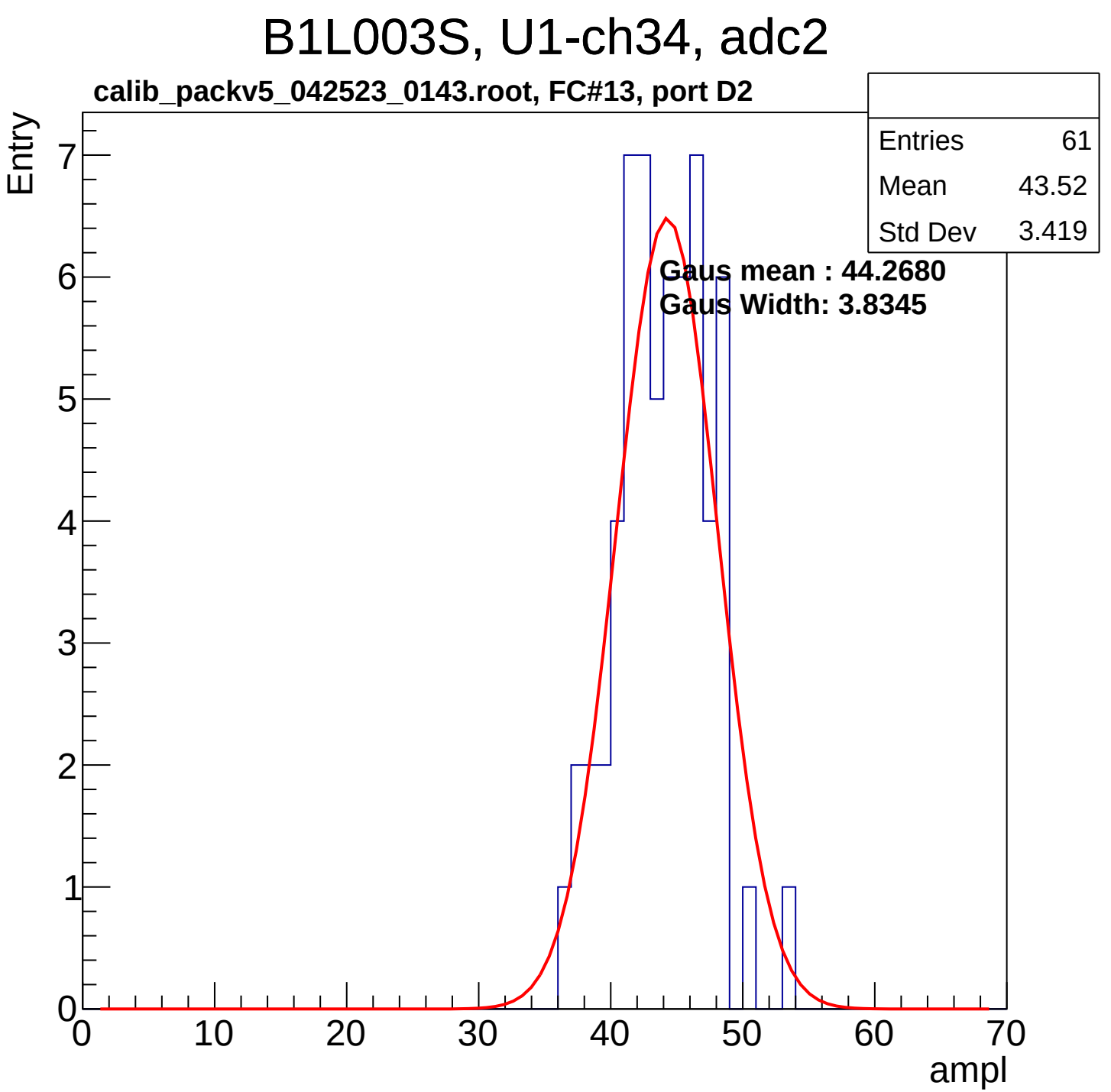
B1L003S, U1-ch34, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43.52
Std Dev 3.419
Gaus mean : 44.2680
Gaus Width: 3.8345
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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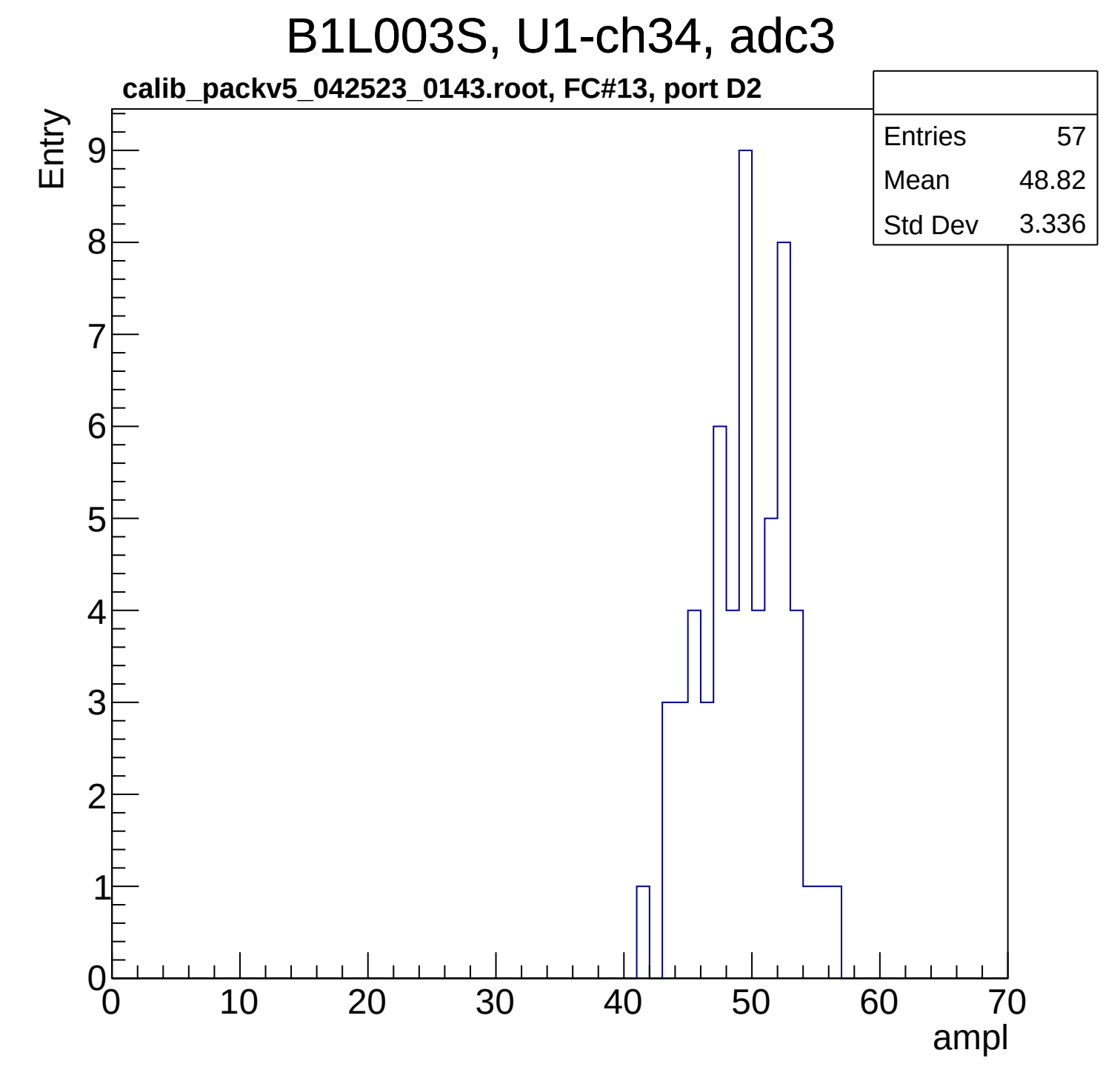
B1L003S, U1-ch34, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.82
Std Dev 3.336
Entry
ampl
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60
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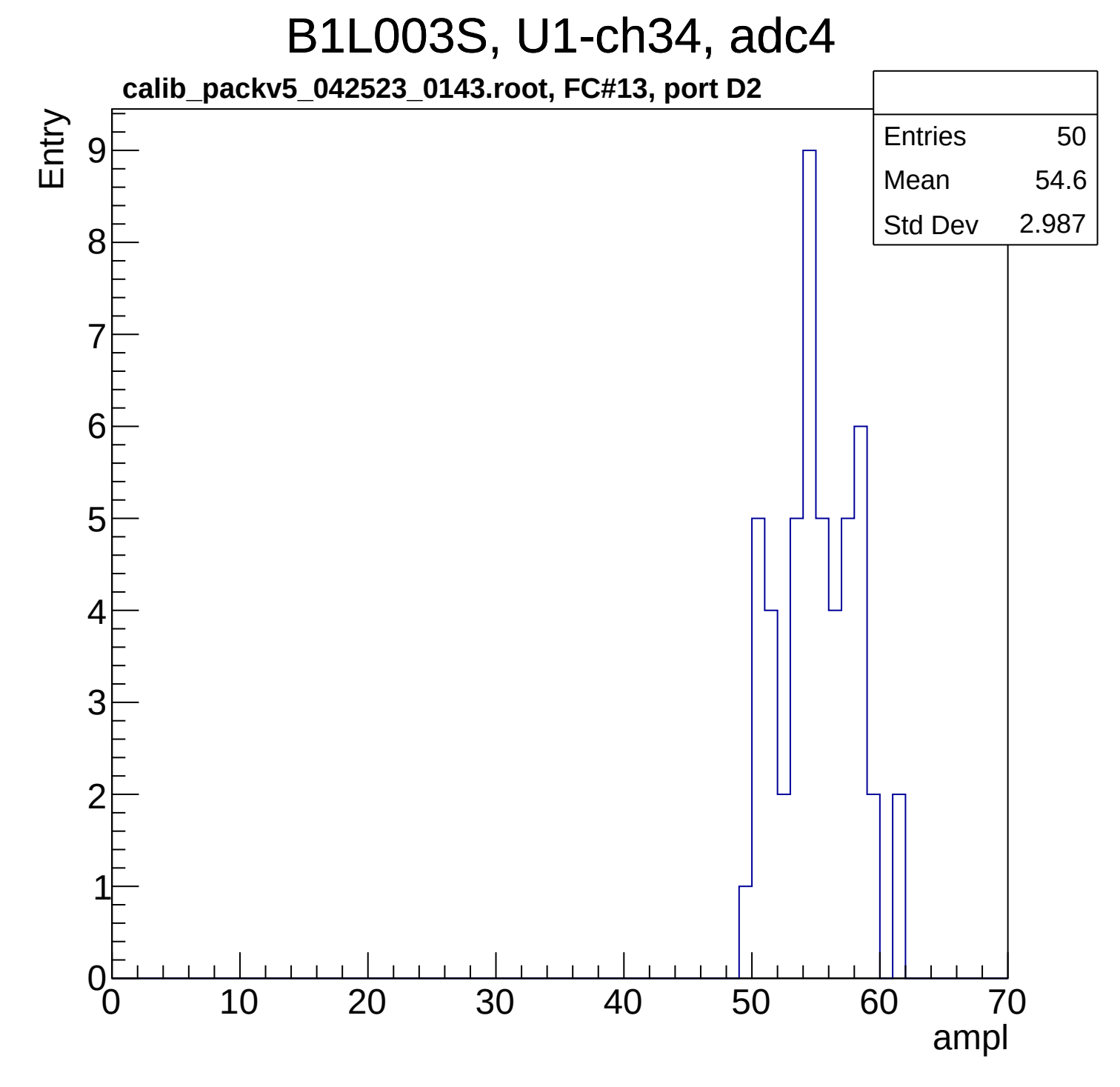
B1L003S, U1-ch34, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.6
Std Dev 2.987
Entry
ampl
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3
4
5
6
7
8
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0
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20
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40
50
60
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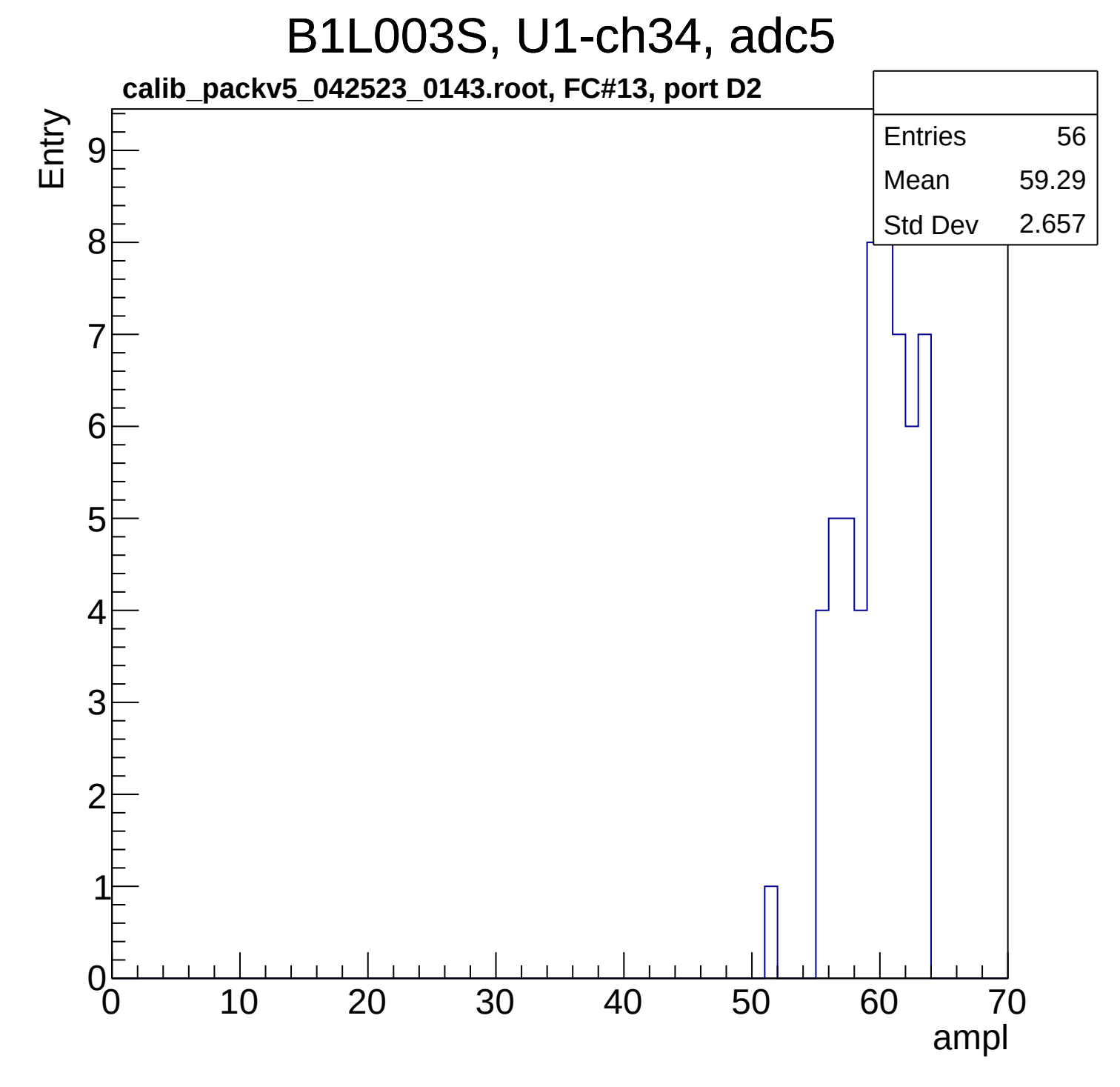
B1L003S, U1-ch34, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.29
Std Dev 2.657
Entry
ampl
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3
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5
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7
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9
0
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60
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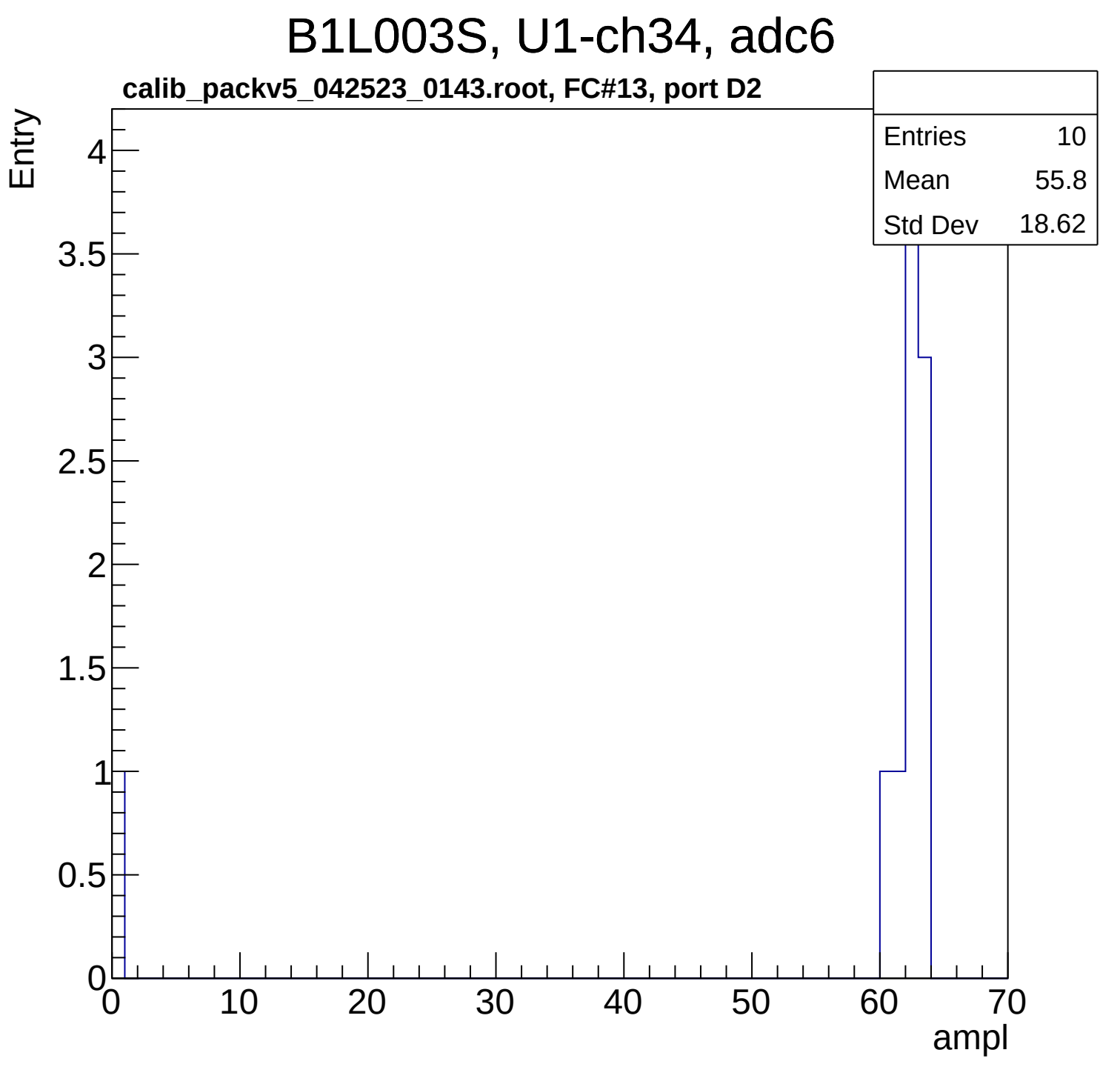
B1L003S, U1-ch34, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.62
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
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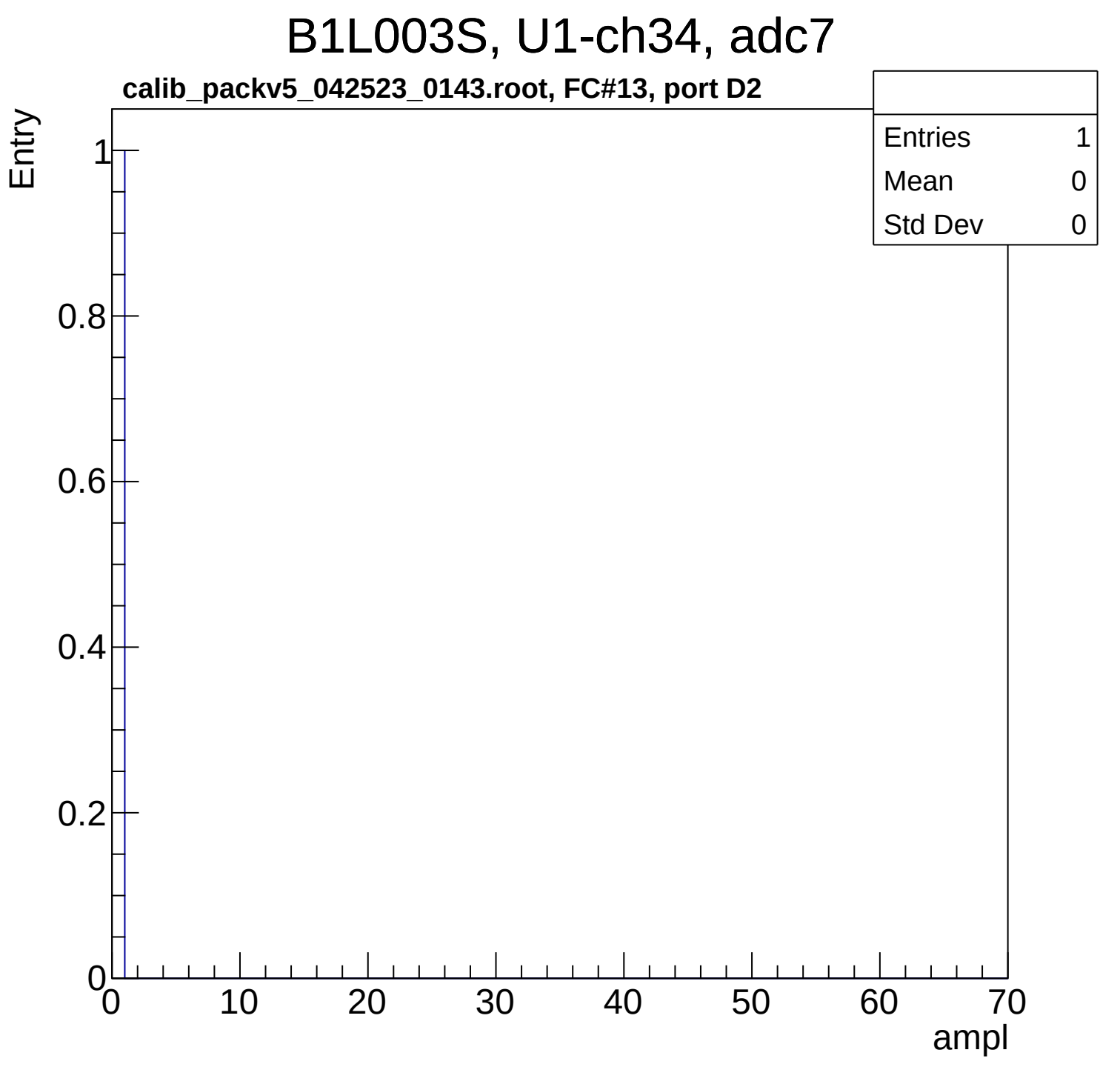
40
50
60
70


B1L003S, U1-ch34, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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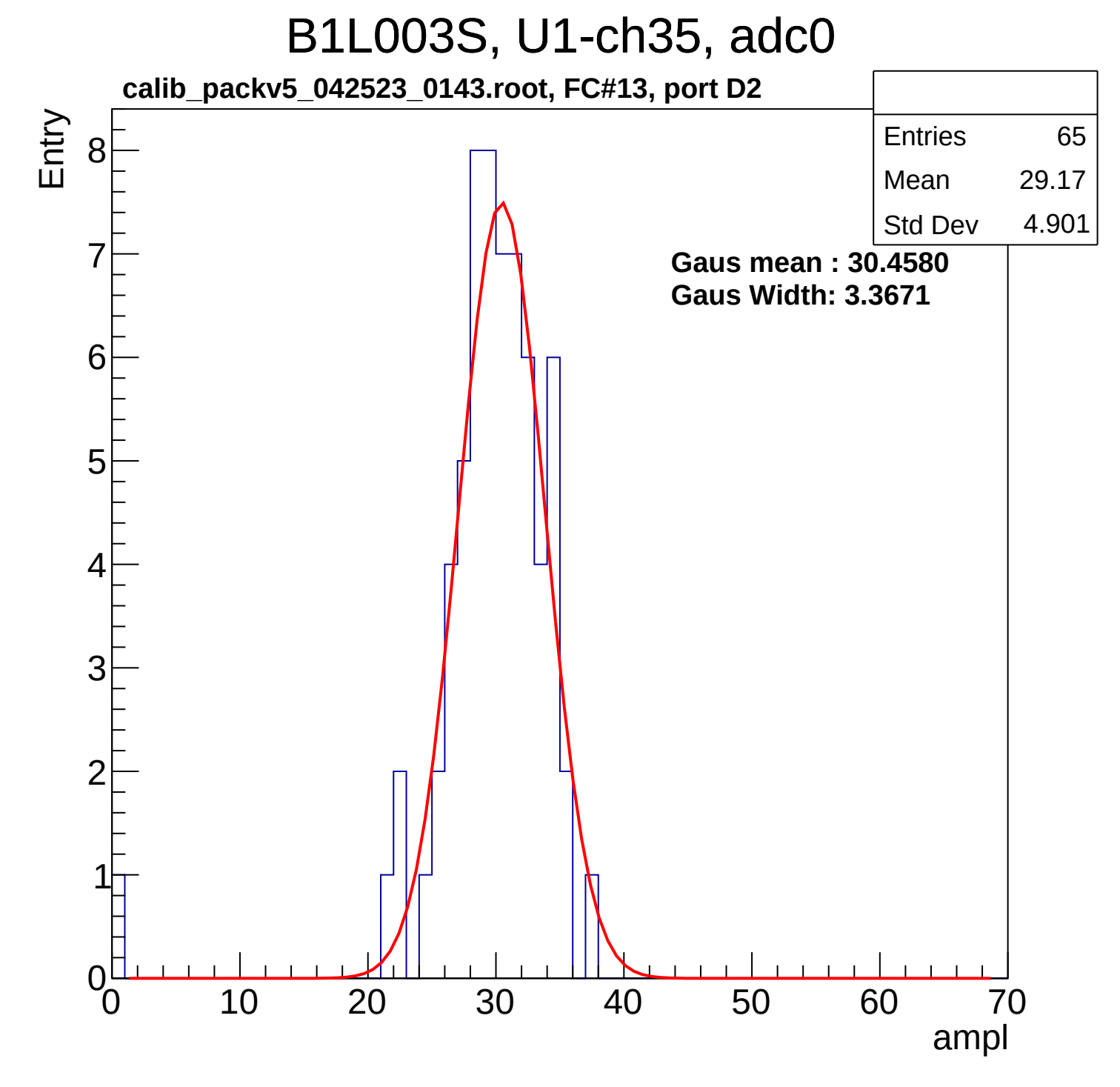
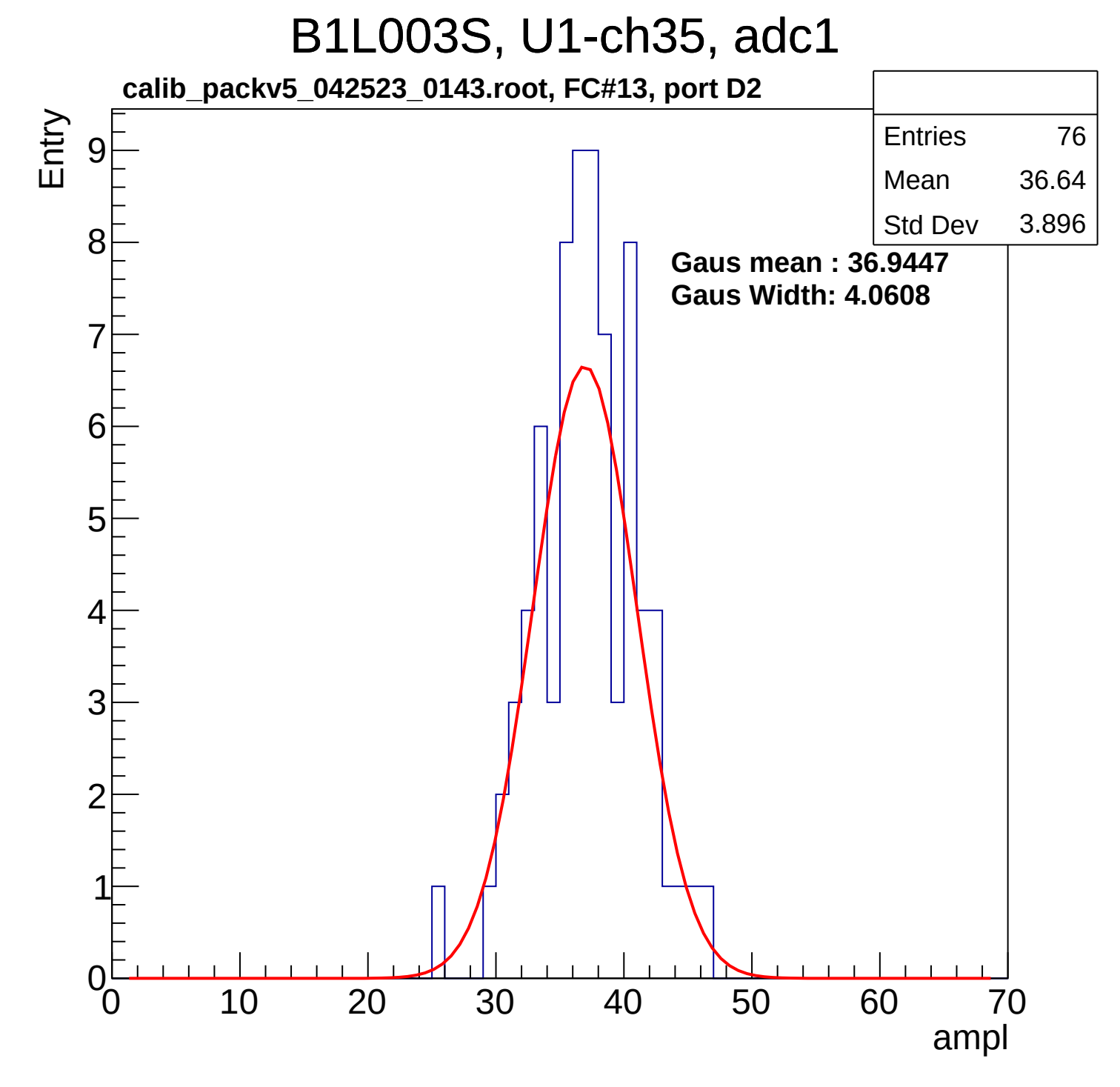
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch35, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.17
Std Dev 4.901
Gaus mean : 30.4580
Gaus Width: 3.3671
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch35, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 36.64
Std Dev 3.896
Gaus mean : 36.9447
Gaus Width: 4.0608
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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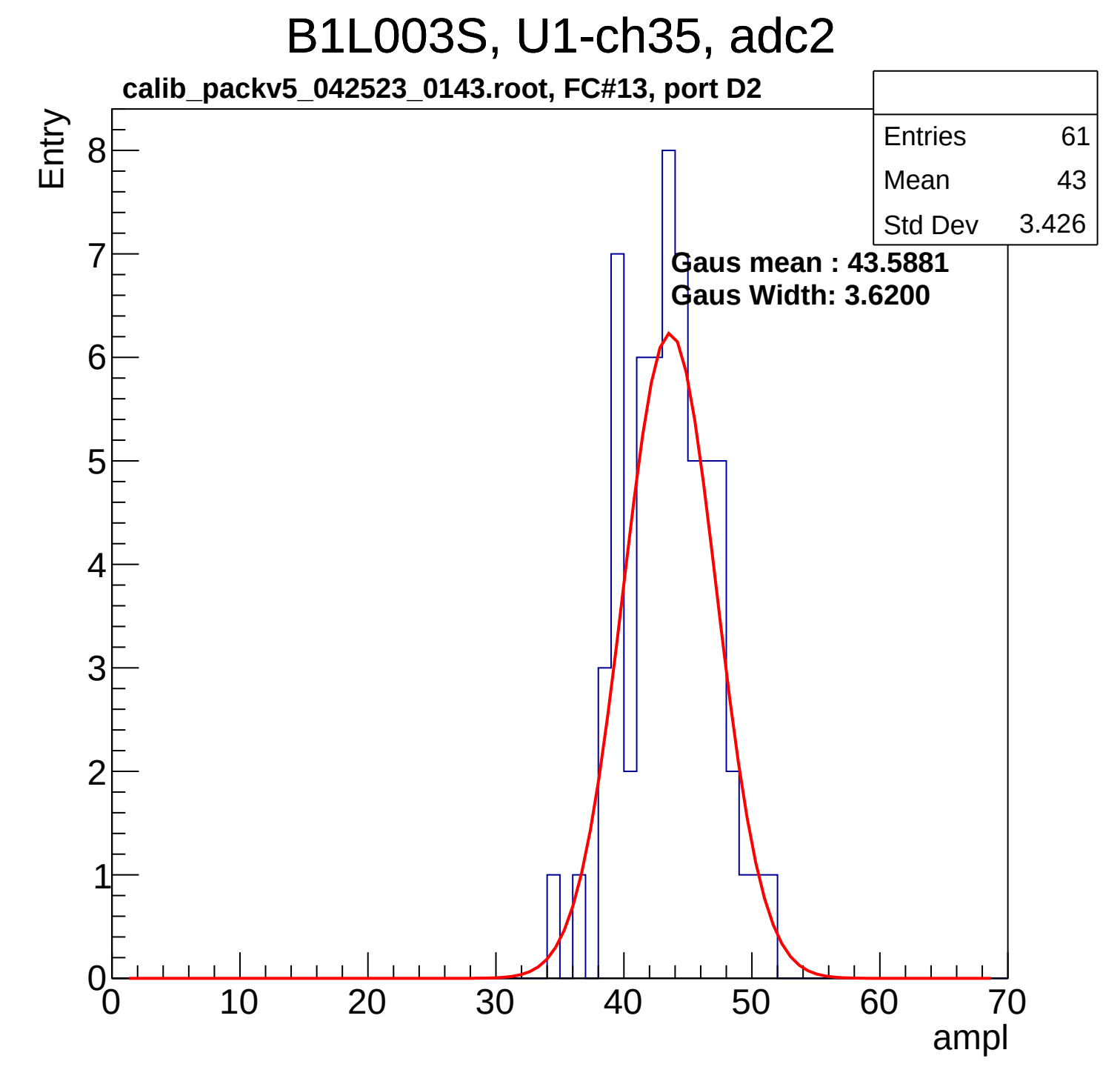
B1L003S, U1-ch35, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 43
Std Dev 3.426
Gaus mean : 43.5881
Gaus Width: 3.6200
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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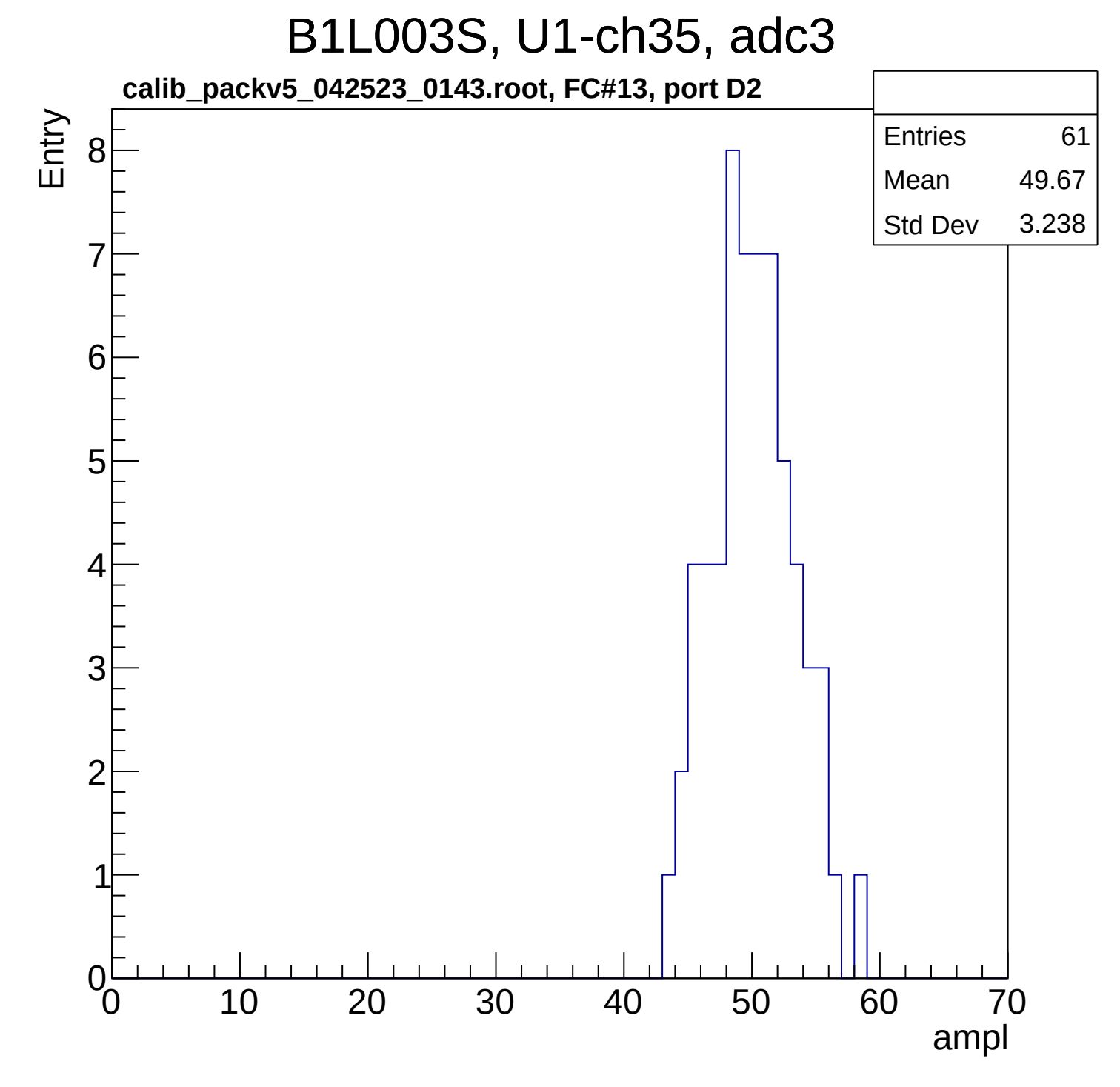
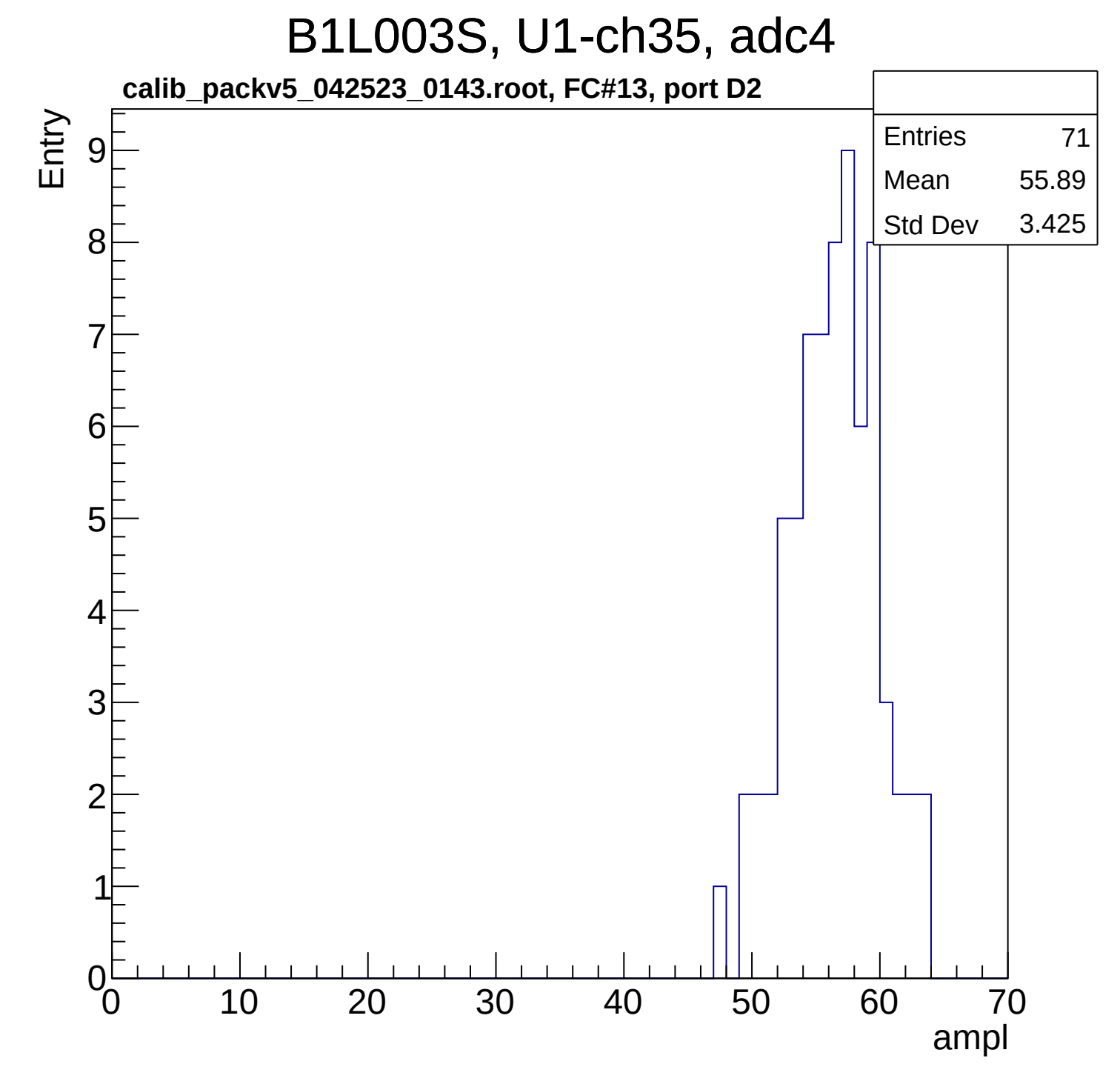

B1L003S, U1-ch35, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.67
Std Dev 3.238
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch35, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 55.89
Std Dev 3.425
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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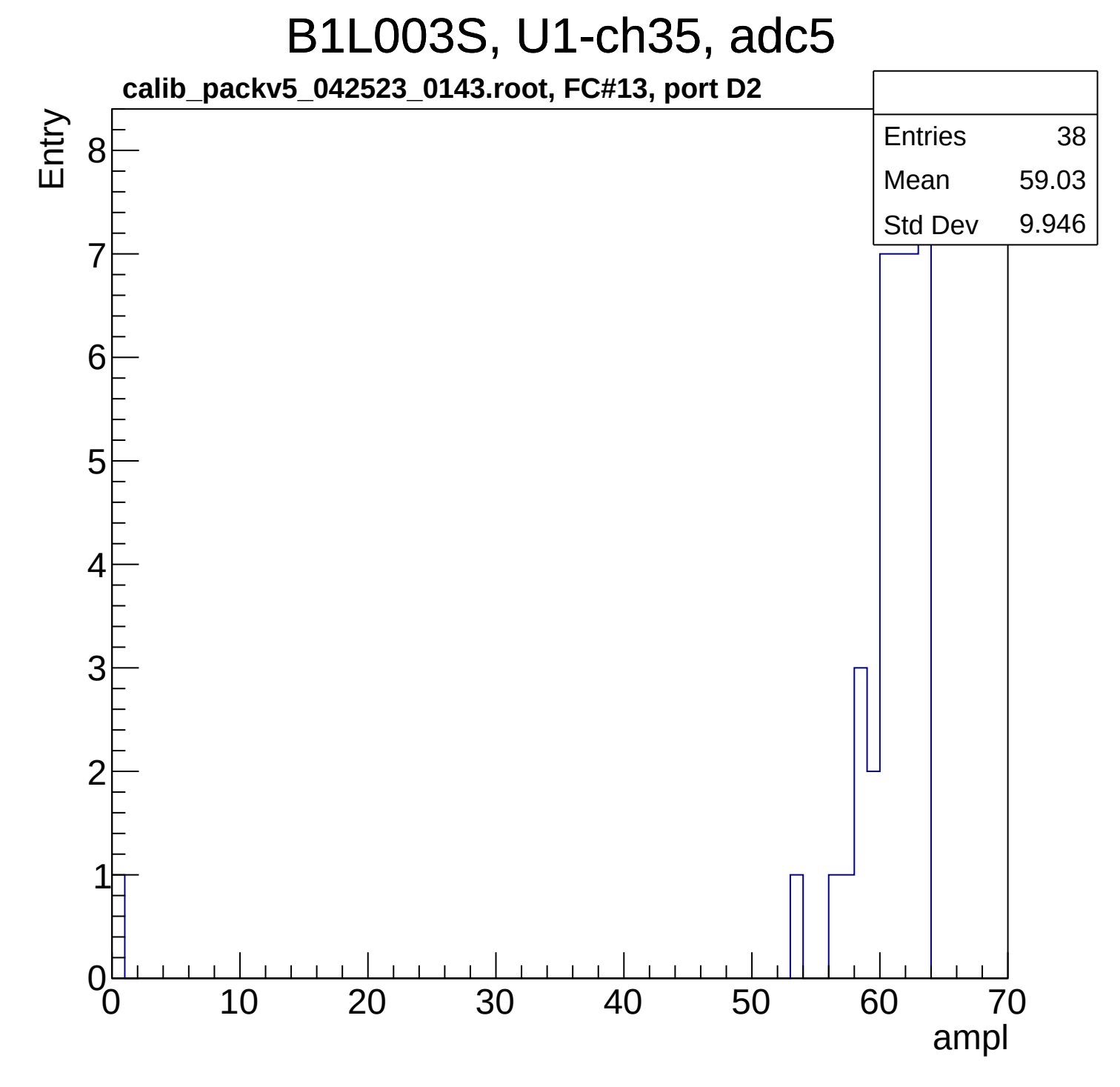
B1L003S, U1-ch35, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 59.03
Std Dev 9.946
Entry
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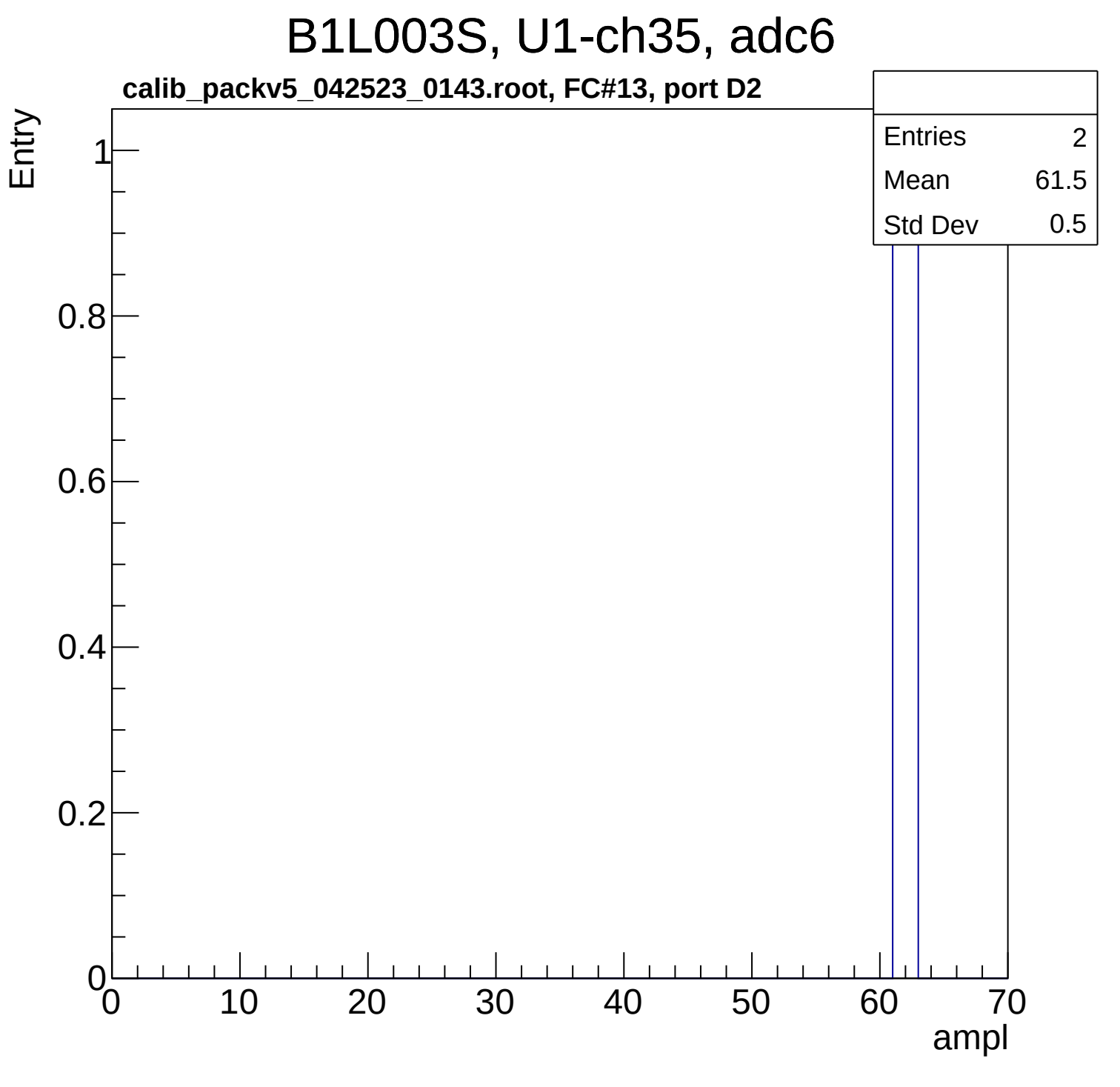
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch35, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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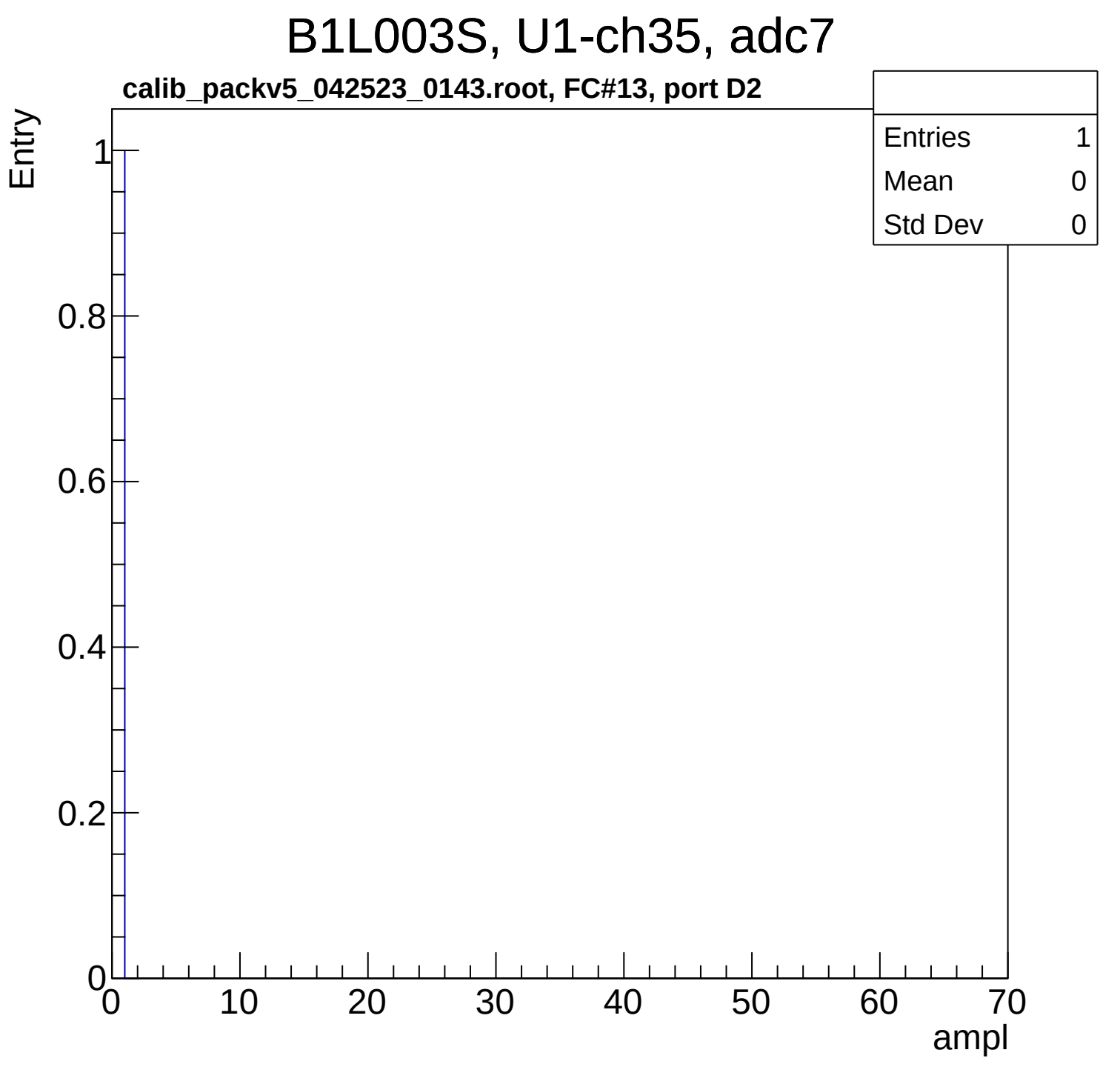
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch35, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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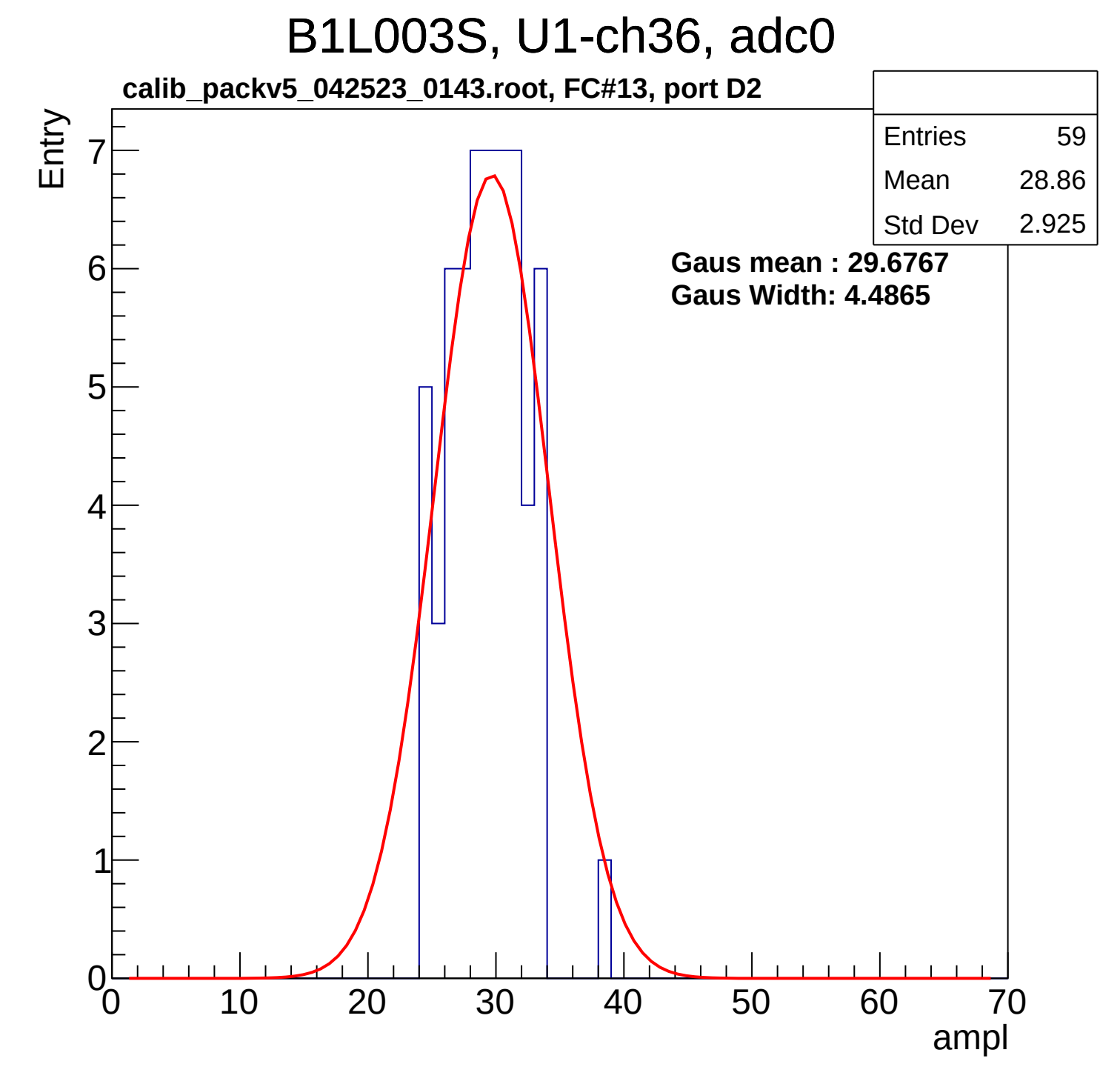
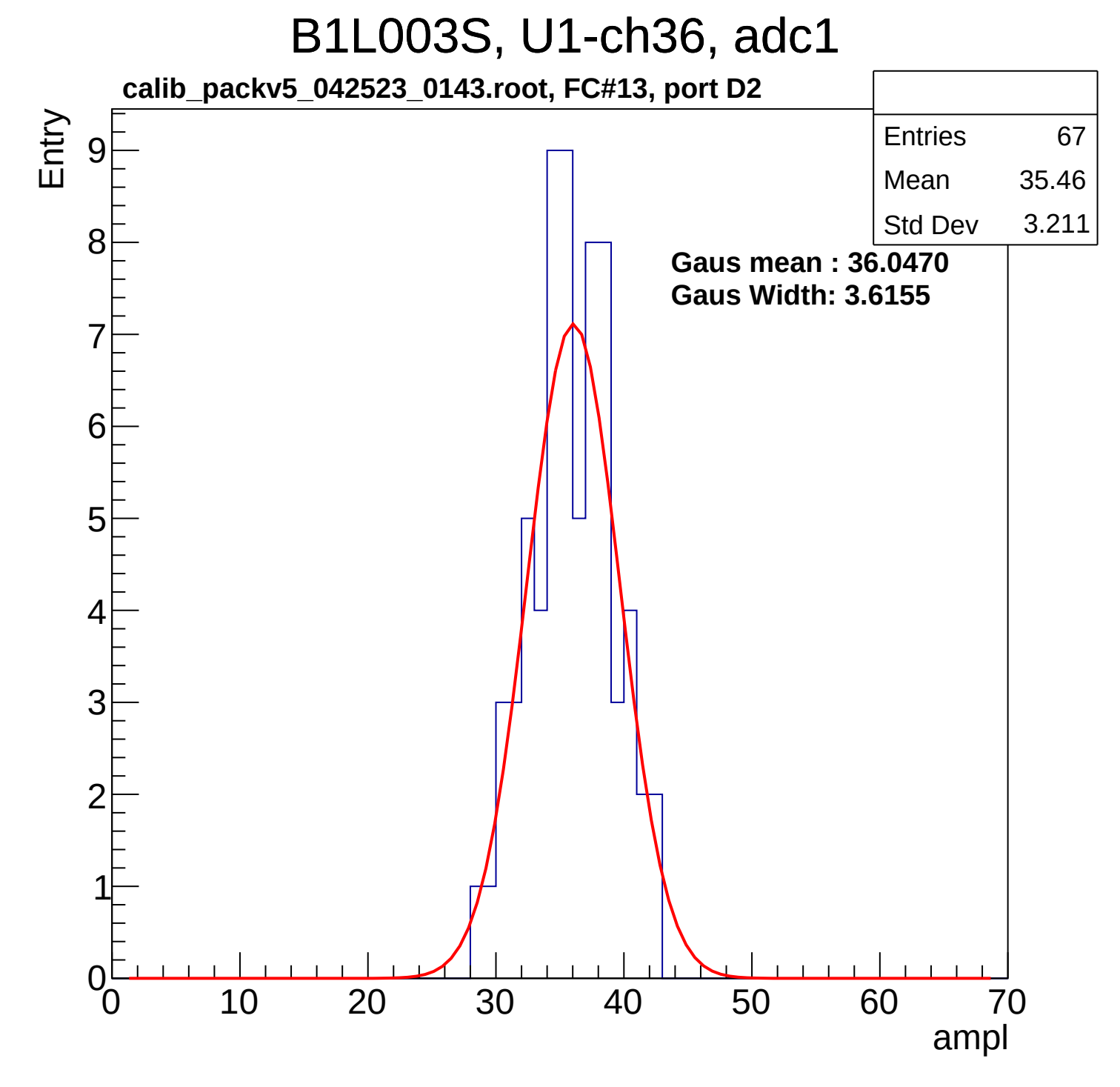
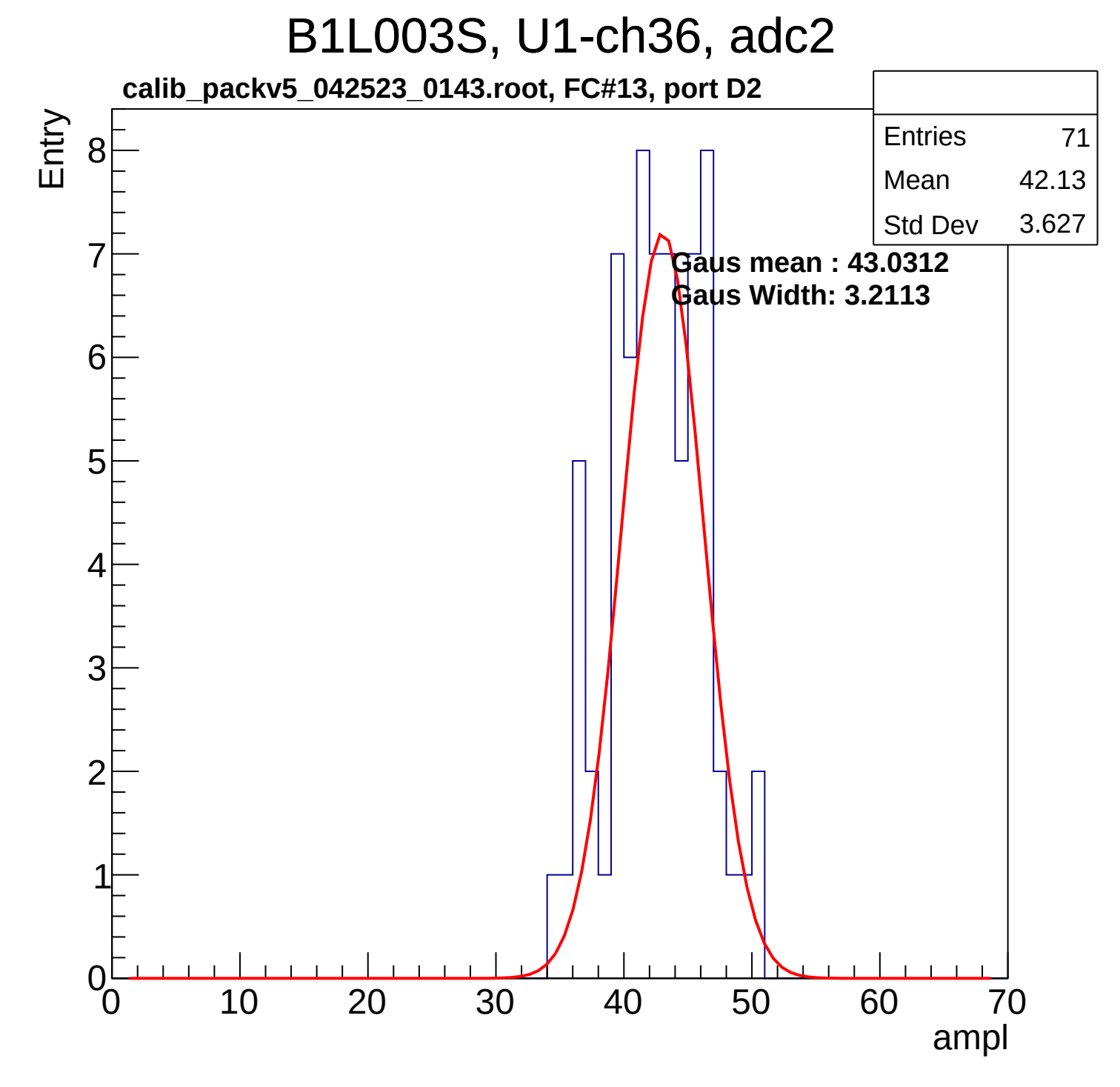
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch36, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.86
Std Dev 2.925
Gaus mean : 29.6767
Gaus Width: 4.4865
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch36, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 35.46
Std Dev 3.211
Gaus mean : 36.0470
Gaus Width: 3.6155
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch36, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.13
Std Dev 3.627
Gaus mean : 43.0312
Gaus Width: 3.2113
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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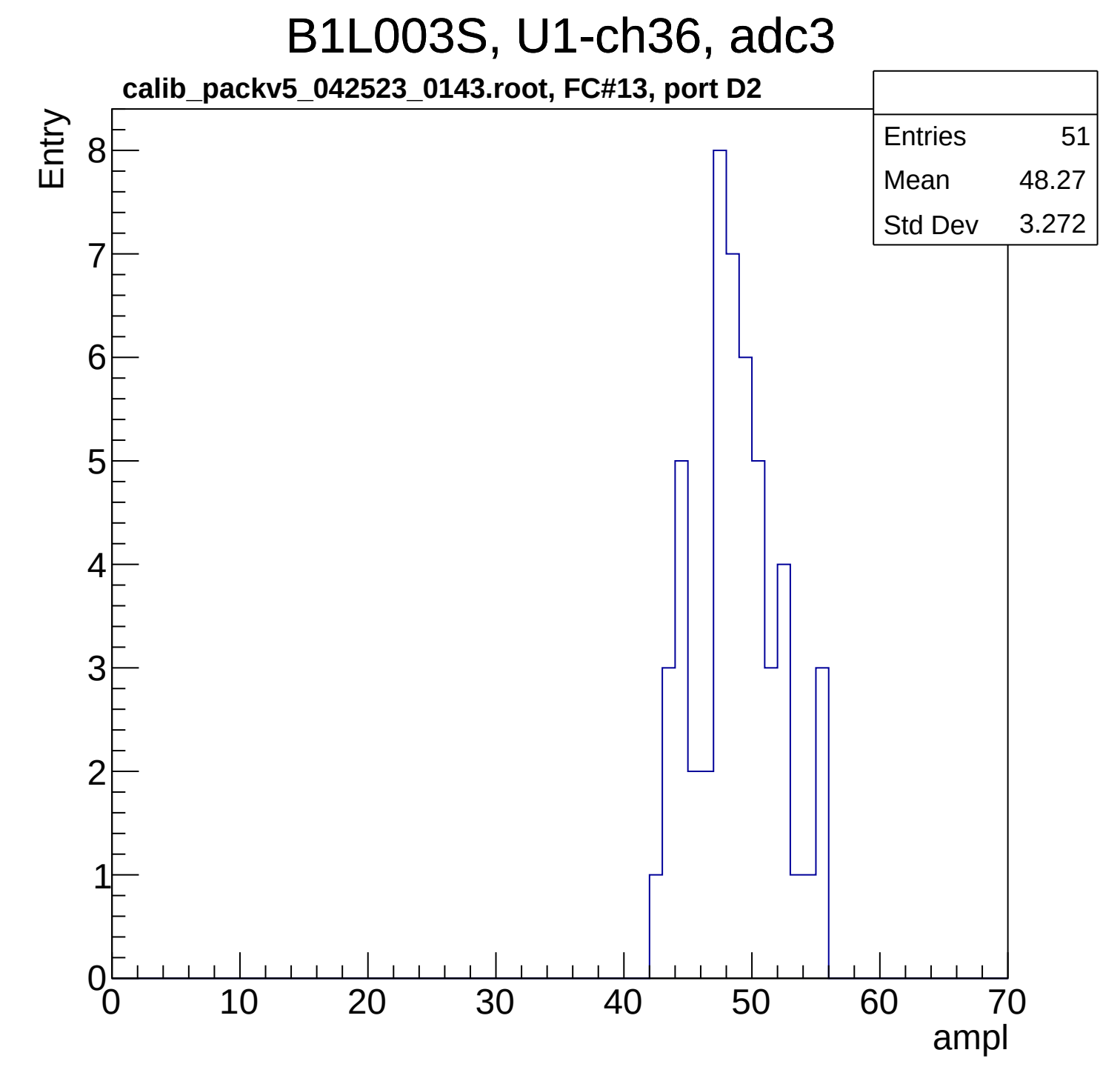
B1L003S, U1-ch36, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 48.27
Std Dev 3.272
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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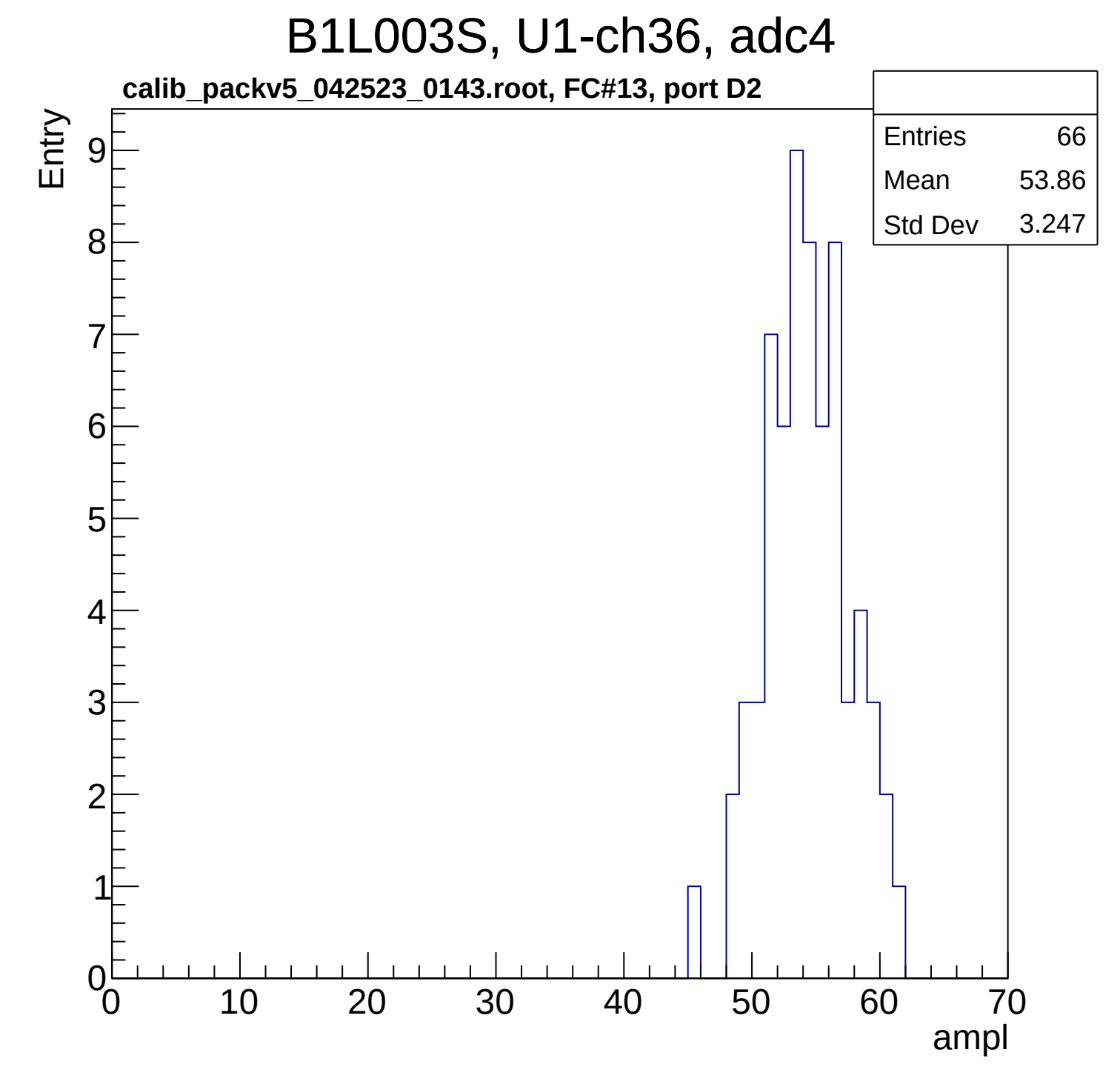
B1L003S, U1-ch36, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 53.86
Std Dev 3.247
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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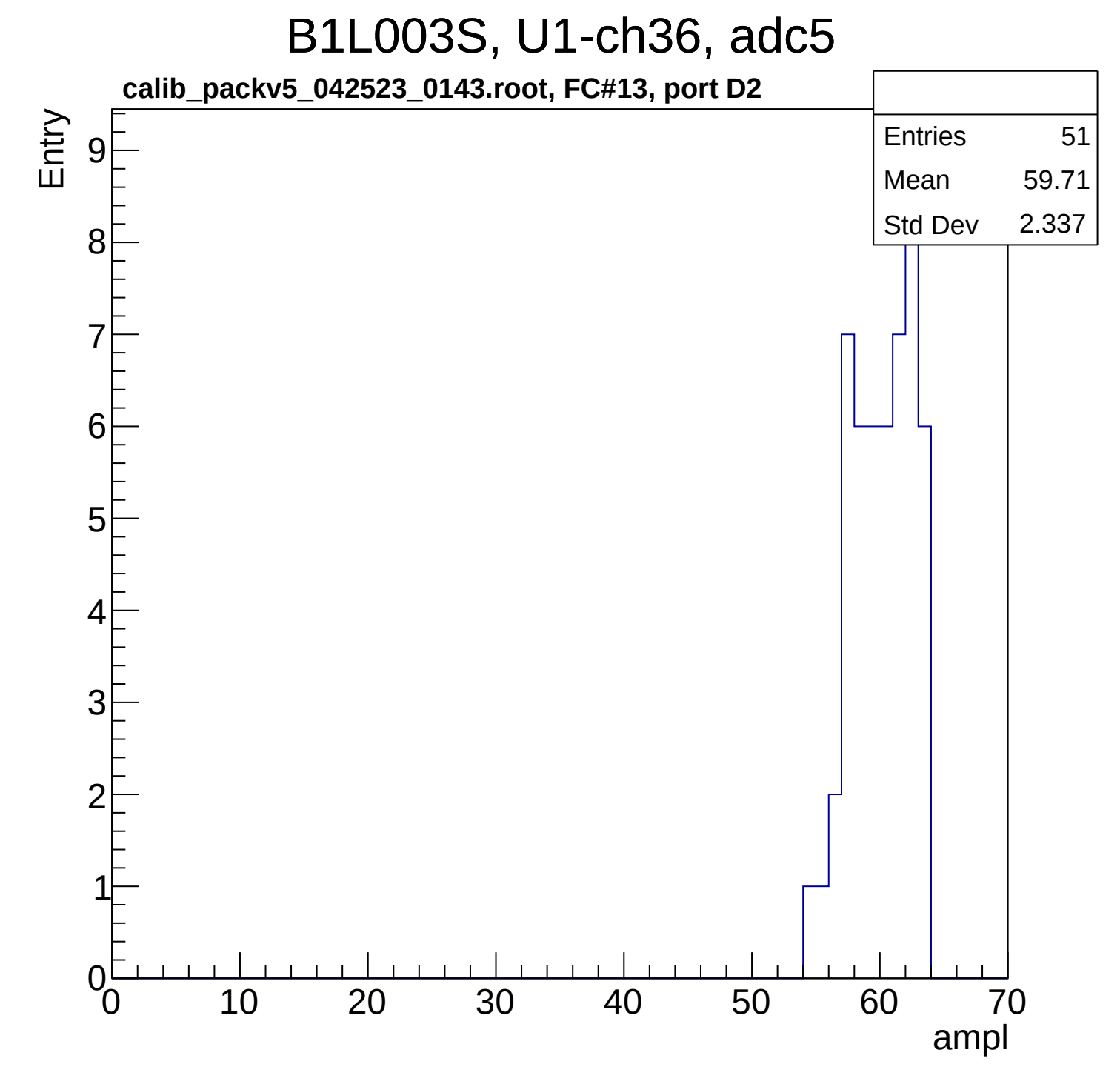
B1L003S, U1-ch36, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 59.71
Std Dev 2.337
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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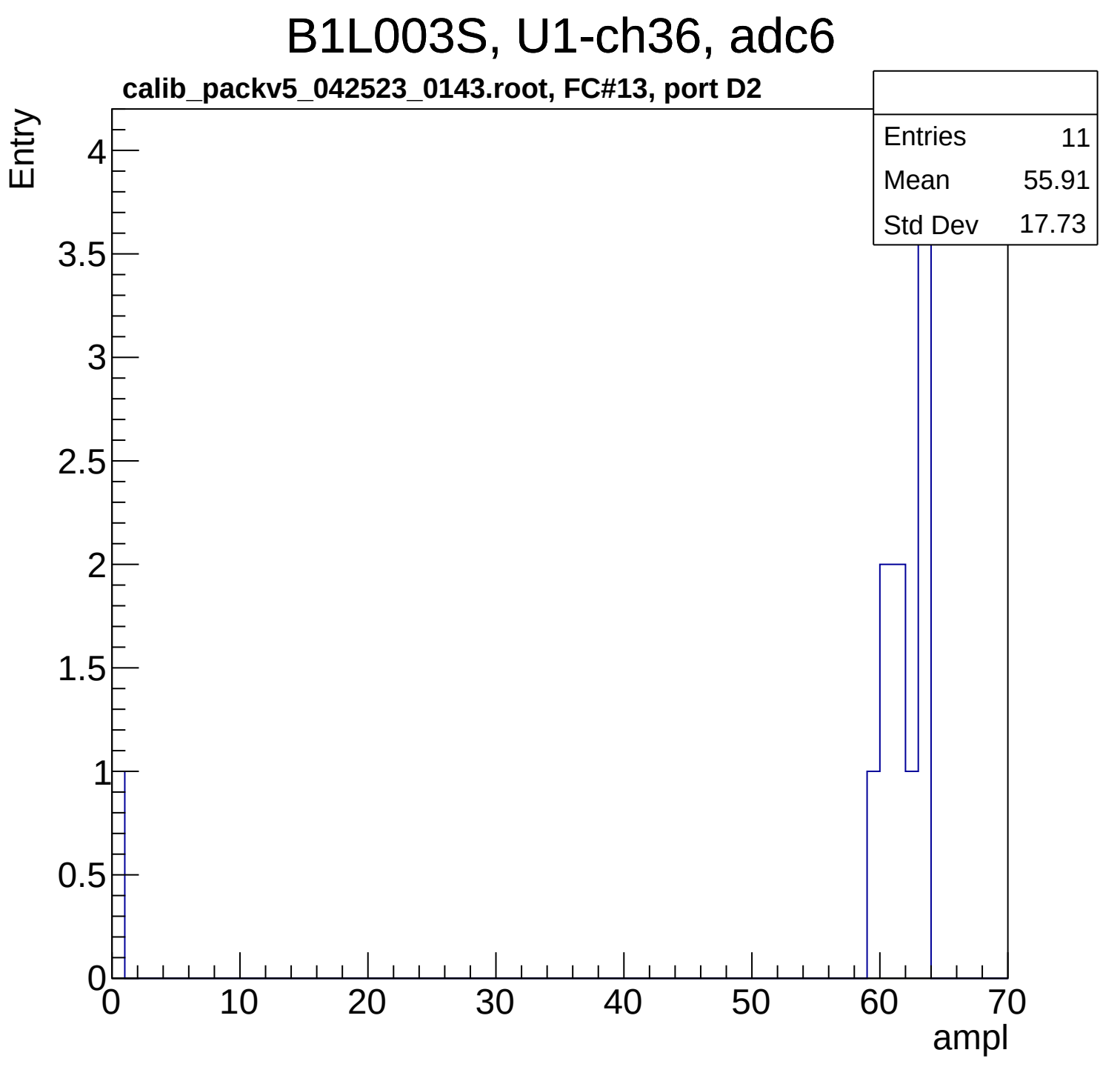
B1L003S, U1-ch36, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.91
Std Dev 17.73
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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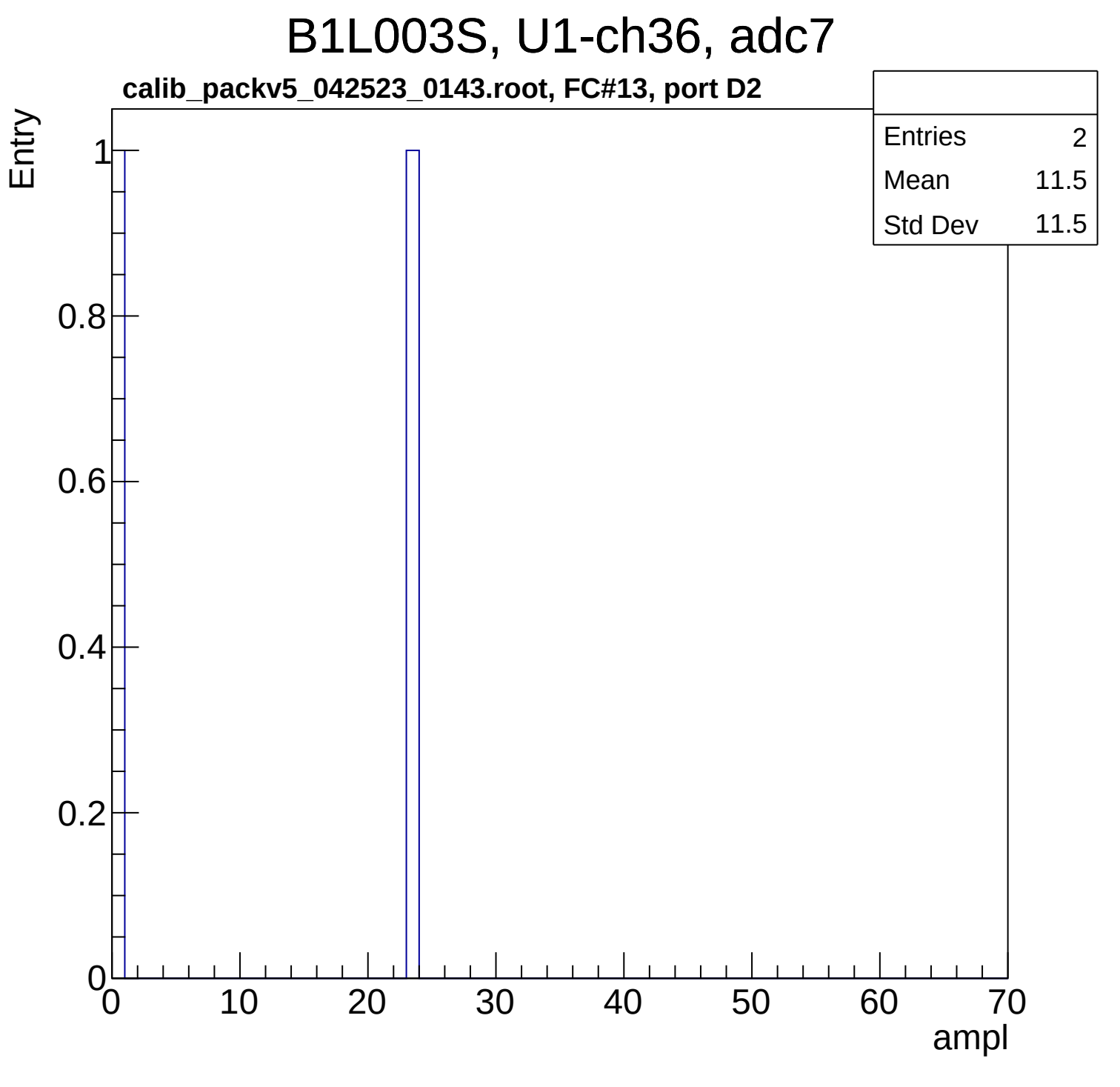
B1L003S, U1-ch36, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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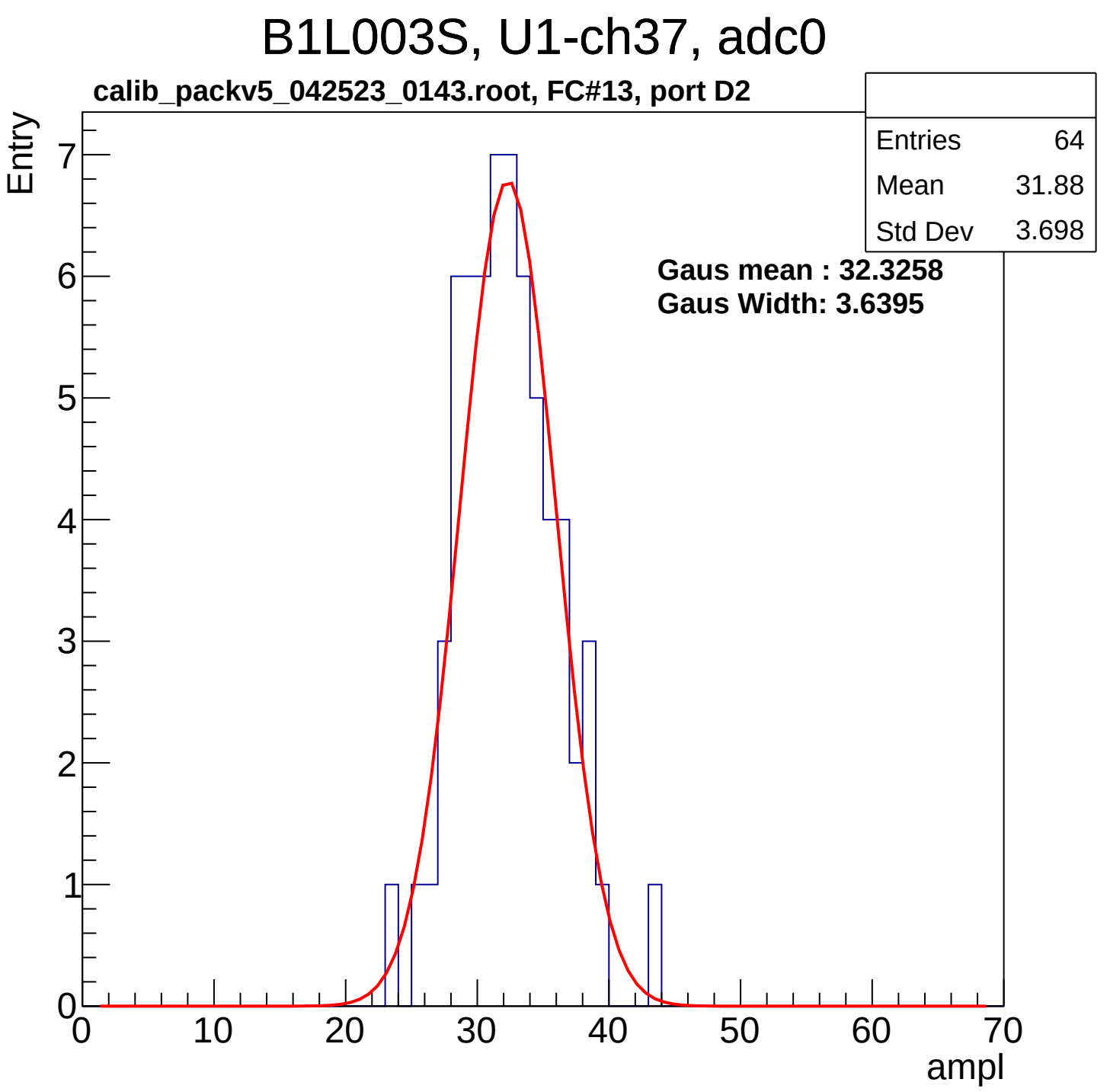
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch37, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 31.88
Std Dev 3.698
Gaus mean : 32.3258
Gaus Width: 3.6395
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
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60
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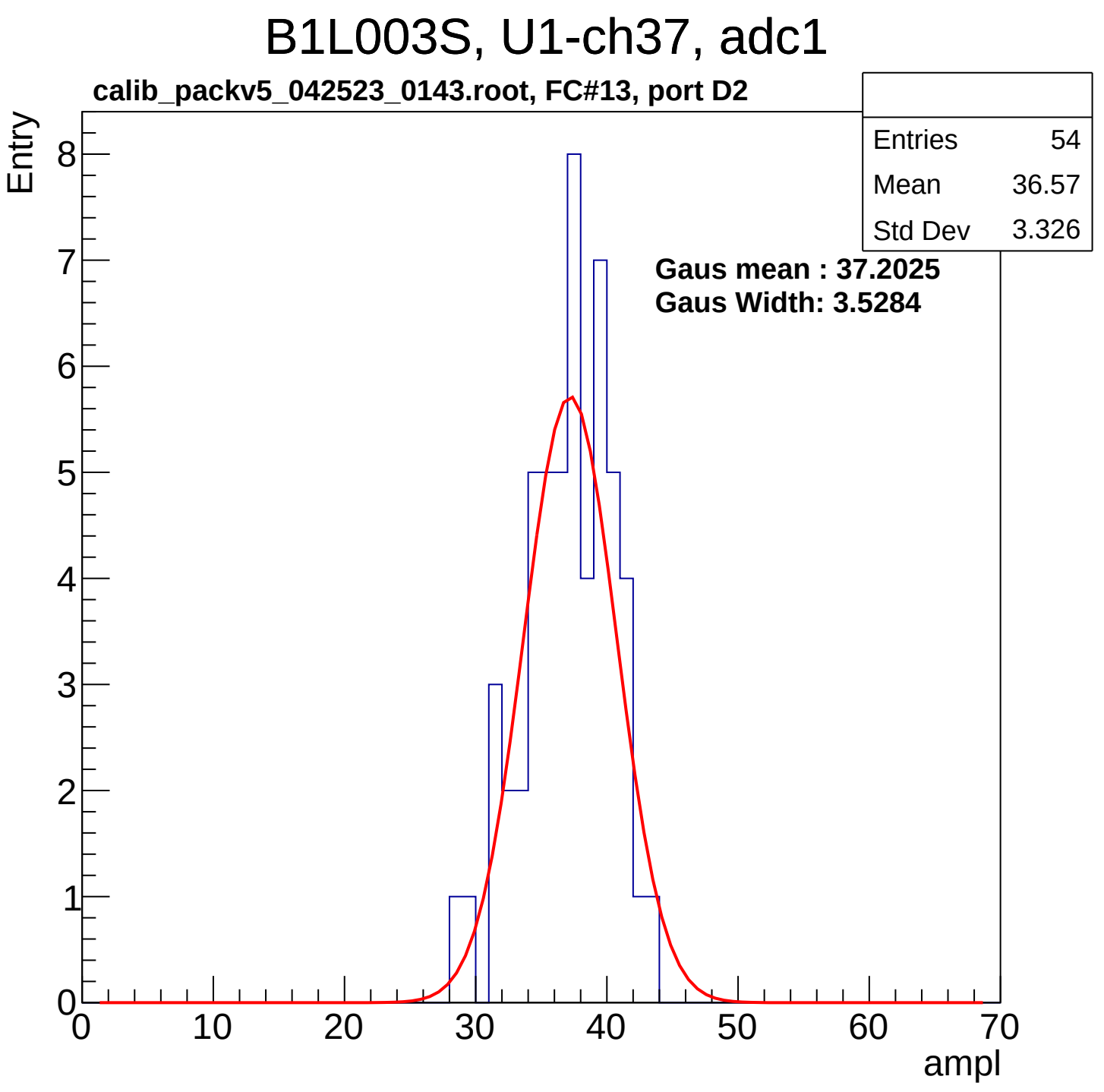
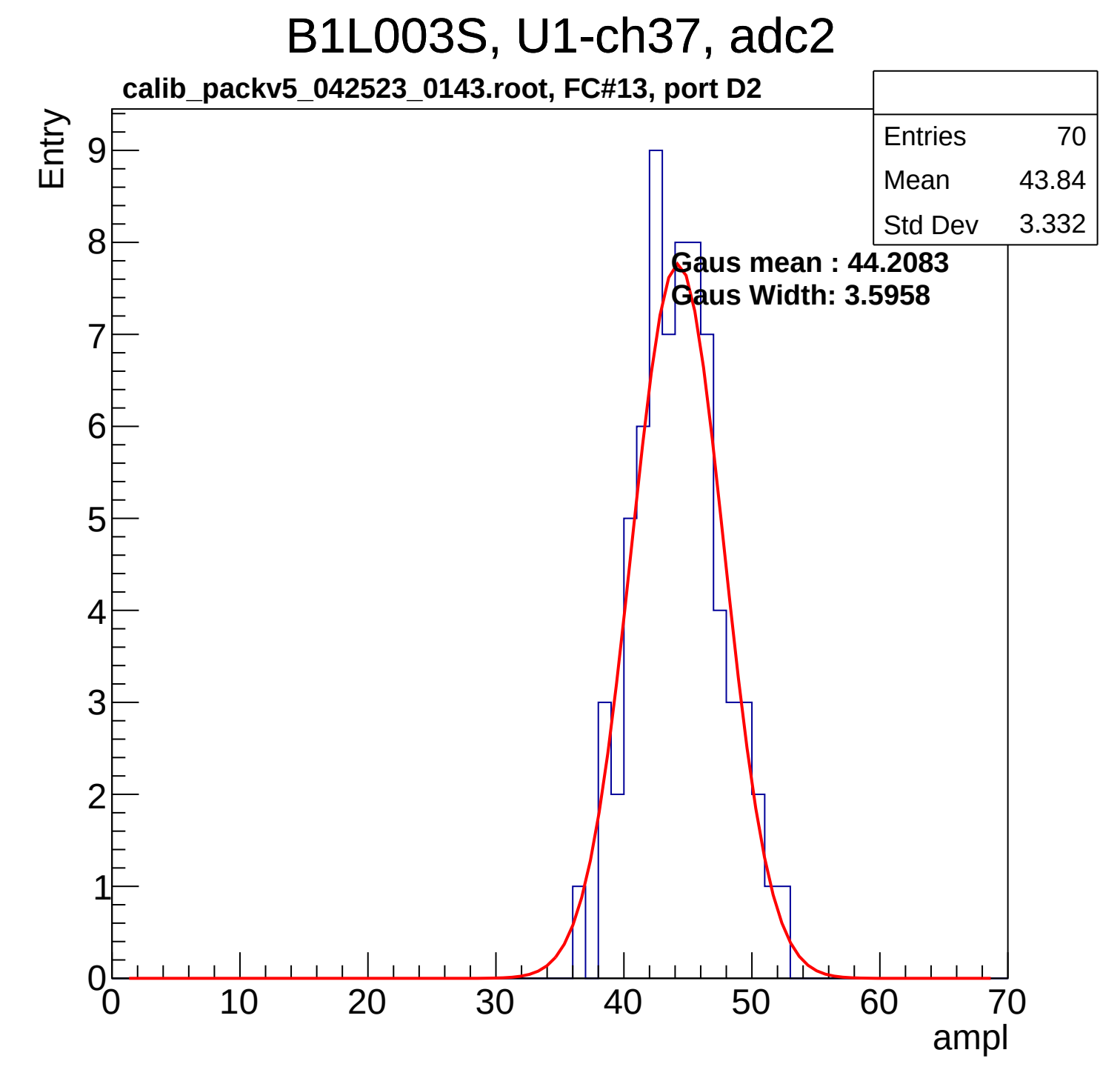

B1L003S, U1-ch37, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 36.57
Std Dev 3.326
Gaus mean : 37.2025
Gaus Width: 3.5284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch37, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.84
Std Dev 3.332
Gaus mean : 44.2083
Gaus Width: 3.5958
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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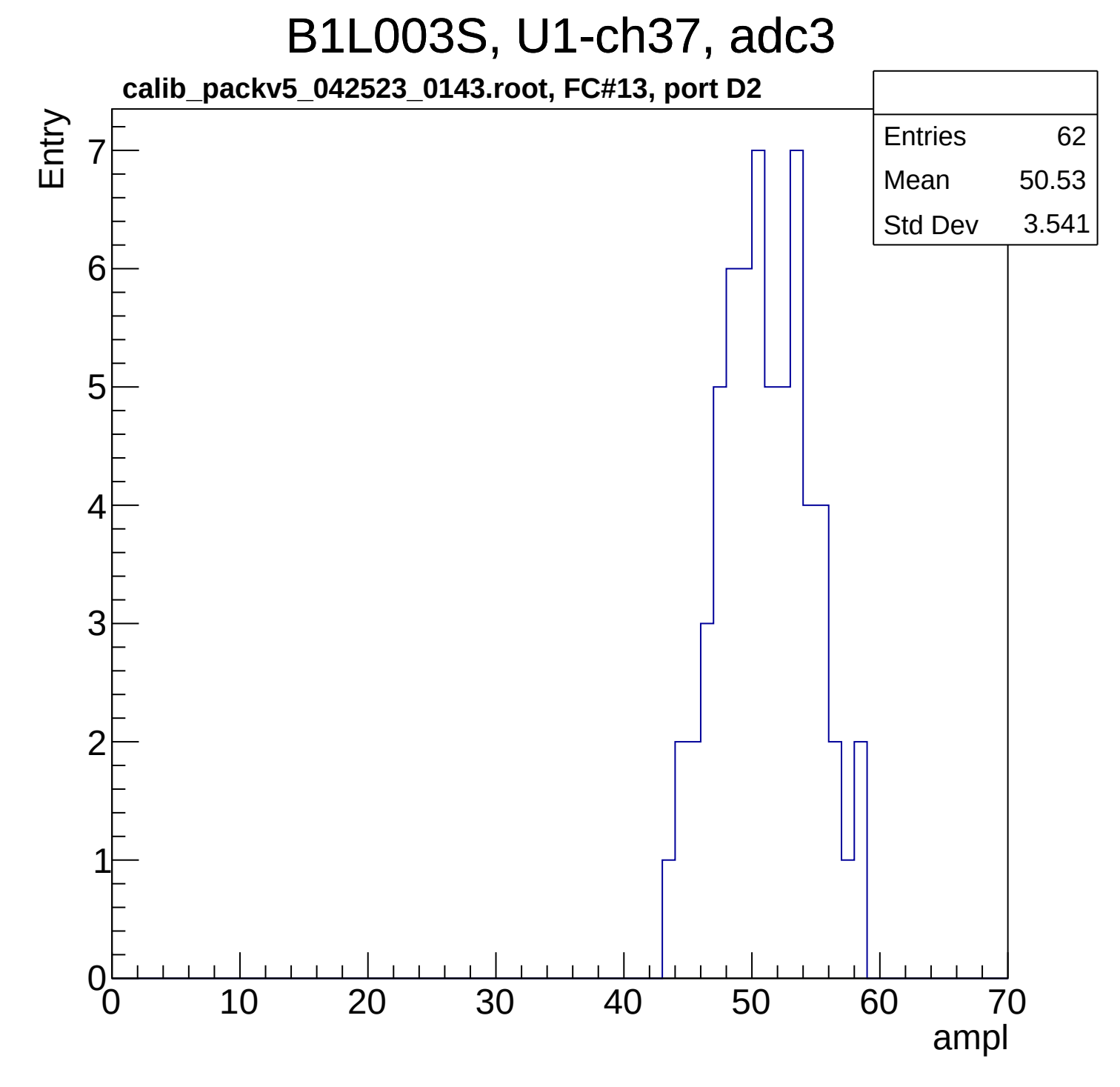
B1L003S, U1-ch37, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 50.53
Std Dev 3.541
Entry
ampl
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2
3
4
5
6
7
0
10
20
30
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60
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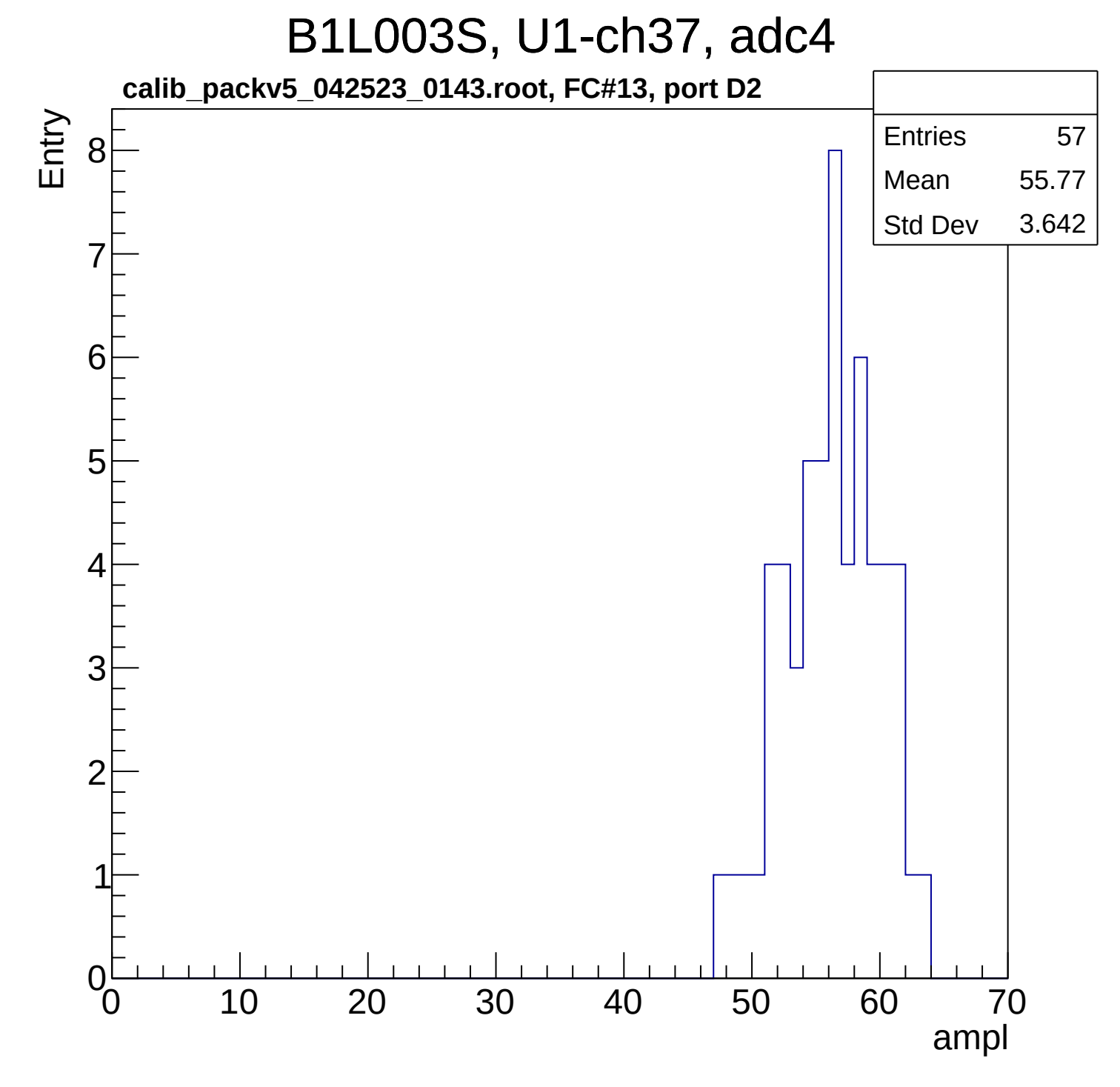
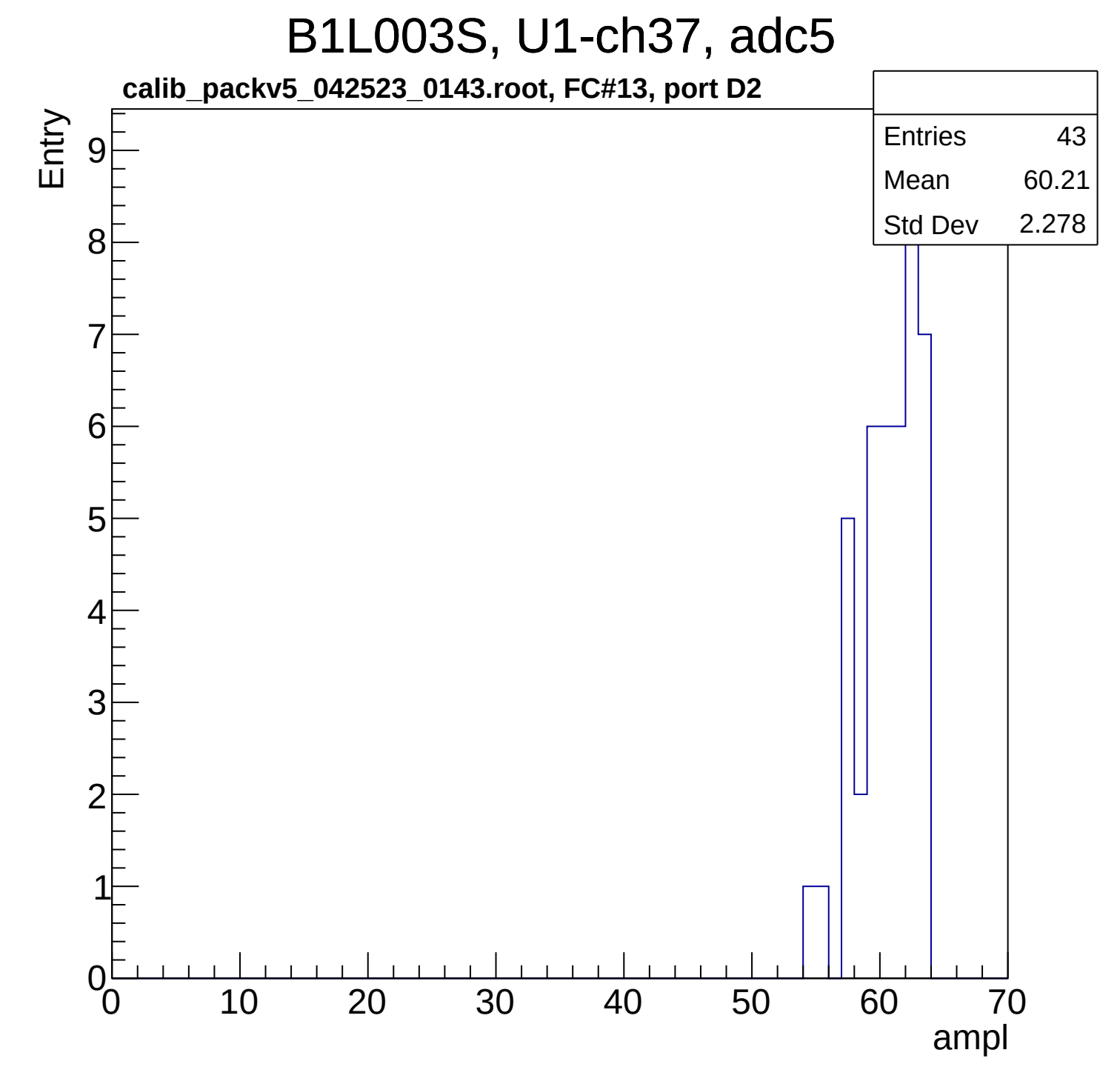

B1L003S, U1-ch37, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.77
Std Dev 3.642
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch37, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 60.21
Std Dev 2.278
Entry
ampl
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2
3
4
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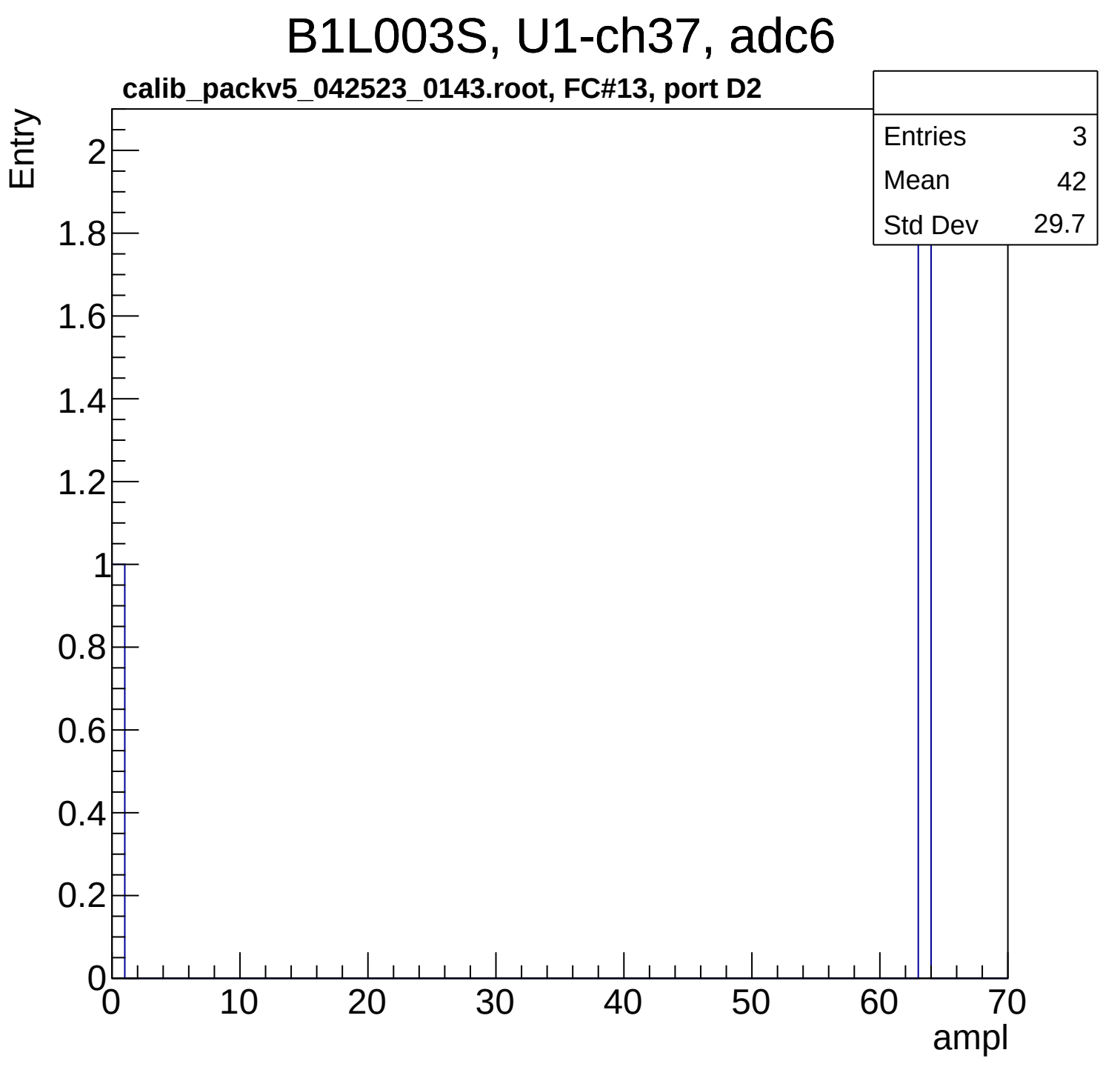
B1L003S, U1-ch37, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
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60
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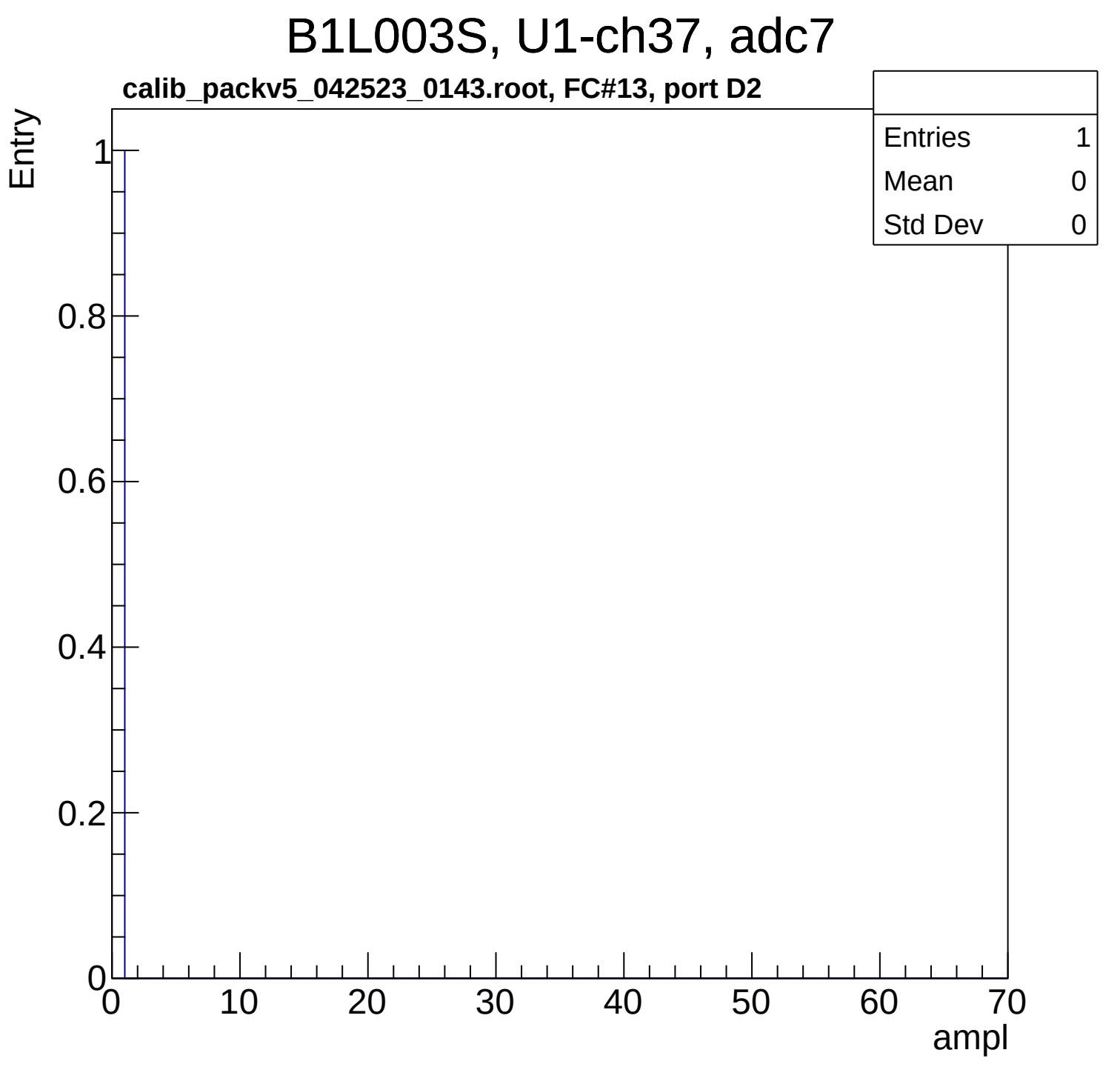
B1L003S, U1-ch37, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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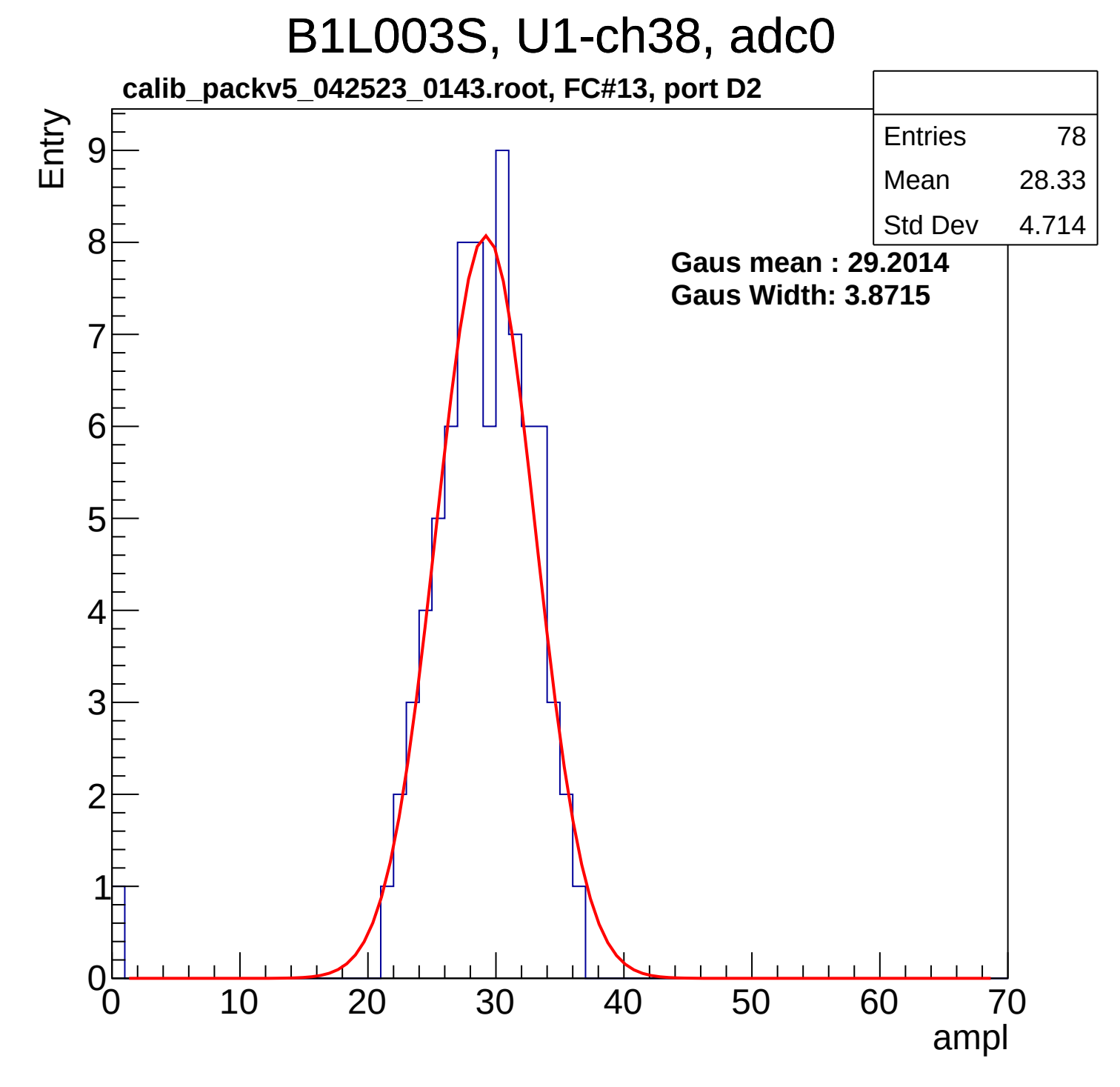
40
50
60
70
ampl


B1L003S, U1-ch38, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 28.33
Std Dev 4.714
Gaus mean : 29.2014
Gaus Width: 3.8715
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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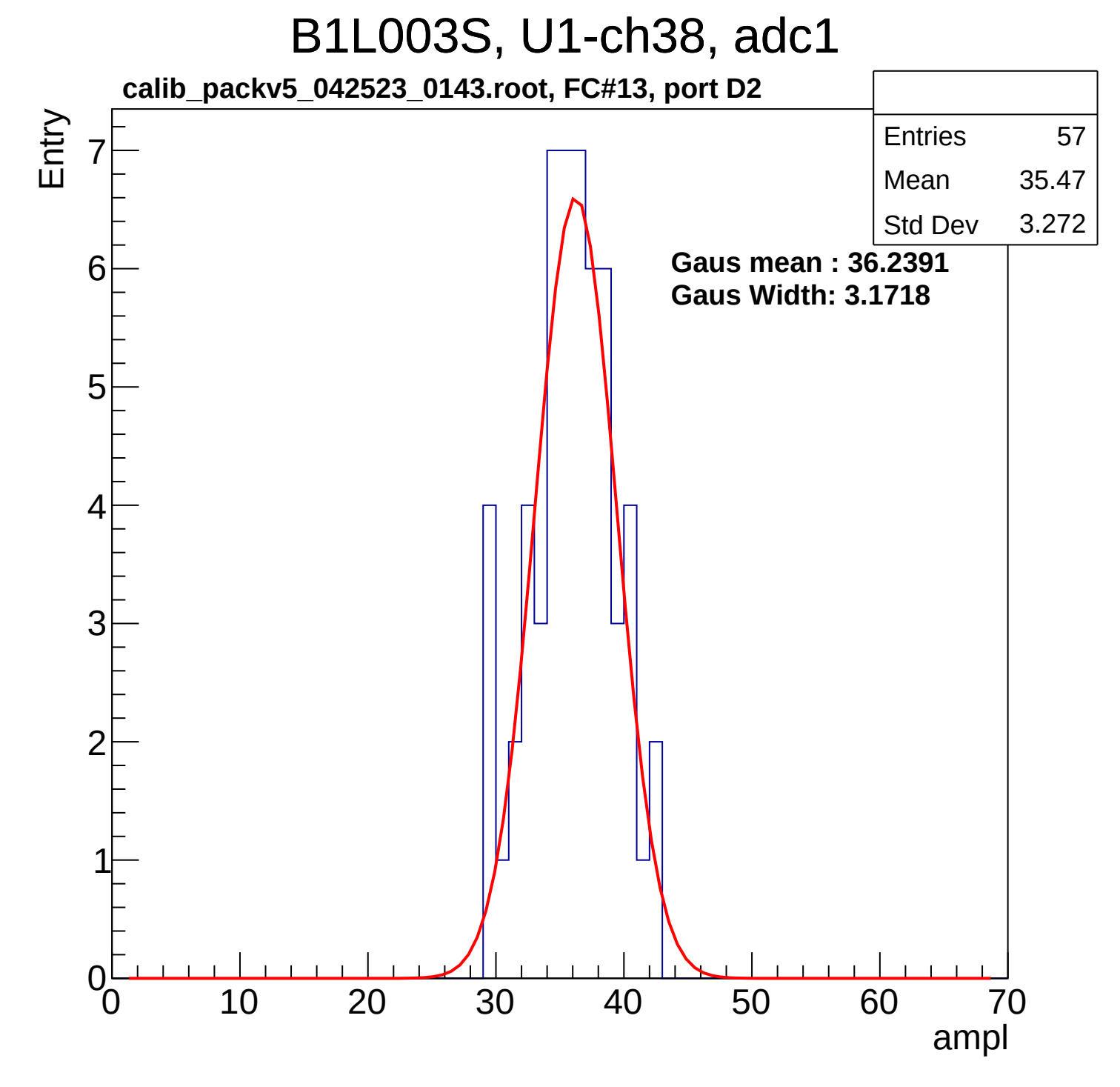
B1L003S, U1-ch38, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 35.47
Std Dev 3.272
Gaus mean : 36.2391
Gaus Width: 3.1718
Entry
ampl
0
1
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3
4
5
6
7
0
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40
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60
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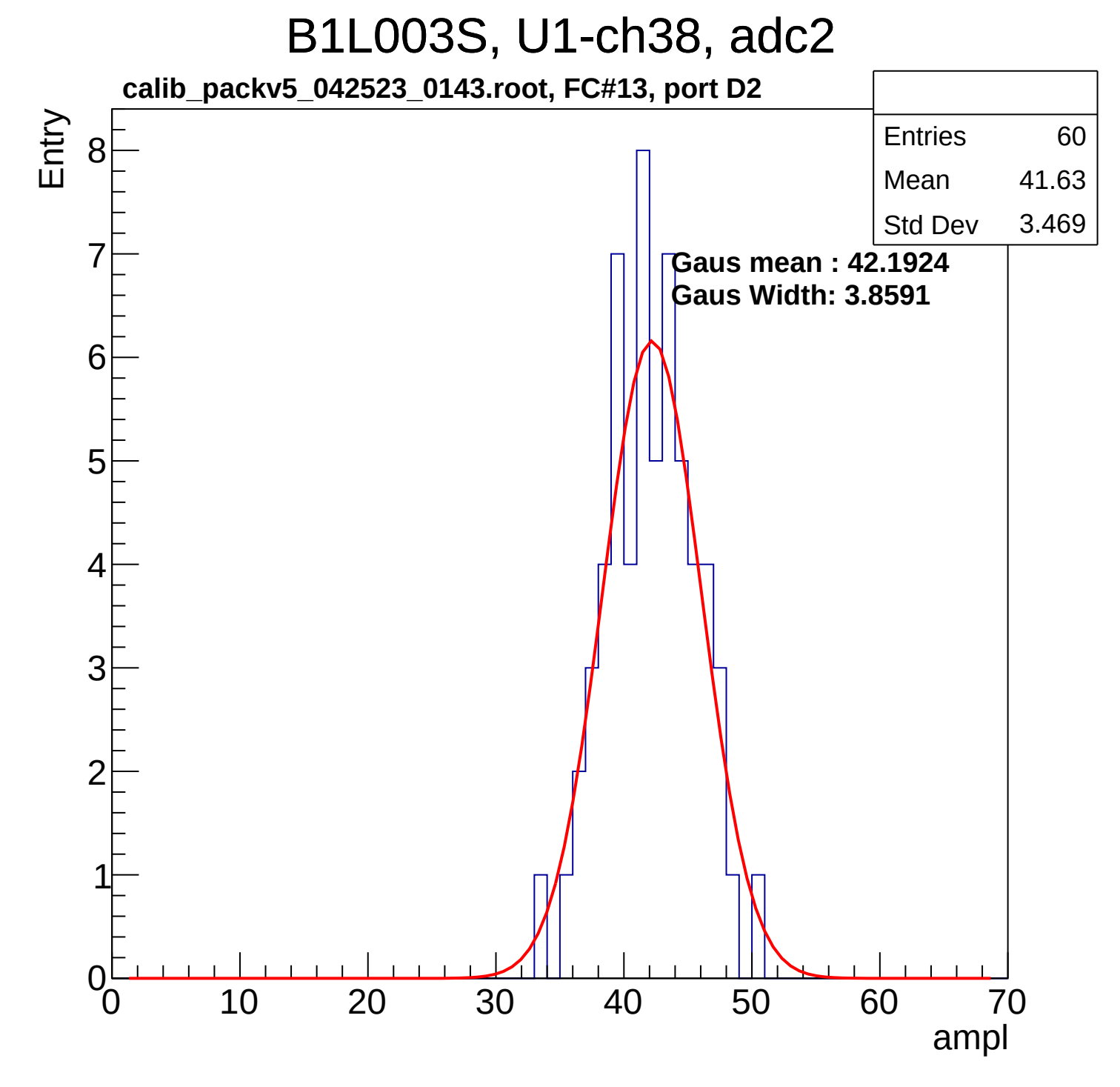
B1L003S, U1-ch38, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.63
Std Dev 3.469
Gaus mean : 42.1924
Gaus Width: 3.8591
Entry
ampl
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1
2
3
4
5
6
7
8
0
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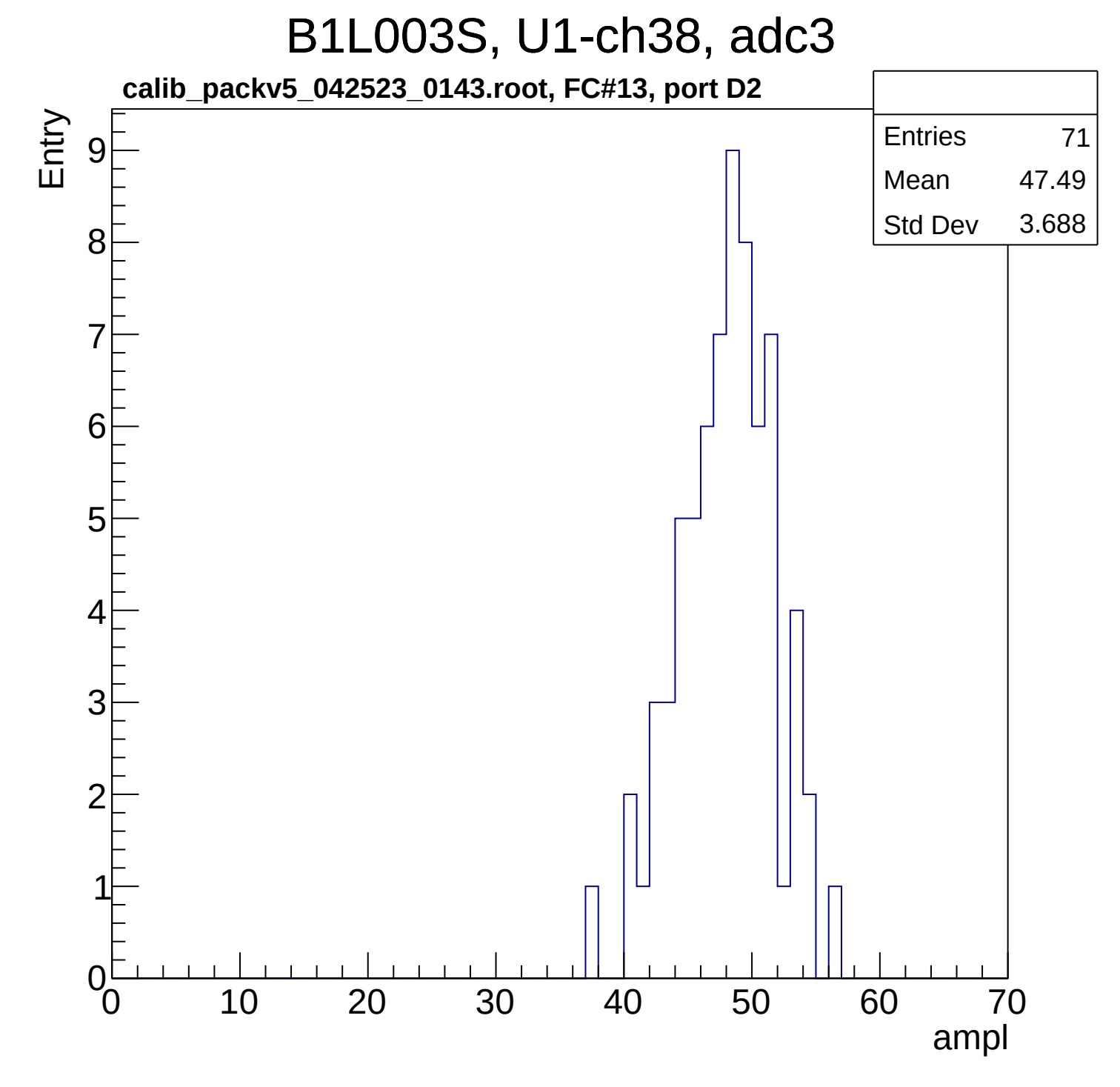
40
50
60
70


B1L003S, U1-ch38, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.49
Std Dev 3.688
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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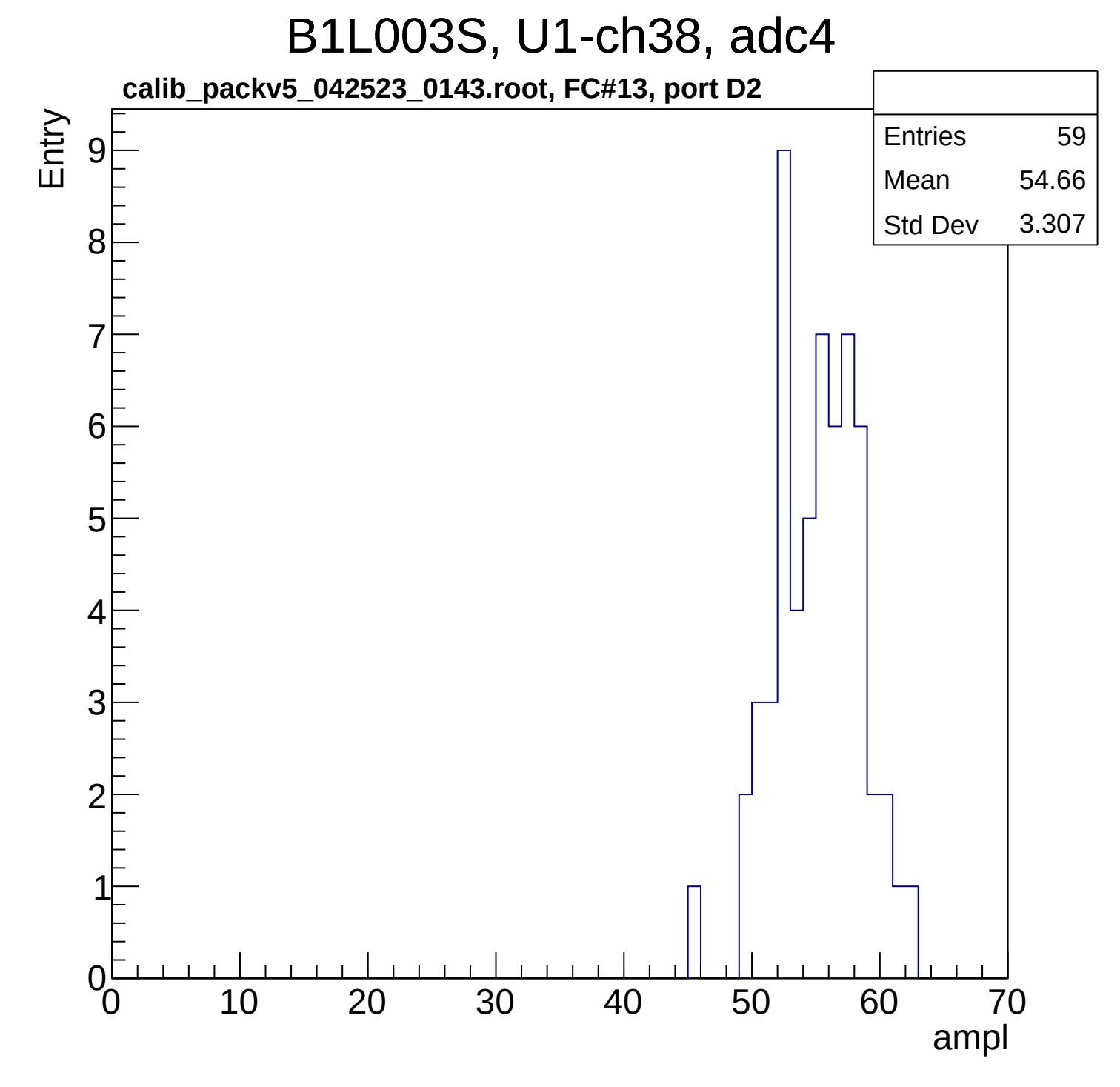
B1L003S, U1-ch38, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.66
Std Dev 3.307
Entry
ampl
0
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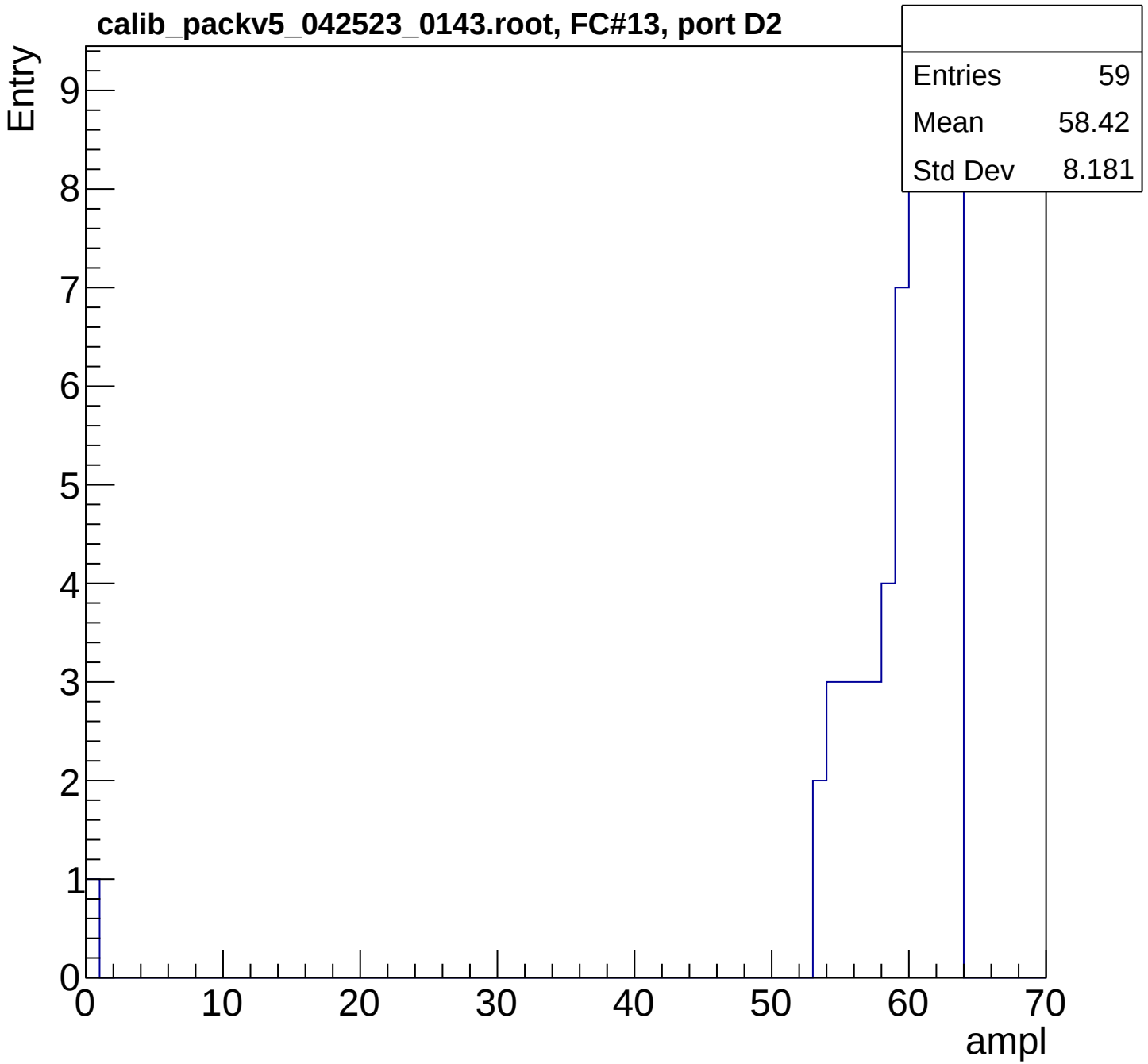
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch38, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.42
Std Dev 8.181
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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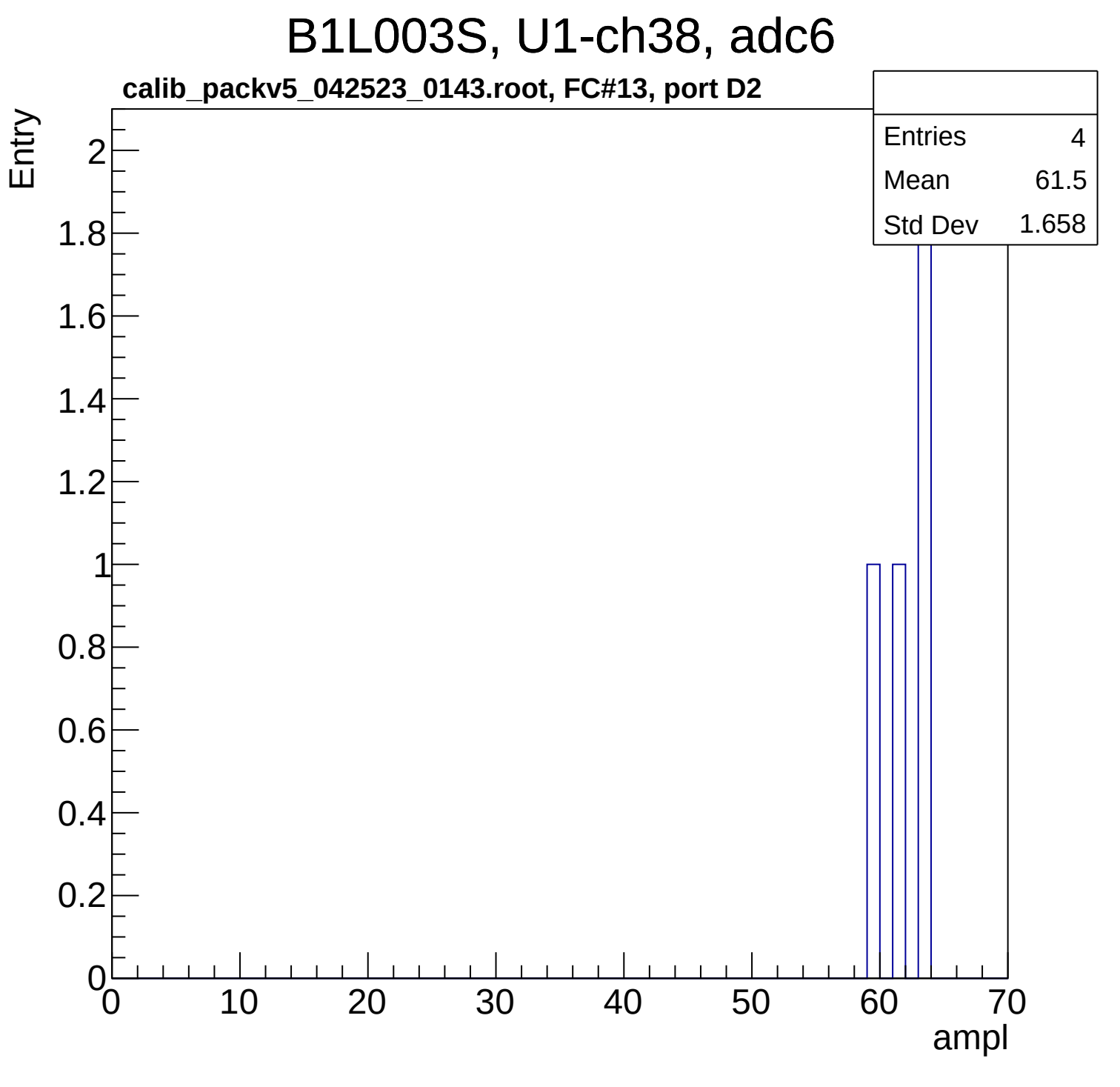
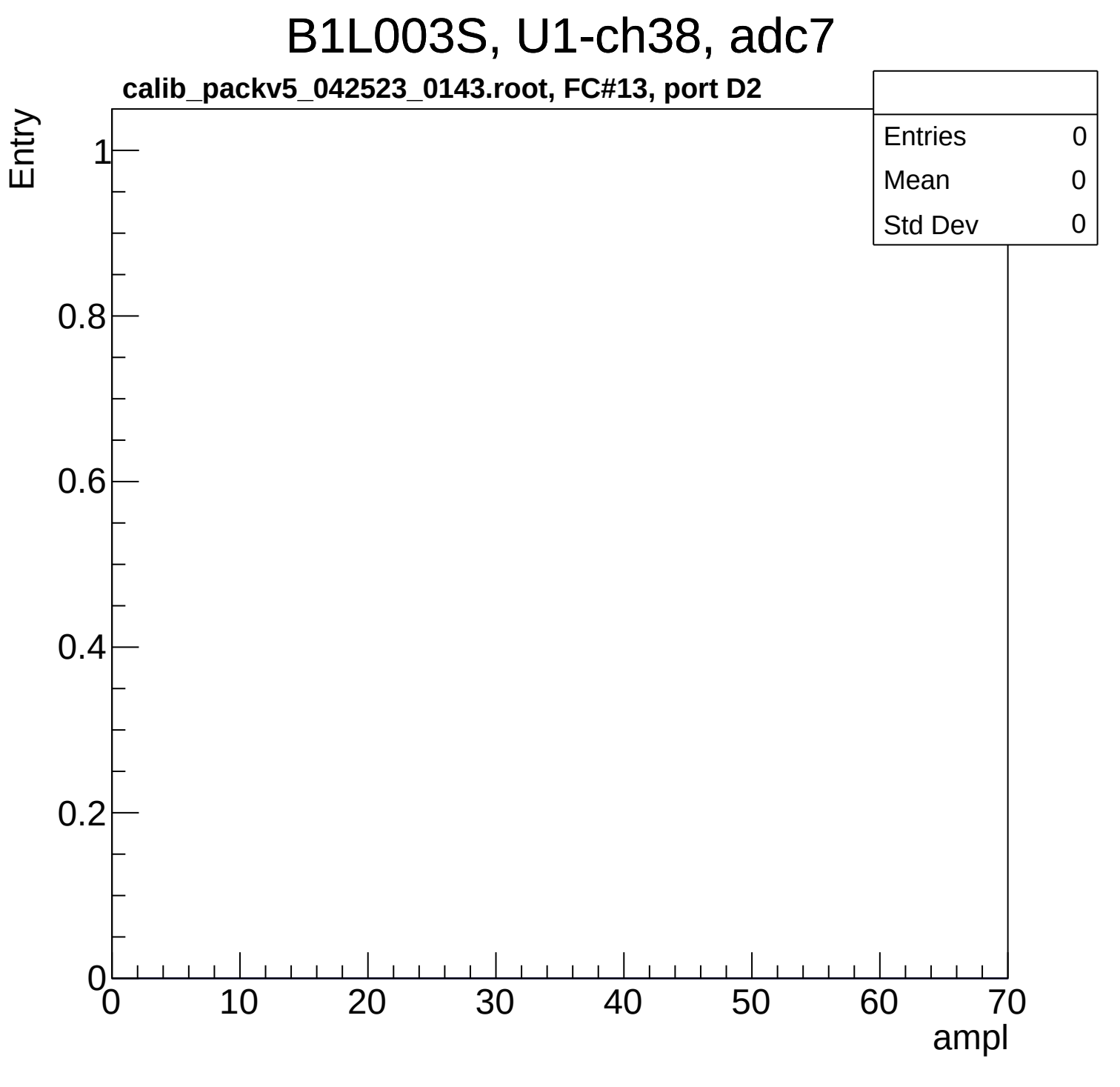

B1L003S, U1-ch38, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.5
Std Dev 1.658
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch38, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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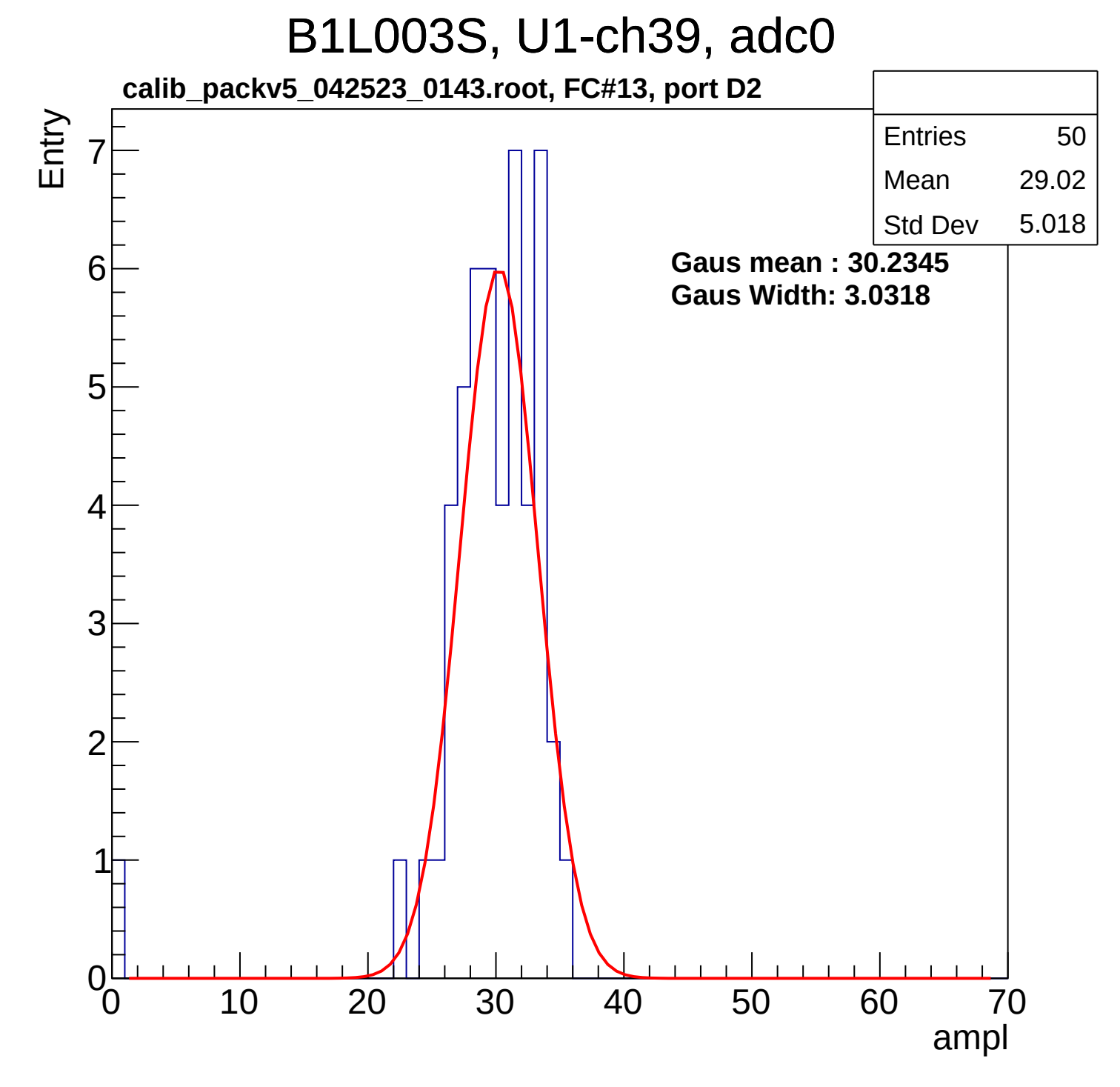
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch39, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 29.02
Std Dev 5.018
Gaus mean : 30.2345
Gaus Width: 3.0318
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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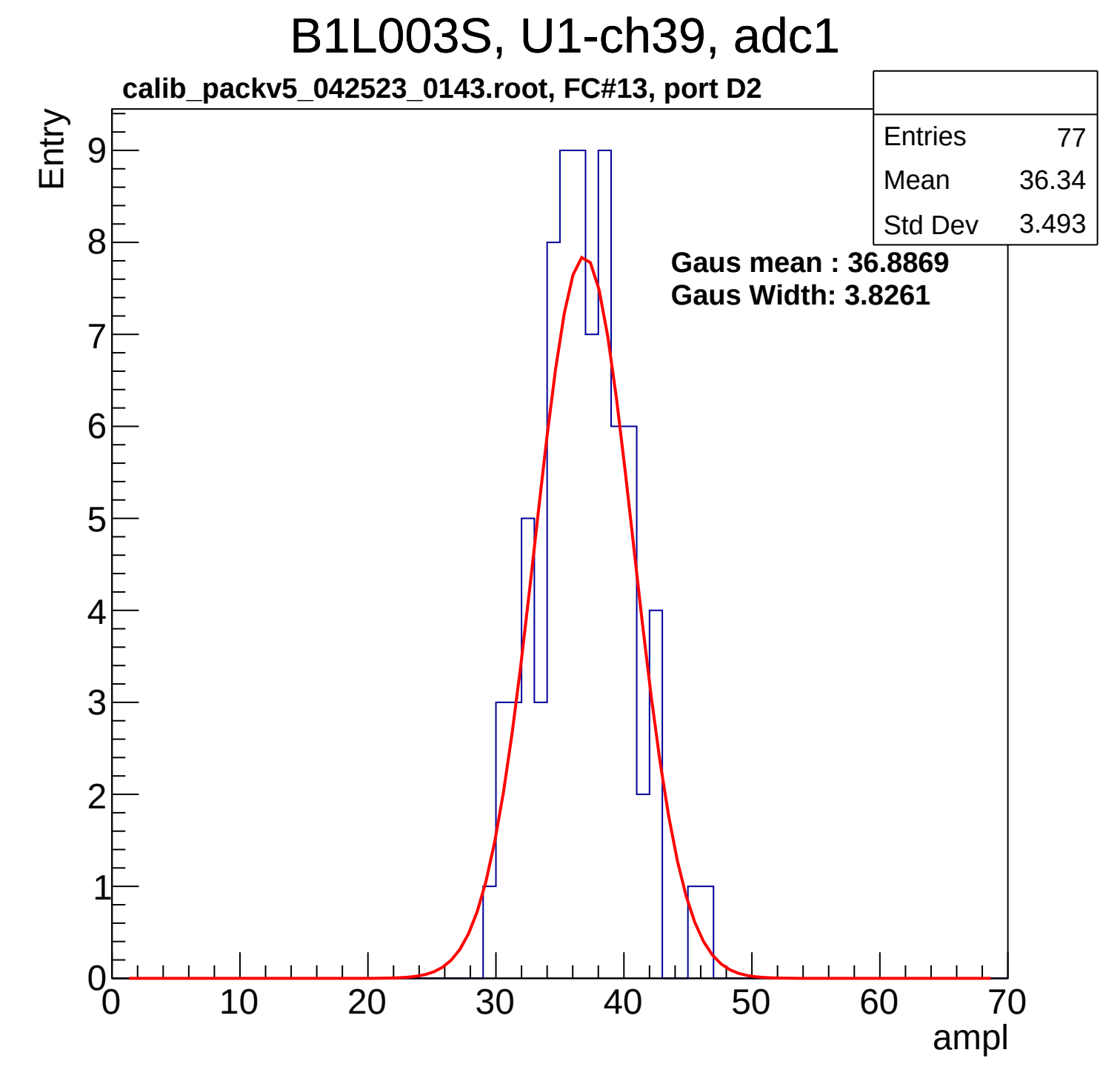
B1L003S, U1-ch39, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.34
Std Dev 3.493
Gaus mean : 36.8869
Gaus Width: 3.8261
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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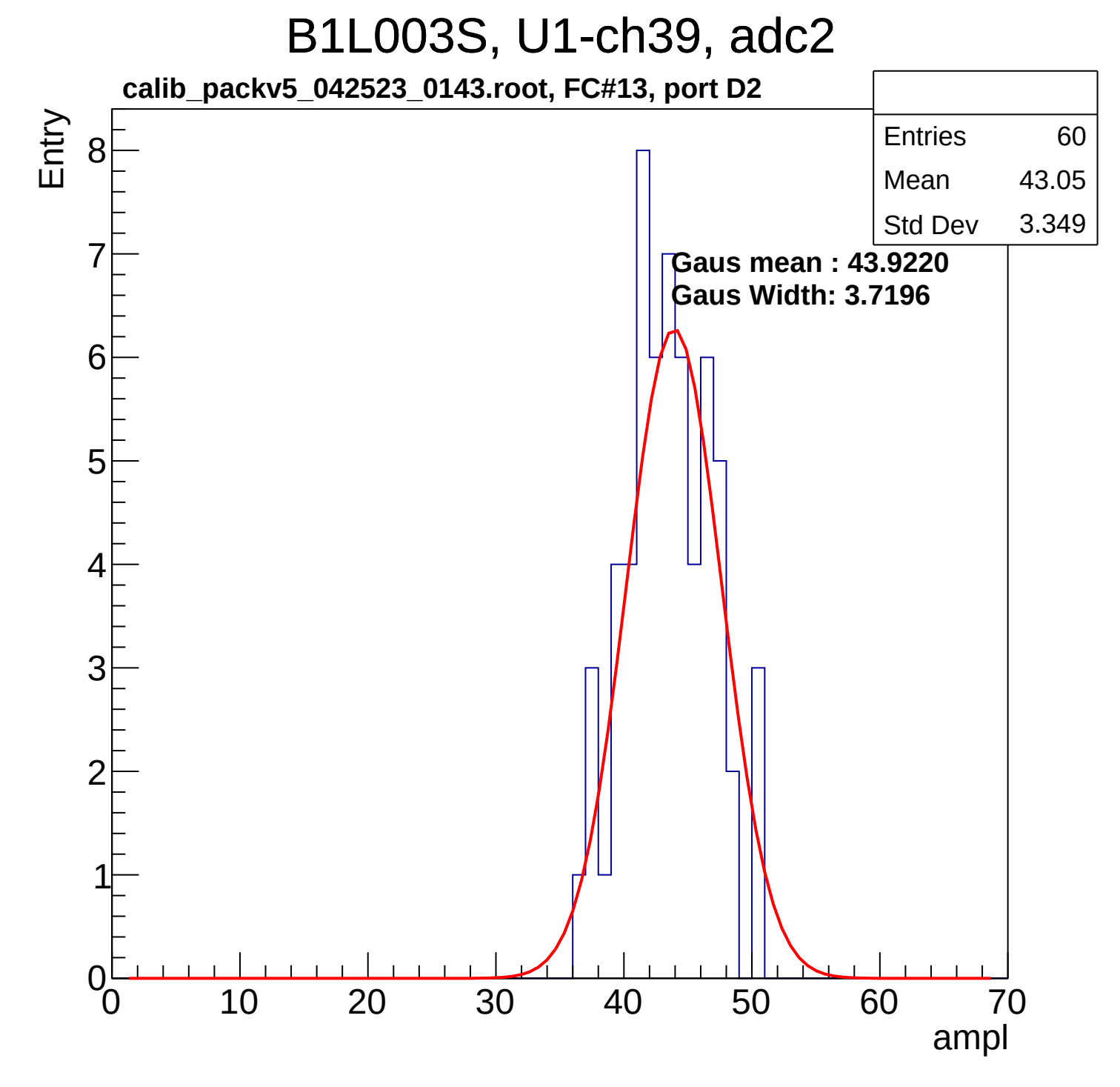
60
70


B1L003S, U1-ch39, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.05
Std Dev 3.349
Gaus mean : 43.9220
Gaus Width: 3.7196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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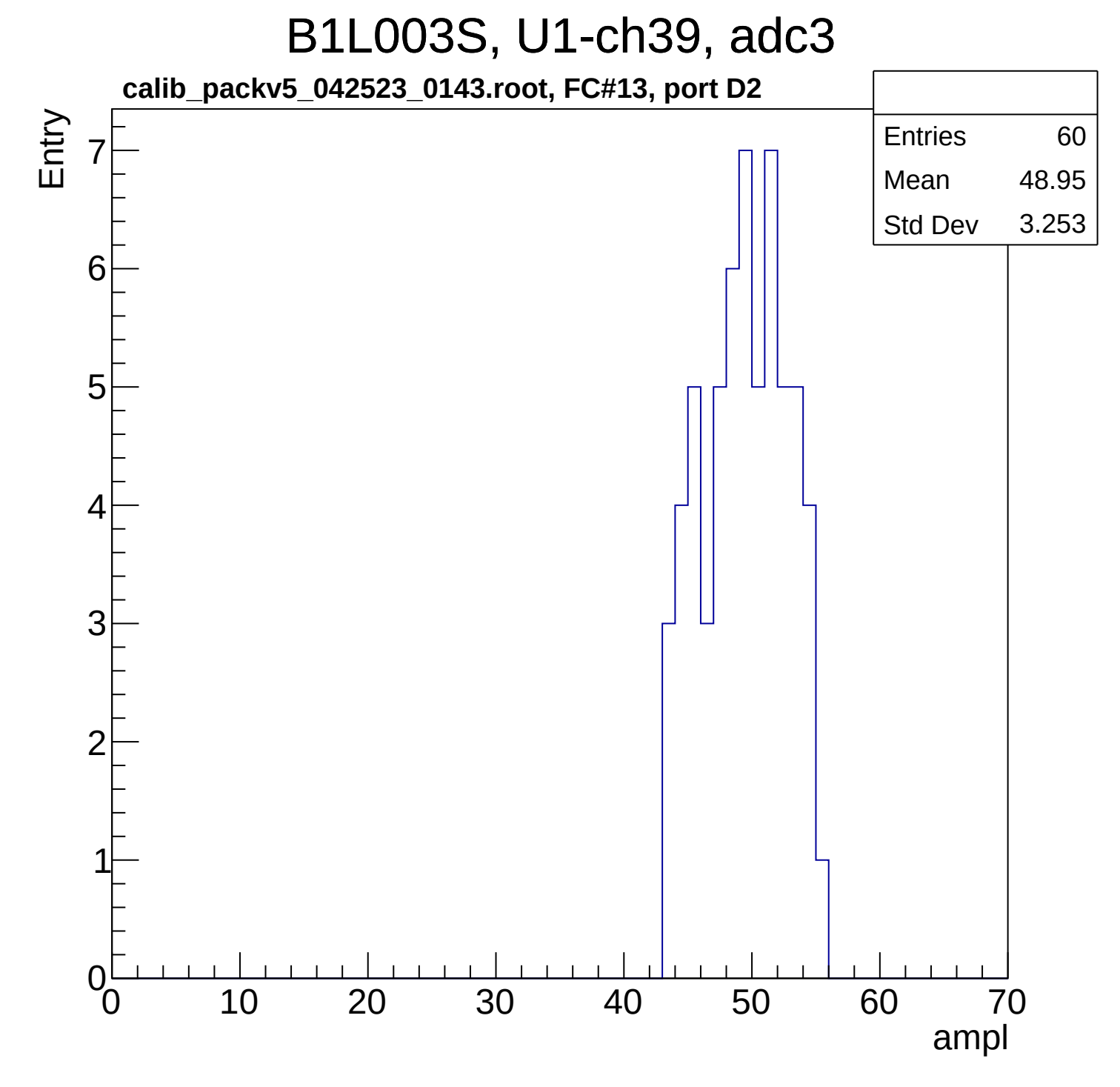
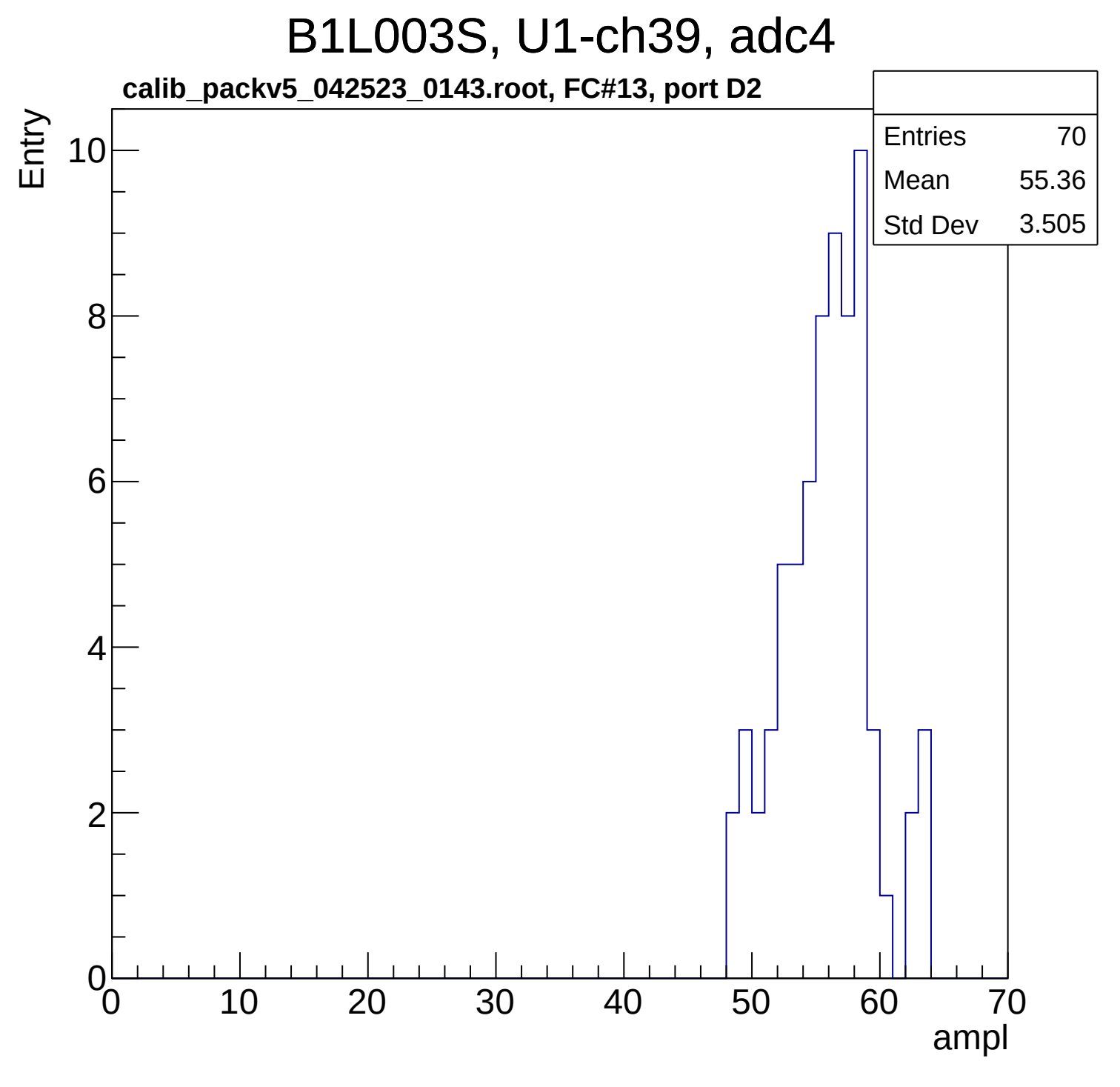

B1L003S, U1-ch39, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.95
Std Dev 3.253
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch39, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 55.36
Std Dev 3.505
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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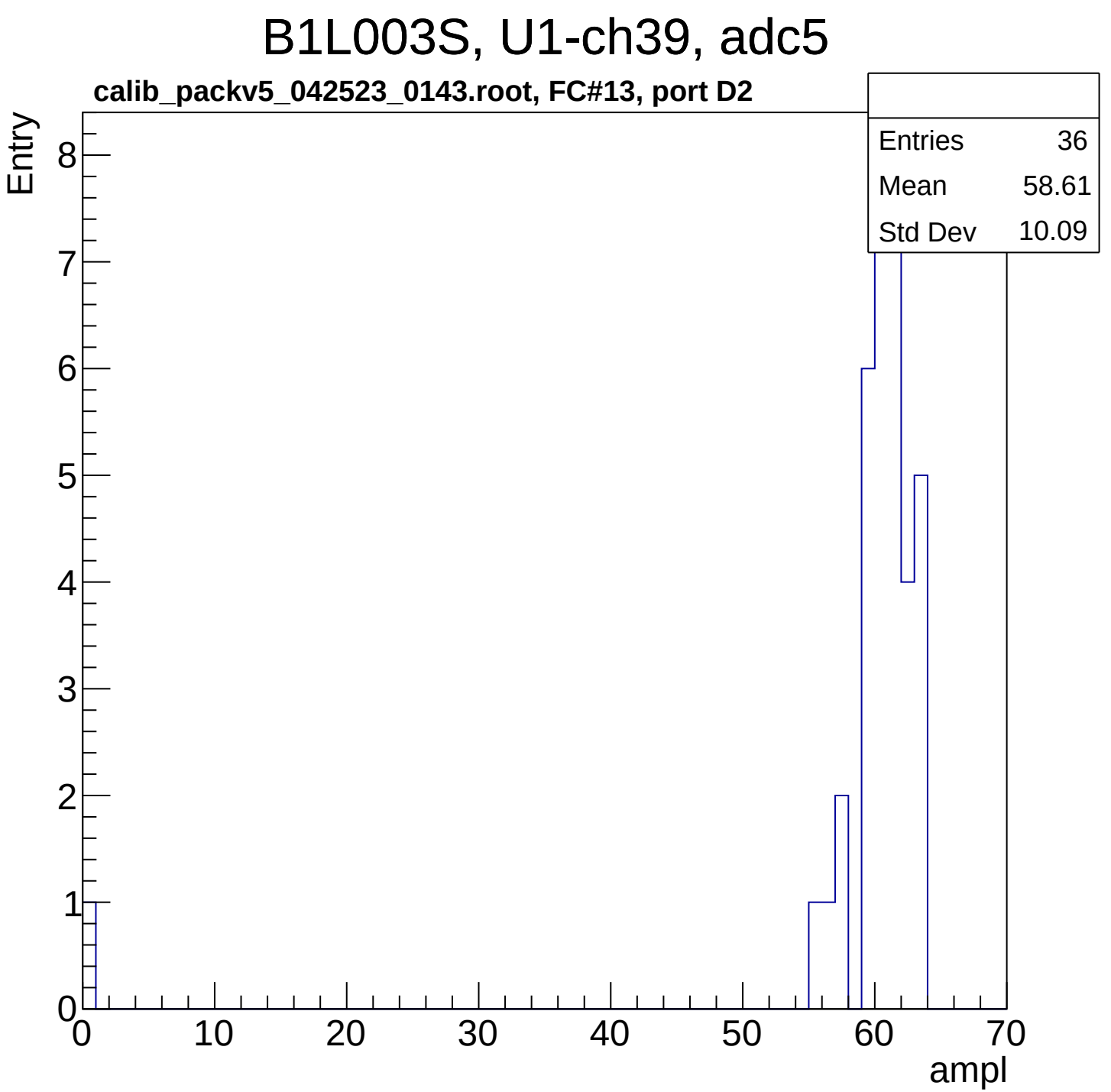
B1L003S, U1-ch39, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 58.61
Std Dev 10.09
Entry
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2
3
4
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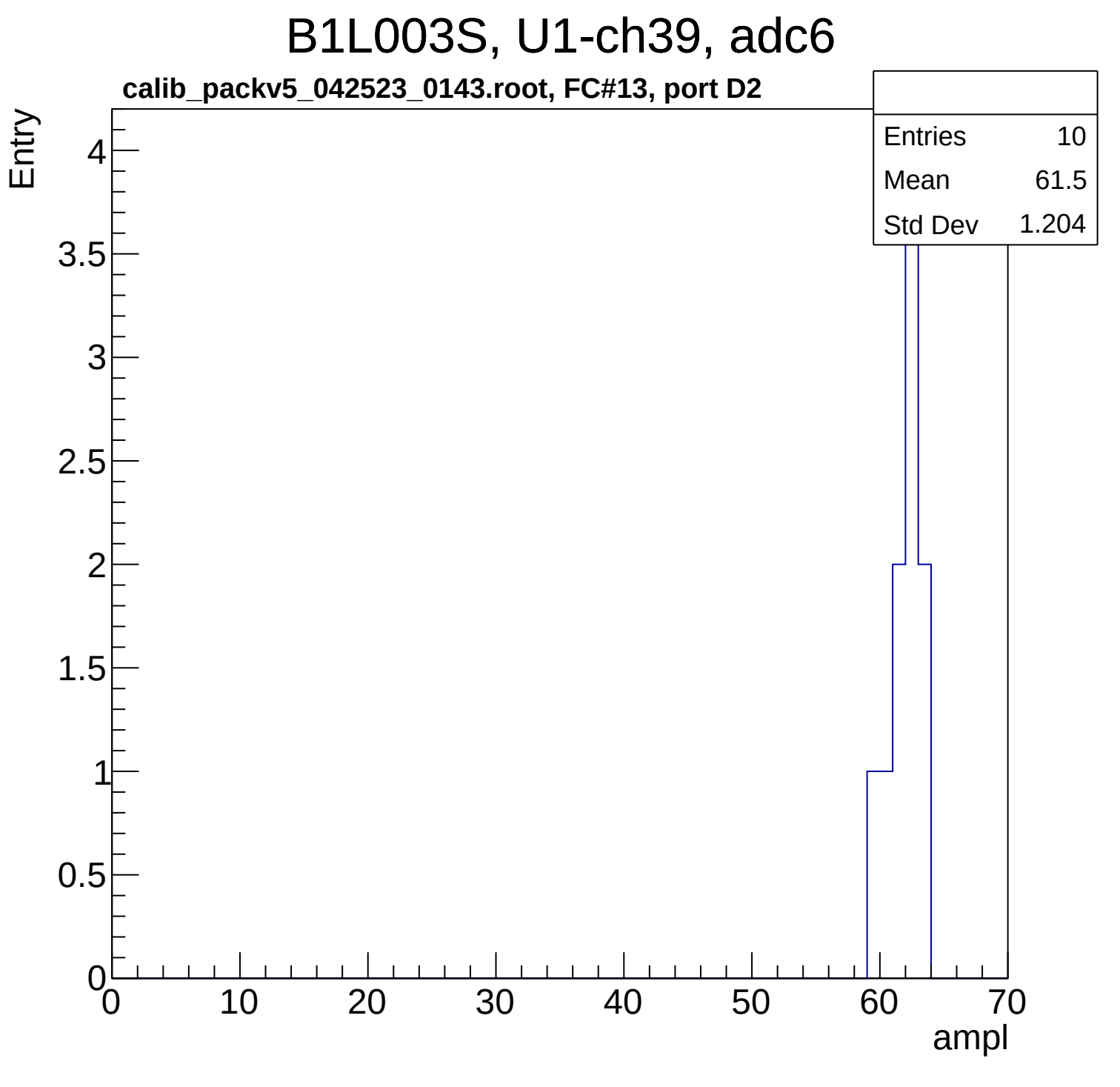
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch39, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.5
Std Dev 1.204
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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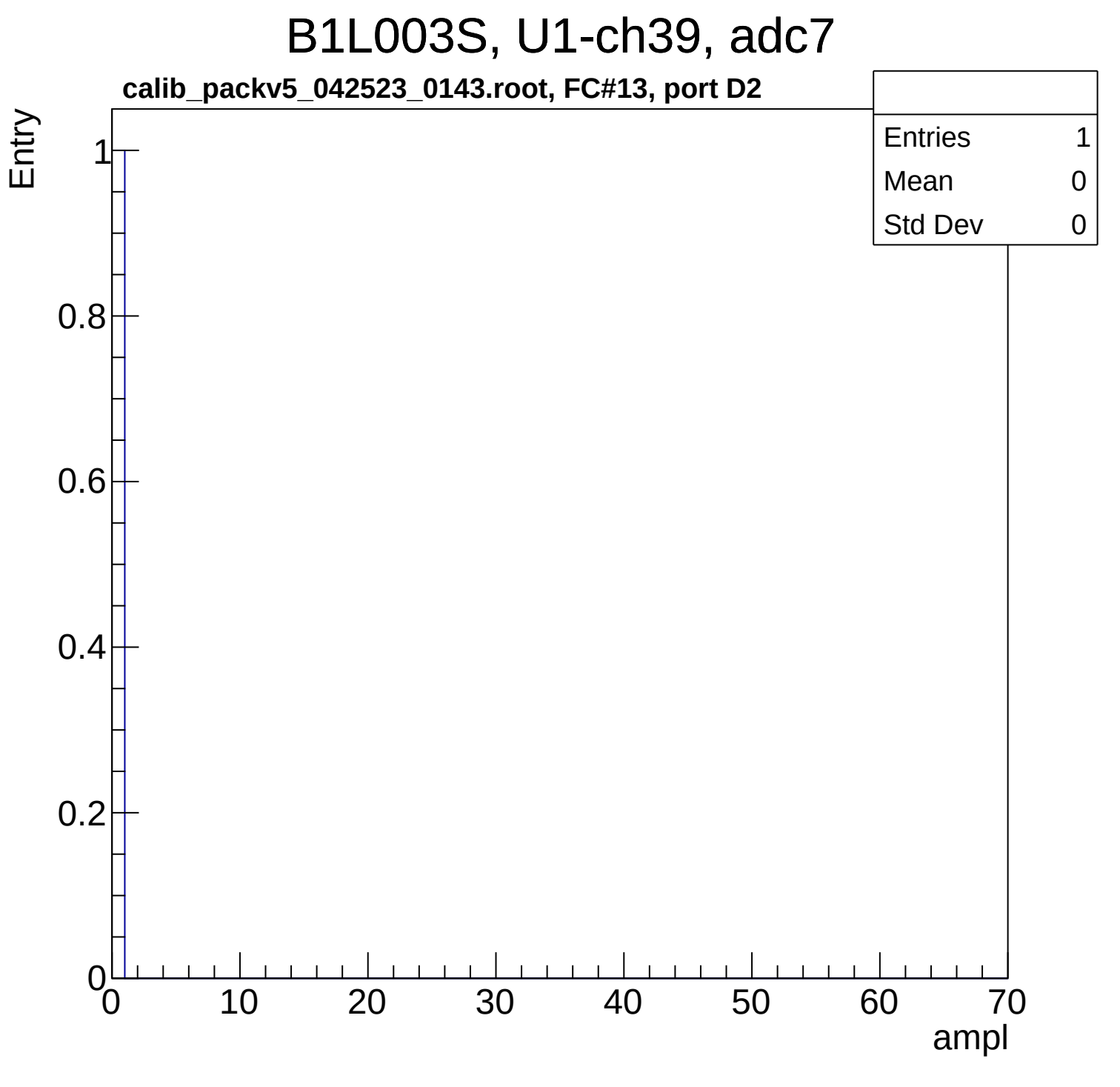
60
70


B1L003S, U1-ch39, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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30
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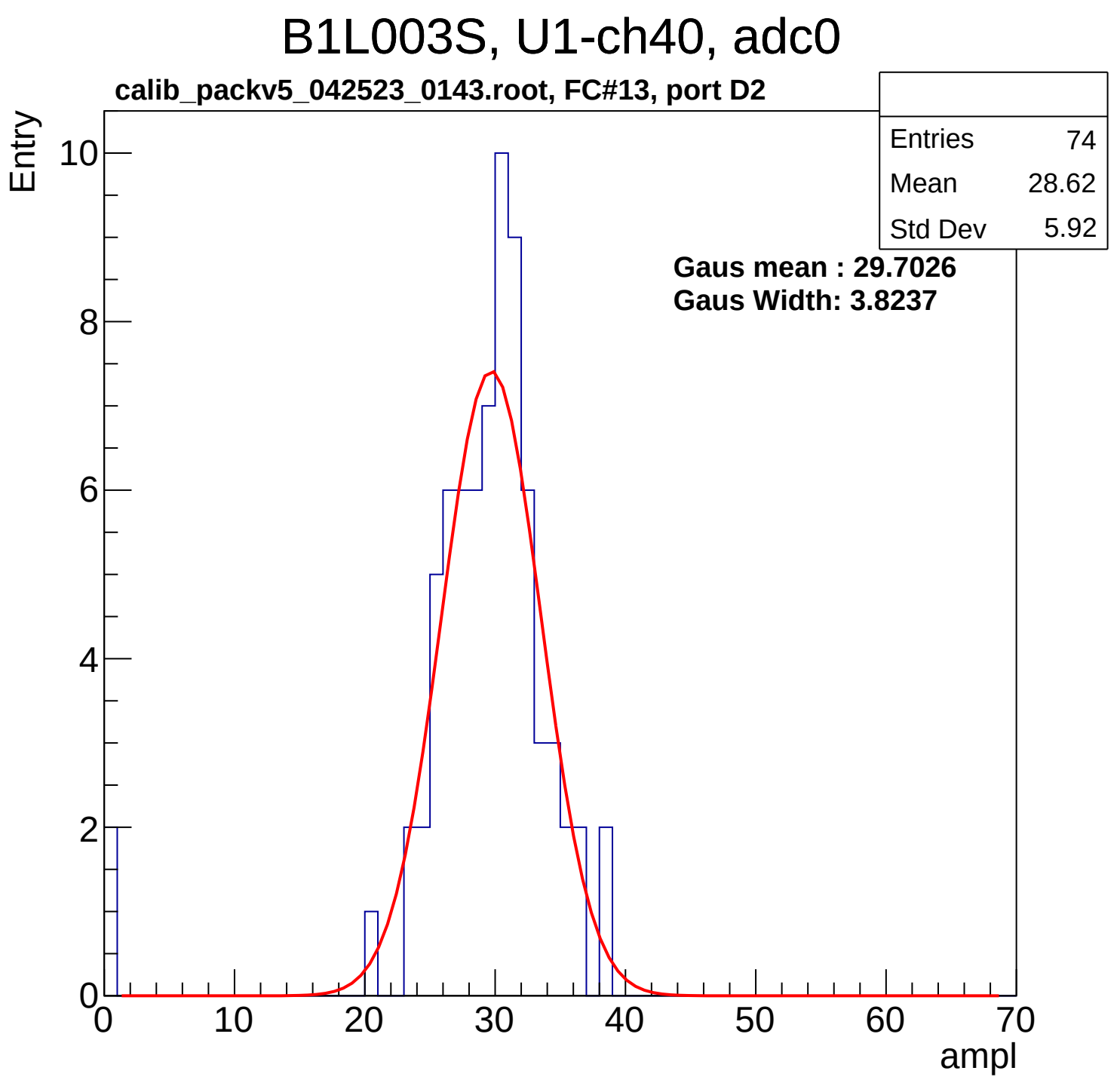
50
60
70
ampl


B1L003S, U1-ch40, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 28.62
Std Dev 5.92
Gaus mean : 29.7026
Gaus Width: 3.8237
Entry
ampl
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40
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60
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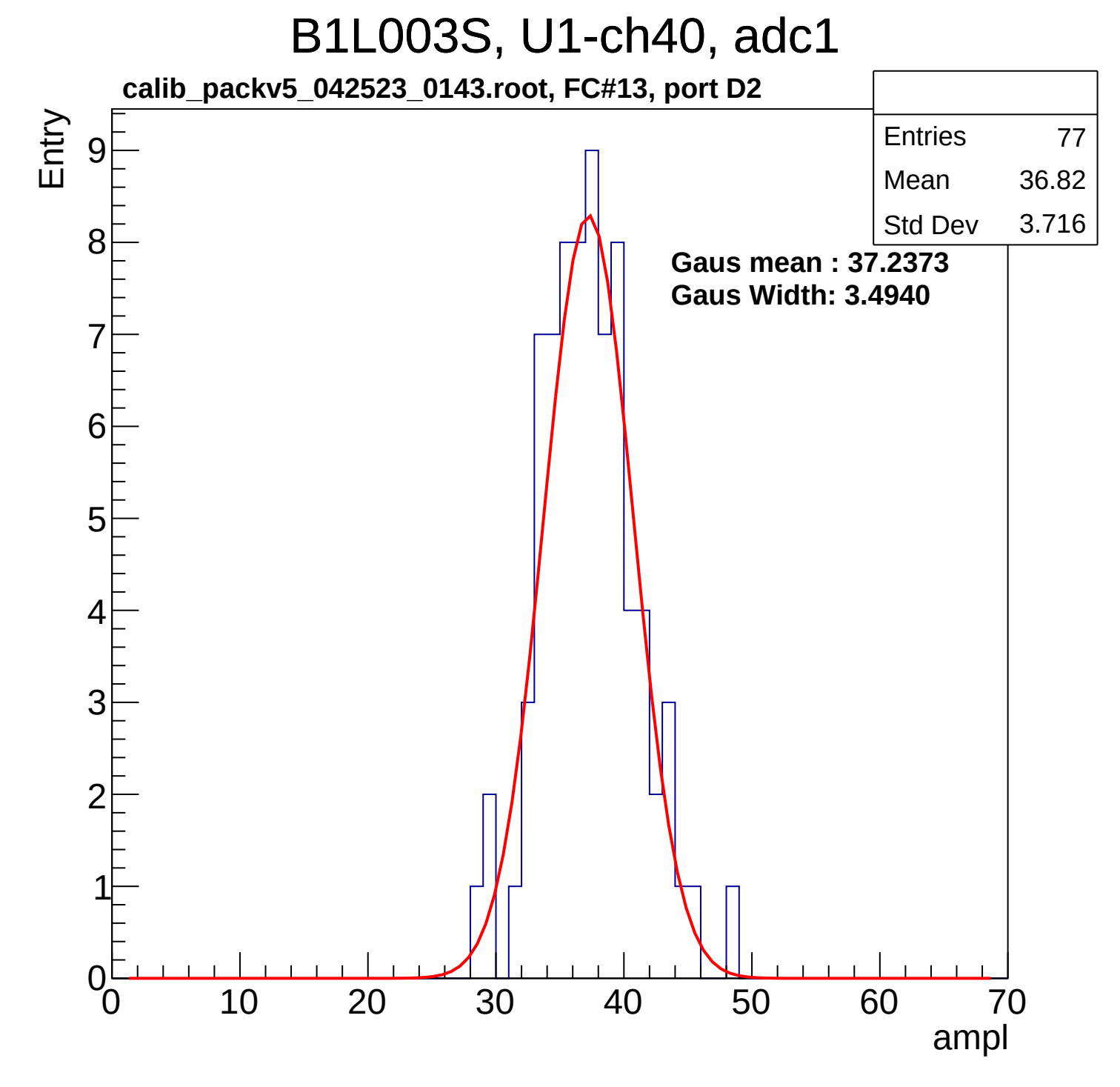
B1L003S, U1-ch40, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.82
Std Dev 3.716
Gaus mean : 37.2373
Gaus Width: 3.4940
Entry
ampl
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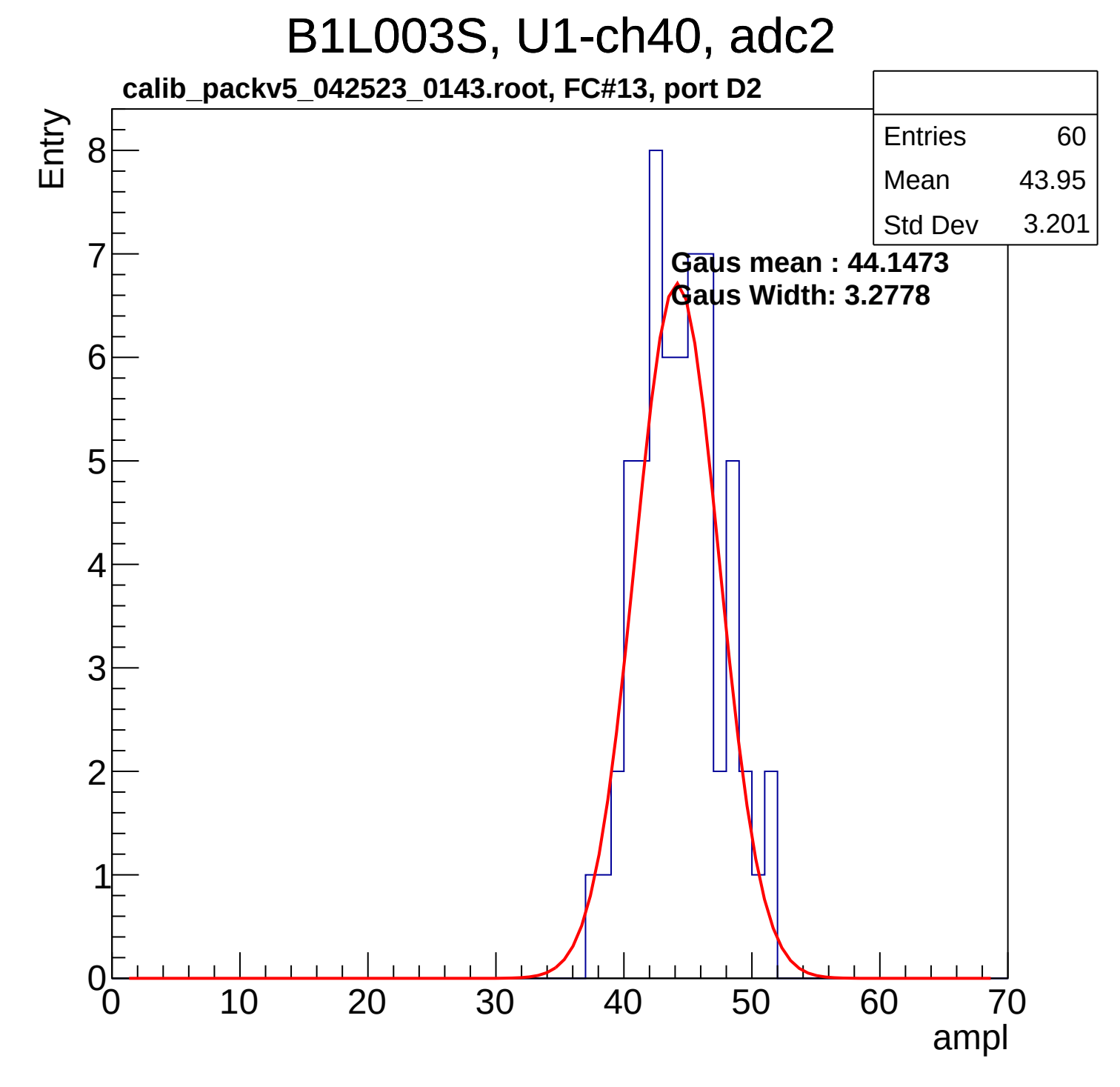
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch40, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.95
Std Dev 3.201
Gaus mean : 44.1473
Gaus Width: 3.2778
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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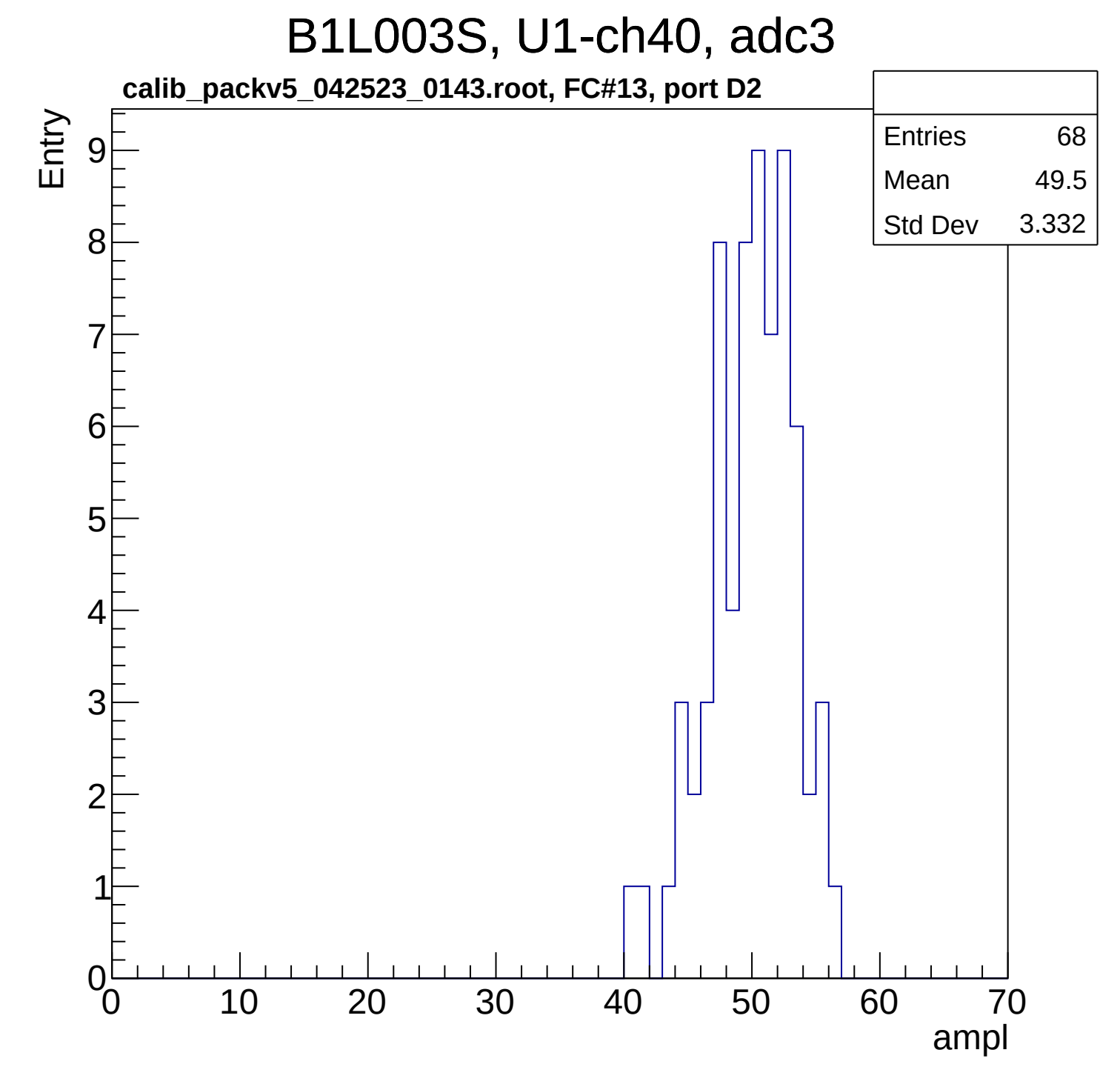
B1L003S, U1-ch40, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.5
Std Dev 3.332
Entry
ampl
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4
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6
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8
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0
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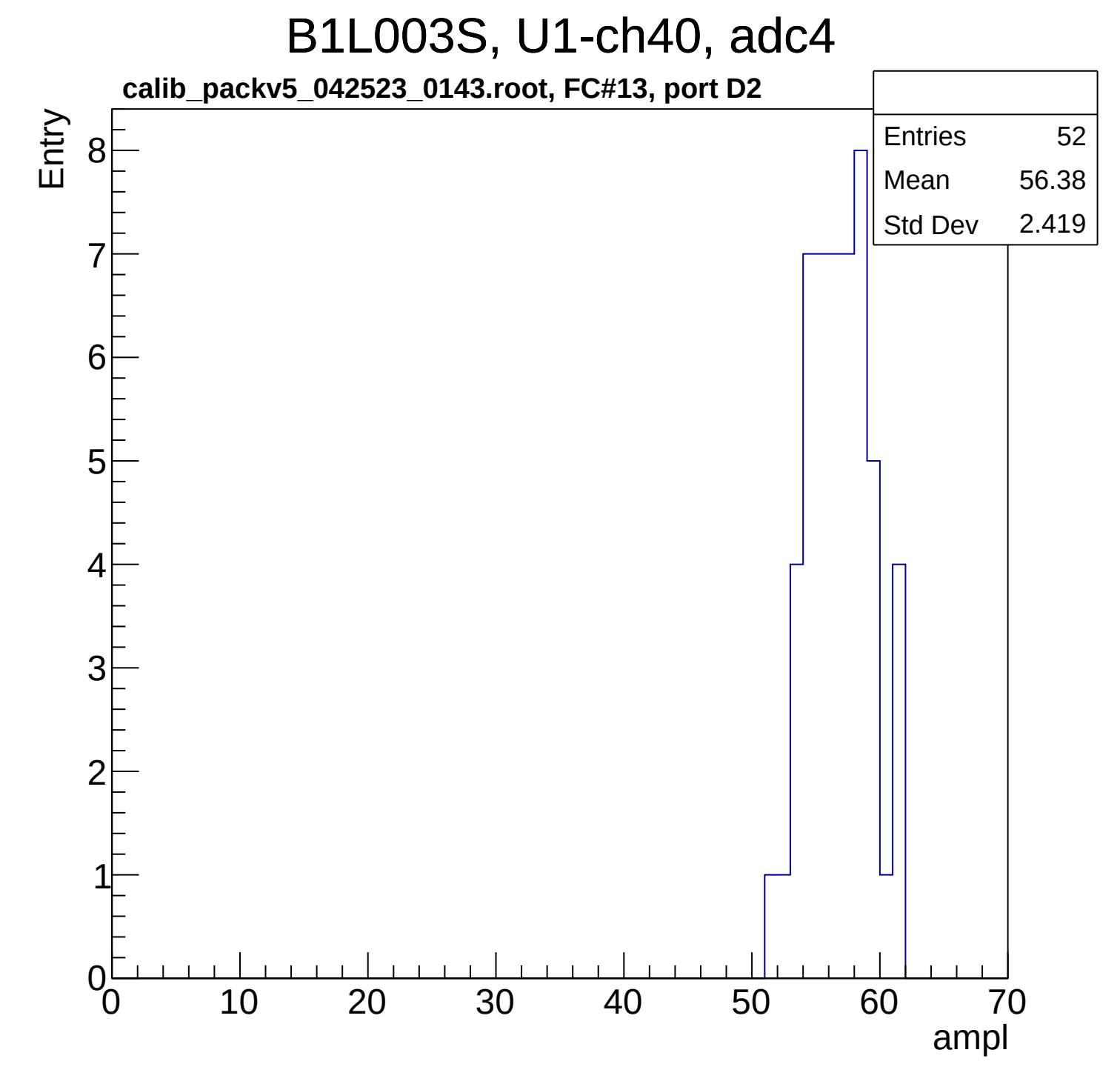
B1L003S, U1-ch40, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 56.38
Std Dev 2.419
Entry
ampl
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2
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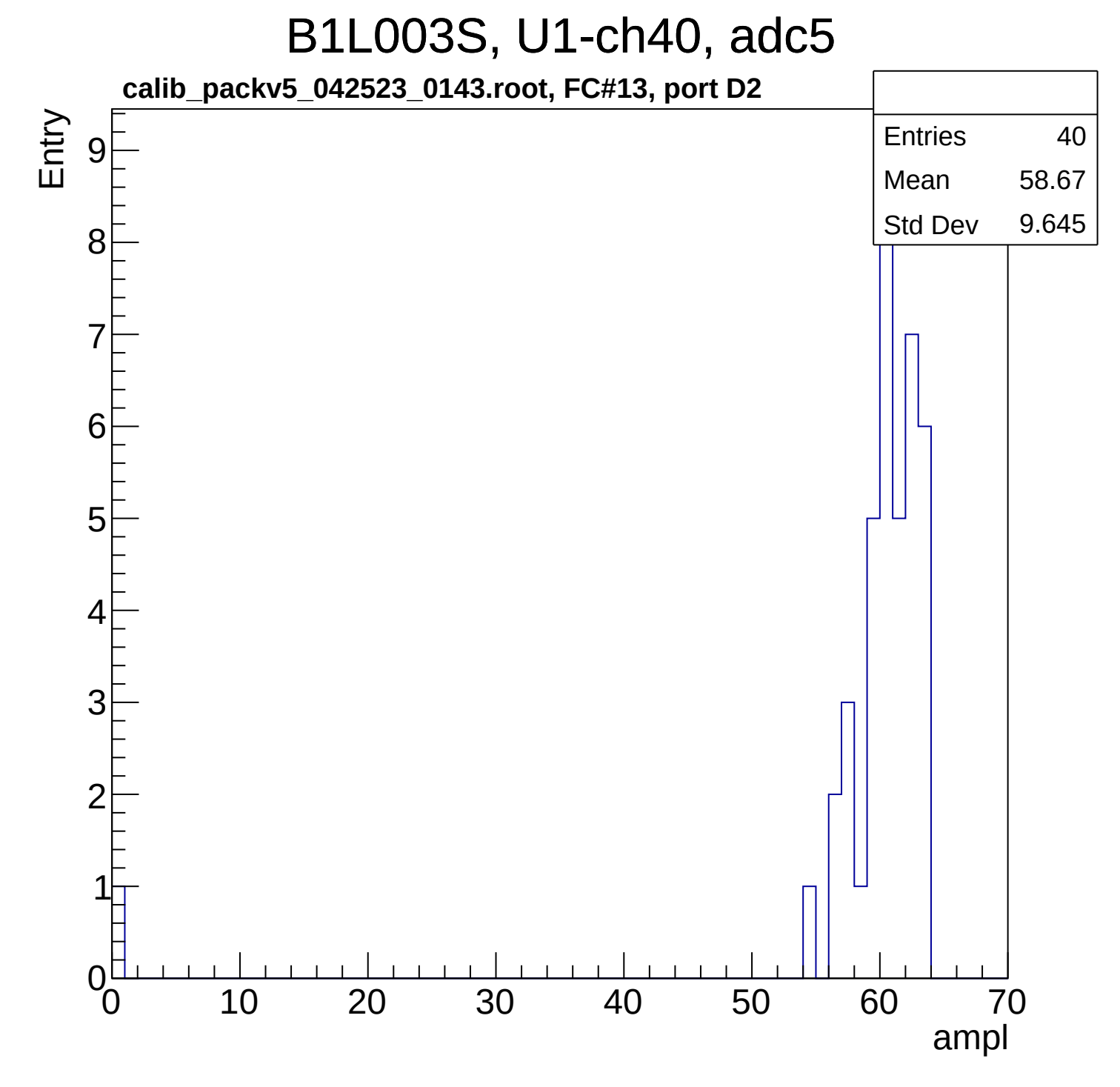
B1L003S, U1-ch40, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 58.67
Std Dev 9.645
Entry
ampl
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3
4
5
6
7
8
9
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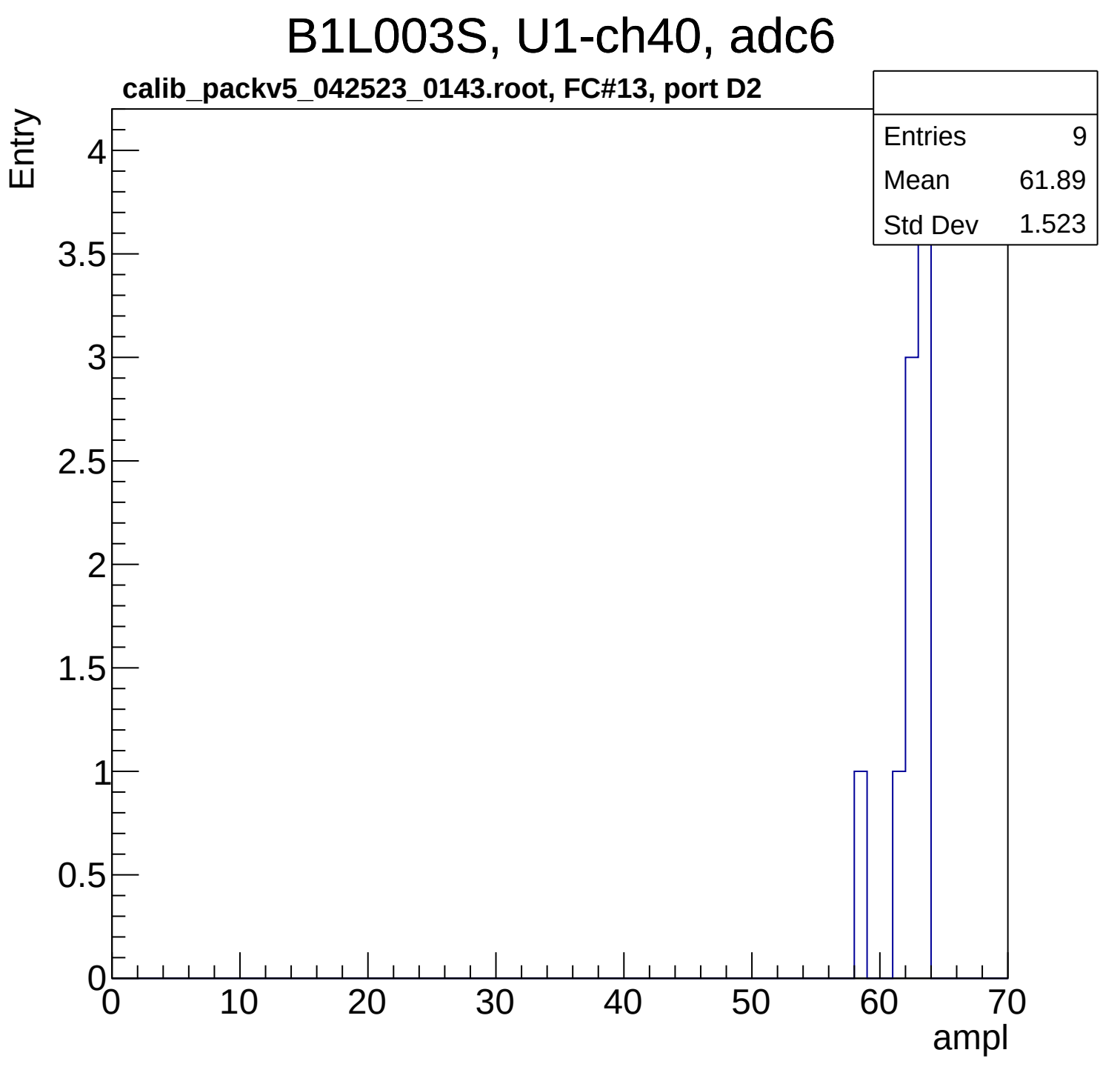
B1L003S, U1-ch40, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.89
Std Dev 1.523
Entry
ampl
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1.5
2
2.5
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3.5
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0
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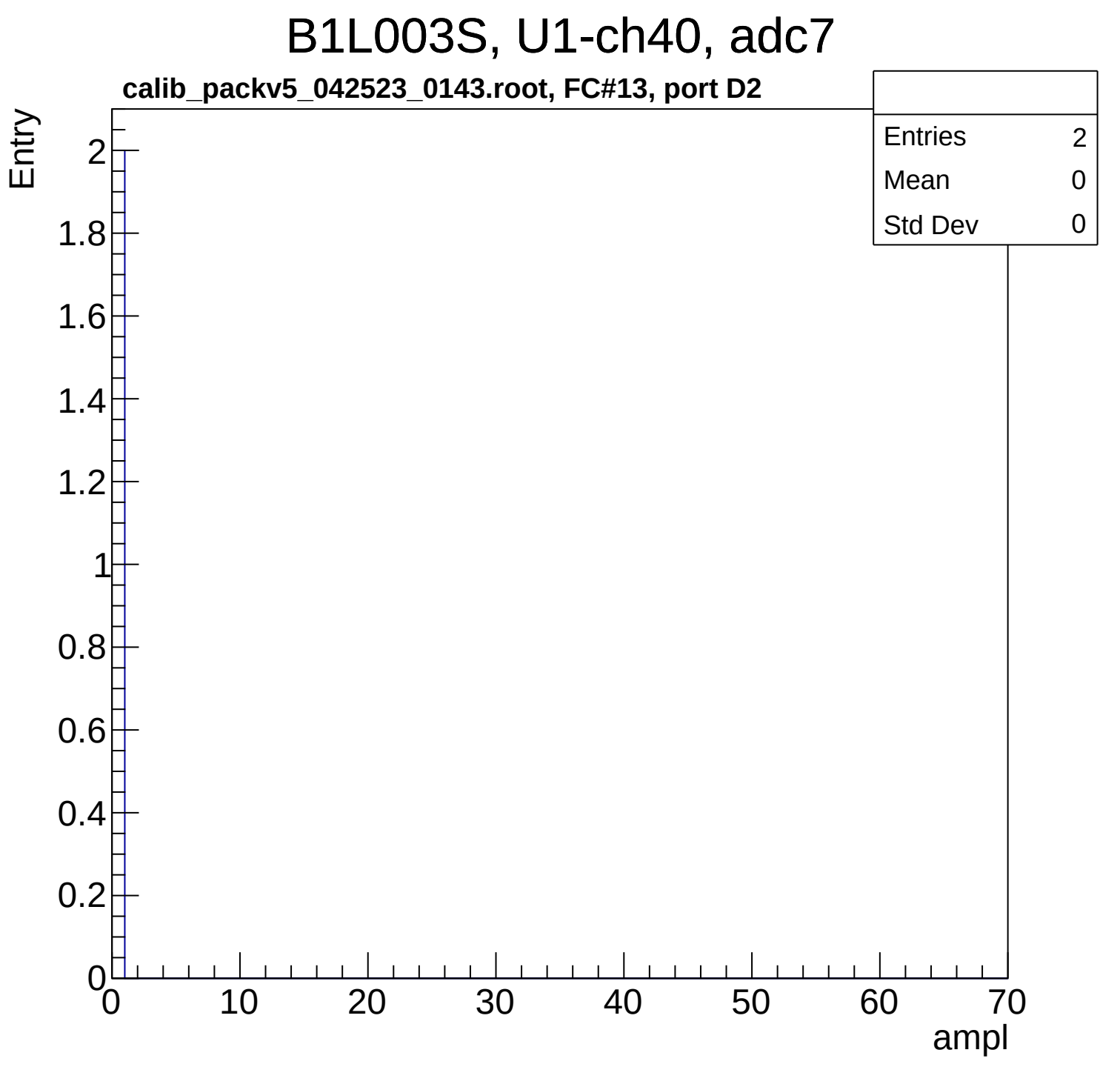
B1L003S, U1-ch40, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
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0
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20
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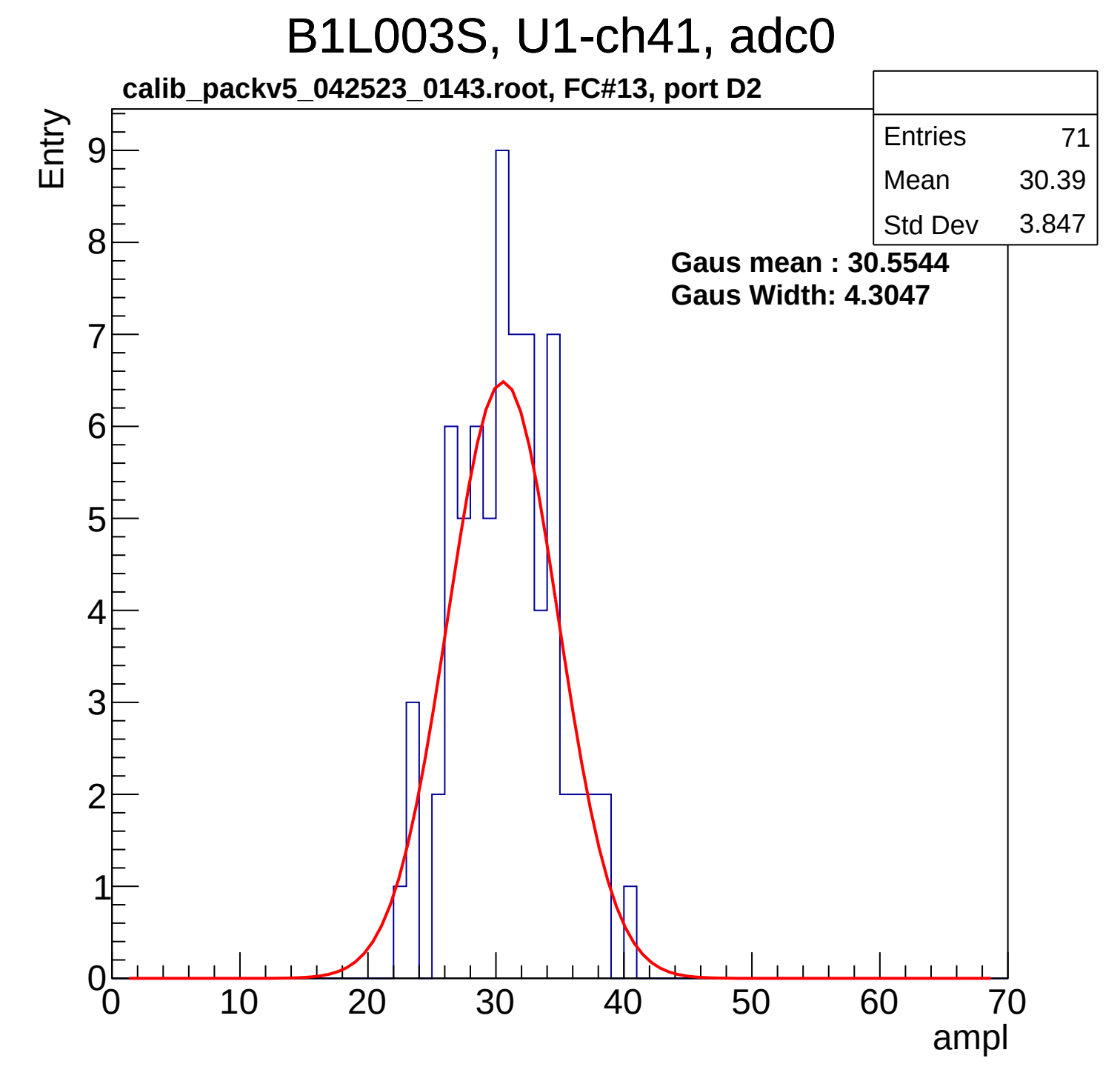
B1L003S, U1-ch41, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 30.39
Std Dev 3.847
Gaus mean : 30.5544
Gaus Width: 4.3047
Entry
ampl
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2
3
4
5
6
7
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0
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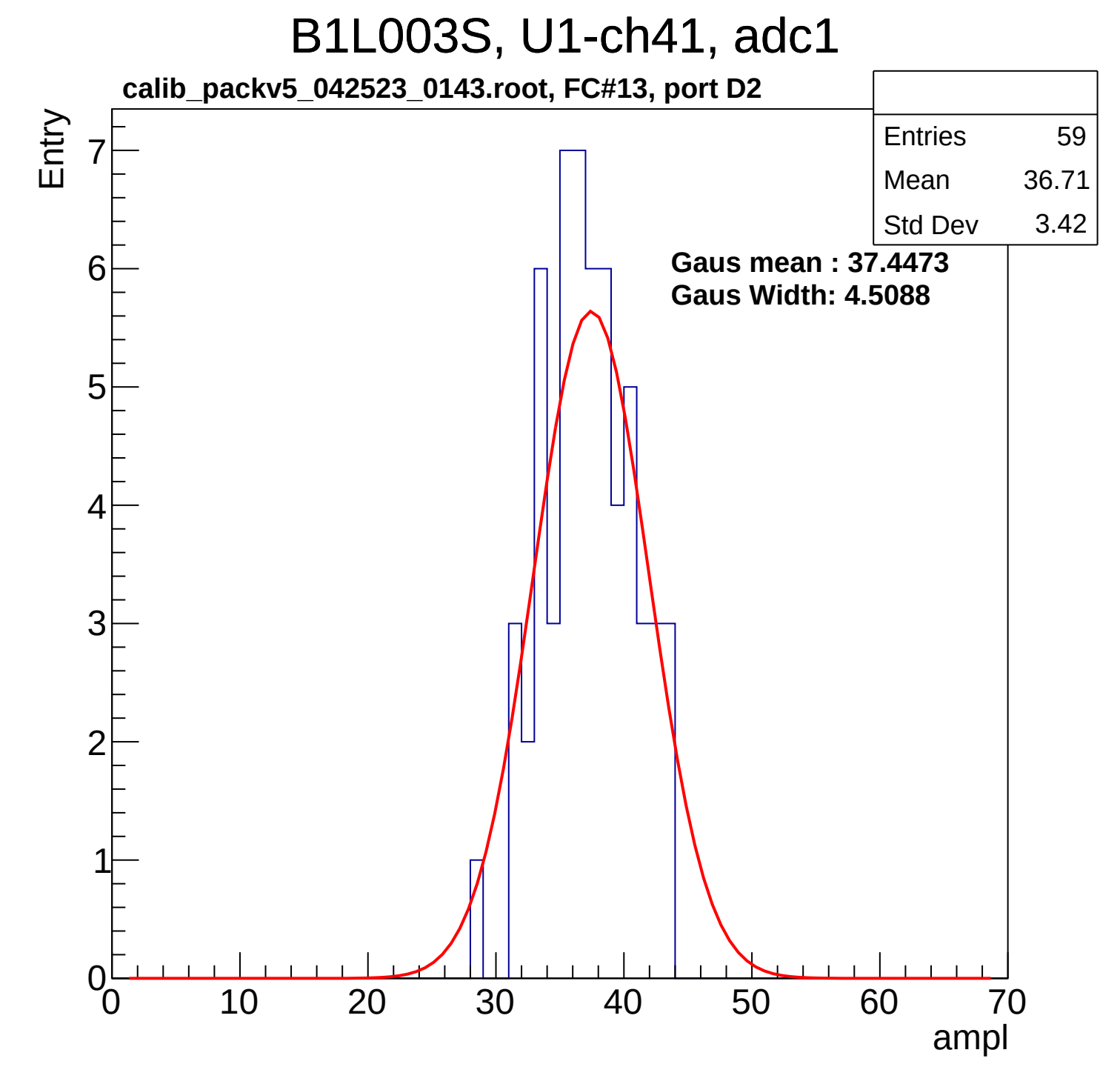
B1L003S, U1-ch41, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.71
Std Dev 3.42
Gaus mean : 37.4473
Gaus Width: 4.5088
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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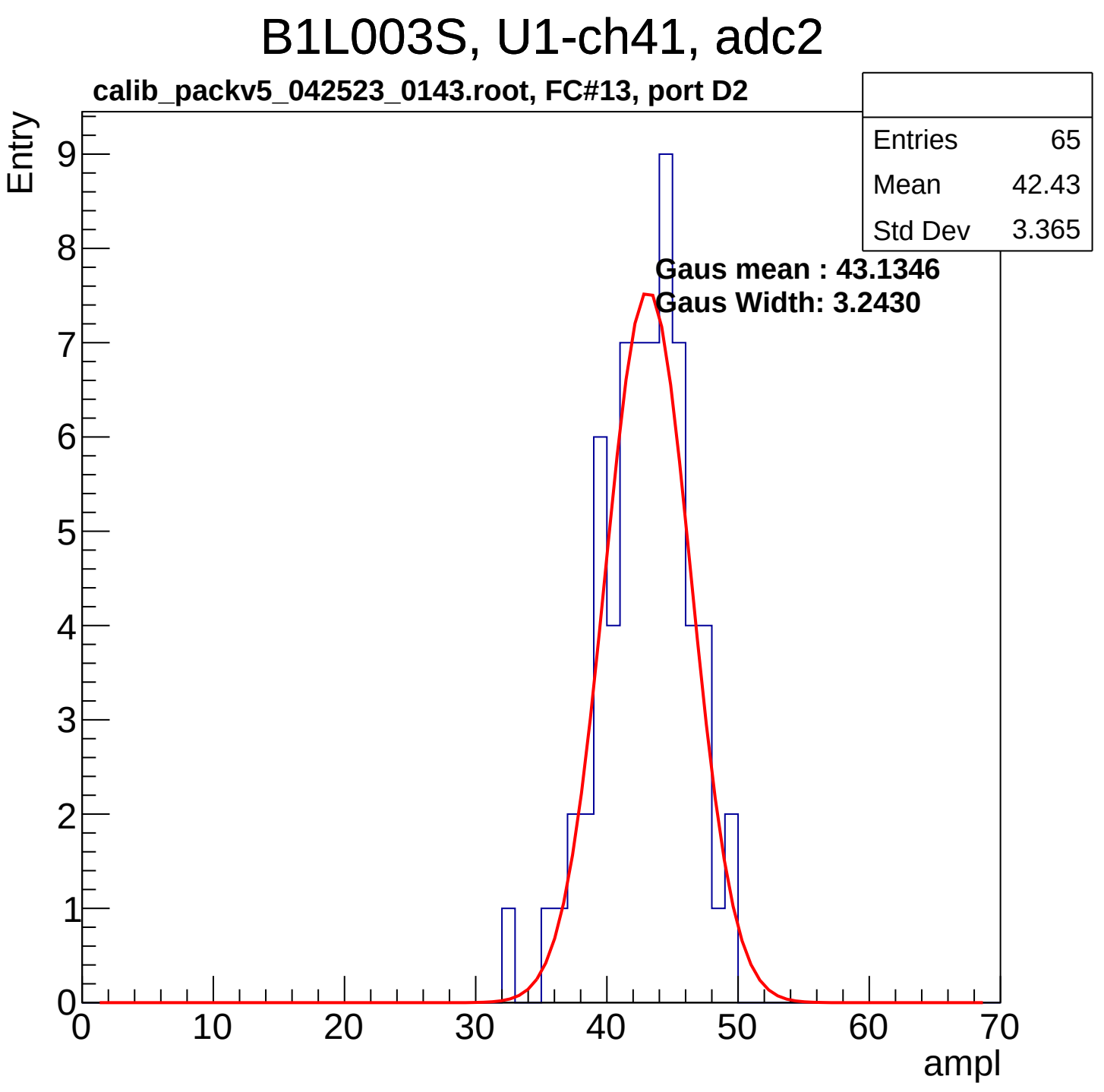
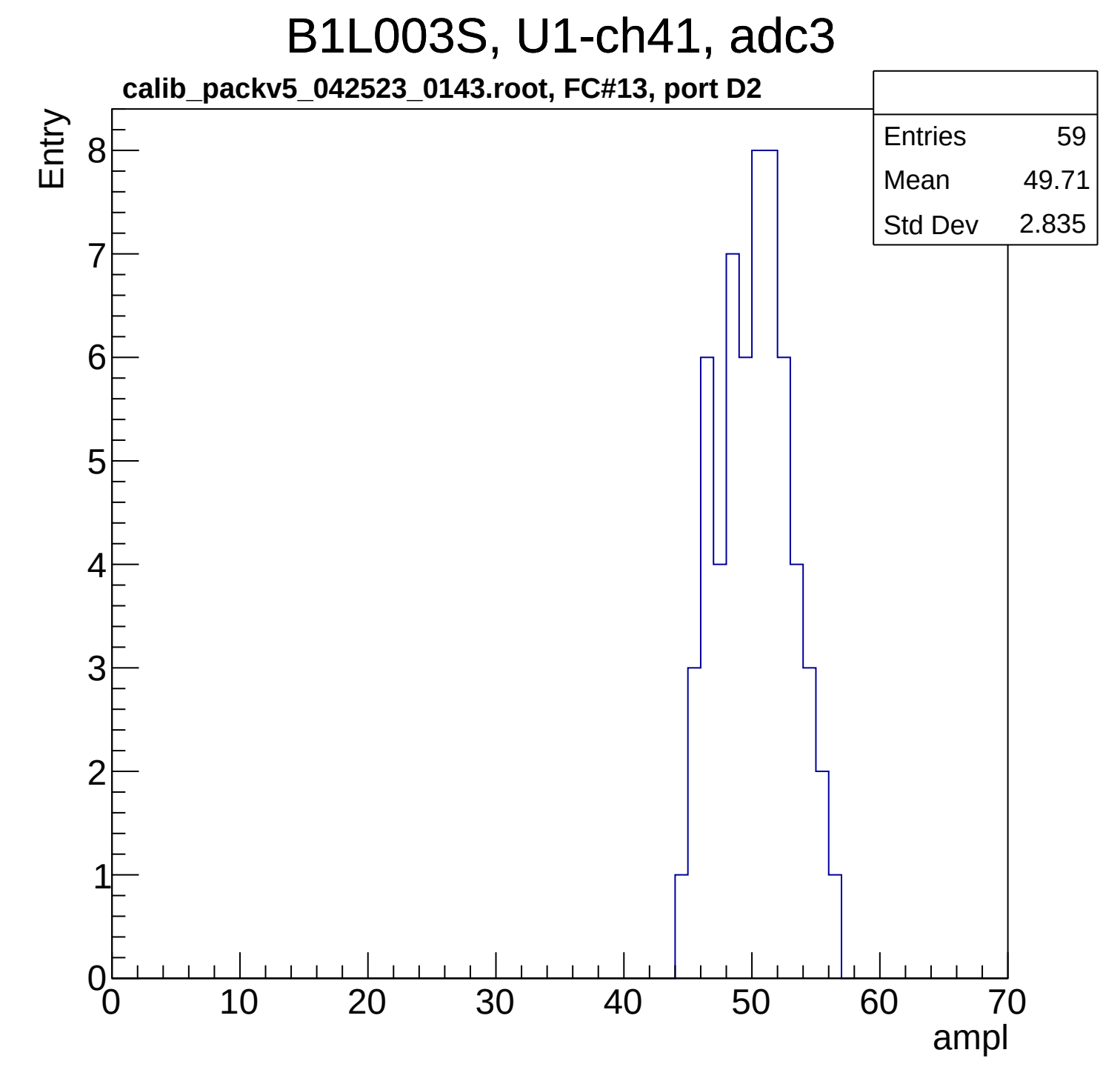

B1L003S, U1-ch41, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.43
Std Dev 3.365
Gaus mean : 43.1346
Gaus Width: 3.2430
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch41, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.71
Std Dev 2.835
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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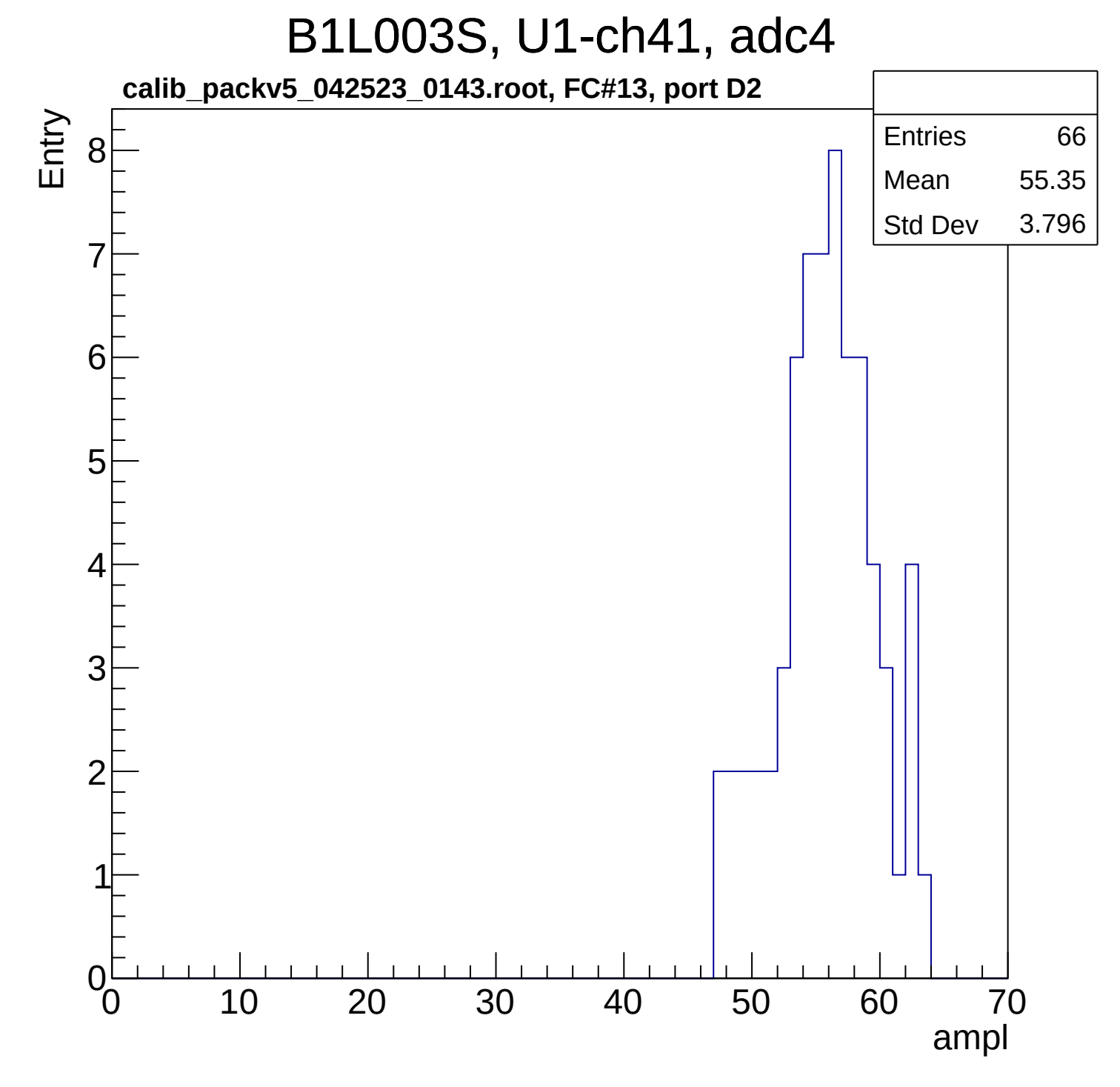
B1L003S, U1-ch41, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.35
Std Dev 3.796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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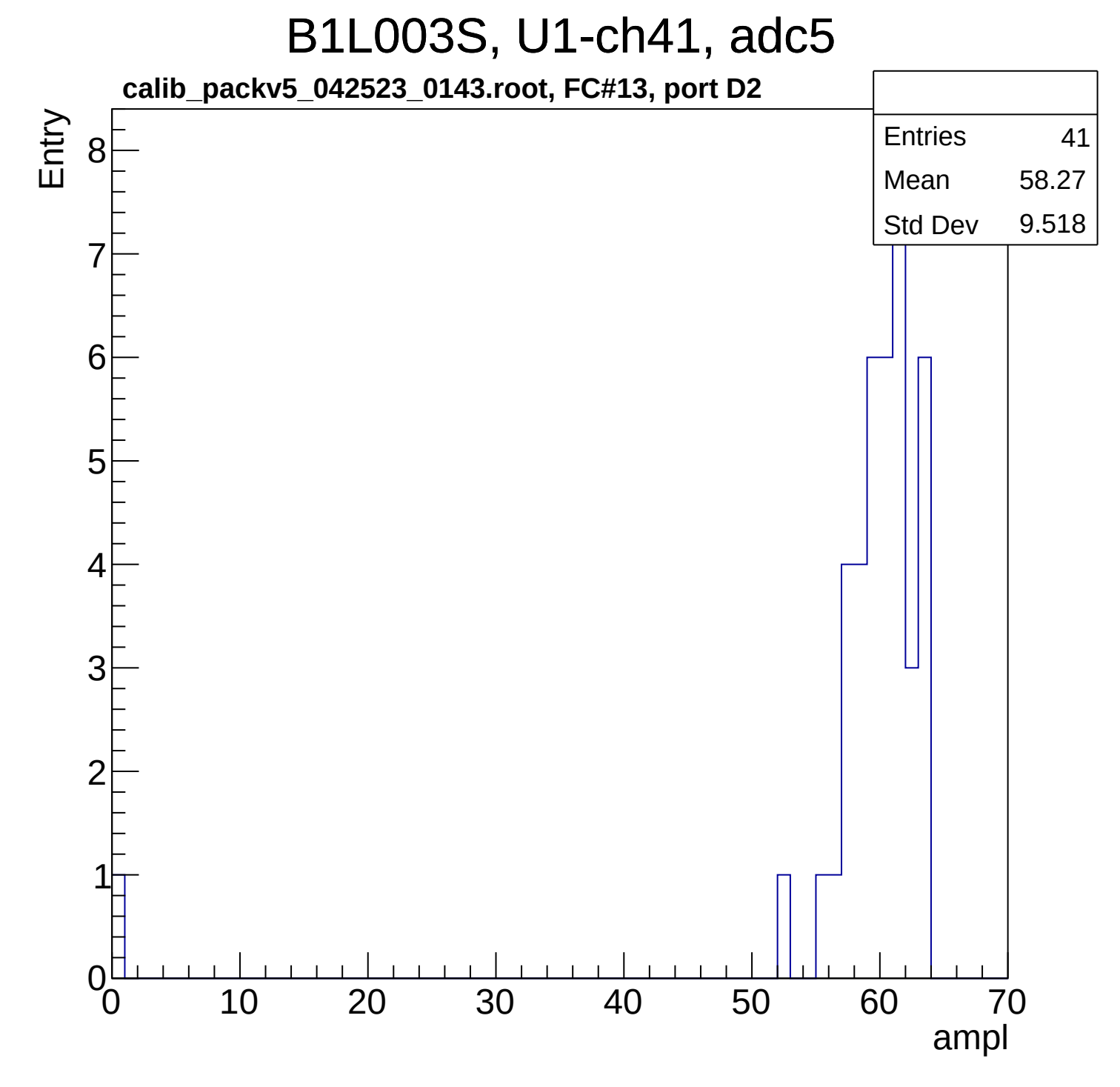
B1L003S, U1-ch41, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 58.27
Std Dev 9.518
Entry
ampl
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4
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0
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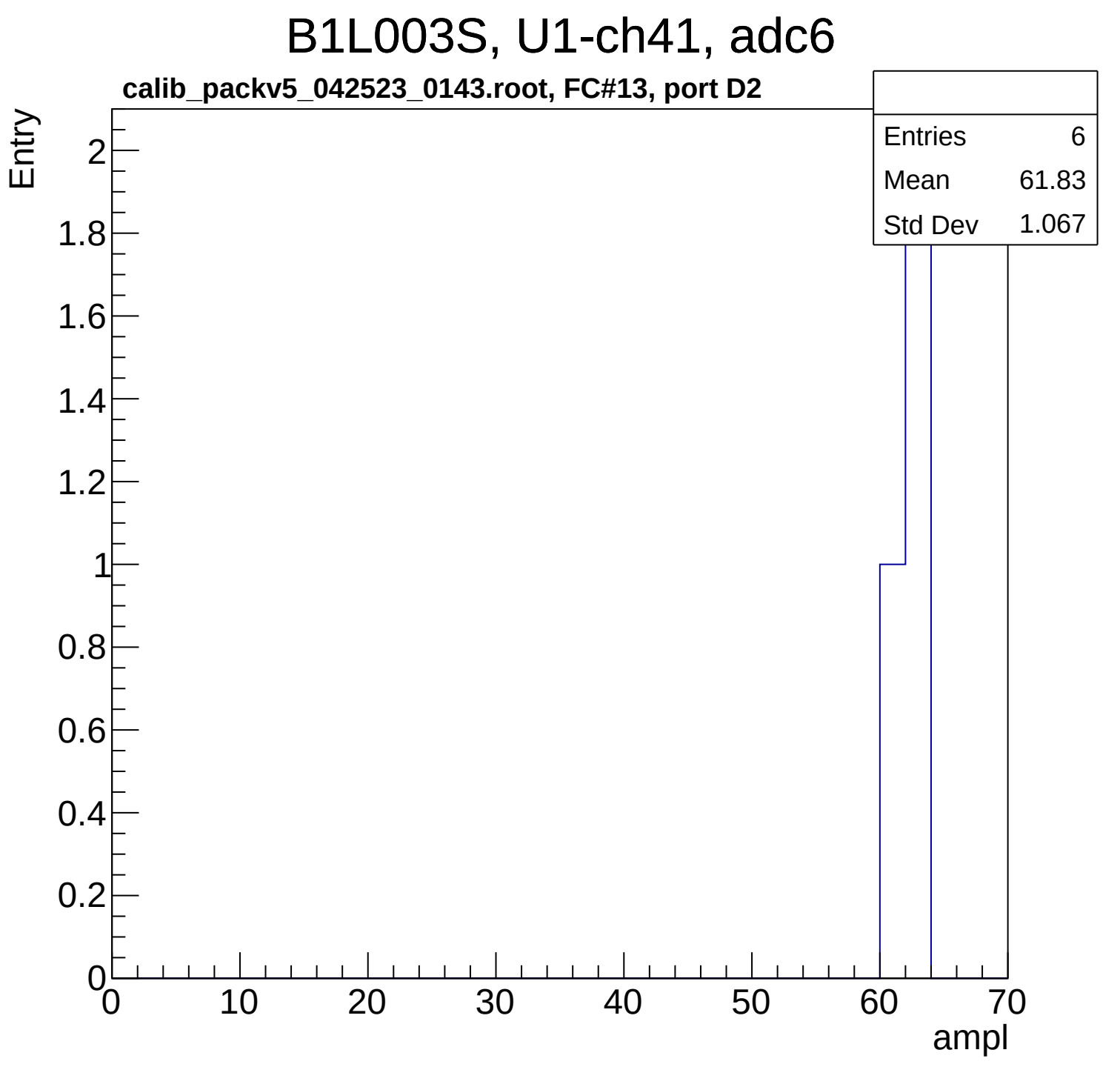
B1L003S, U1-ch41, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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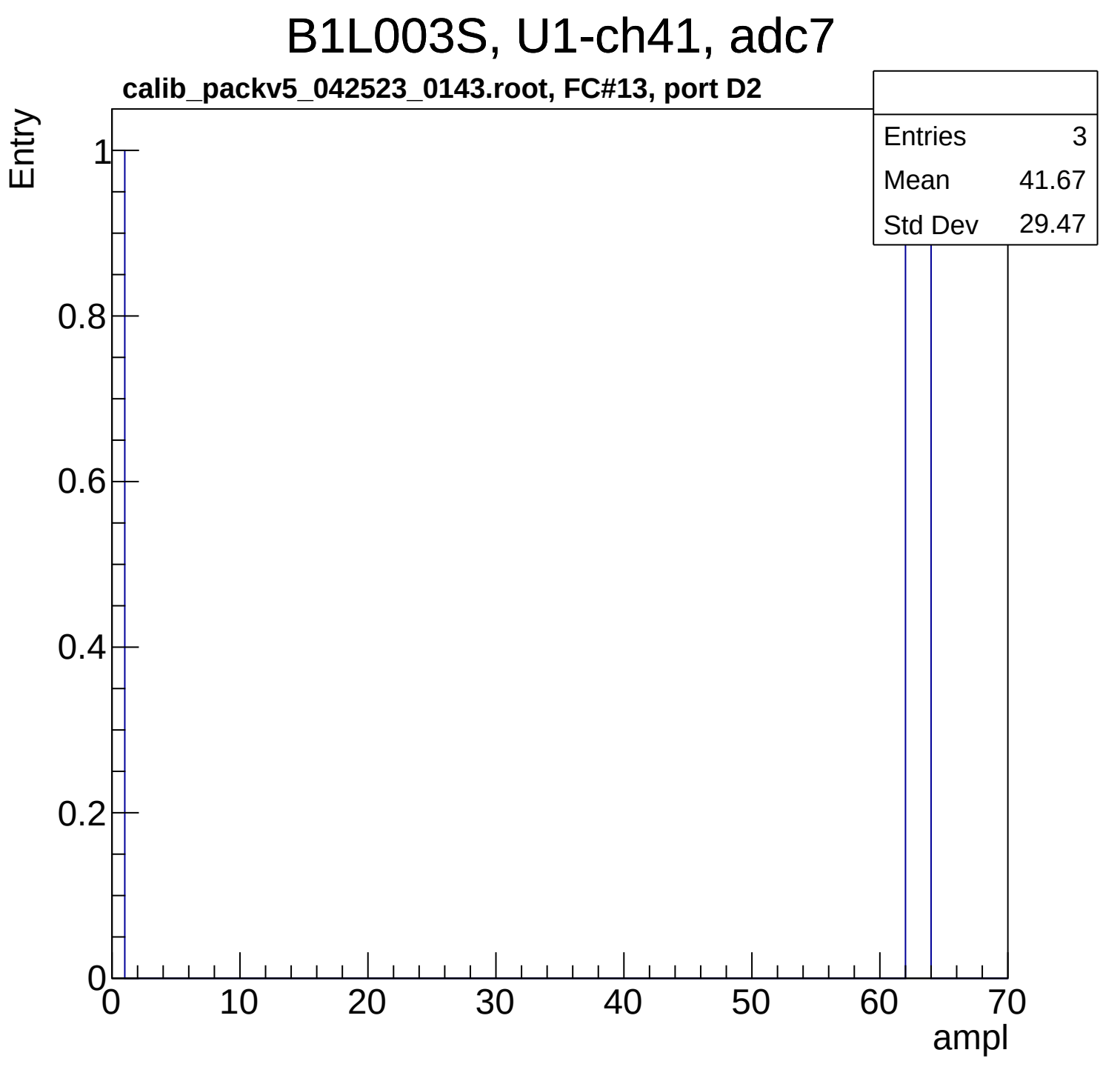
B1L003S, U1-ch41, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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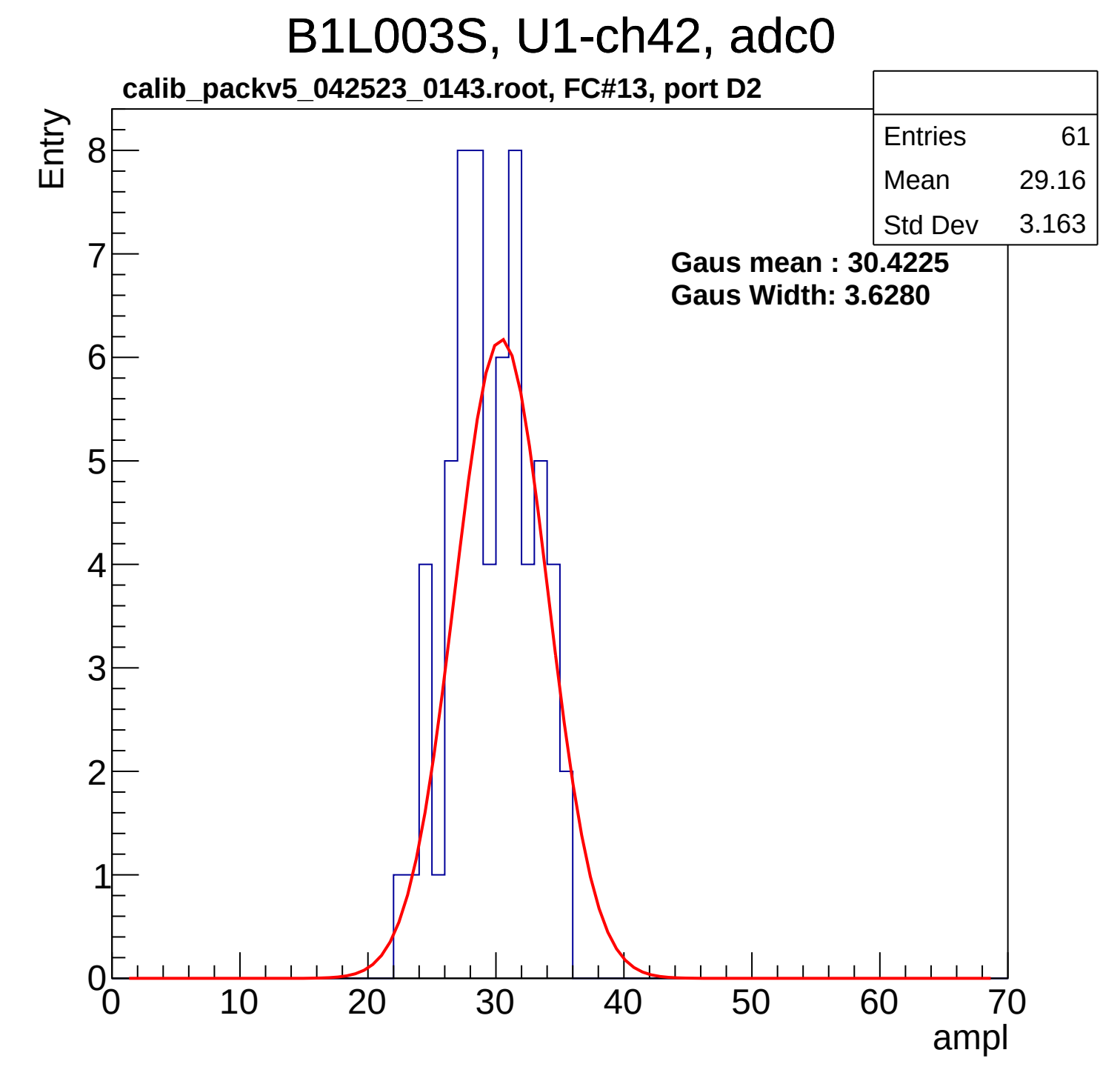
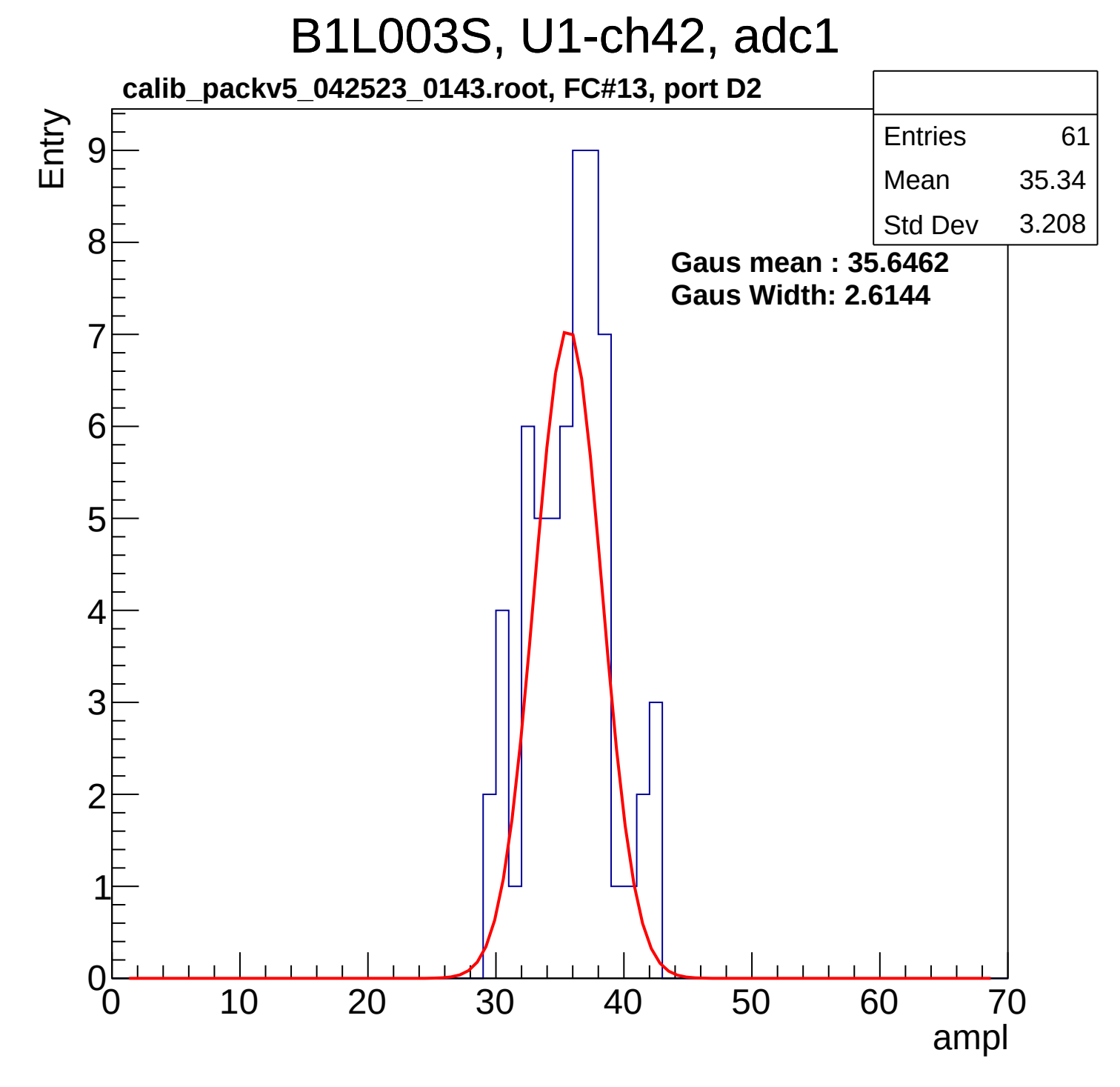
50
60
70
ampl


B1L003S, U1-ch42, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 29.16
Std Dev 3.163
Gaus mean : 30.4225
Gaus Width: 3.6280
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch42, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.34
Std Dev 3.208
Gaus mean : 35.6462
Gaus Width: 2.6144
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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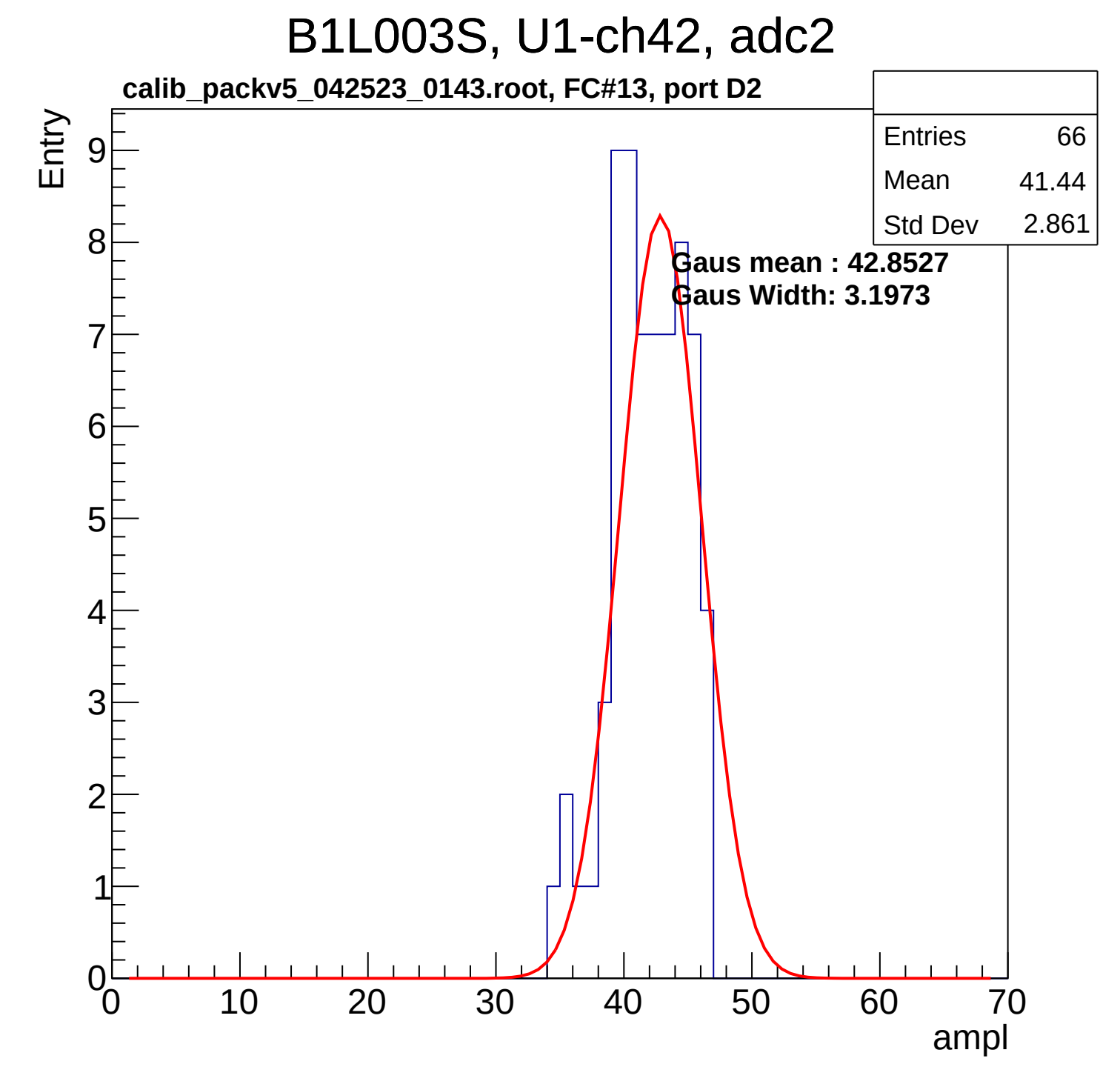
B1L003S, U1-ch42, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 41.44
Std Dev 2.861
Gaus mean : 42.8527
Gaus Width: 3.1973
Entry
ampl
0
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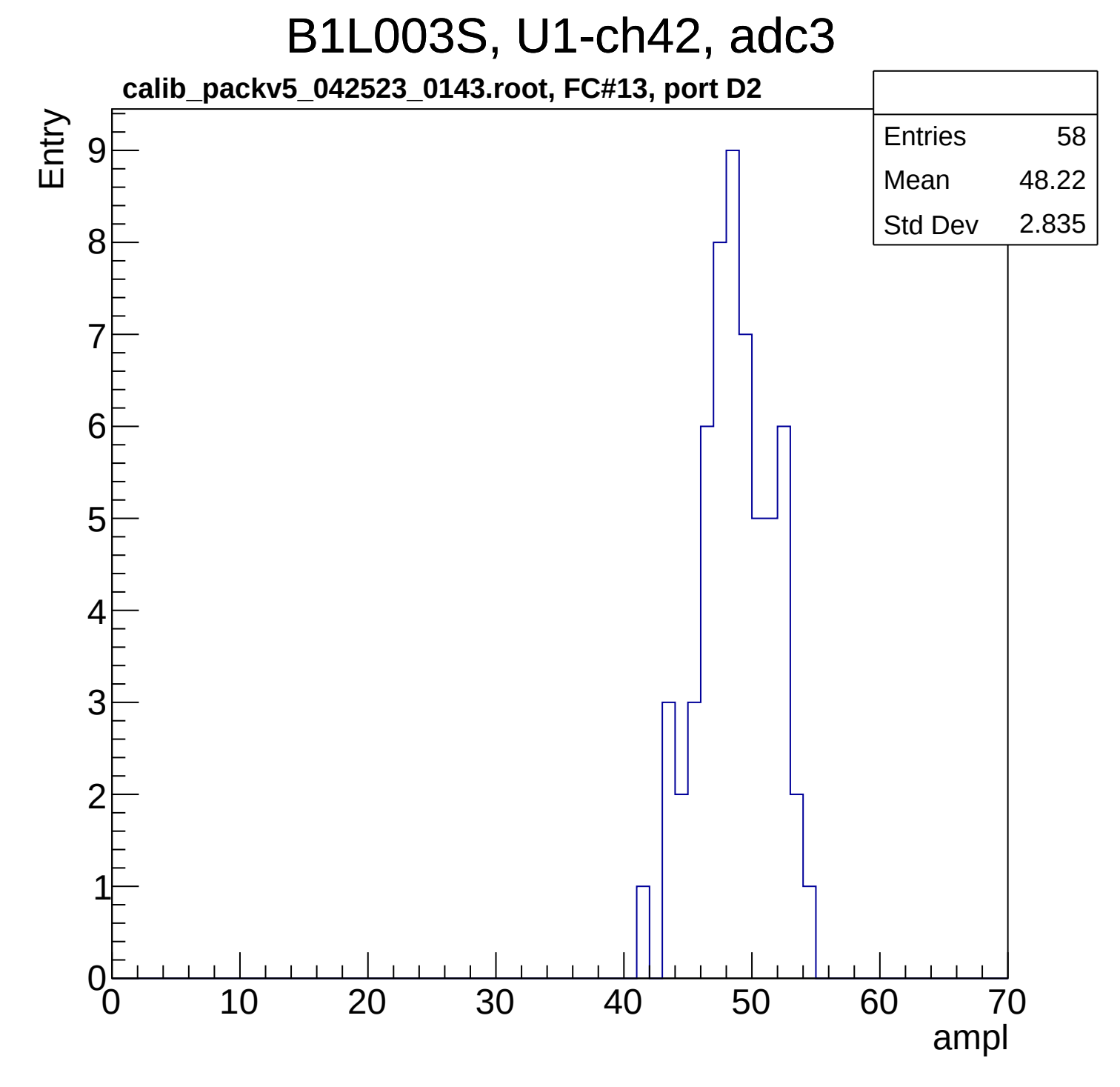
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch42, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.22
Std Dev 2.835
Entry
ampl
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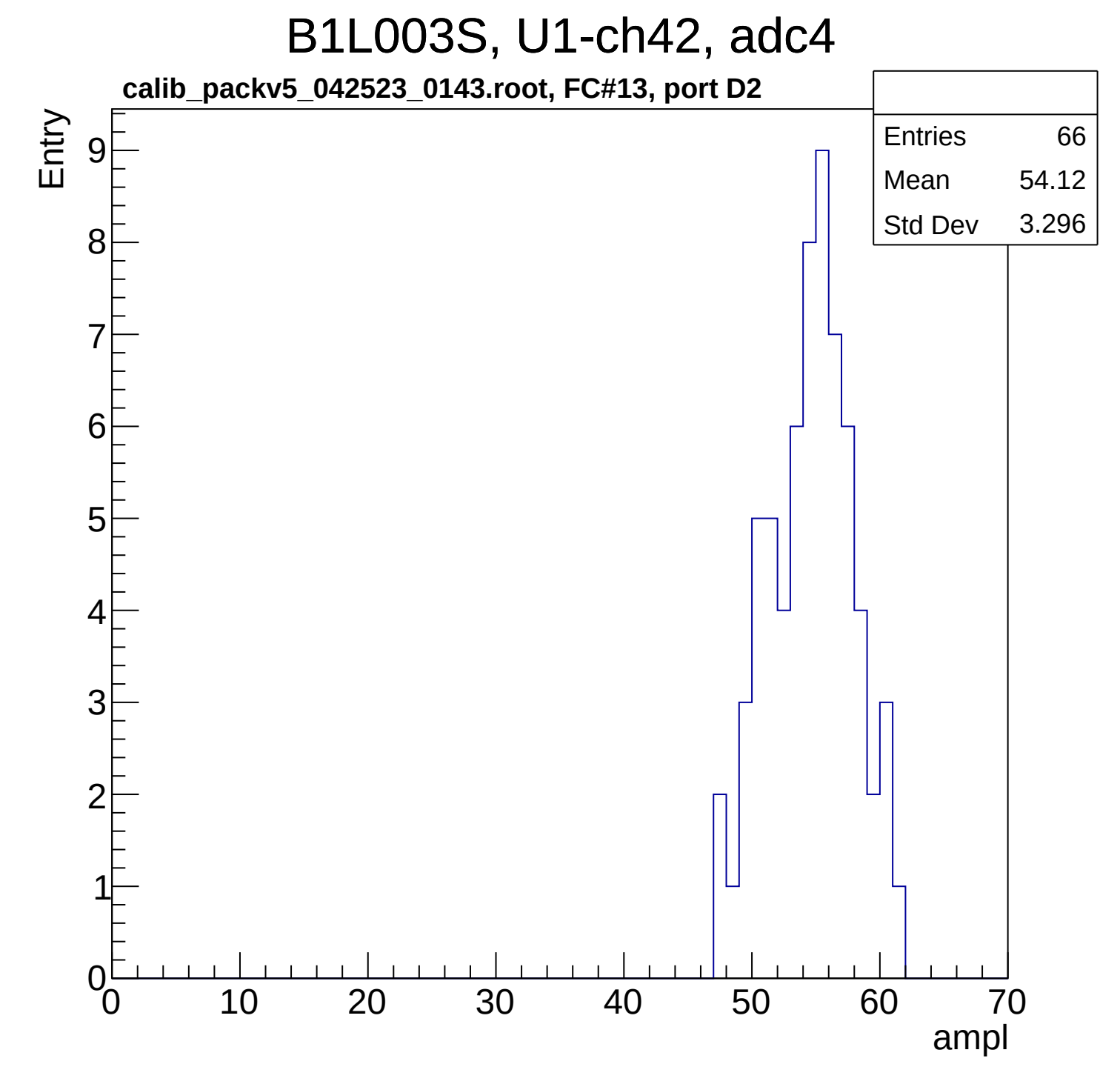
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch42, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.12
Std Dev 3.296
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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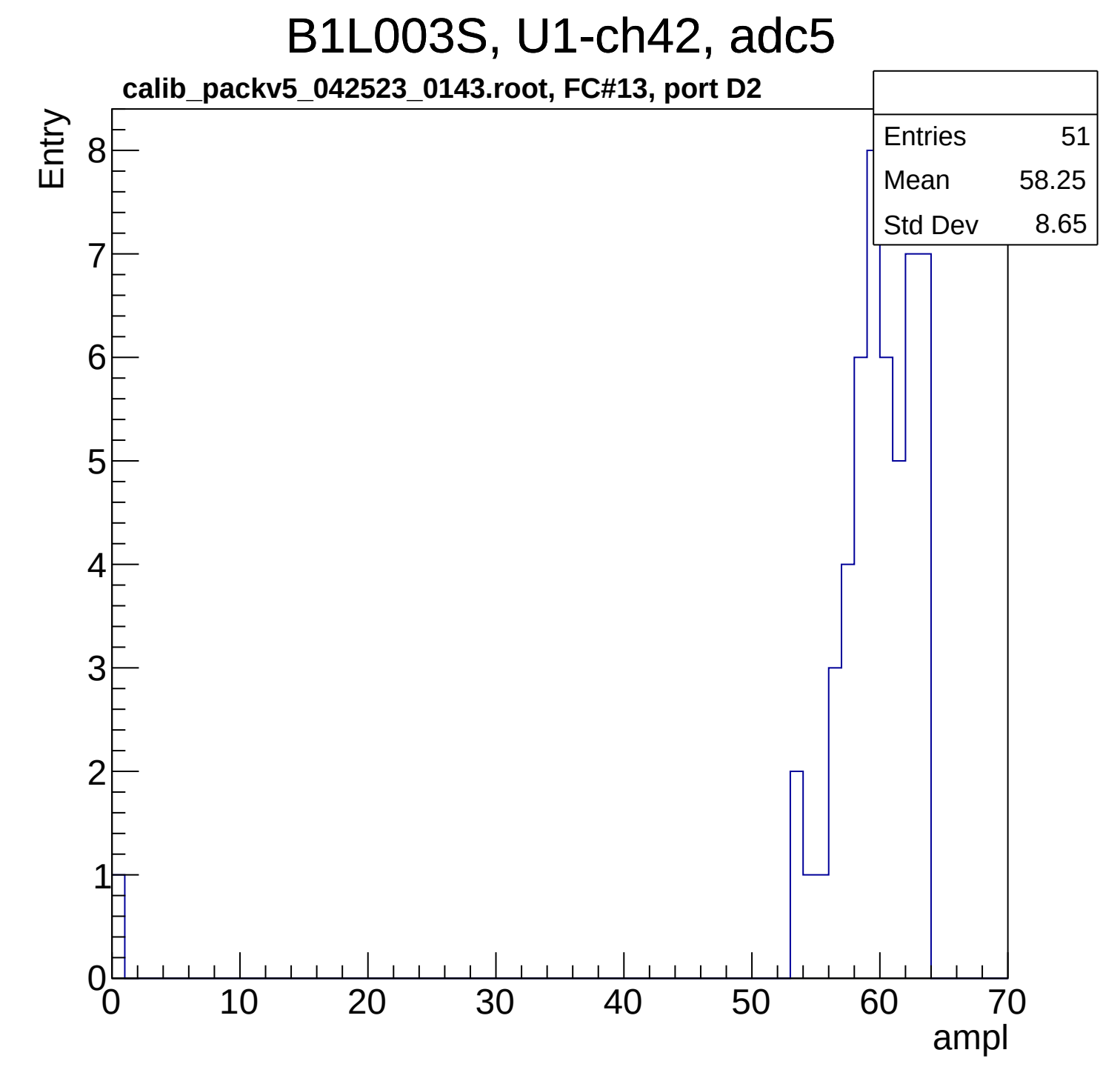
40
50
60
70


B1L003S, U1-ch42, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.25
Std Dev 8.65
Entry
ampl
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1
2
3
4
5
6
7
8
0
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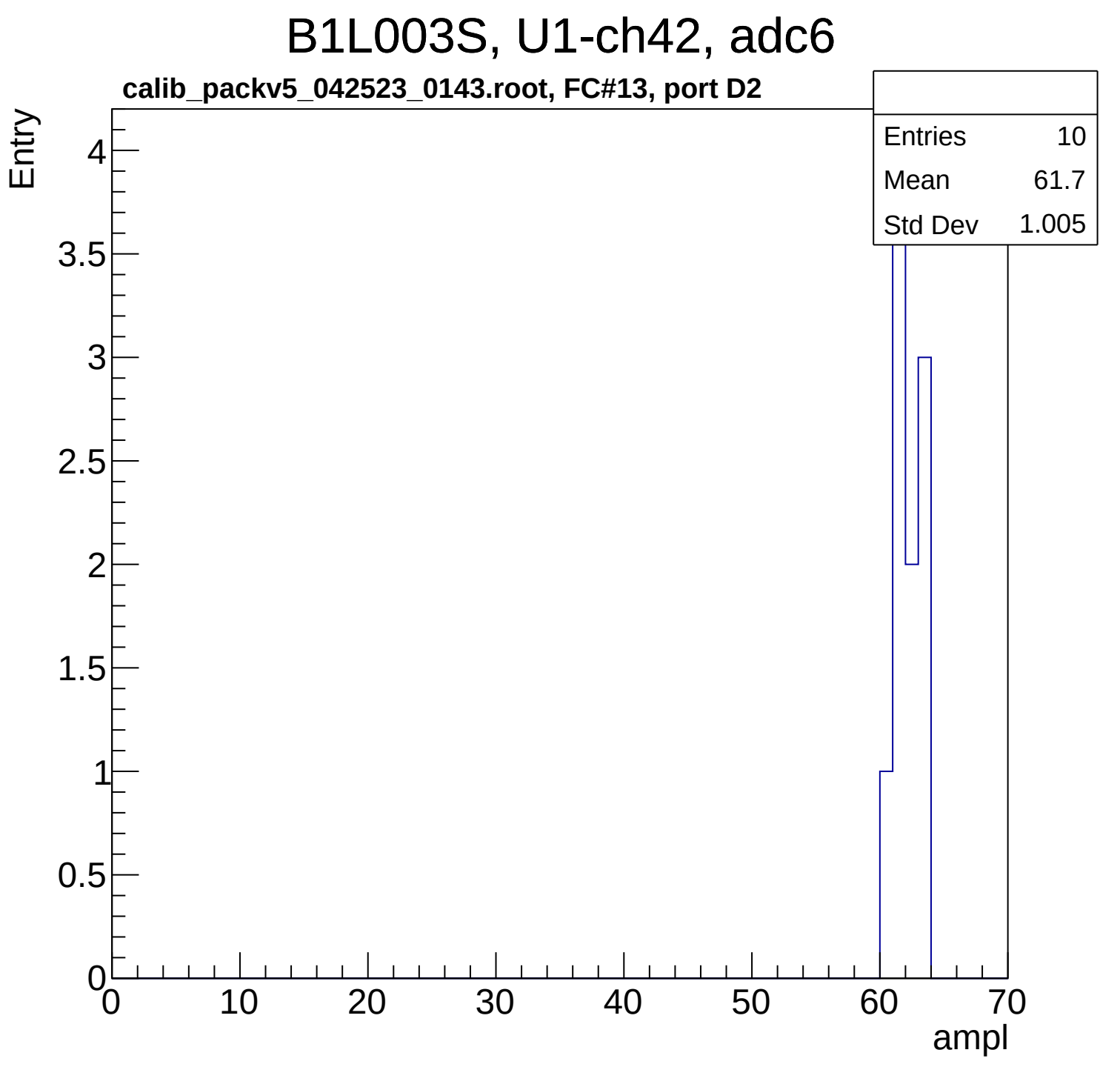
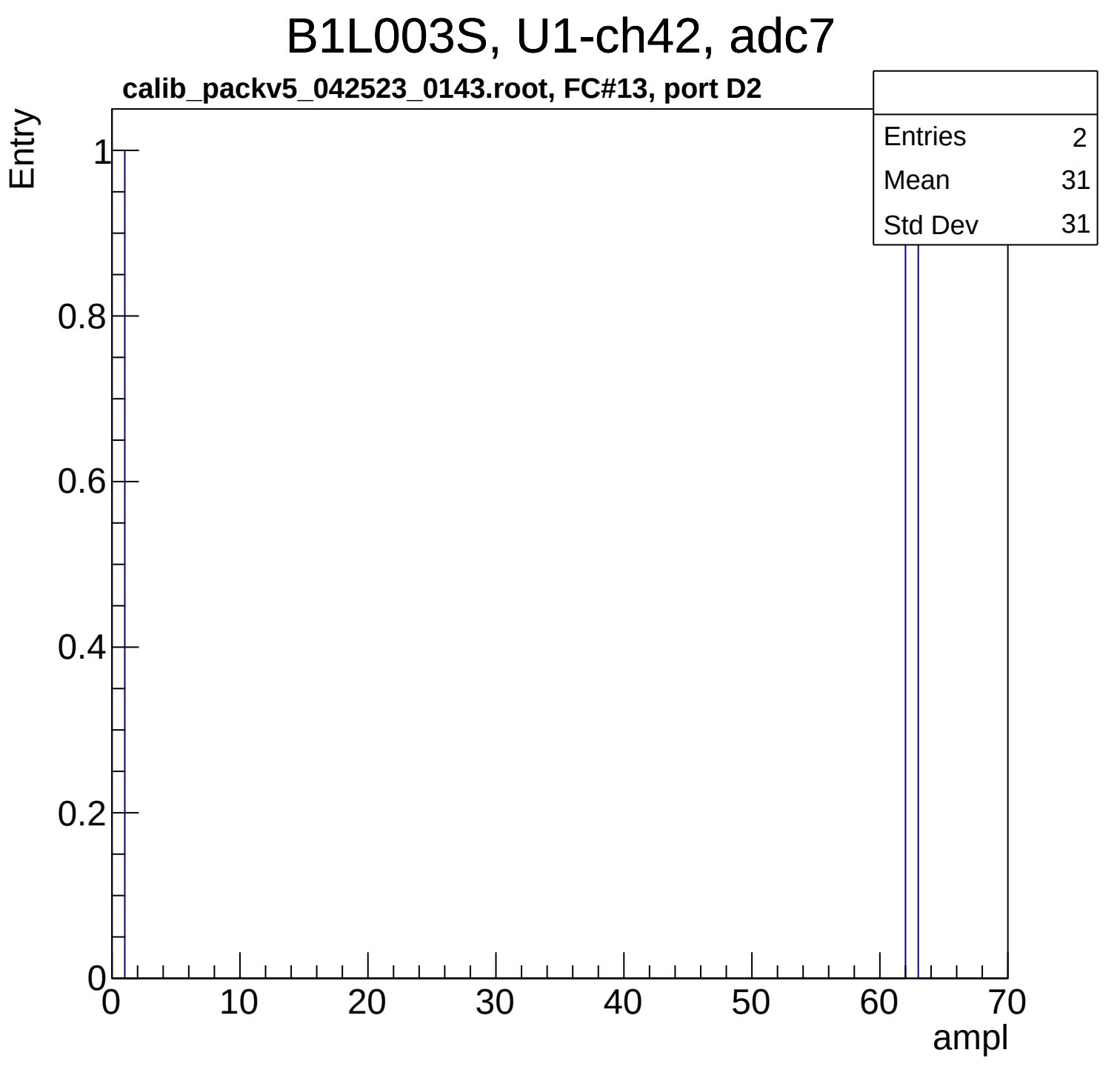

B1L003S, U1-ch42, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 61.7
Std Dev 1.005
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch42, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
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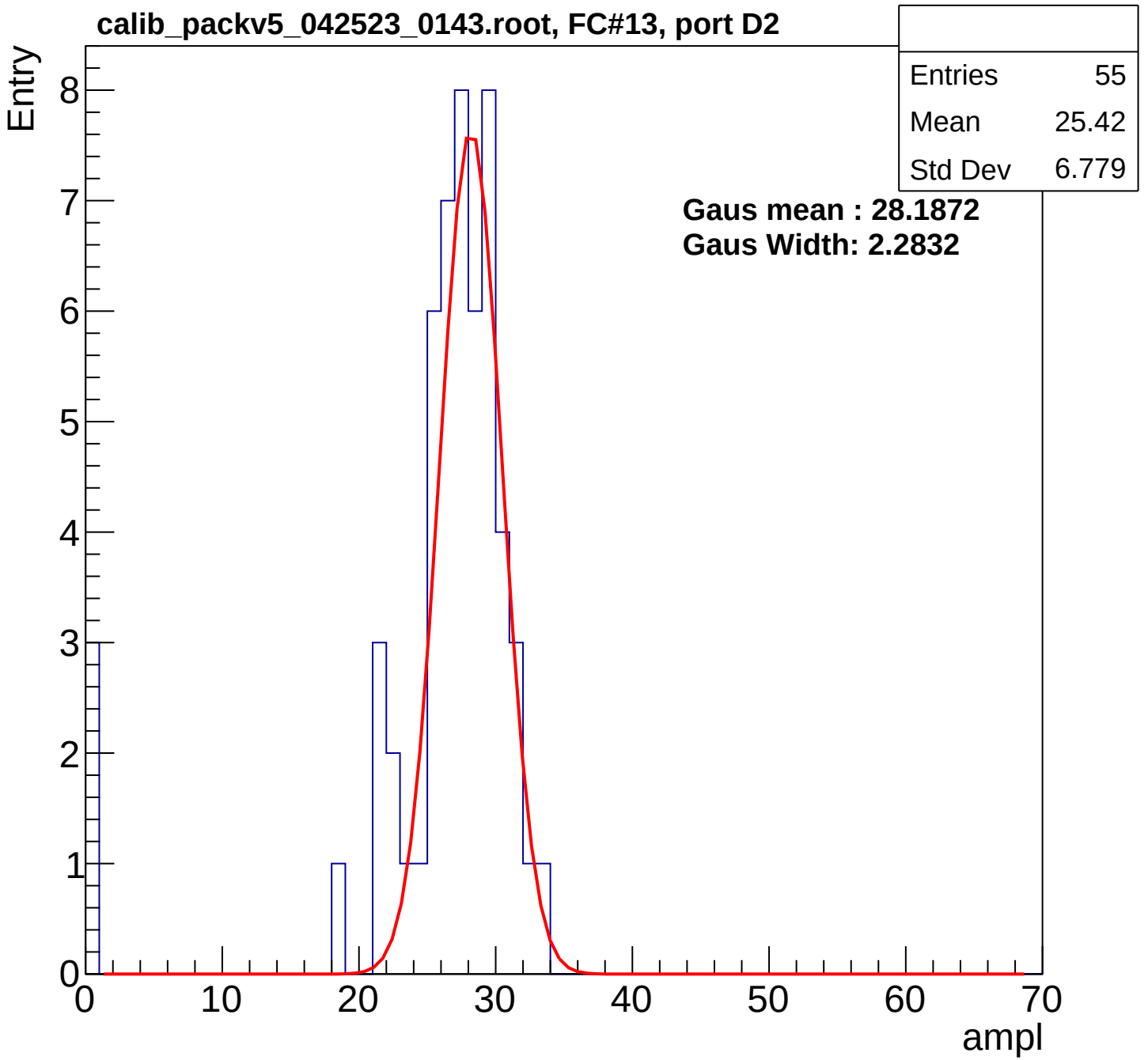
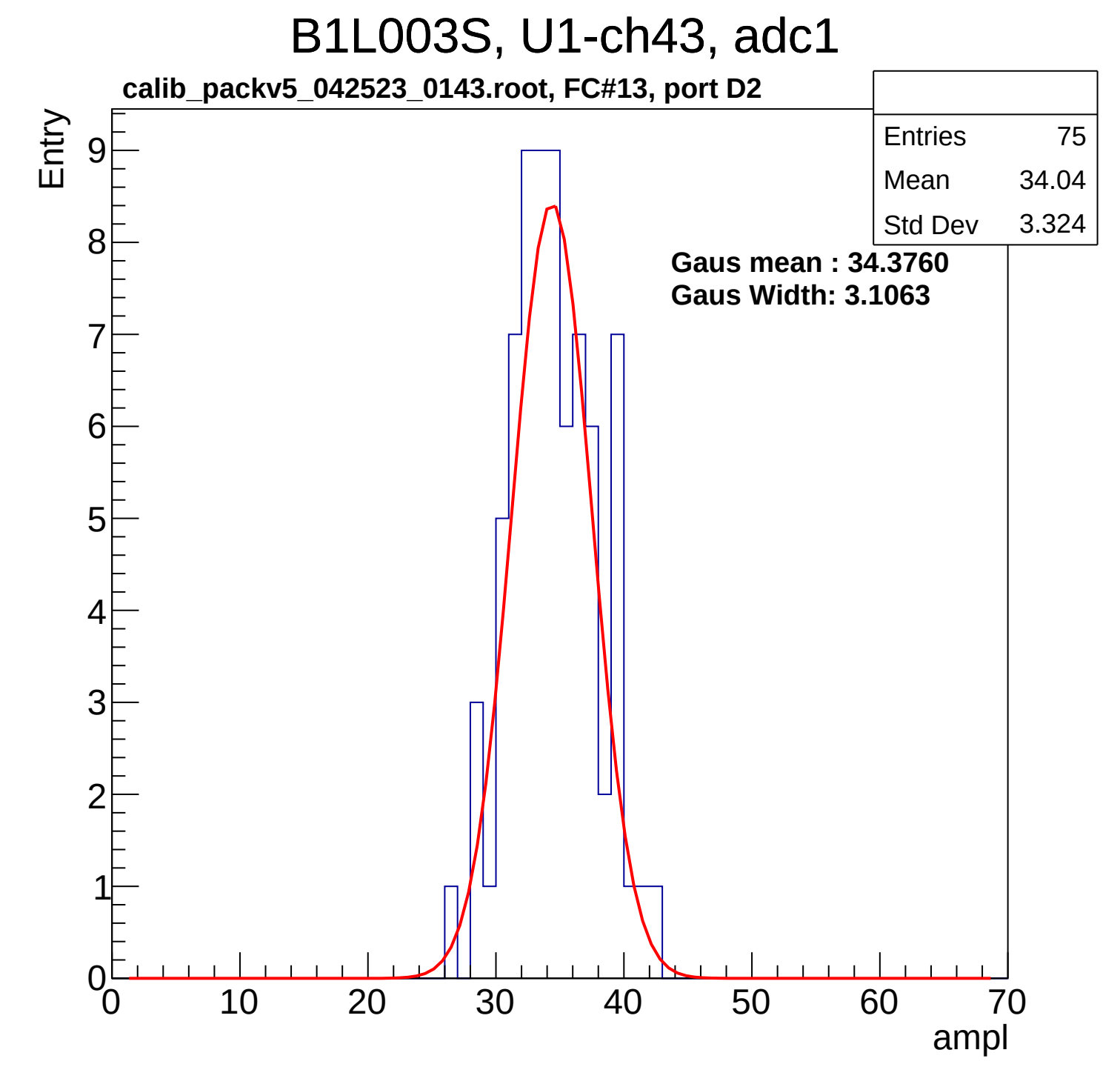
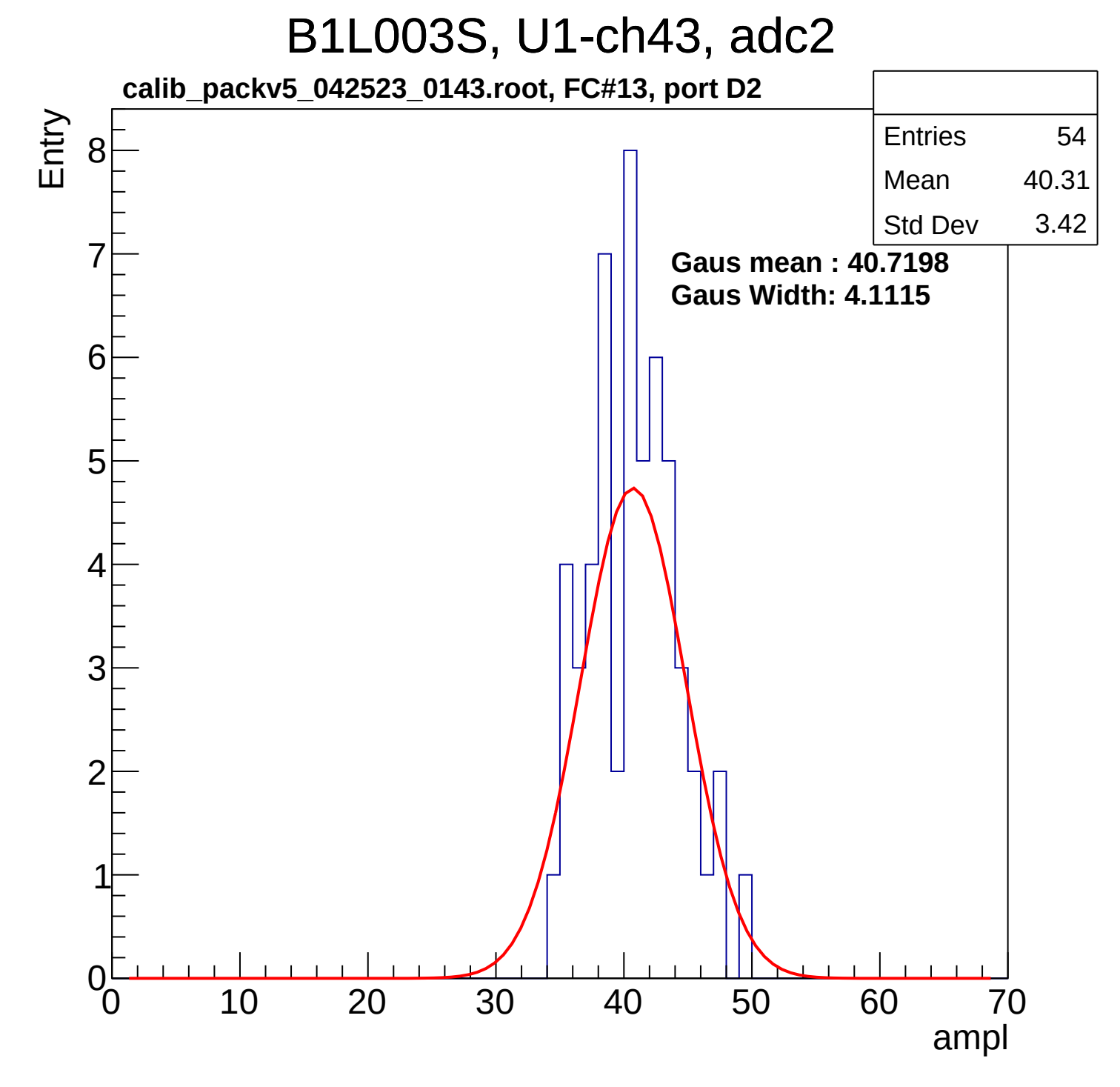
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch43, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 25.42
Std Dev 6.779
Gaus mean : 28.1872
Gaus Width: 2.2832
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch43, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 34.04
Std Dev 3.324
Gaus mean : 34.3760
Gaus Width: 3.1063
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch43, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 40.31
Std Dev 3.42
Gaus mean : 40.7198
Gaus Width: 4.1115
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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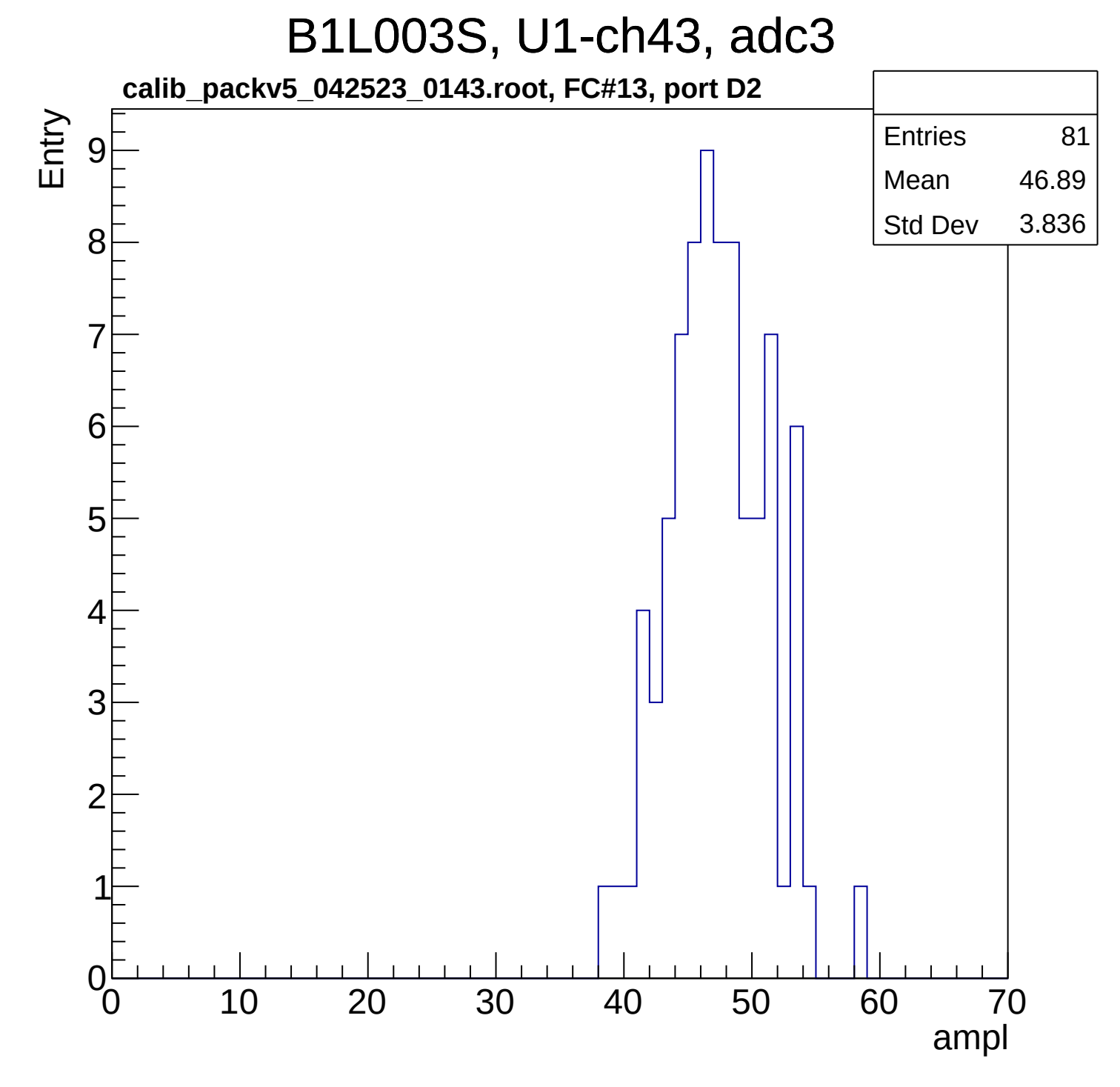
B1L003S, U1-ch43, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 46.89
Std Dev 3.836
Entry
ampl
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2
3
4
5
6
7
8
9
0
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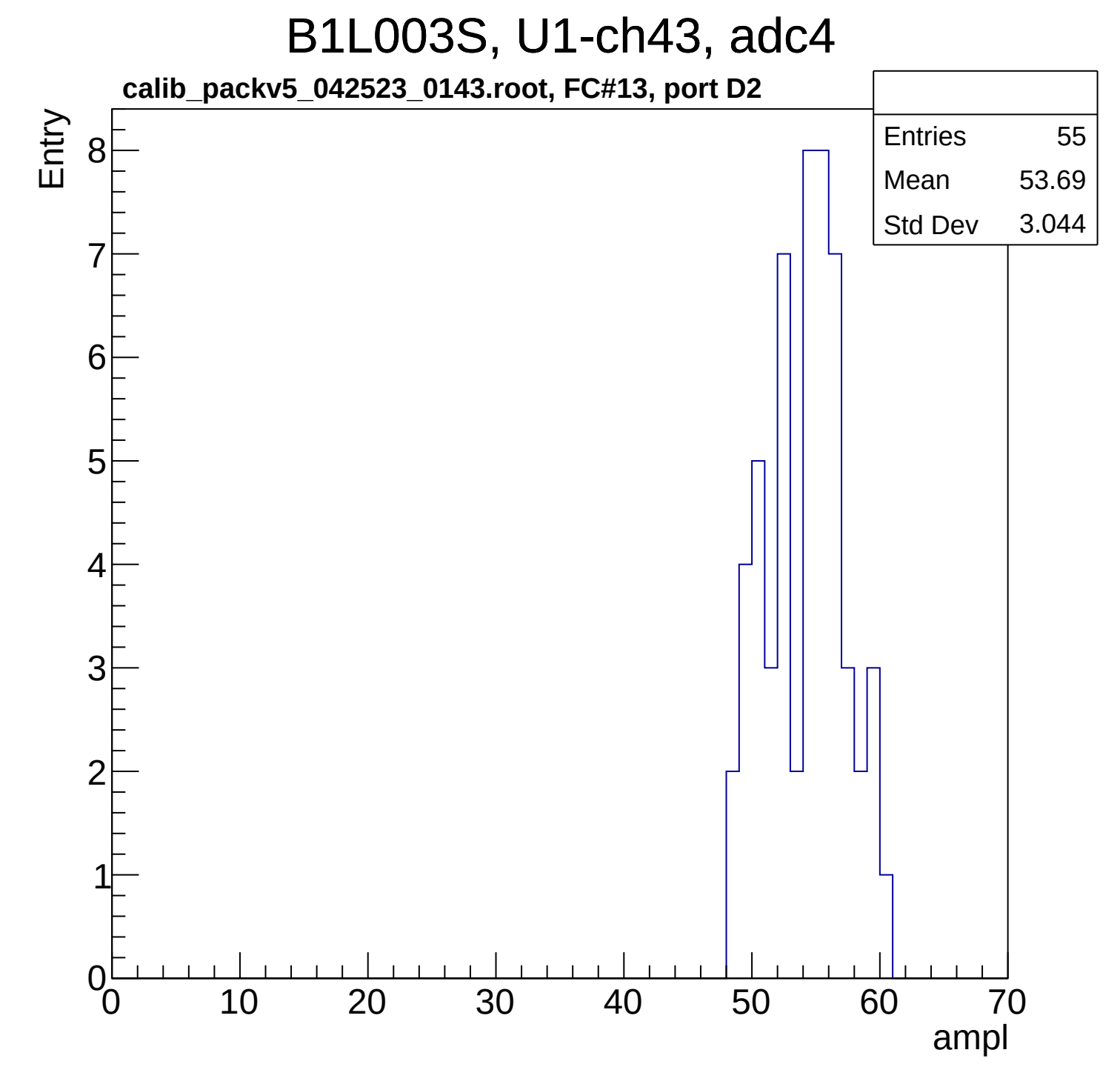
60
70


B1L003S, U1-ch43, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.69
Std Dev 3.044
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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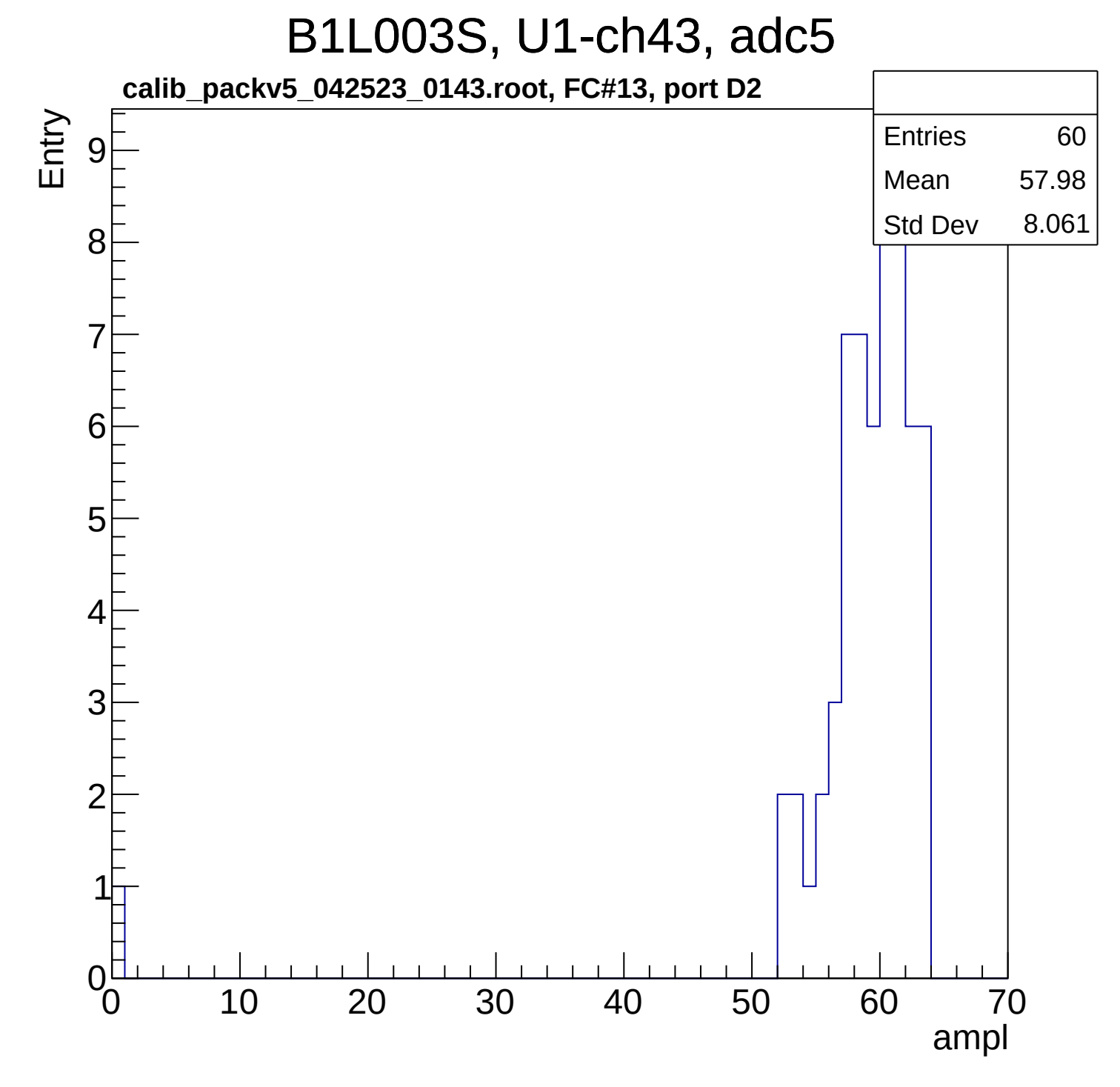
40
50
60
70


B1L003S, U1-ch43, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 57.98
Std Dev 8.061
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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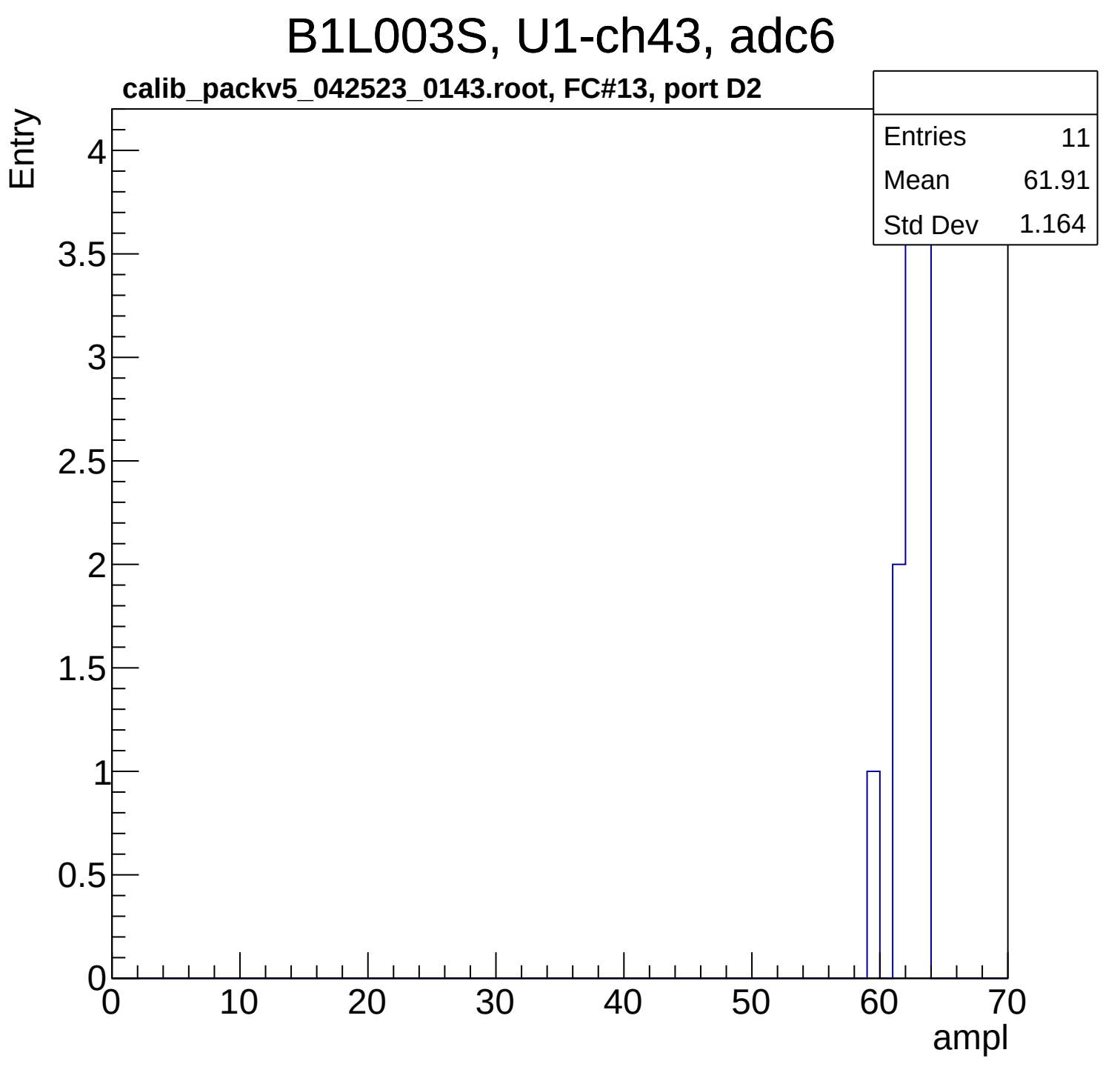
60
70


B1L003S, U1-ch43, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.91
Std Dev 1.164
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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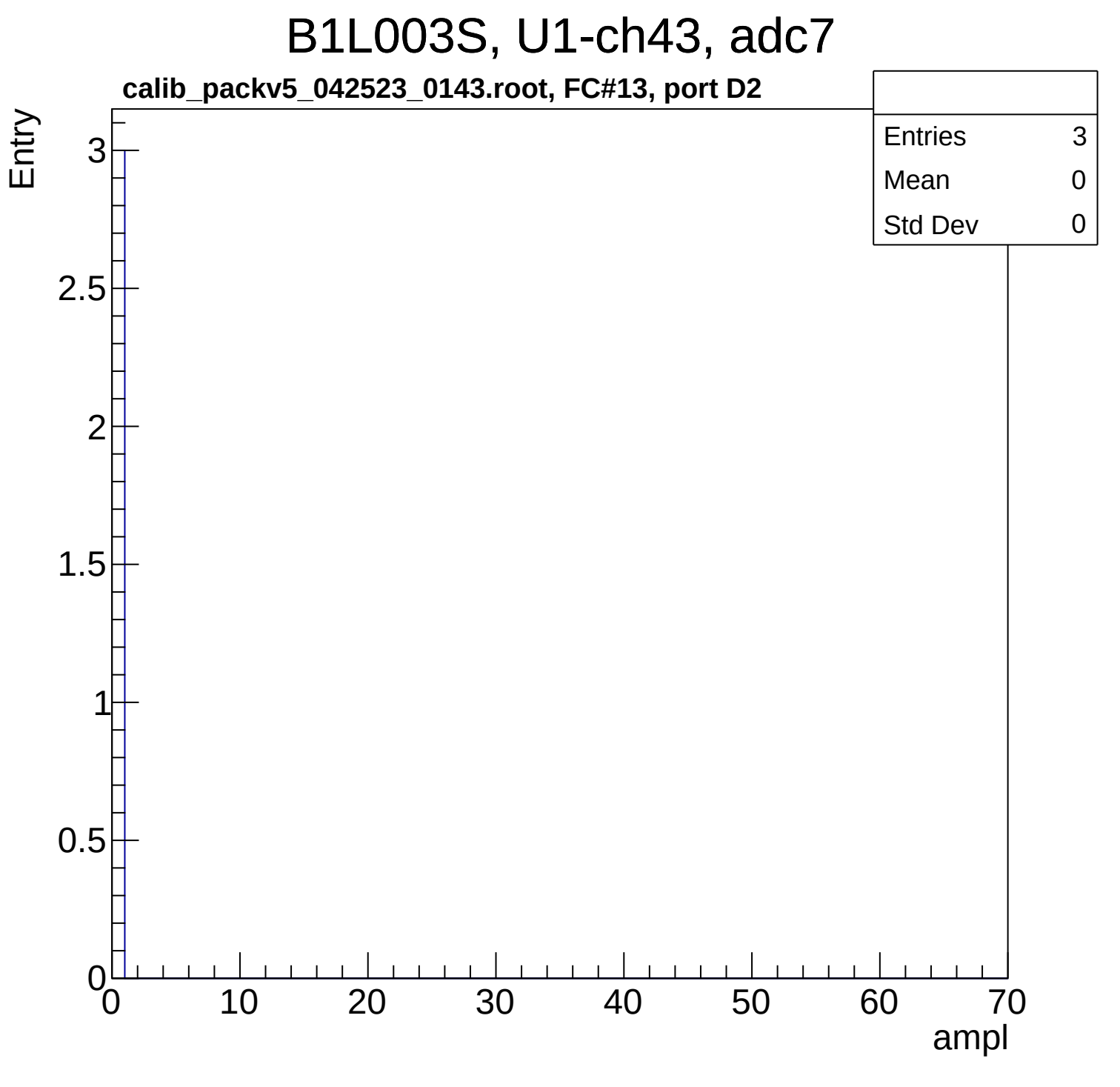
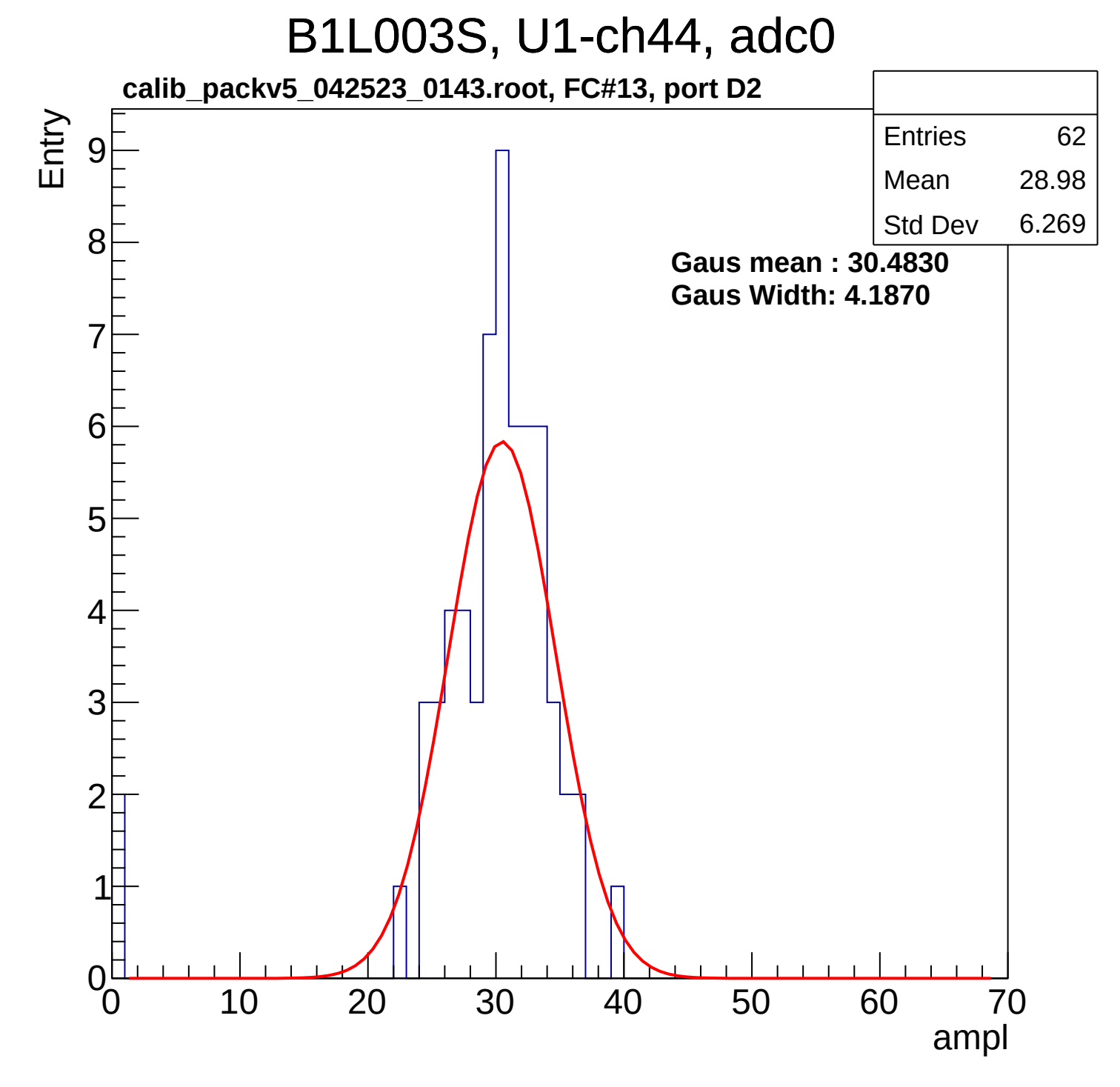
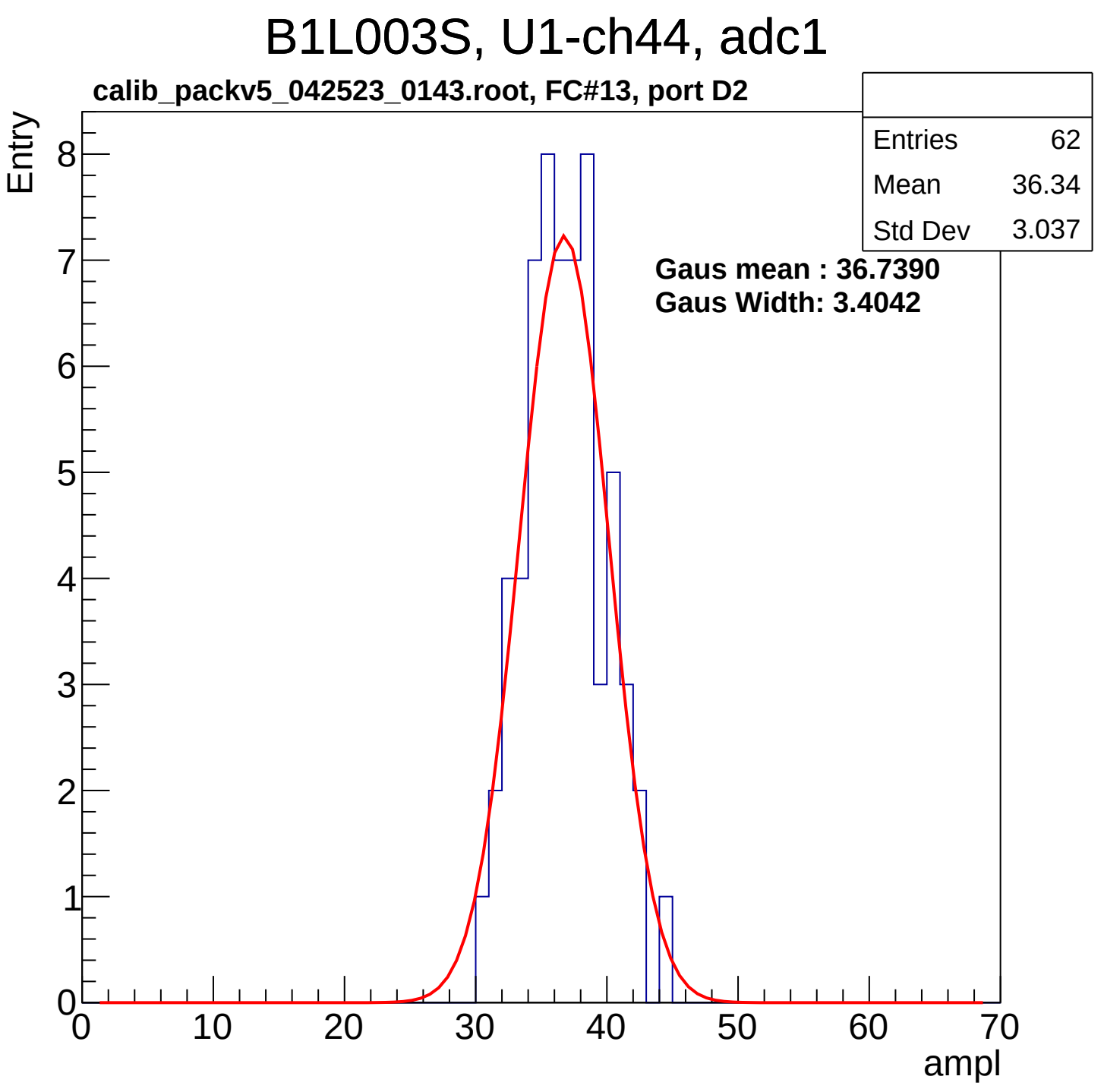
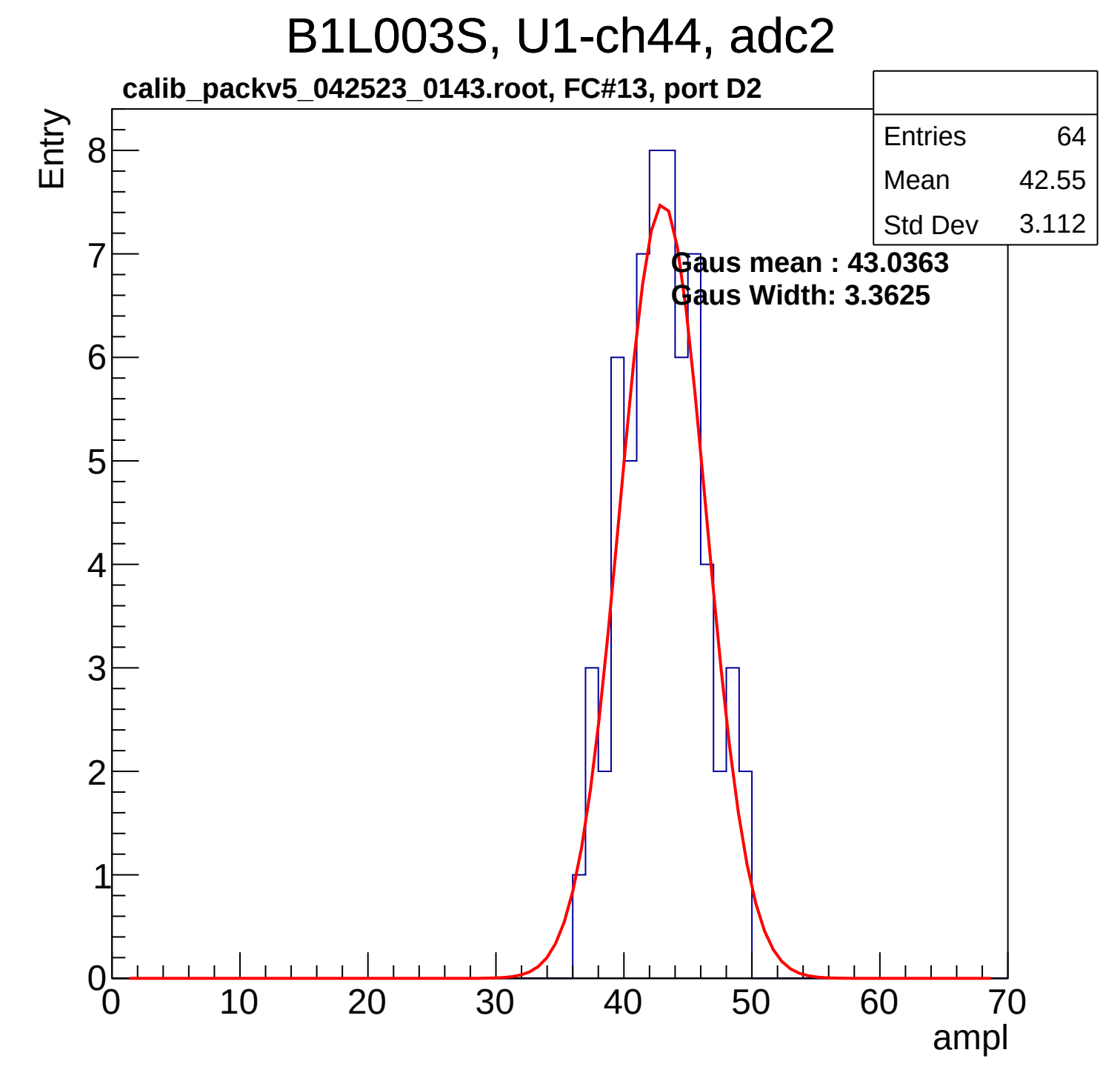
40
50
60
70


B1L003S, U1-ch43, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch44, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.98
Std Dev 6.269
Gaus mean : 30.4830
Gaus Width: 4.1870
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch44, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.34
Std Dev 3.037
Gaus mean : 36.7390
Gaus Width: 3.4042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch44, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.55
Std Dev 3.112
Gaus mean : 43.0363
Gaus Width: 3.3625
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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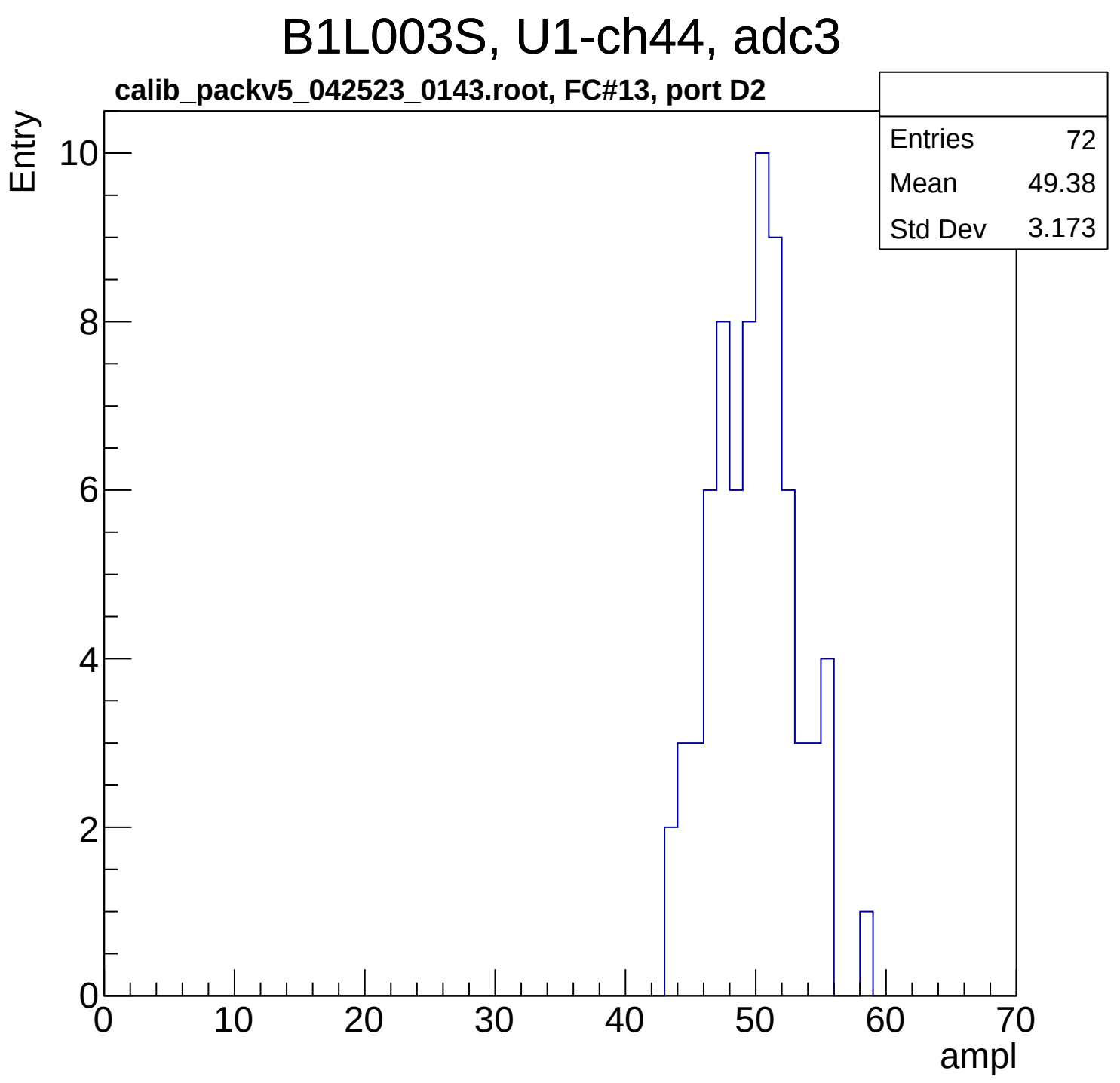
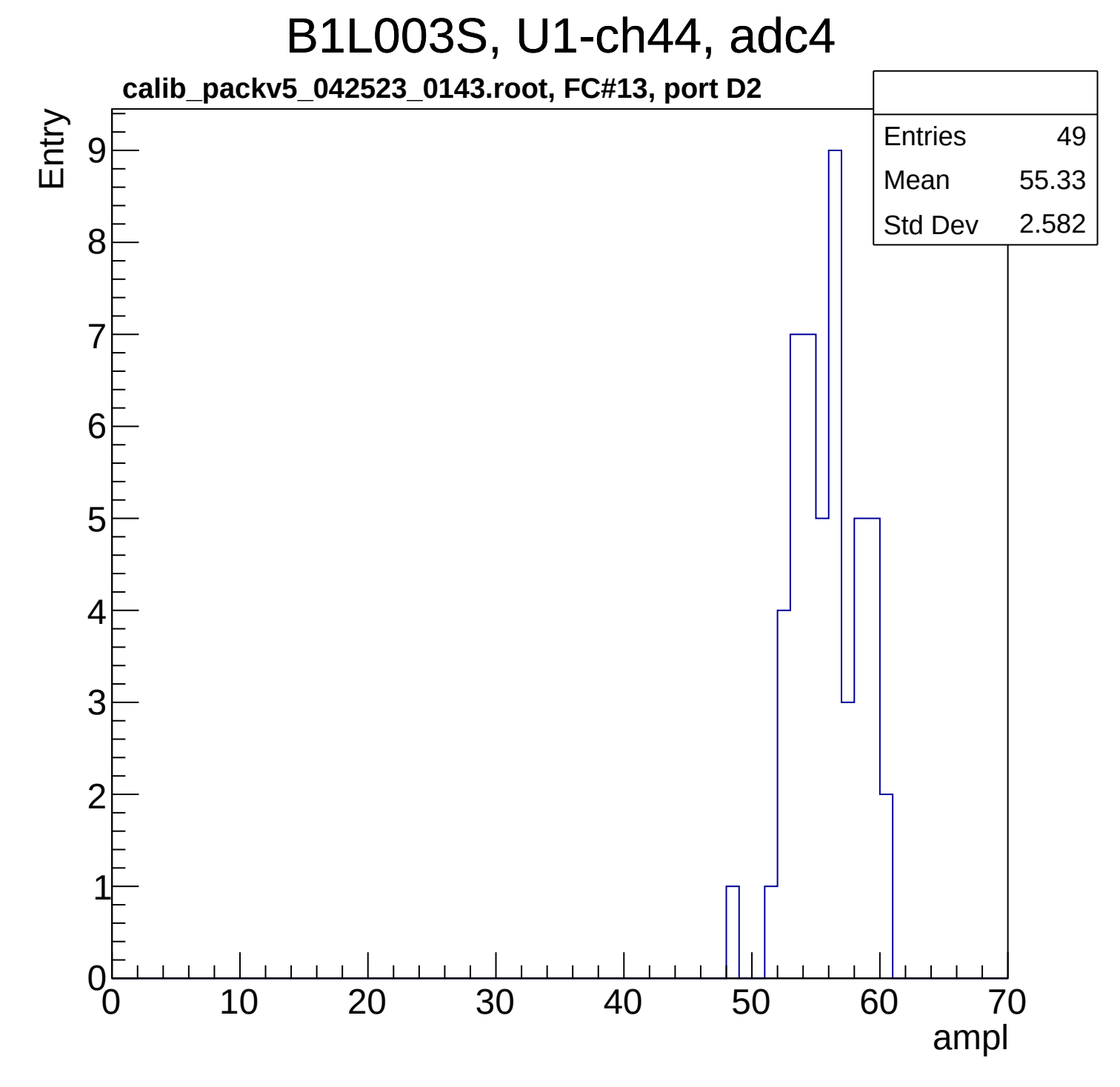
40
50
60
70


B1L003S, U1-ch44, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.38
Std Dev 3.173
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch44, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 55.33
Std Dev 2.582
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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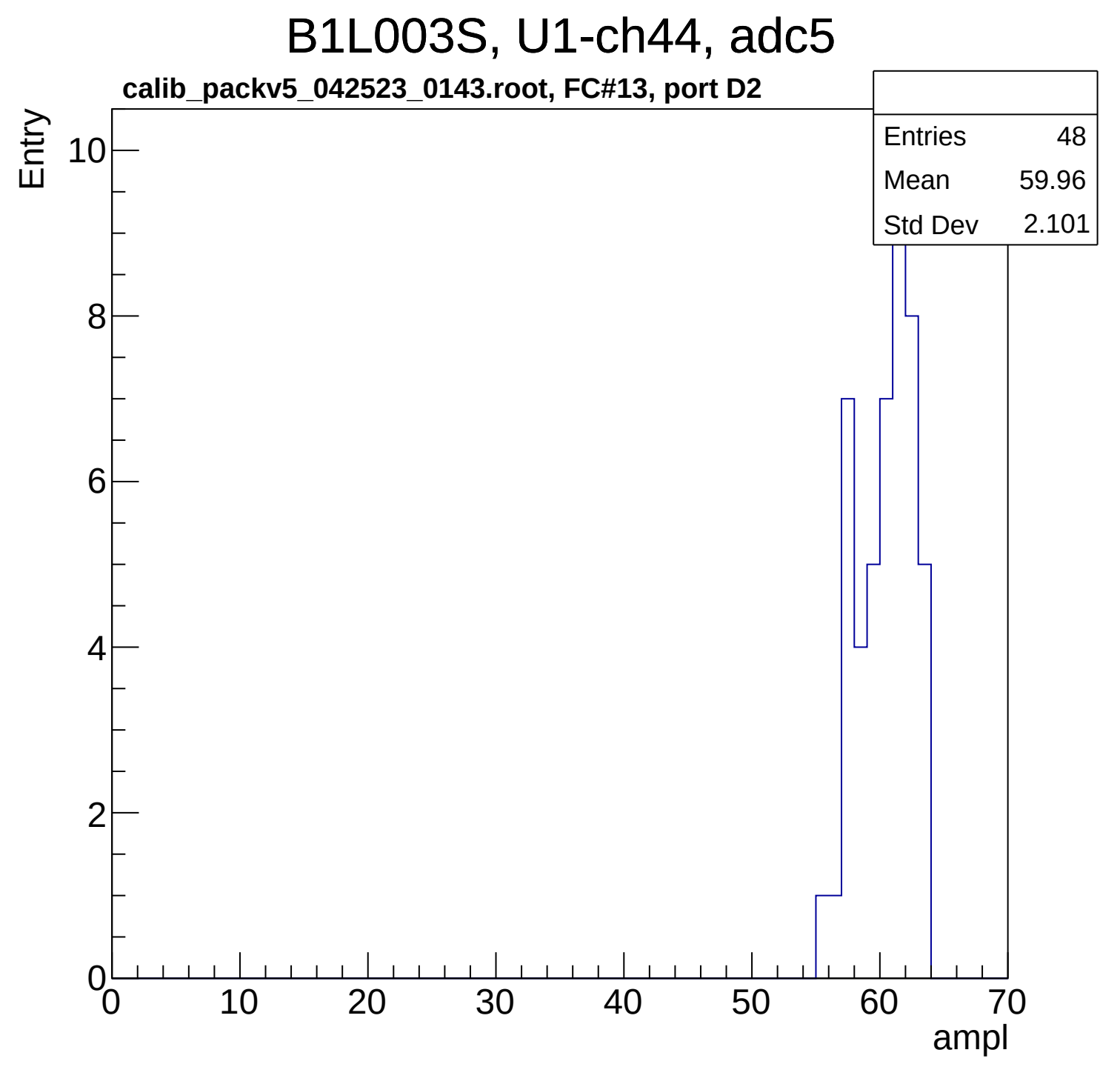
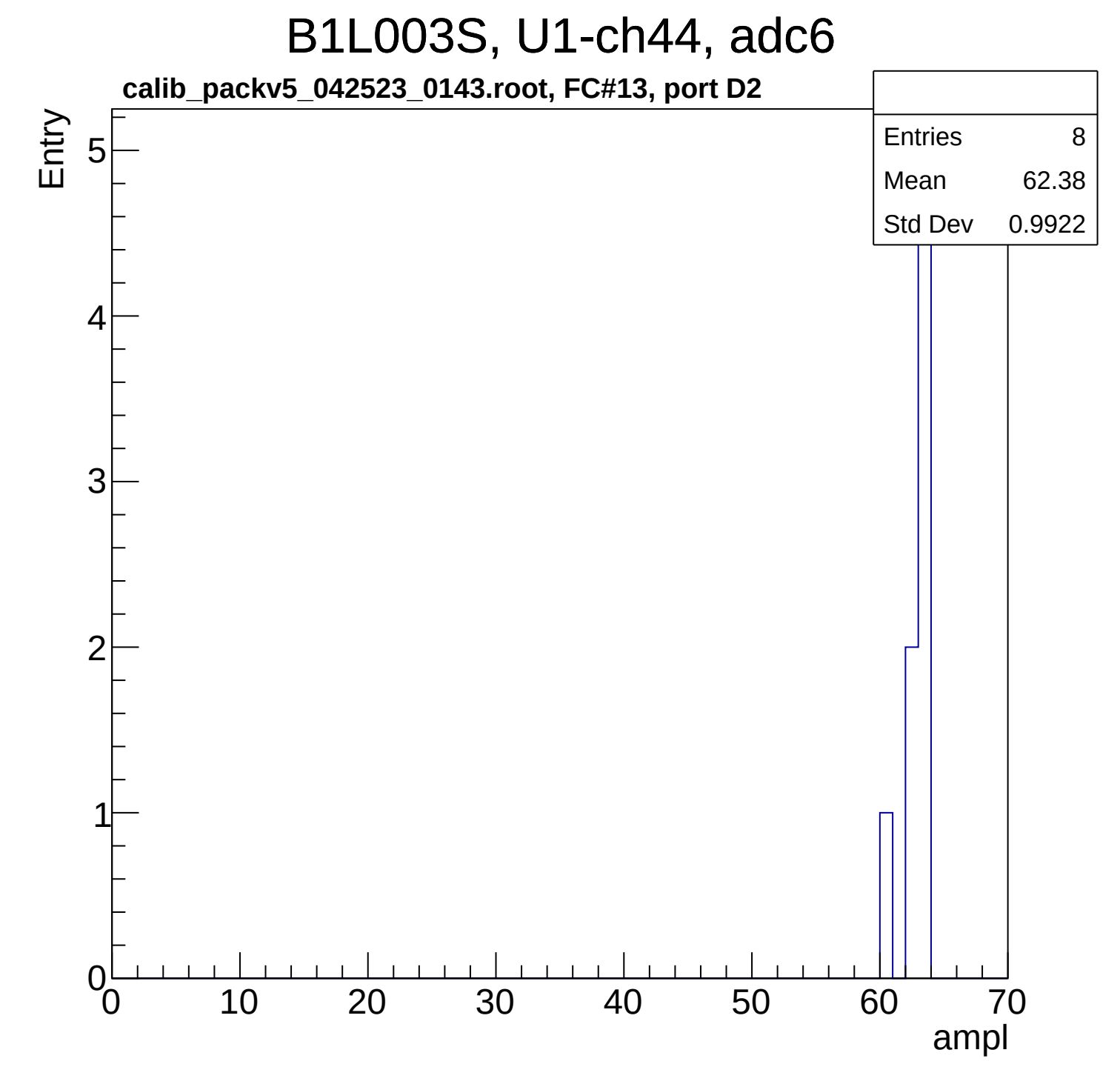
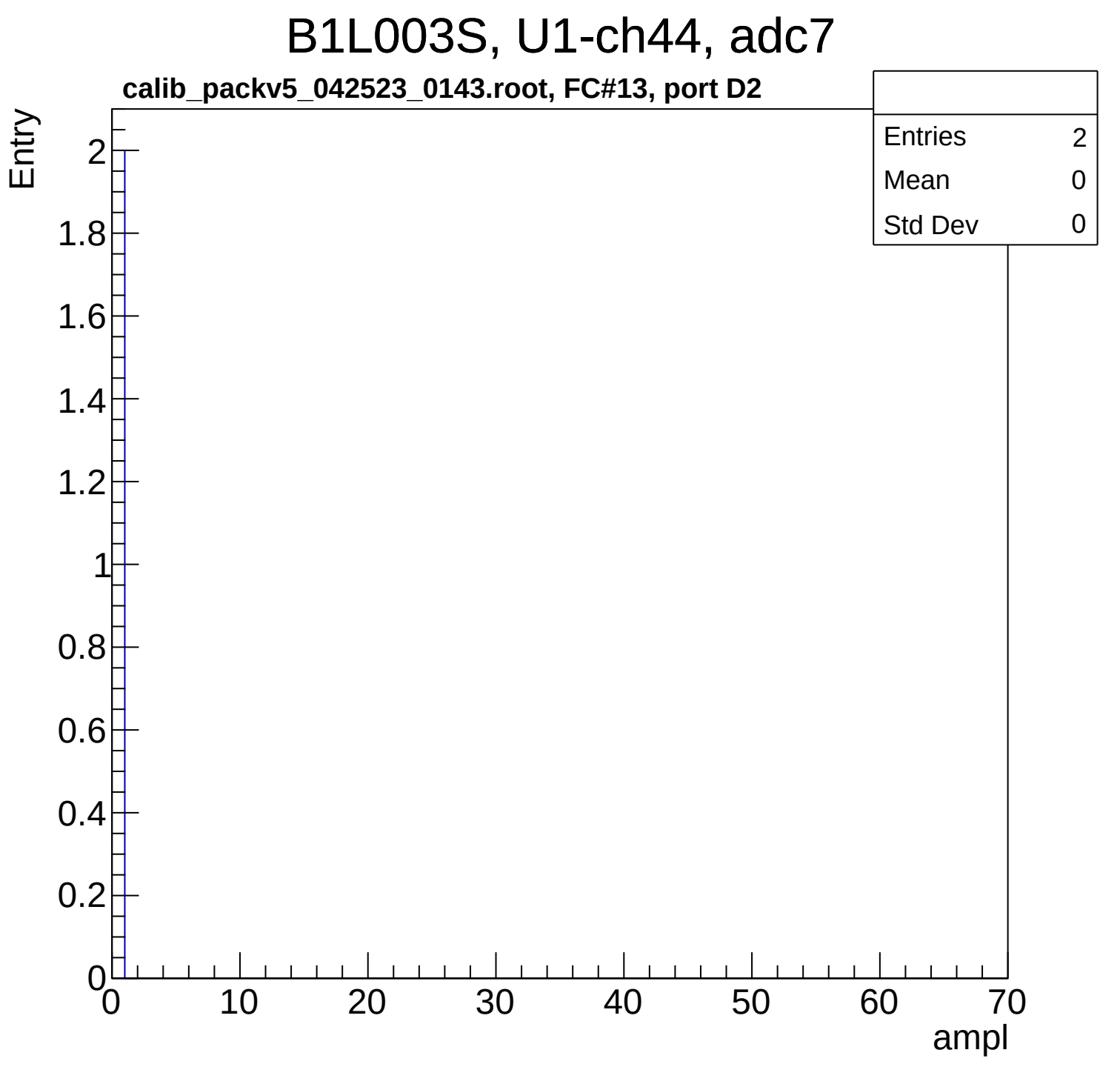
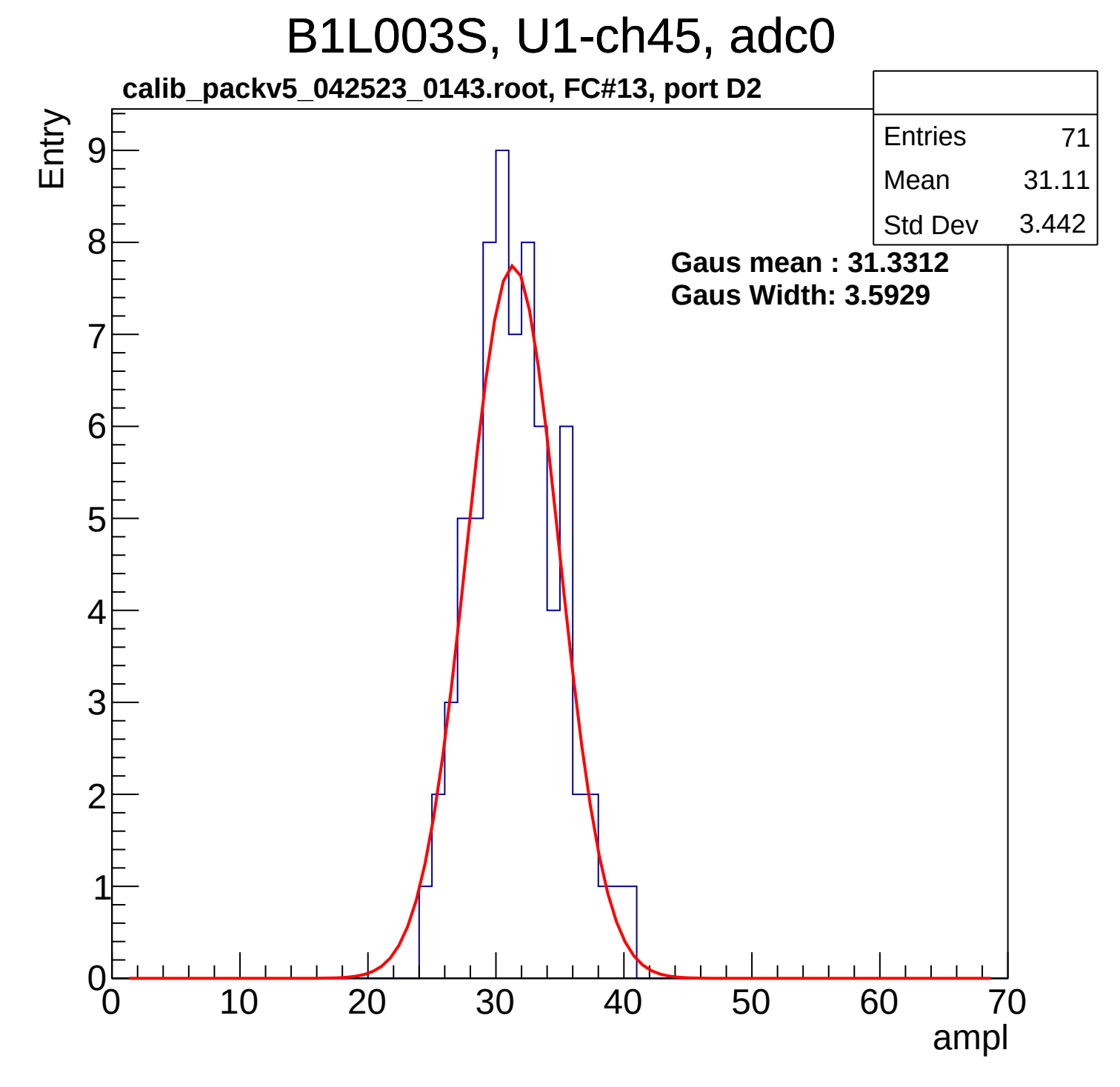
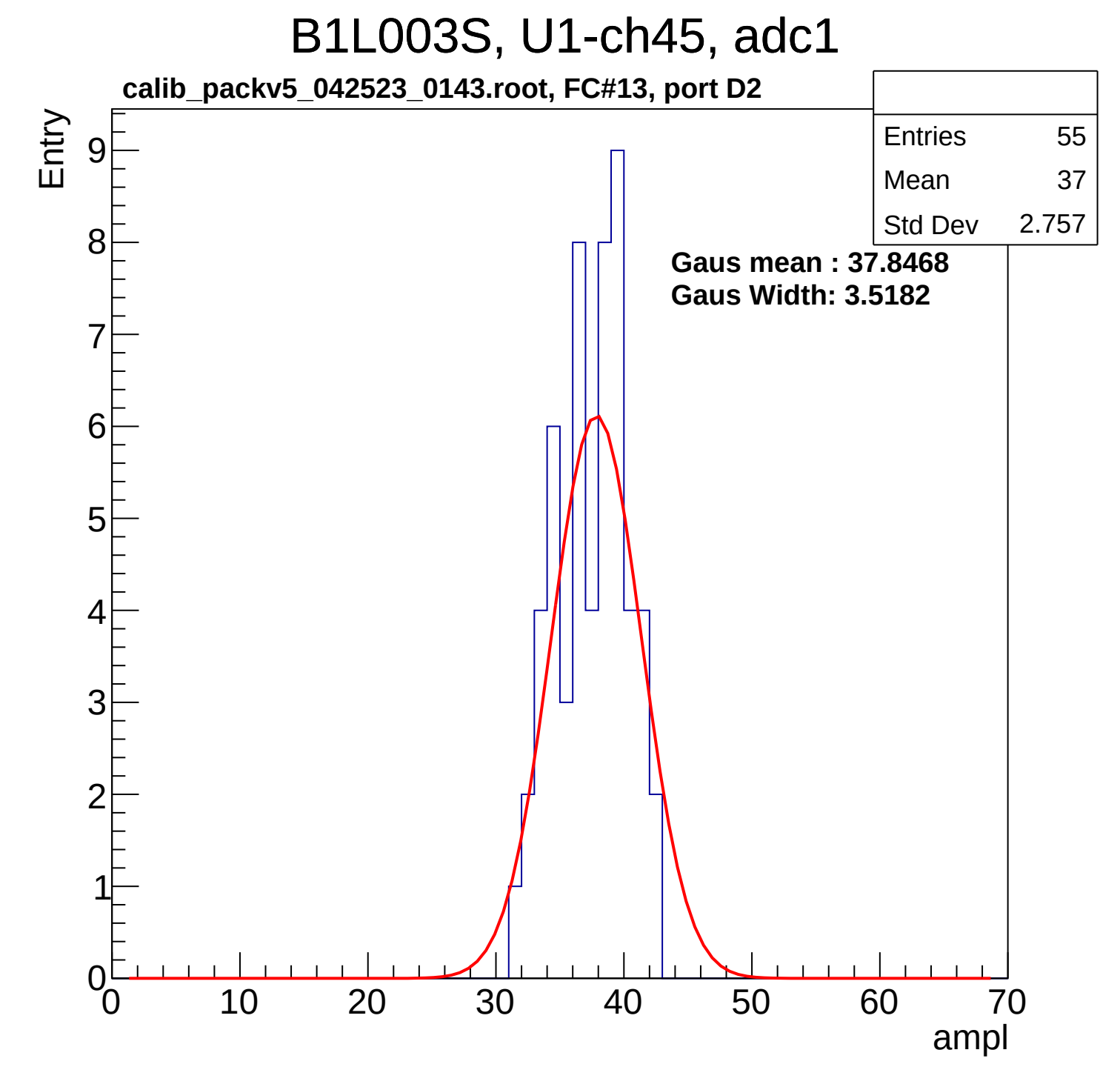
40
50
60
70

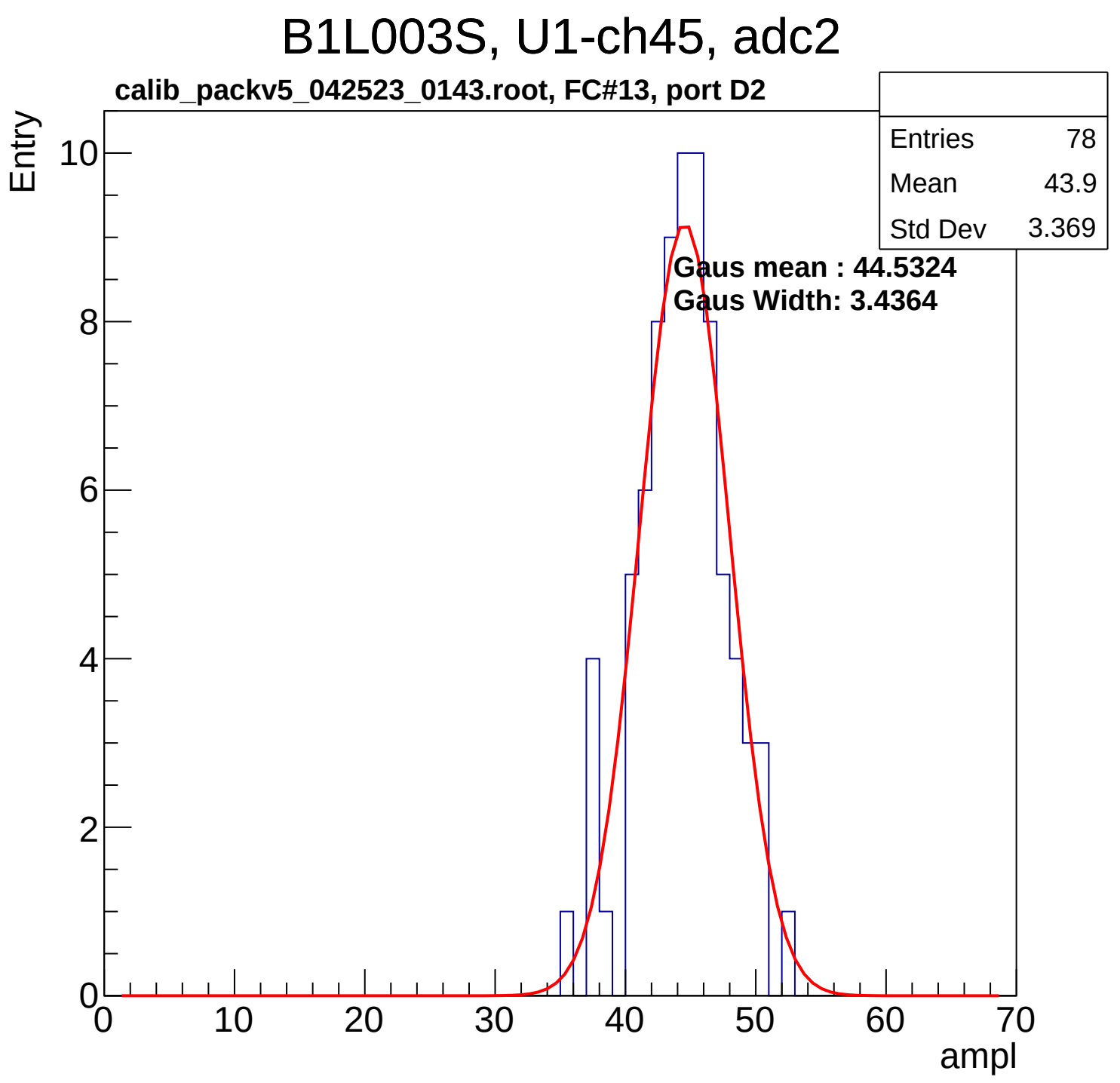

B1L003S, U1-ch44, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 59.96
Std Dev 2.101
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

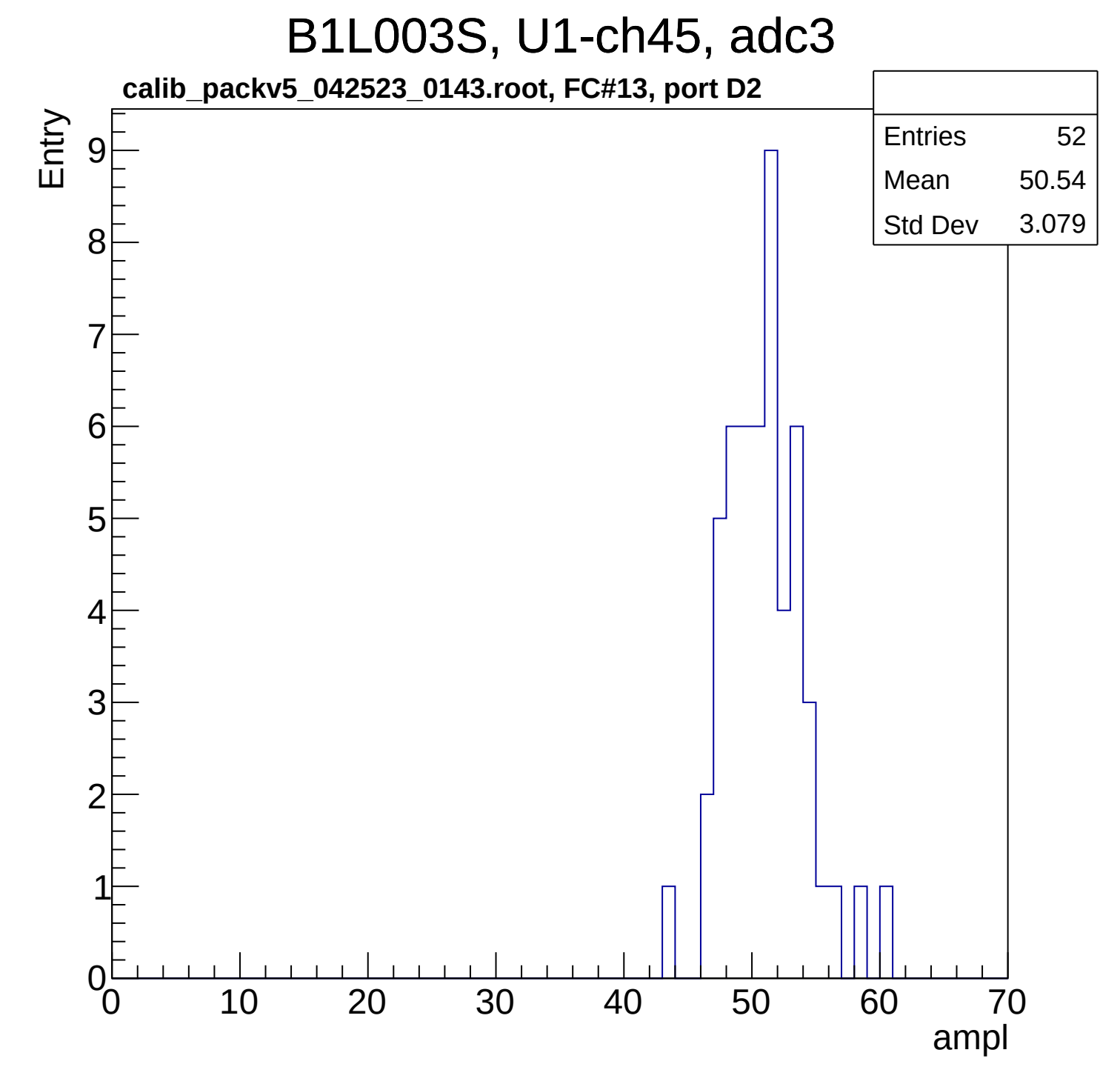

B1L003S, U1-ch44, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.38
Std Dev 0.9922
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

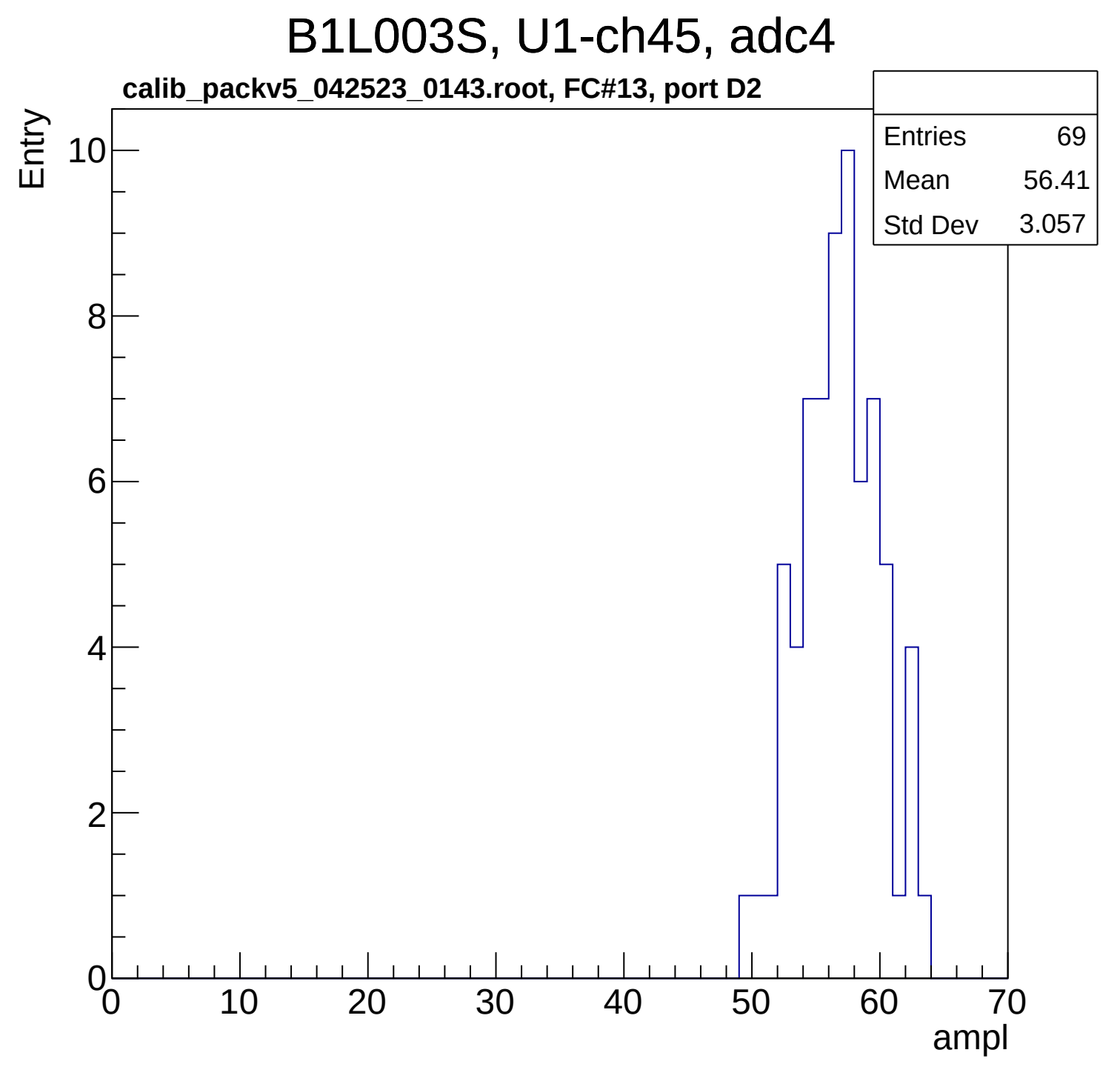

B1L003S, U1-ch44, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

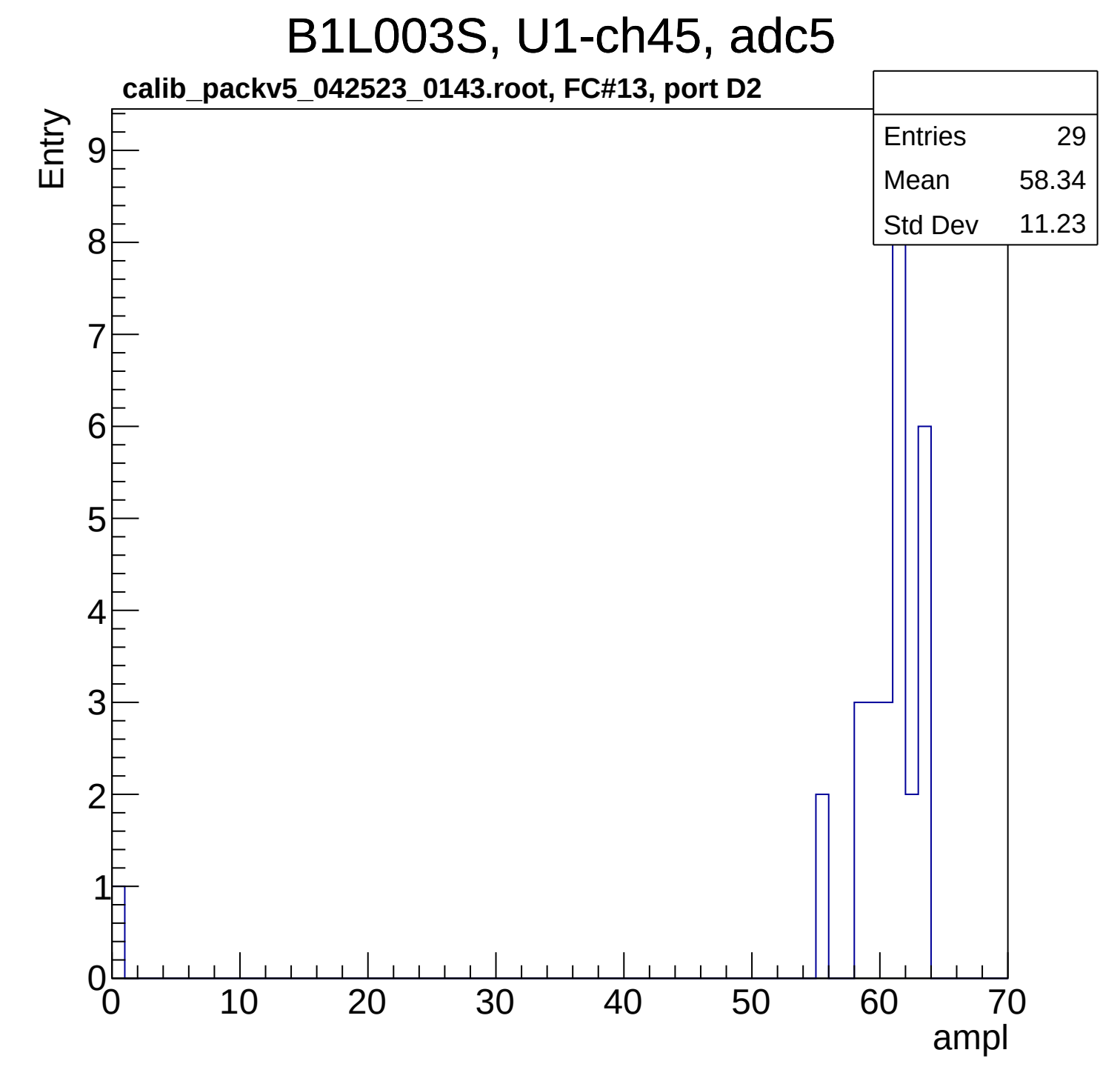

B1L003S, U1-ch45, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 31.11
Std Dev 3.442
Gaus mean : 31.3312
Gaus Width: 3.5929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

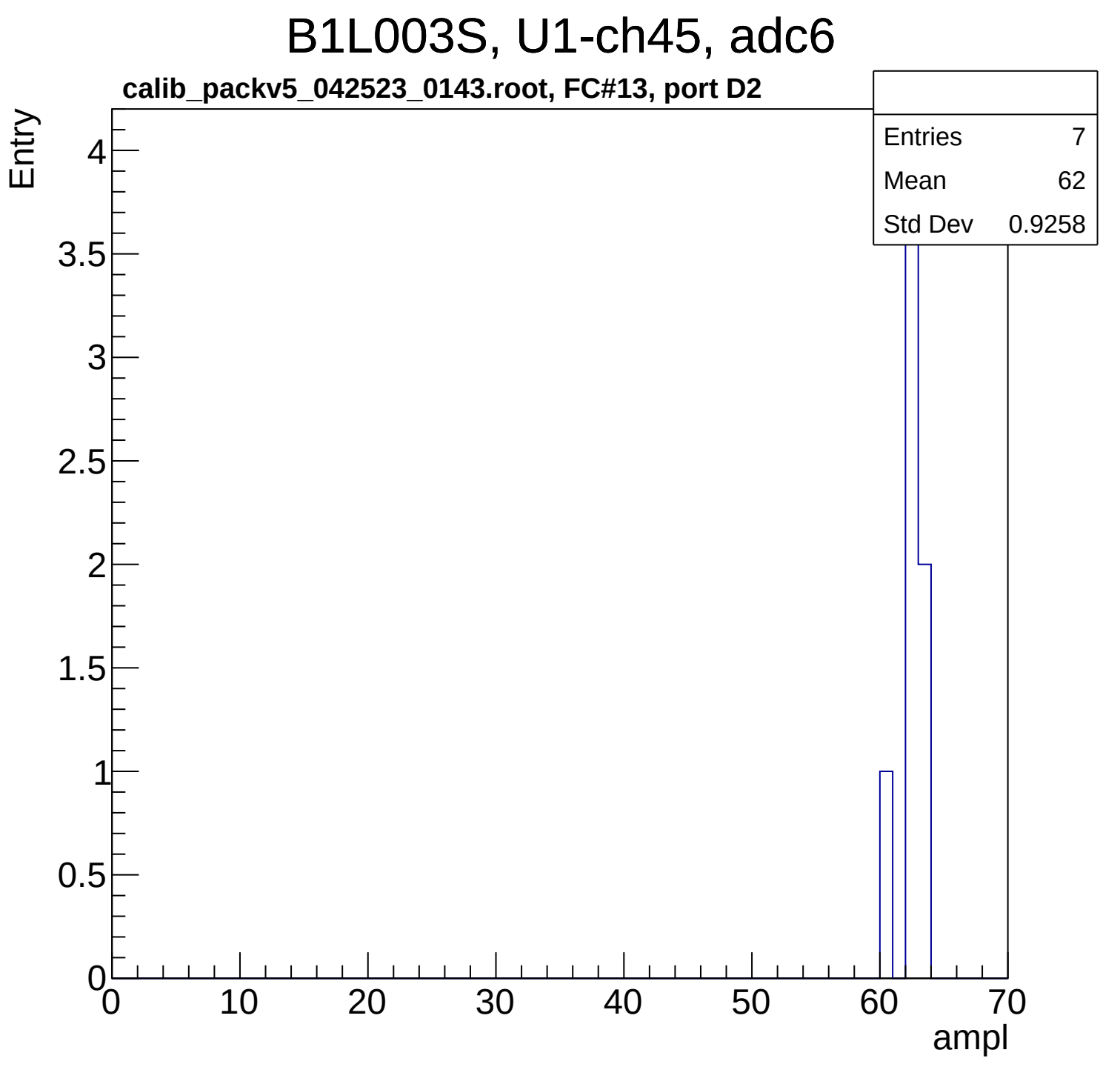

B1L003S, U1-ch45, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 37
Std Dev 2.757
Gaus mean : 37.8468
Gaus Width: 3.5182
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

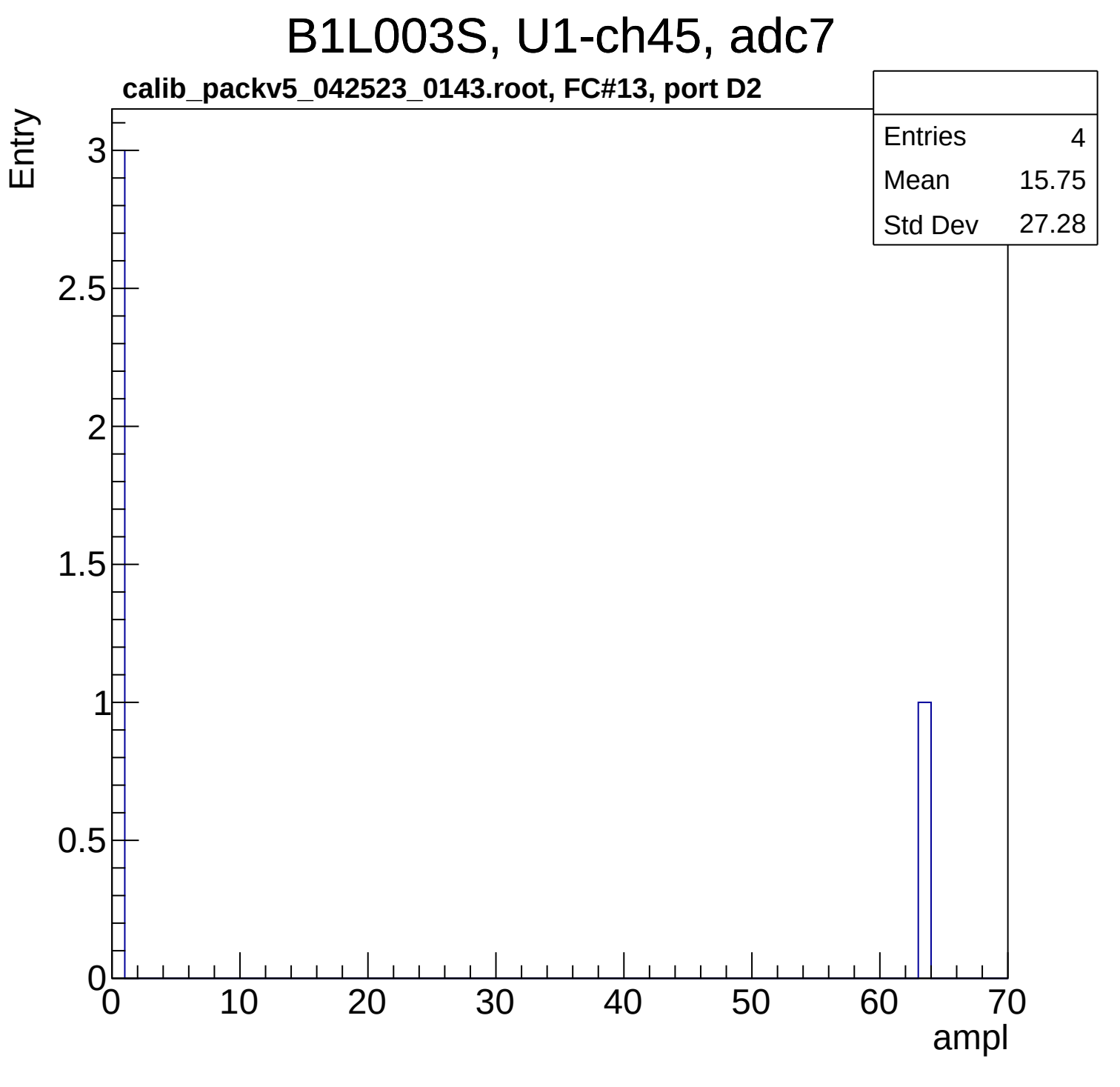

B1L003S, U1-ch45, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 43.9
Std Dev 3.369
Gaus mean : 44.5324
Gaus Width: 3.4364
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

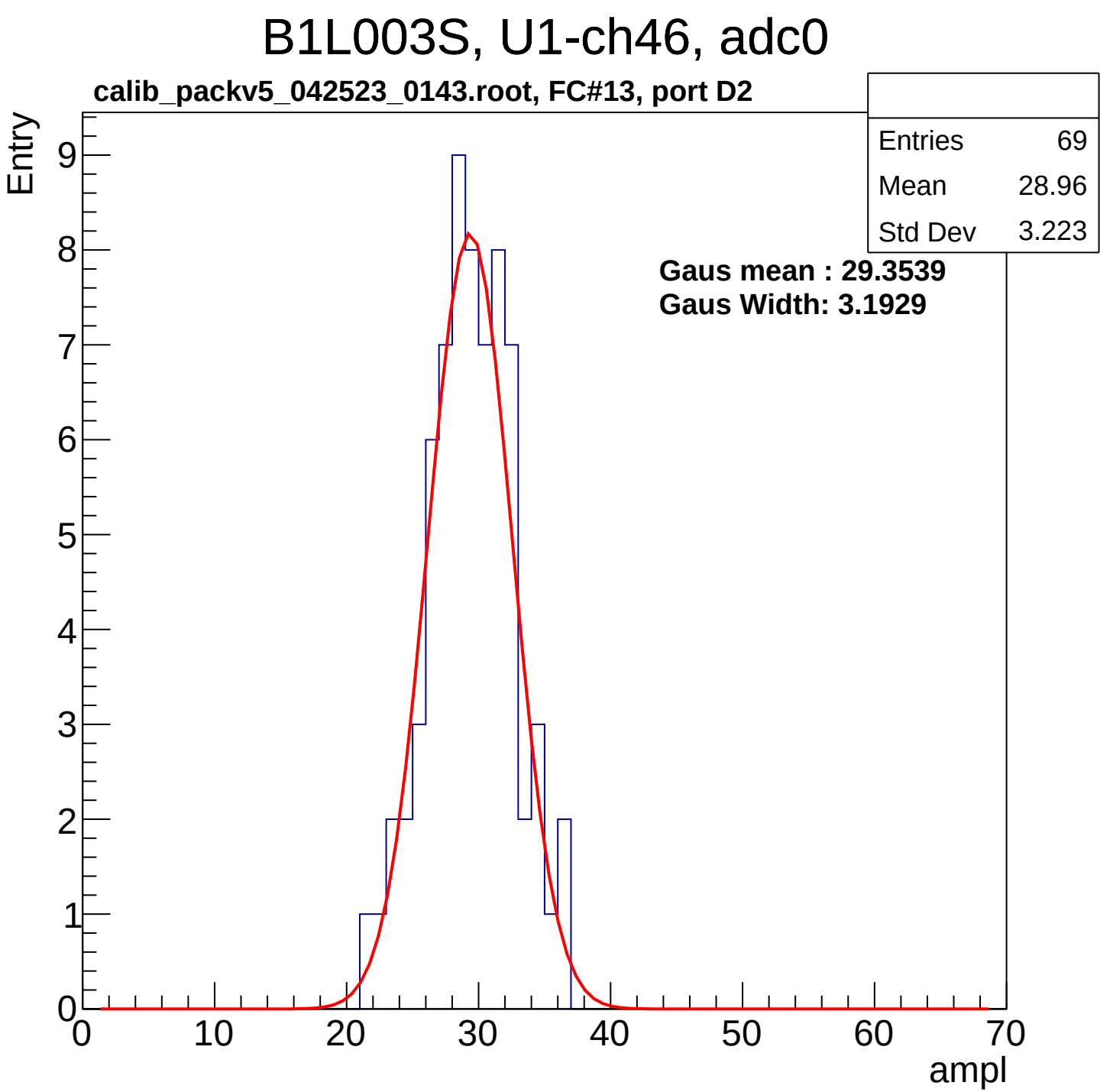

B1L003S, U1-ch45, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 50.54
Std Dev 3.079
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

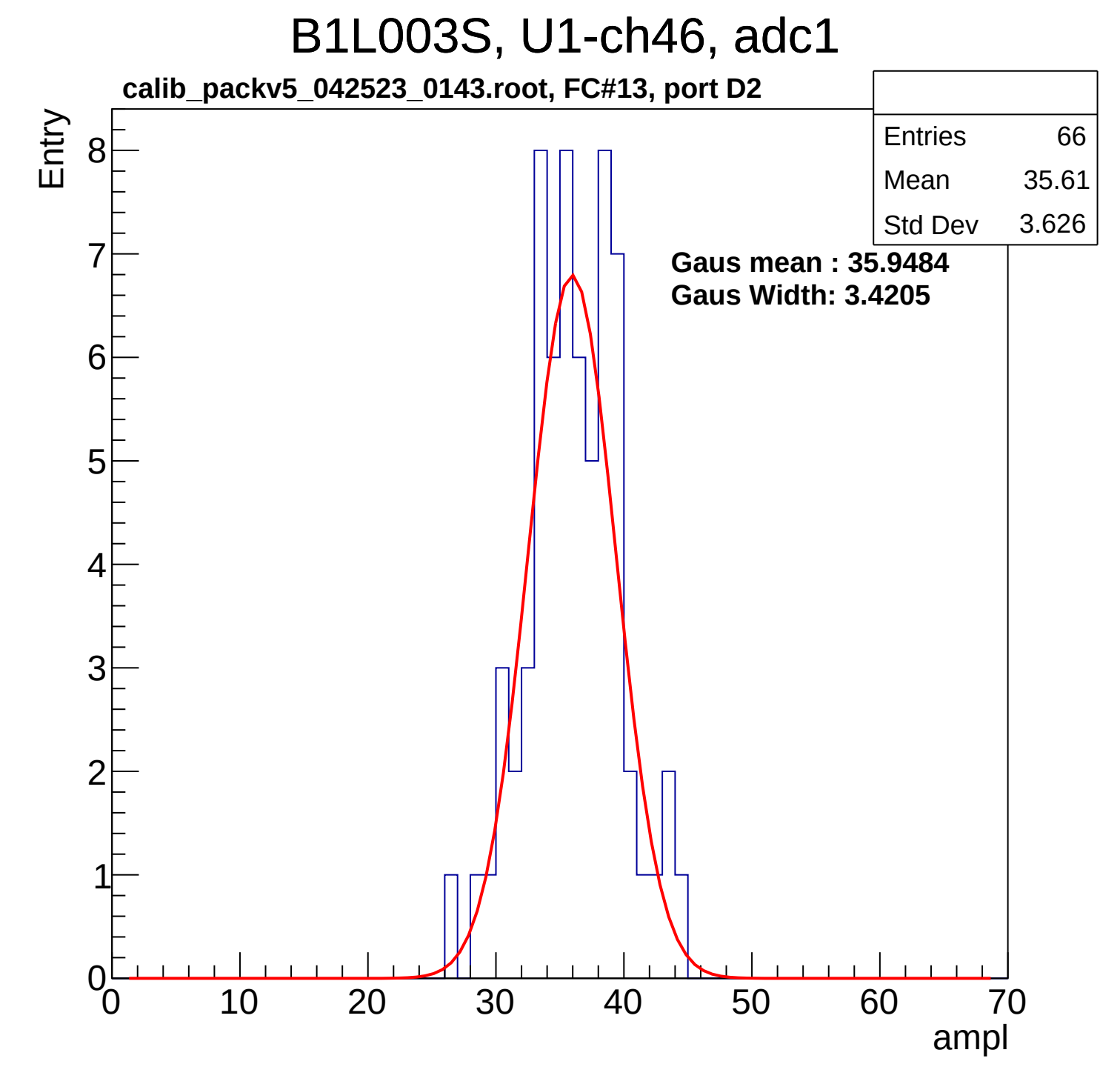

B1L003S, U1-ch45, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 56.41
Std Dev 3.057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

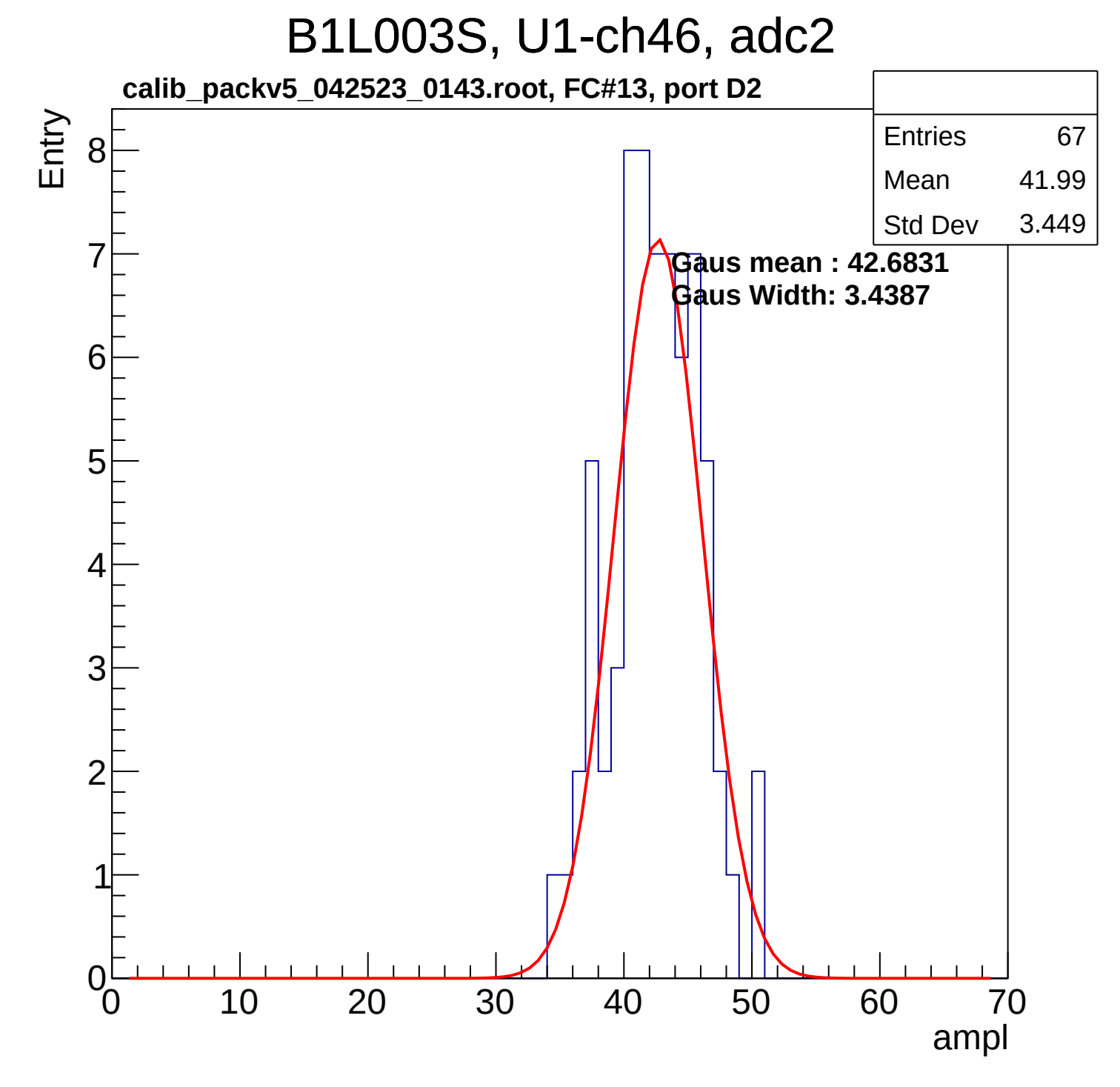

B1L003S, U1-ch45, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 29
Mean 58.34
Std Dev 11.23
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

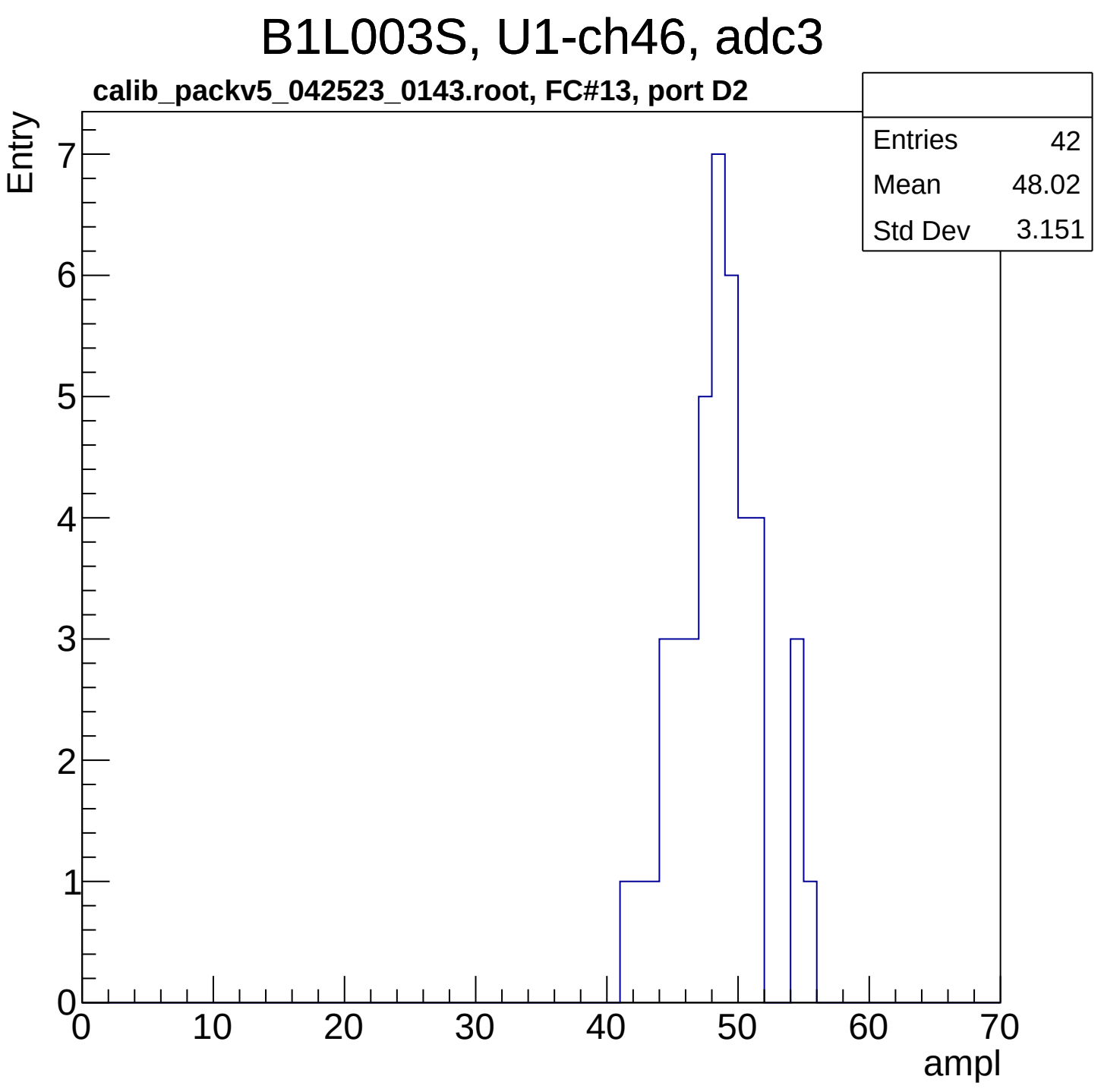

B1L003S, U1-ch45, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62
Std Dev 0.9258
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

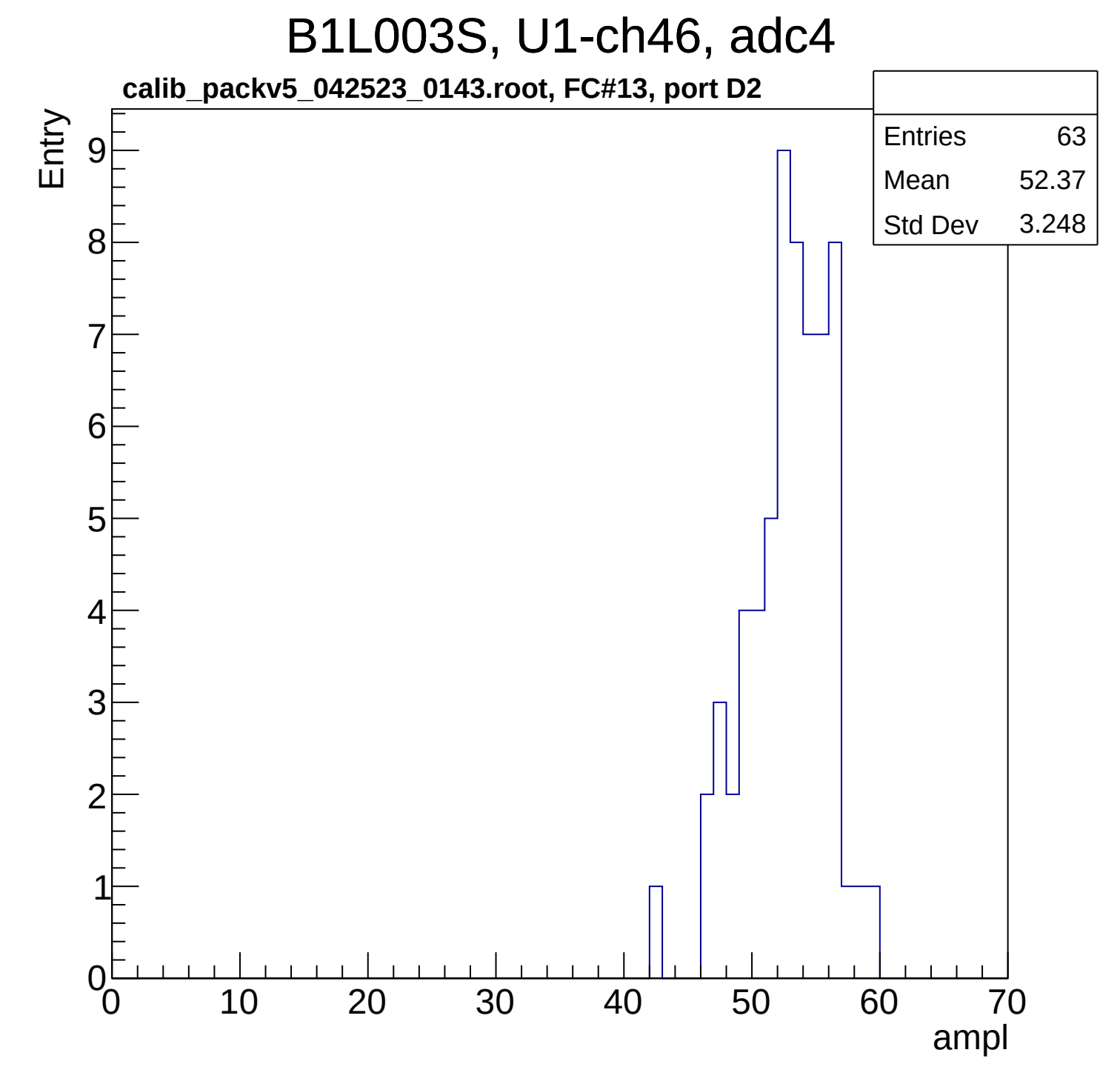

B1L003S, U1-ch45, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 15.75
Std Dev 27.28
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch46, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 28.96
Std Dev 3.223
Gaus mean : 29.3539
Gaus Width: 3.1929
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch46, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.61
Std Dev 3.626
Gaus mean : 35.9484
Gaus Width: 3.4205
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch46, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.99
Std Dev 3.449
Gaus mean : 42.6831
Gaus Width: 3.4387
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch46, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 48.02
Std Dev 3.151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch46, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 52.37
Std Dev 3.248
Entry
ampl
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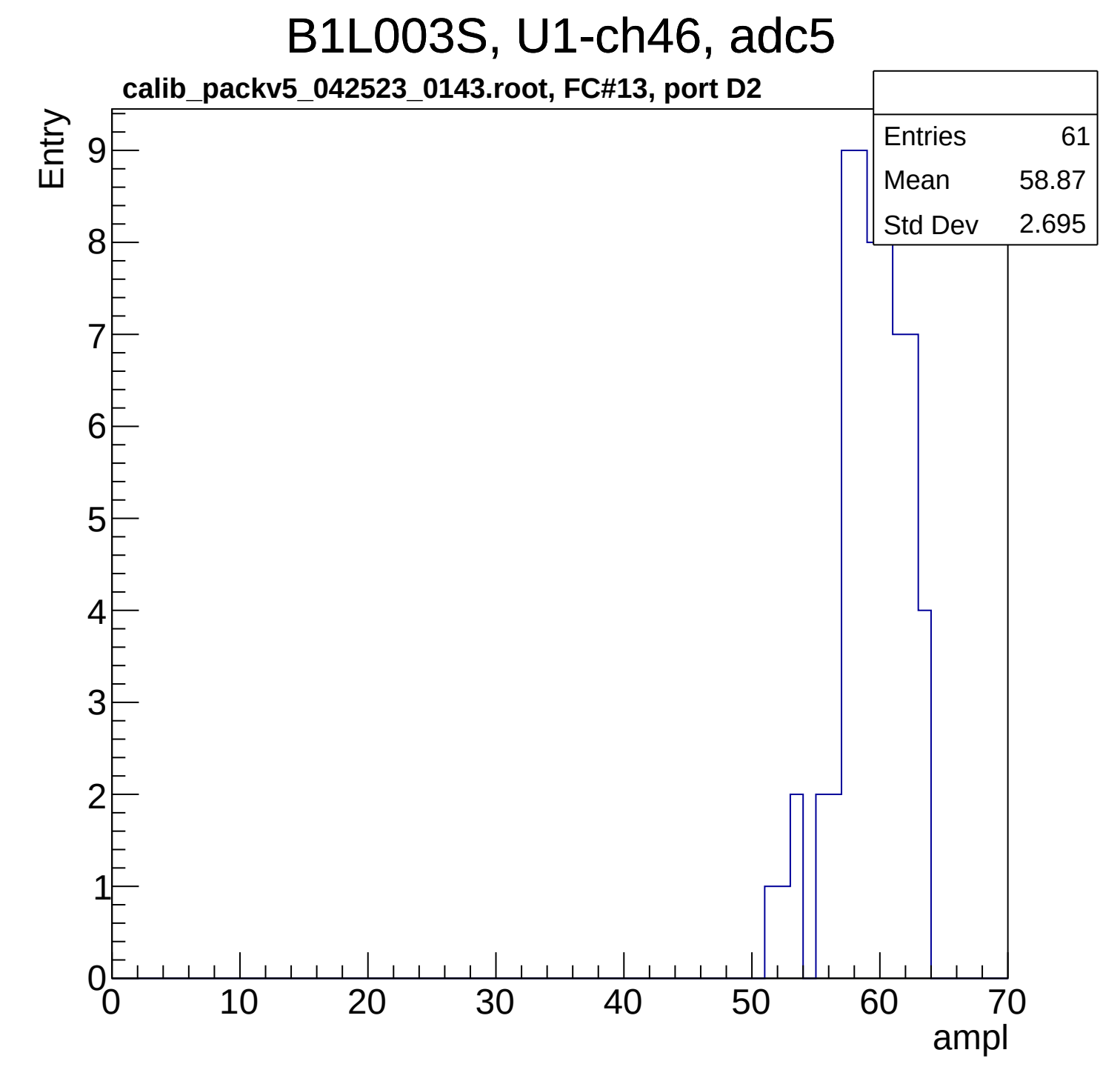
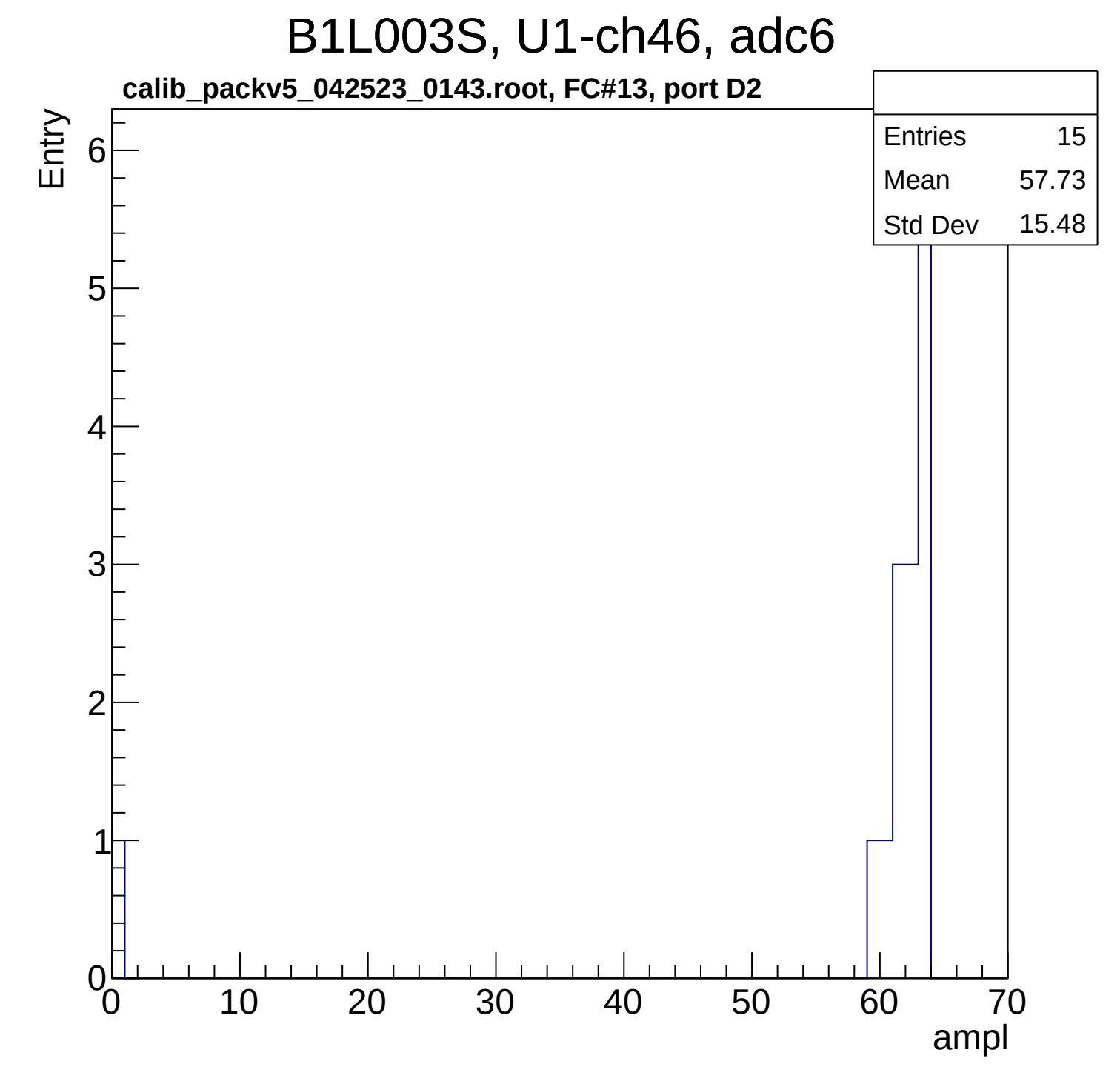
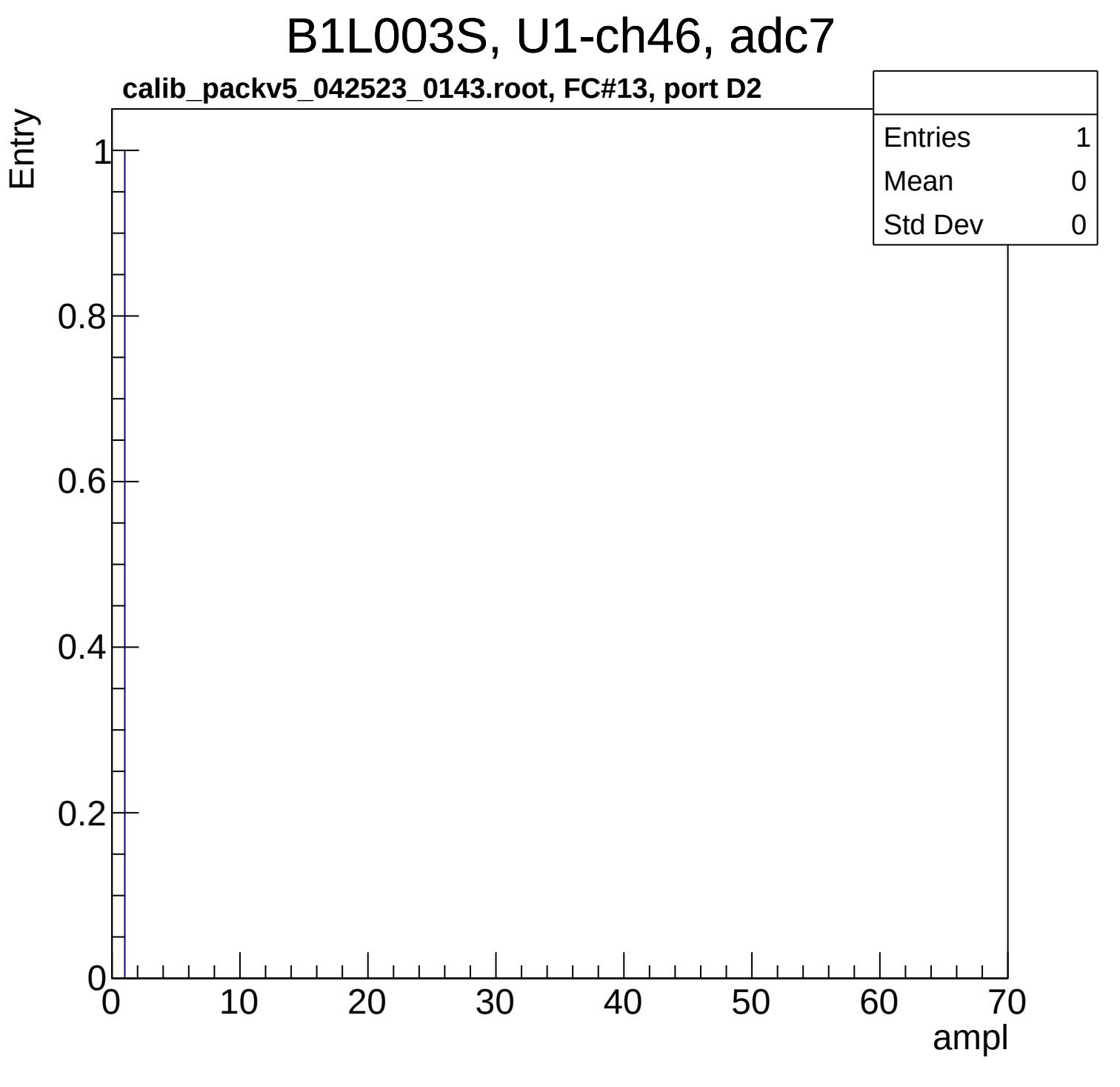
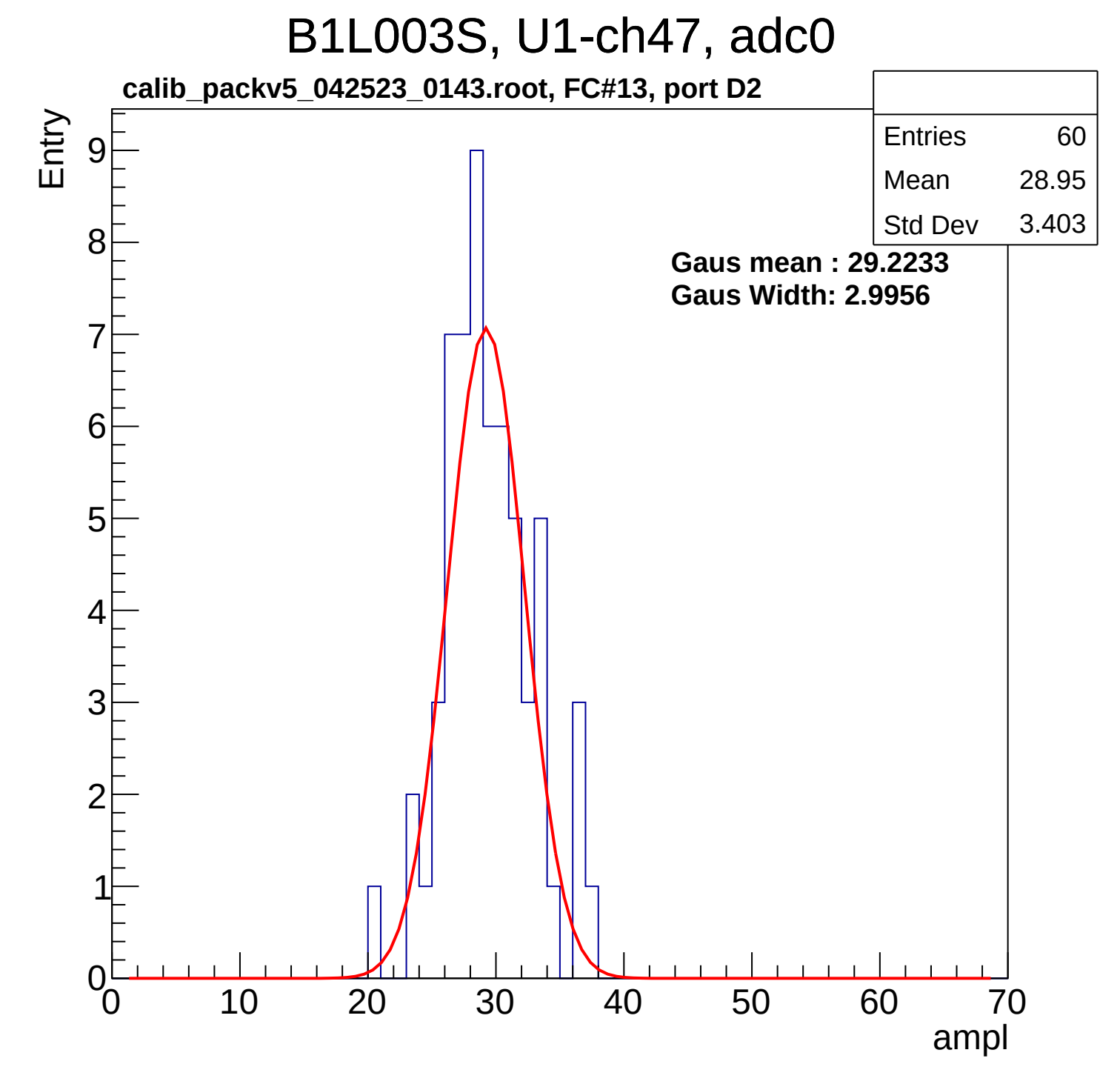
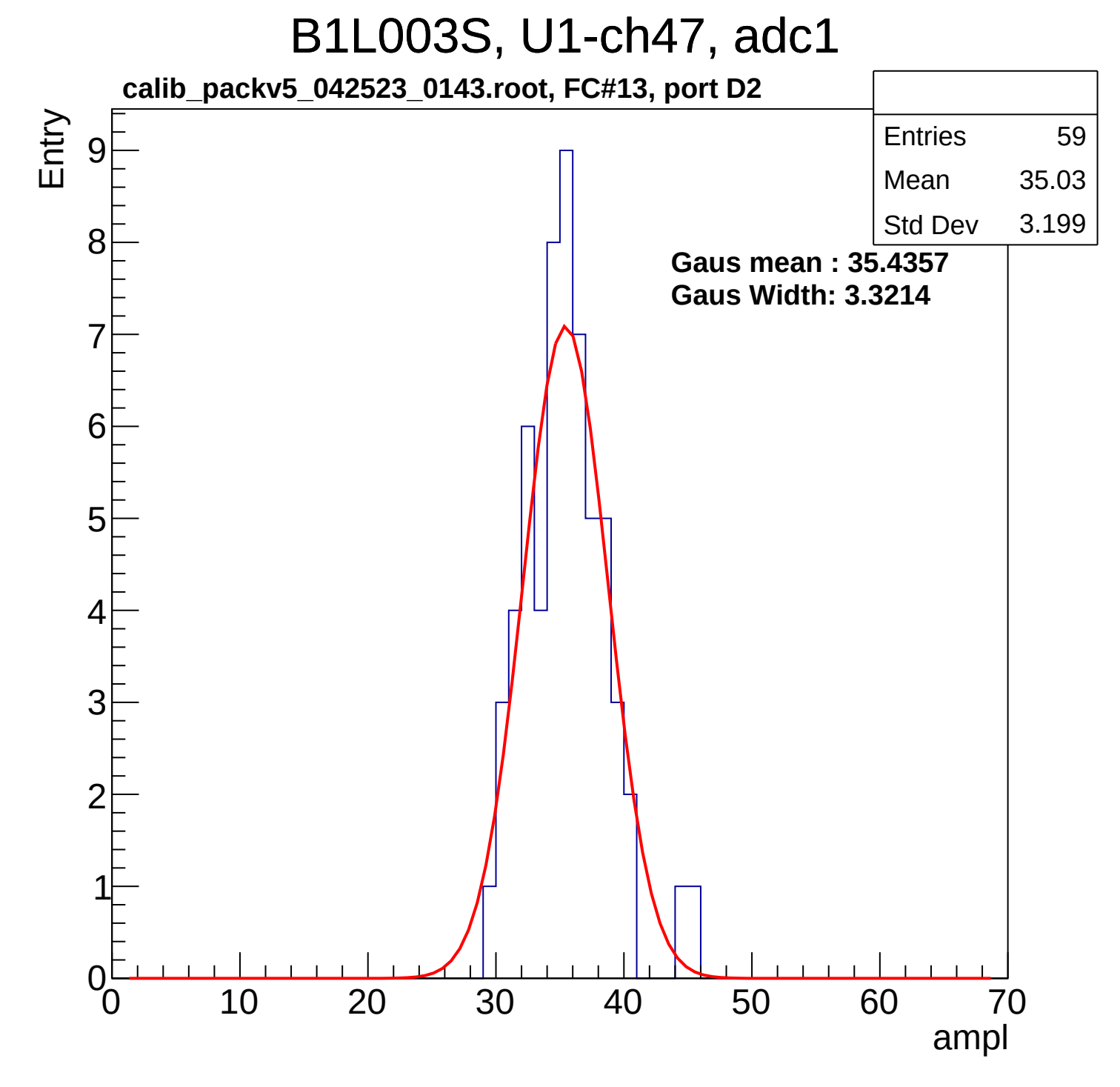
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

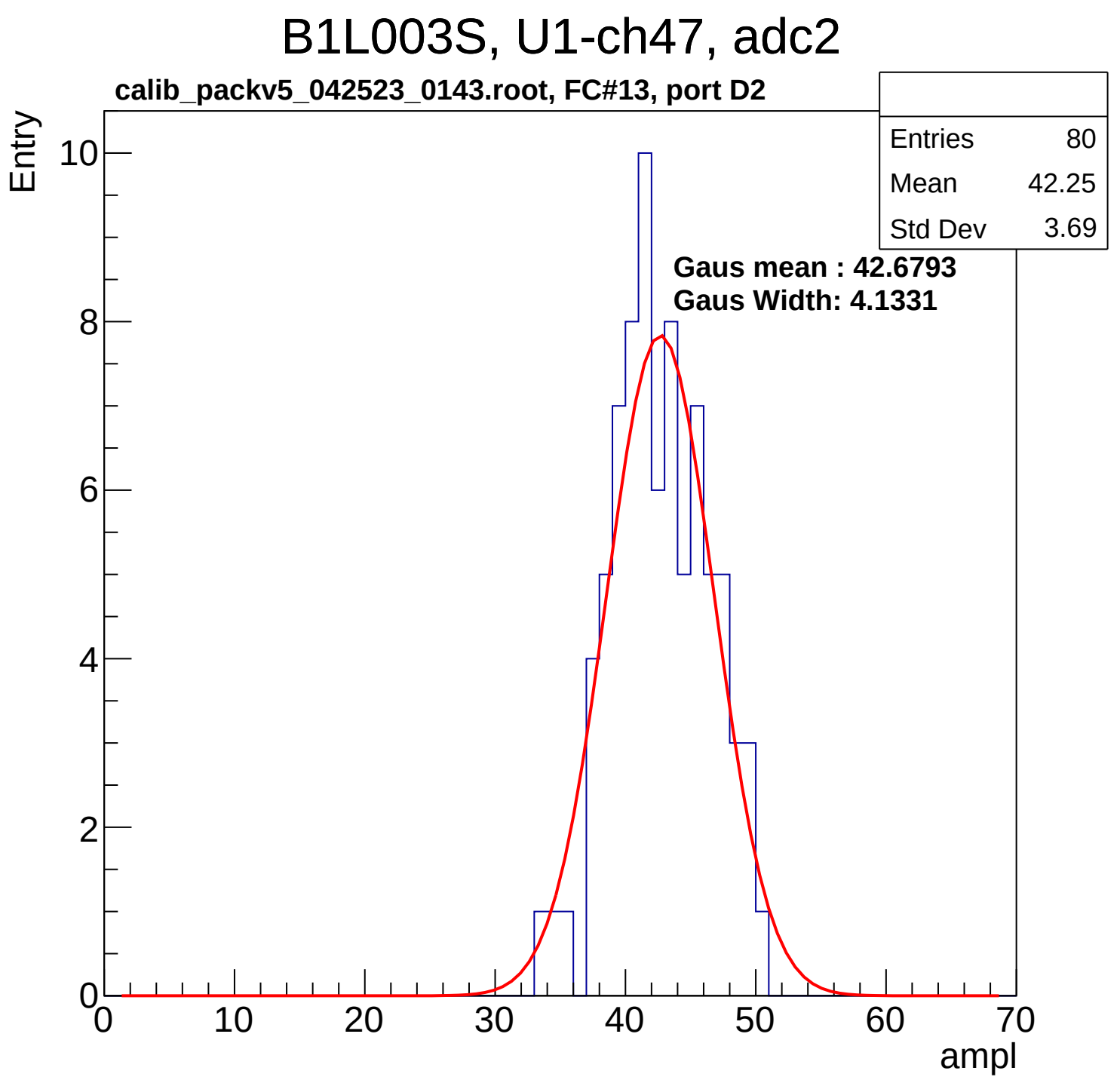

B1L003S, U1-ch46, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.87
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

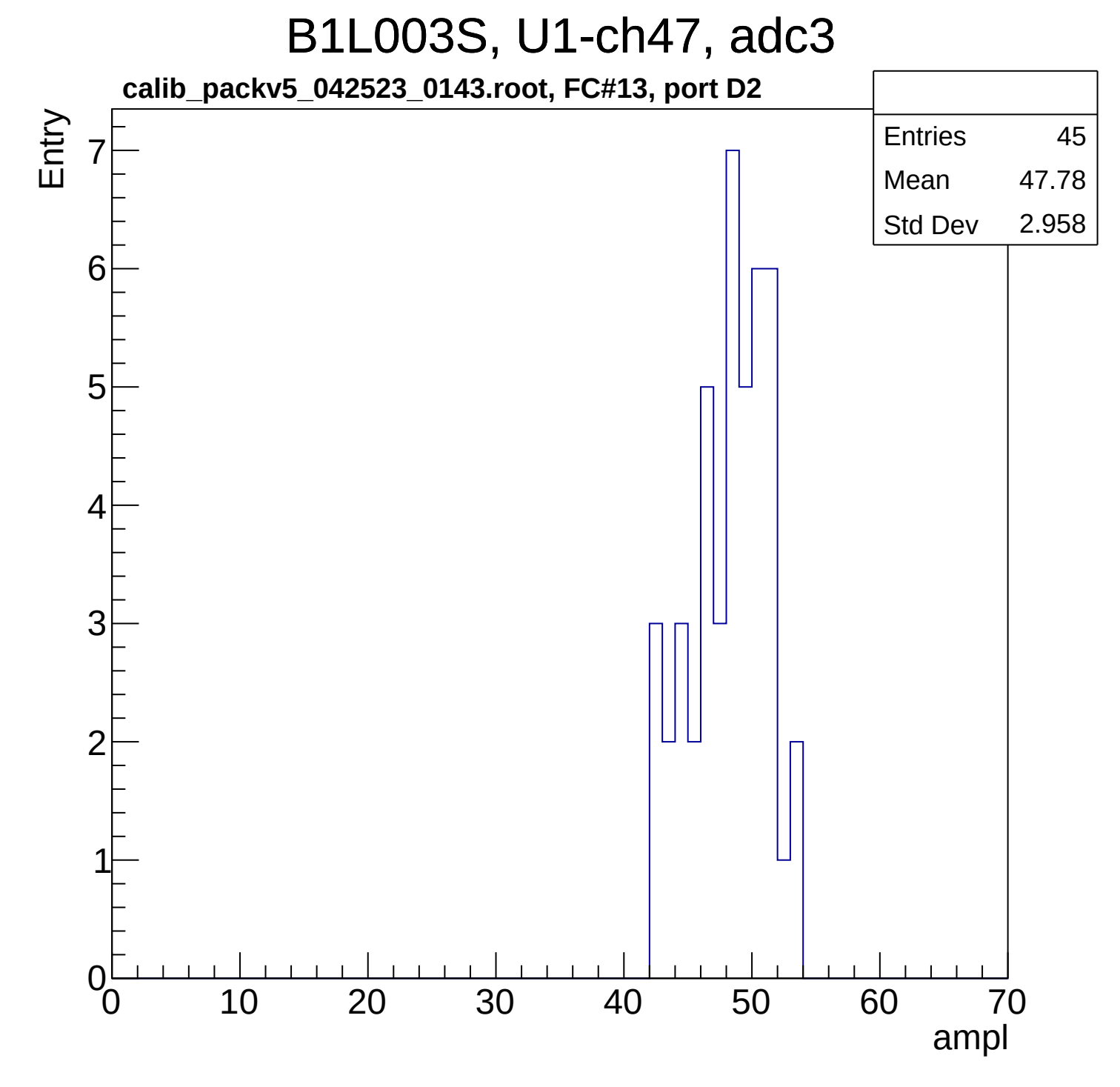

B1L003S, U1-ch46, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 57.73
Std Dev 15.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

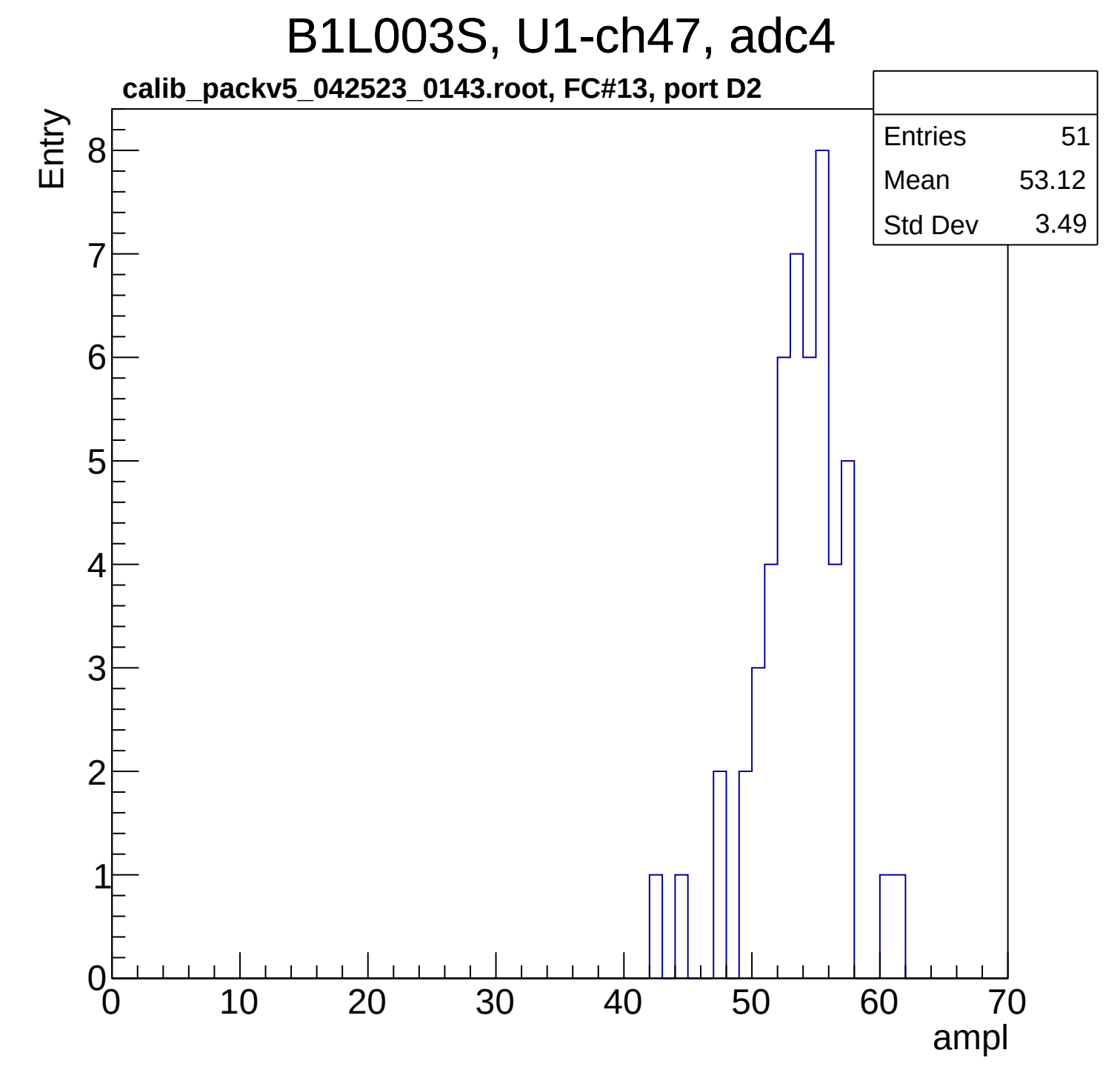

B1L003S, U1-ch46, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

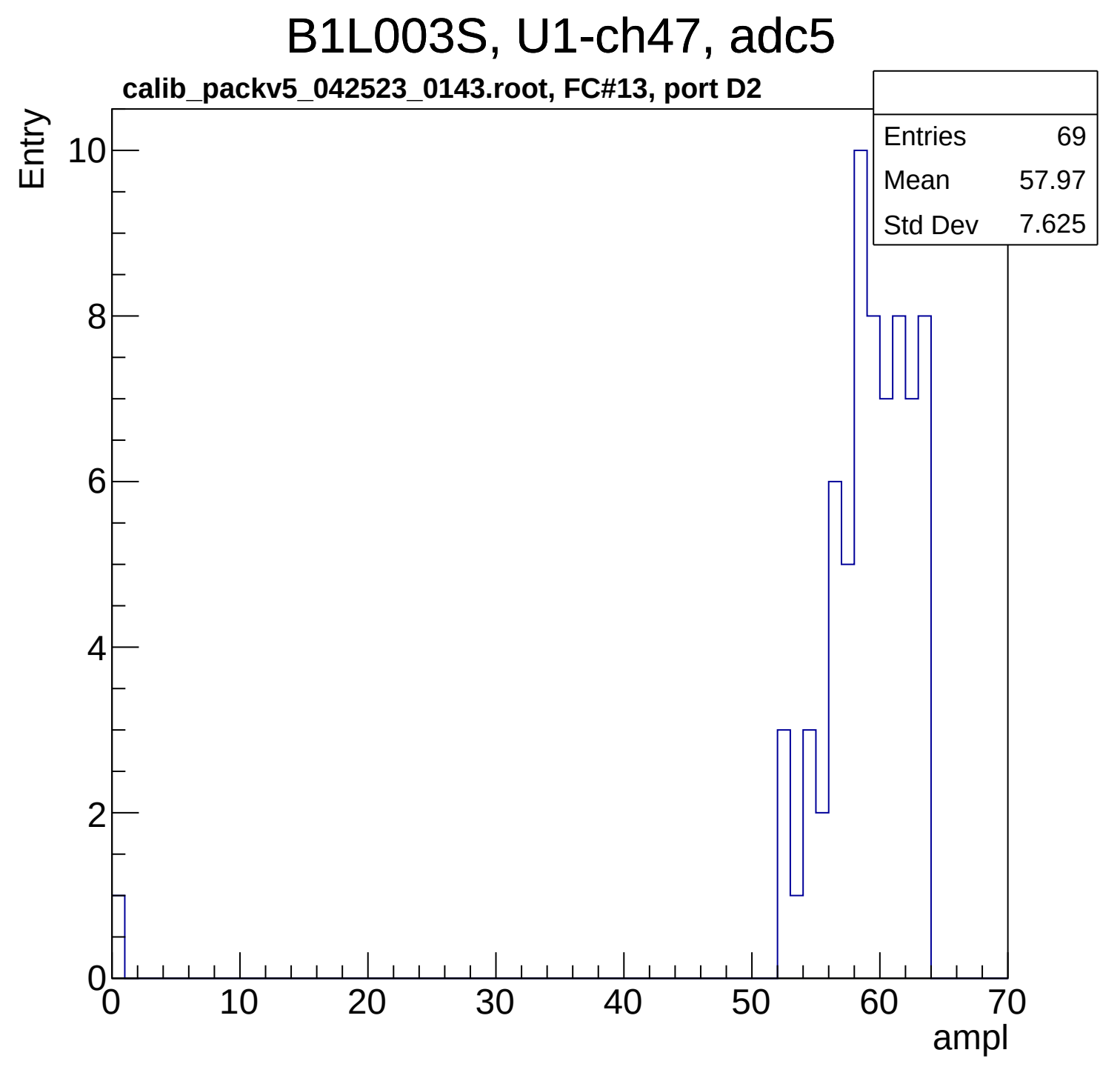

B1L003S, U1-ch47, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.95
Std Dev 3.403
Gaus mean : 29.2233
Gaus Width: 2.9956
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

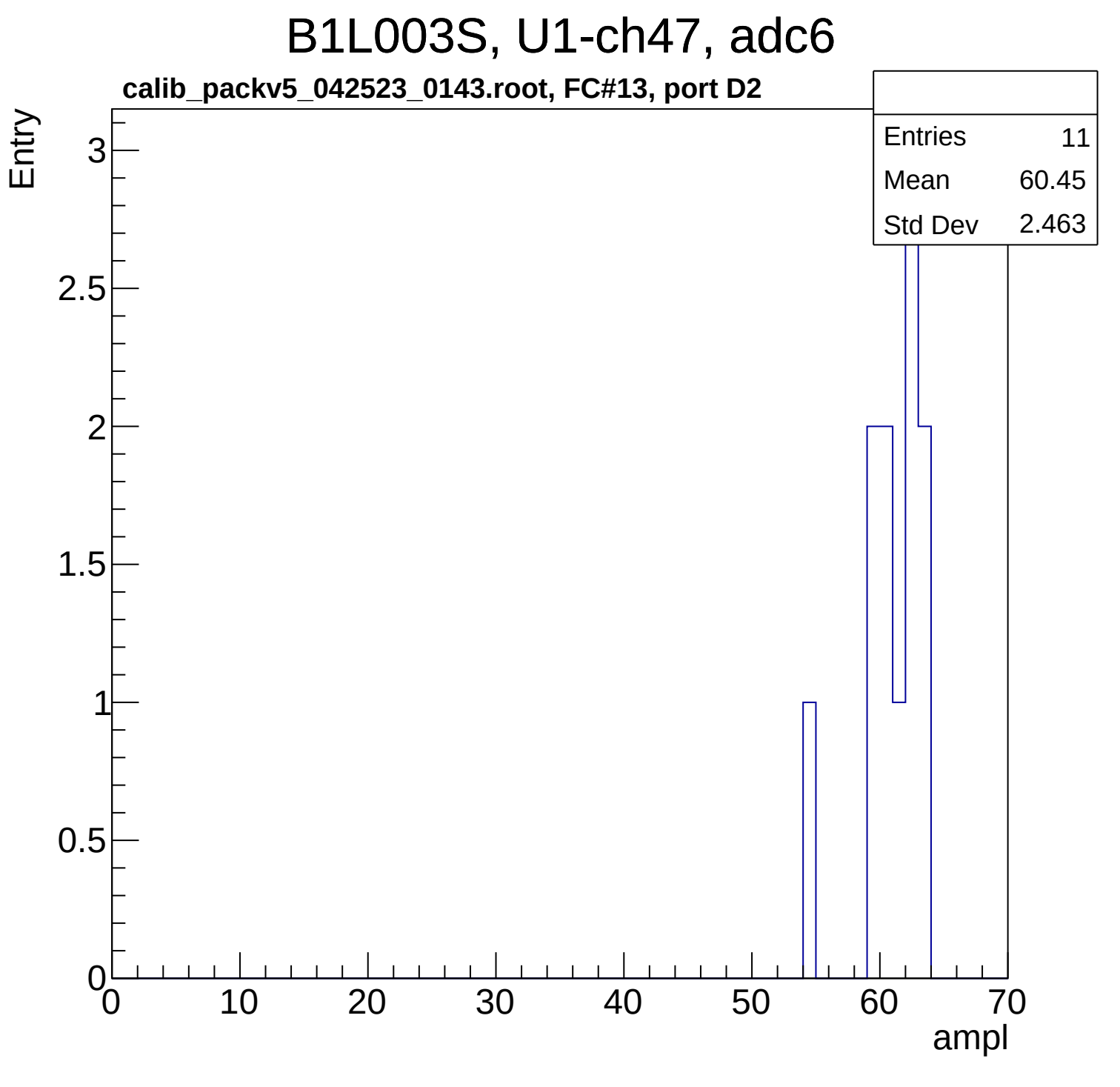

B1L003S, U1-ch47, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 35.03
Std Dev 3.199
Gaus mean : 35.4357
Gaus Width: 3.3214
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch47, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 42.25
Std Dev 3.69
Gaus mean : 42.6793
Gaus Width: 4.1331
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch47, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 47.78
Std Dev 2.958
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch47, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.12
Std Dev 3.49
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch47, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 57.97
Std Dev 7.625
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch47, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 60.45
Std Dev 2.463
Entry
0
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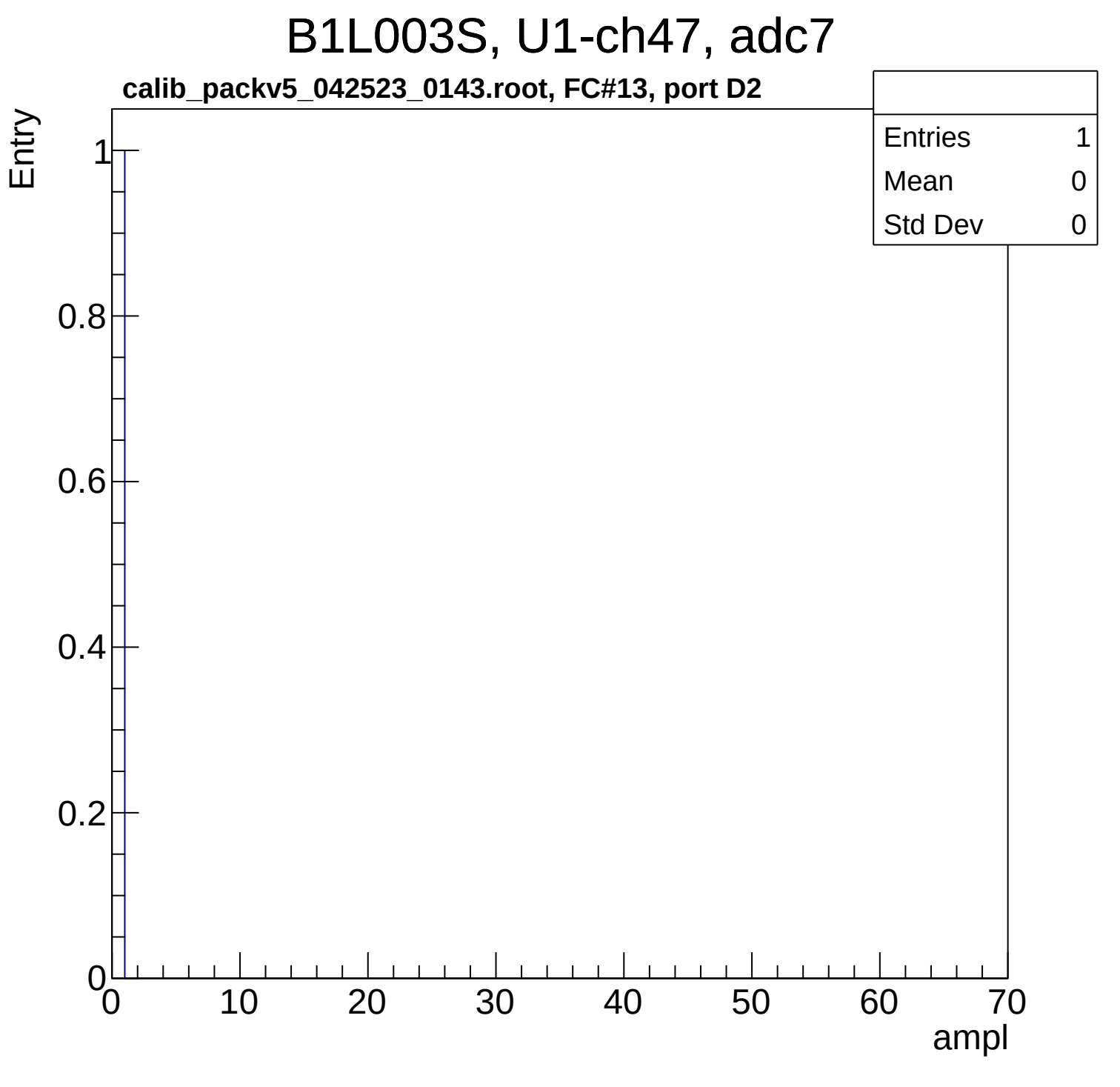
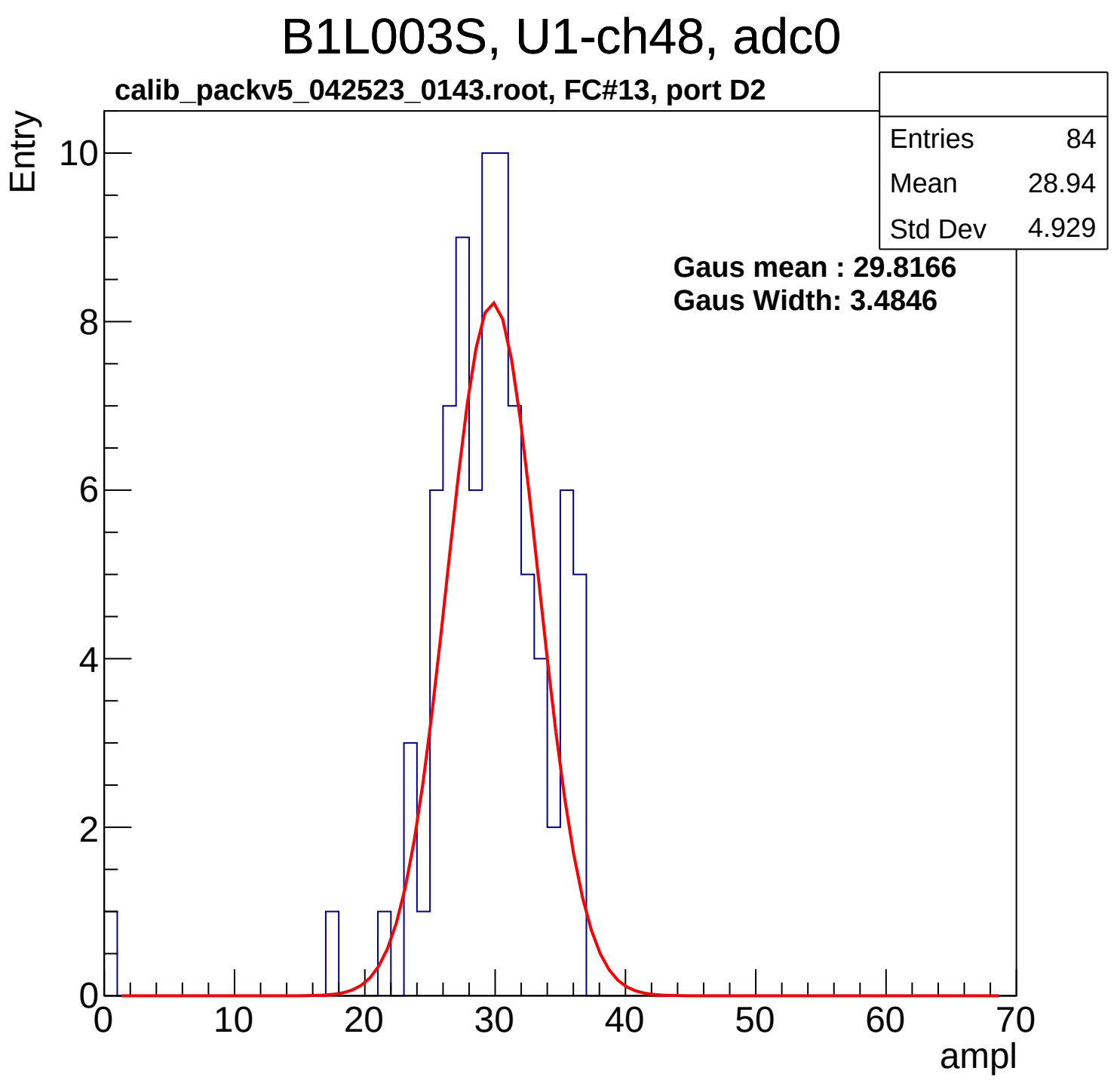
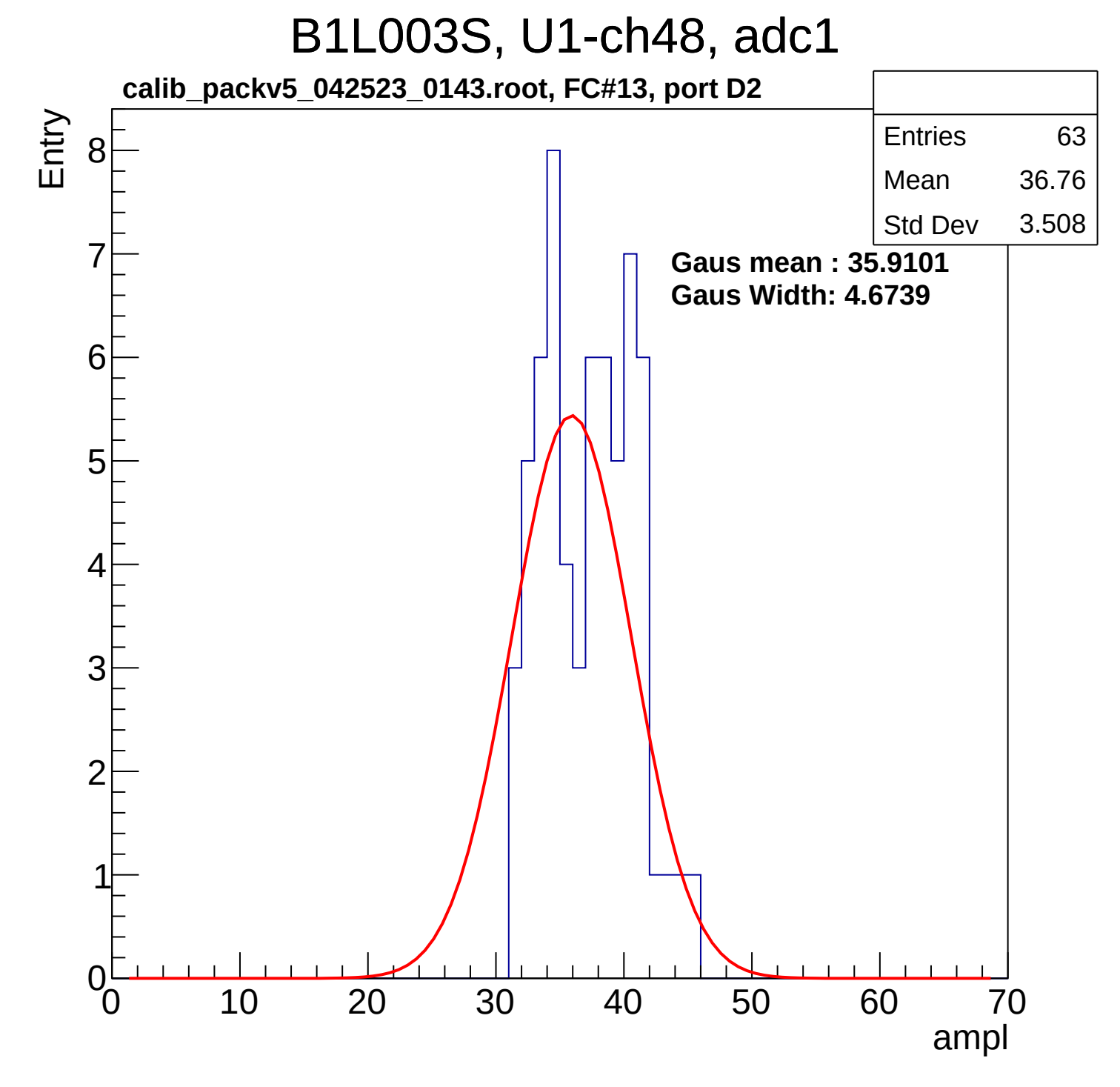
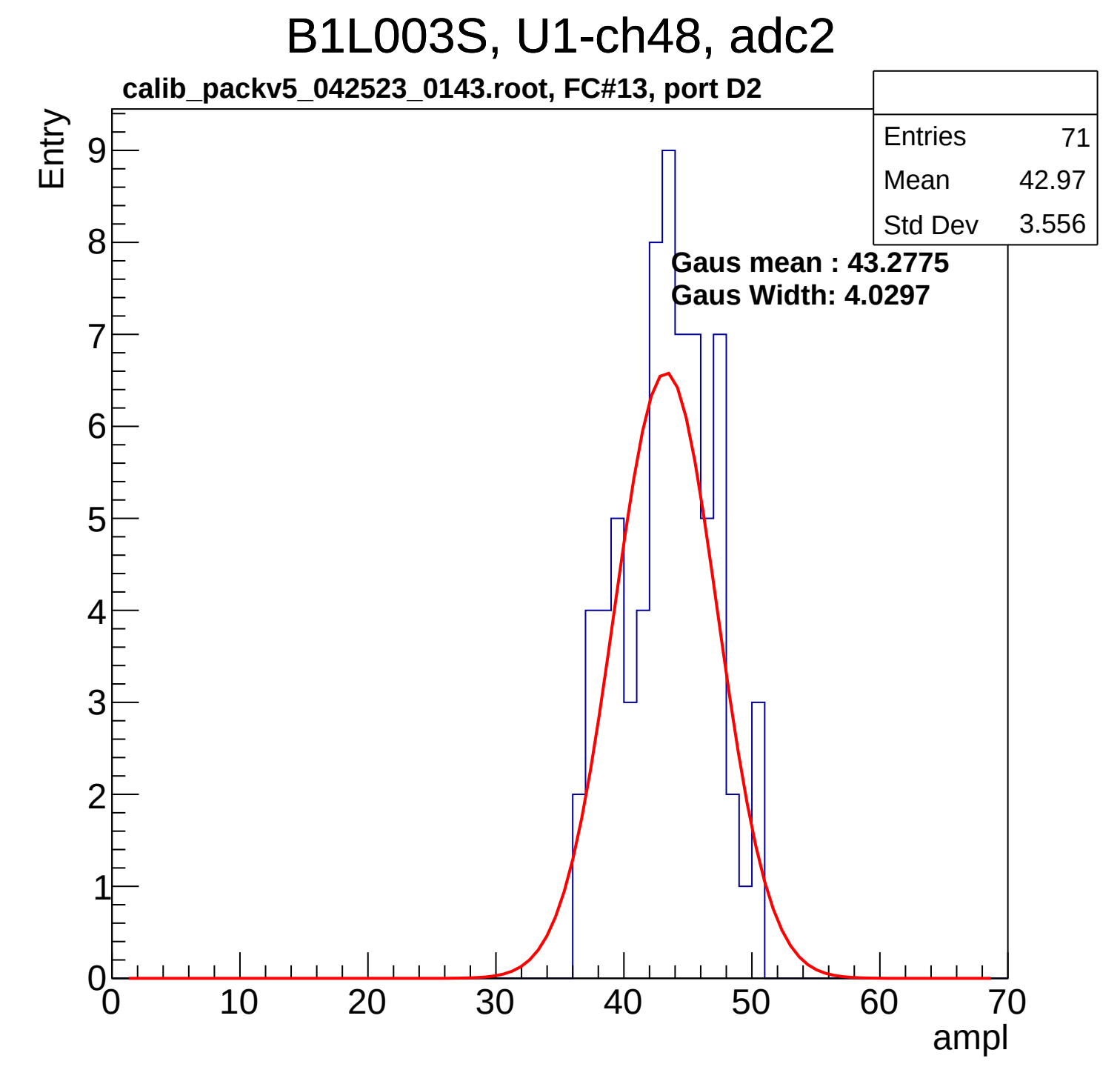
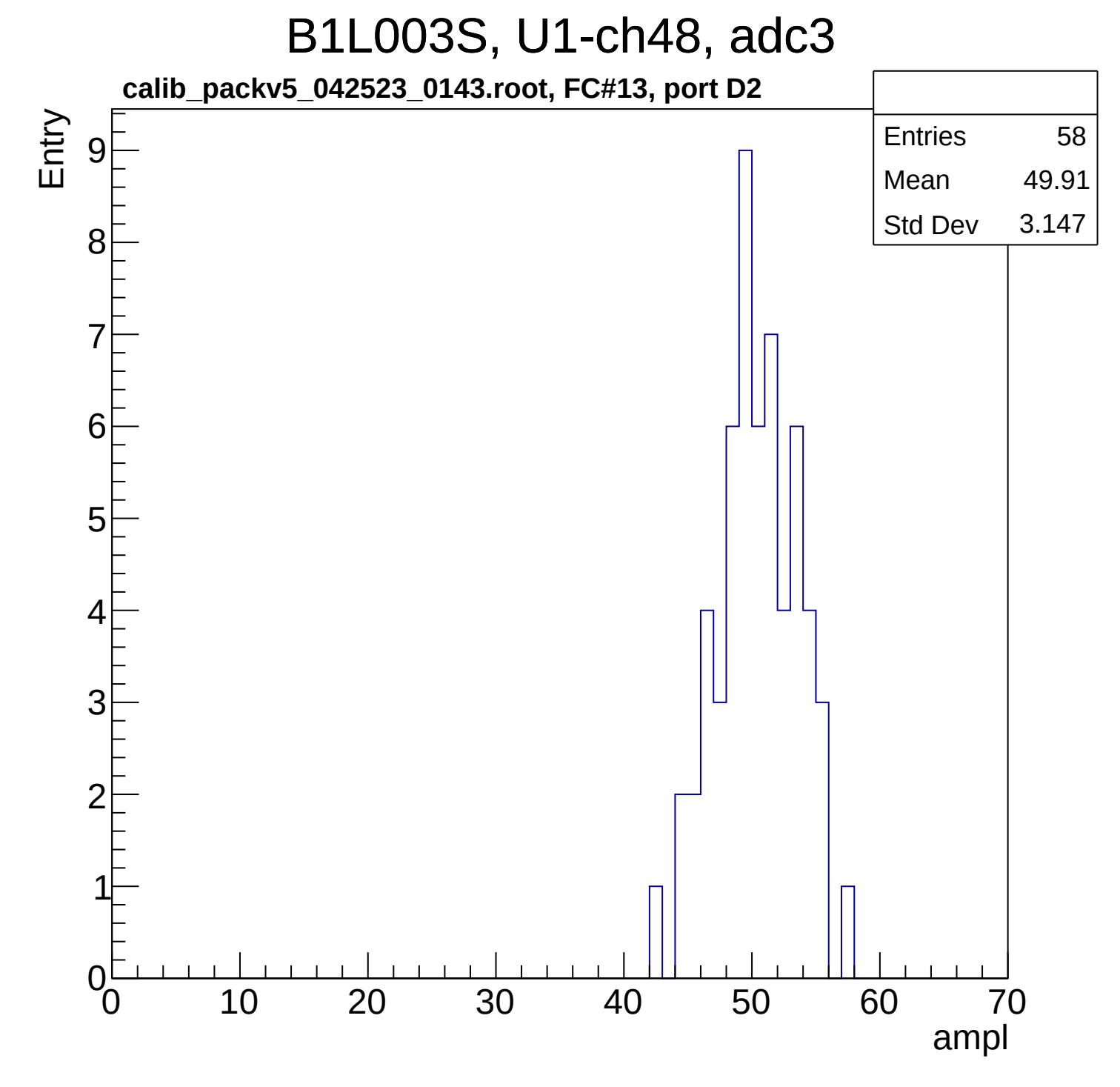
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch47, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch48, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 28.94
Std Dev 4.929
Gaus mean : 29.8166
Gaus Width: 3.4846
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch48, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.76
Std Dev 3.508
Gaus mean : 35.9101
Gaus Width: 4.6739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch48, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.97
Std Dev 3.556
Gaus mean : 43.2775
Gaus Width: 4.0297
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch48, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.91
Std Dev 3.147
Entry
ampl
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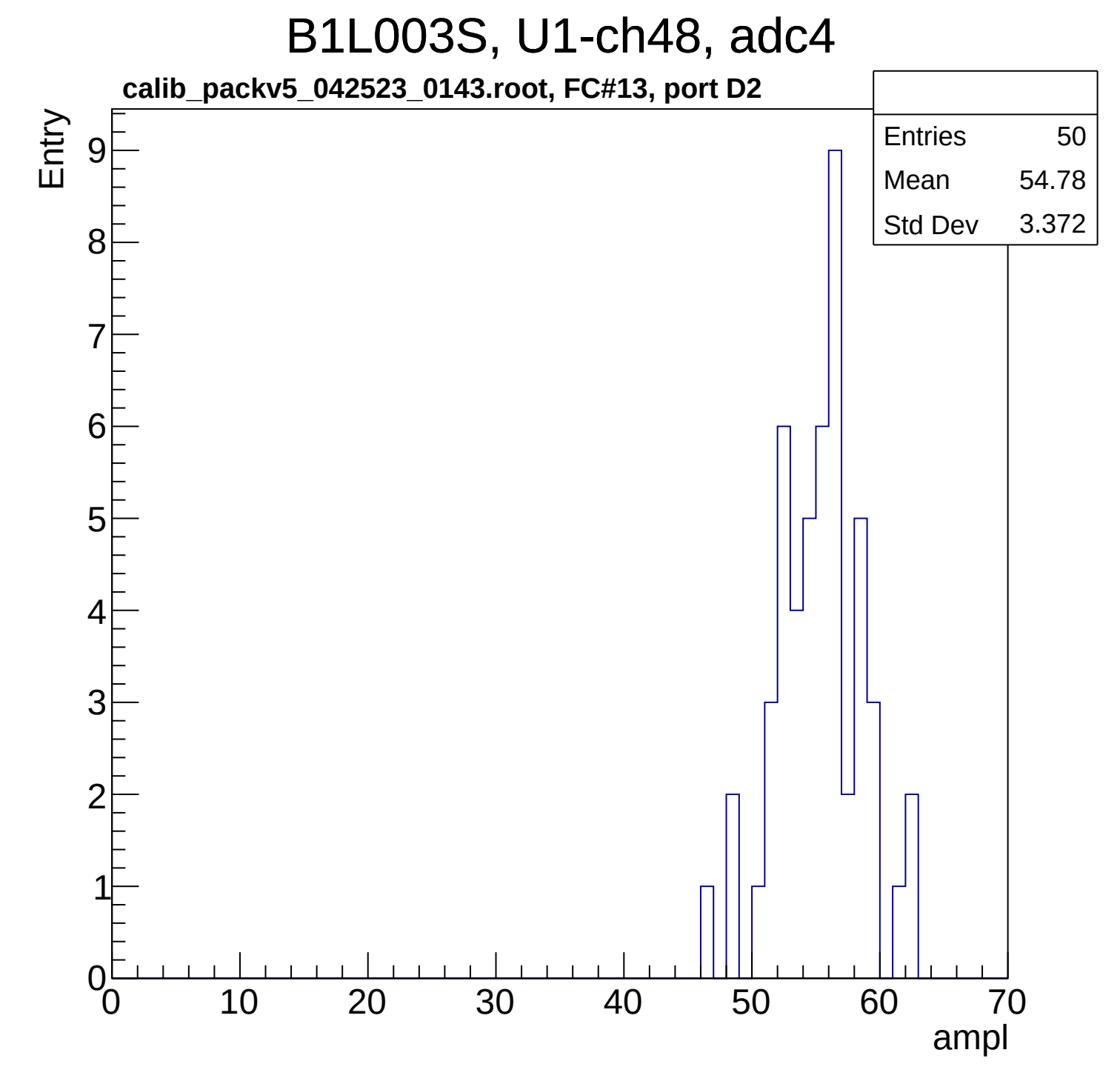
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch48, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 54.78
Std Dev 3.372
Entry
ampl
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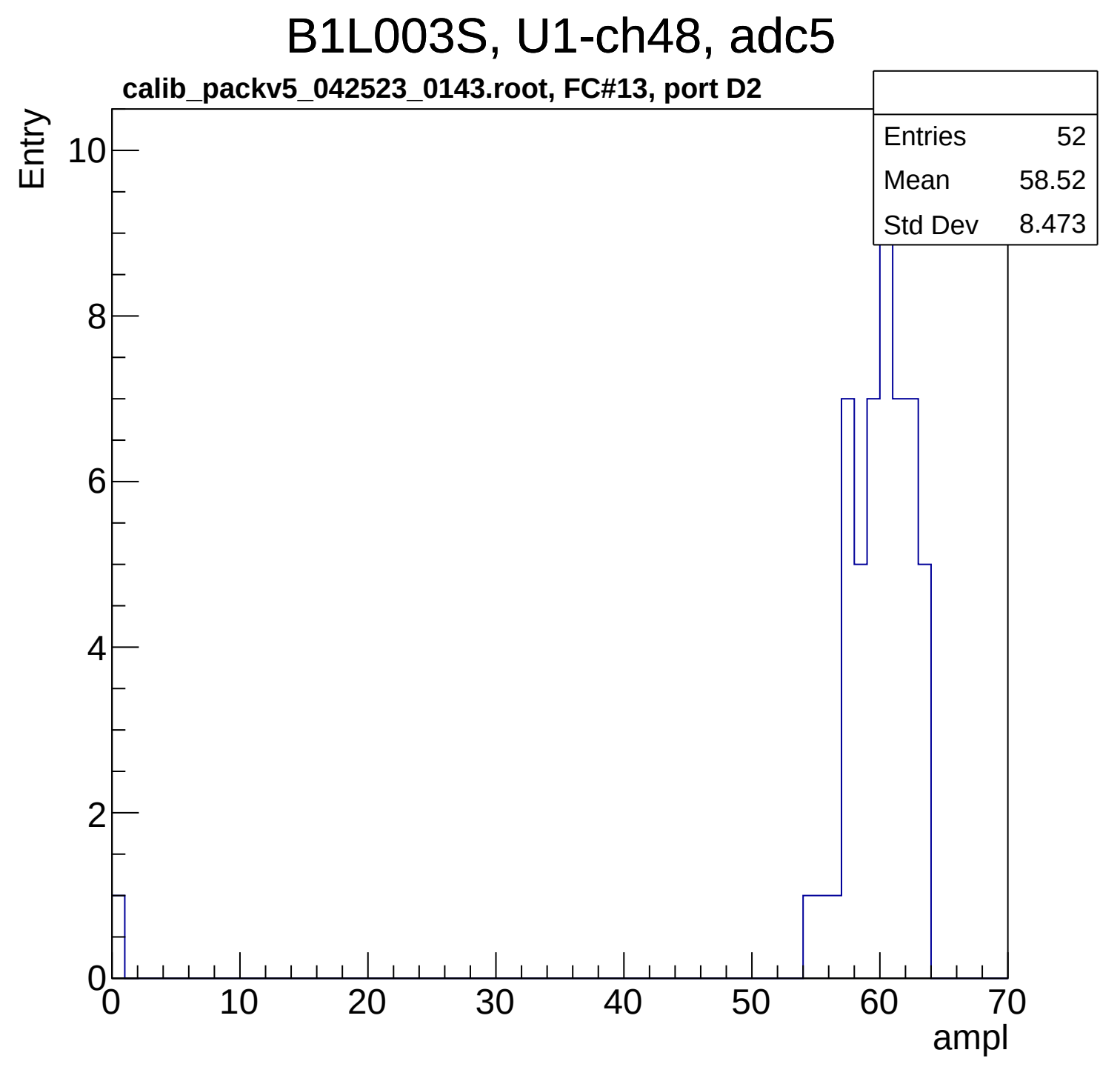
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch48, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.52
Std Dev 8.473
Entry
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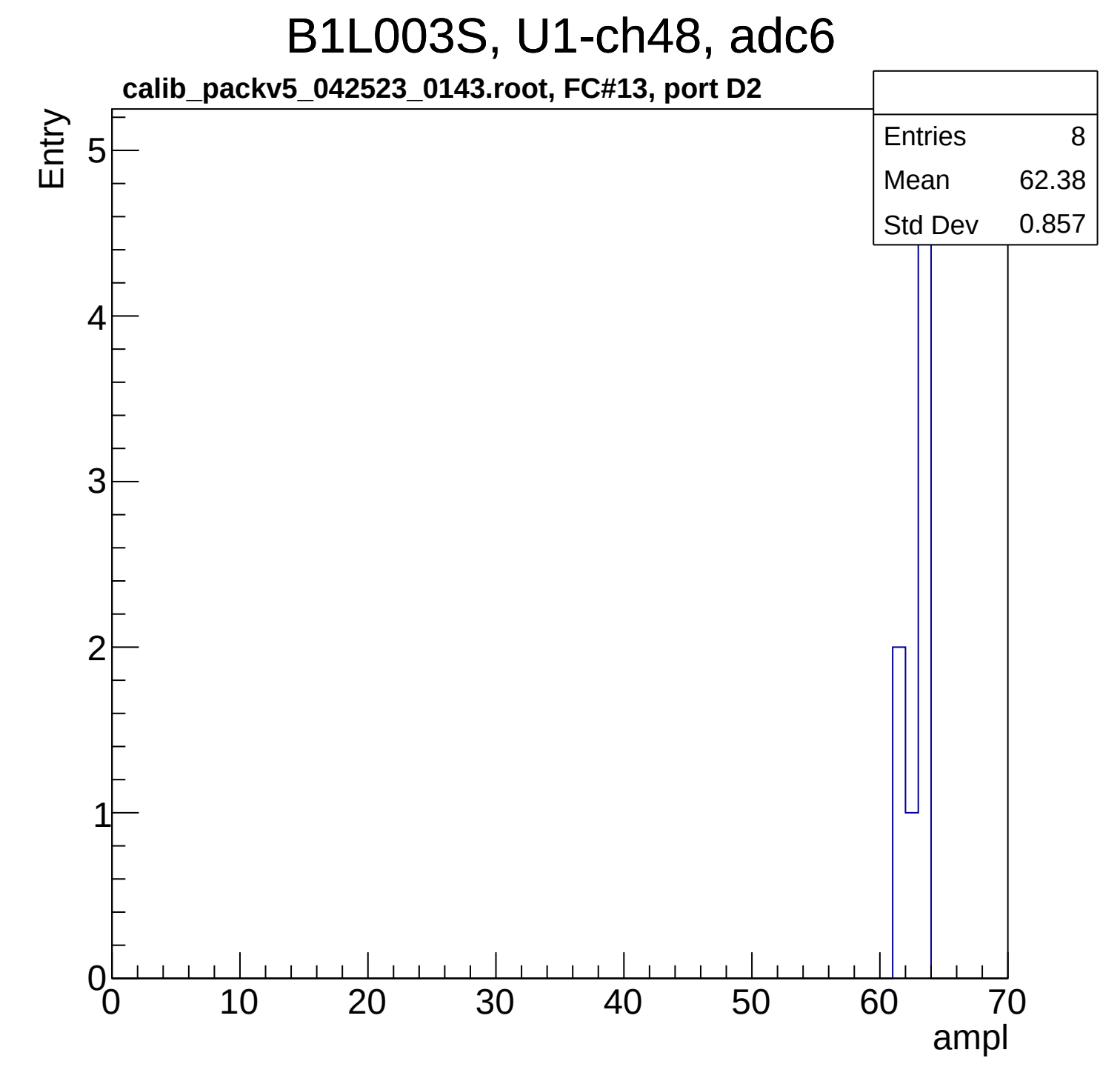
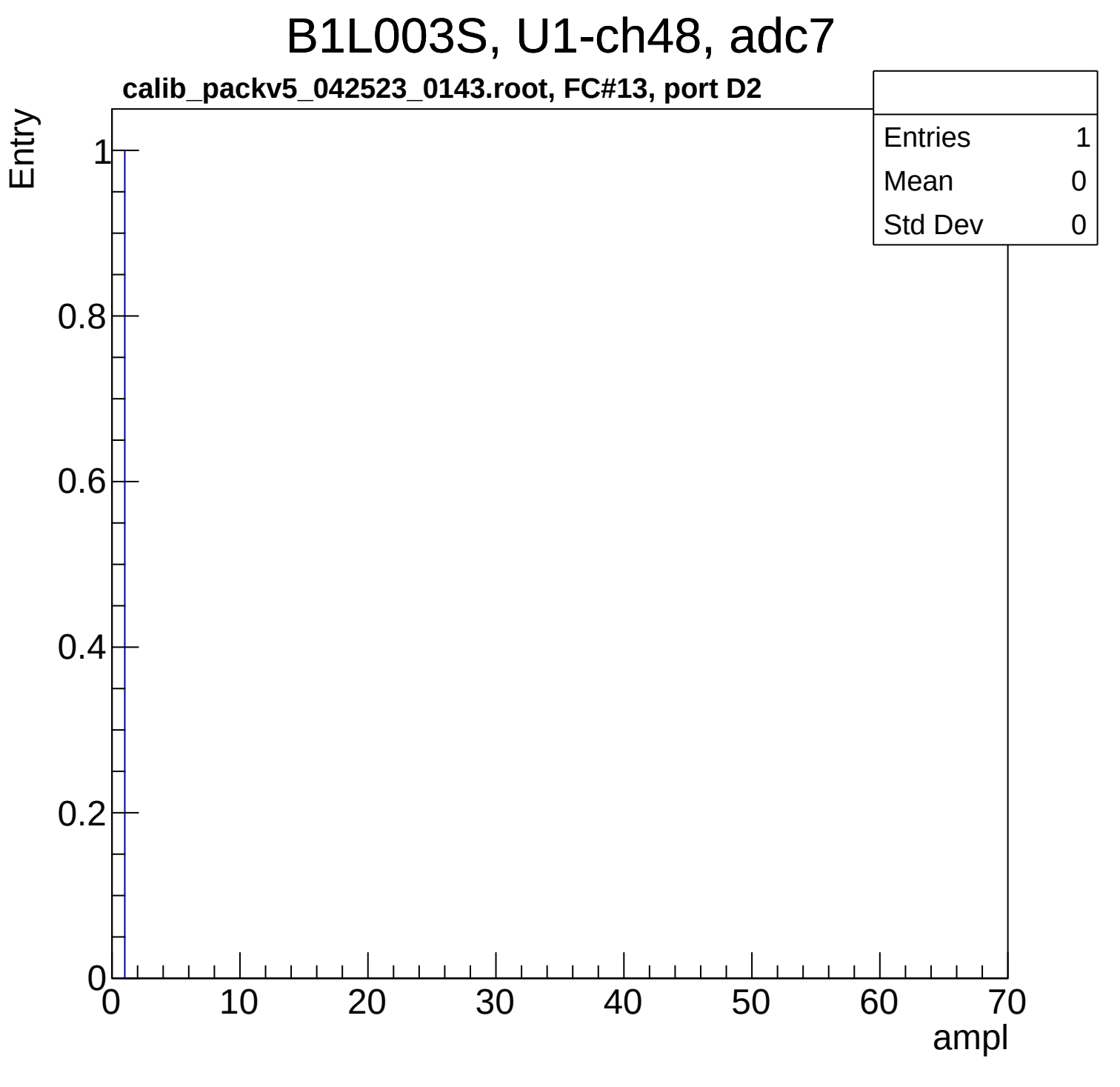
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch48, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 62.38
Std Dev 0.857
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch48, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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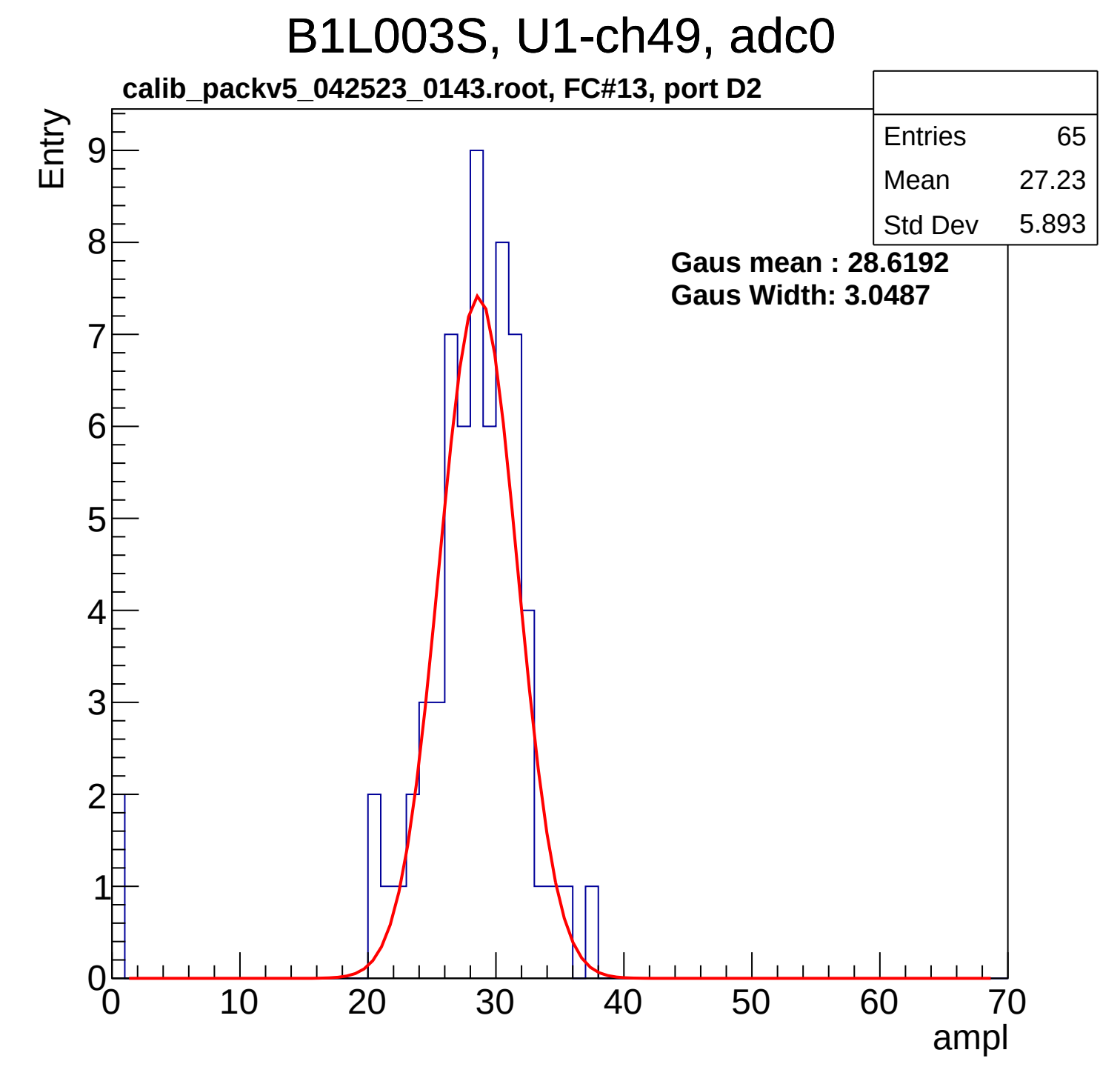
50
60
70
ampl


B1L003S, U1-ch49, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 27.23
Std Dev 5.893
Gaus mean : 28.6192
Gaus Width: 3.0487
Entry
ampl
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2
3
4
5
6
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8
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0
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60
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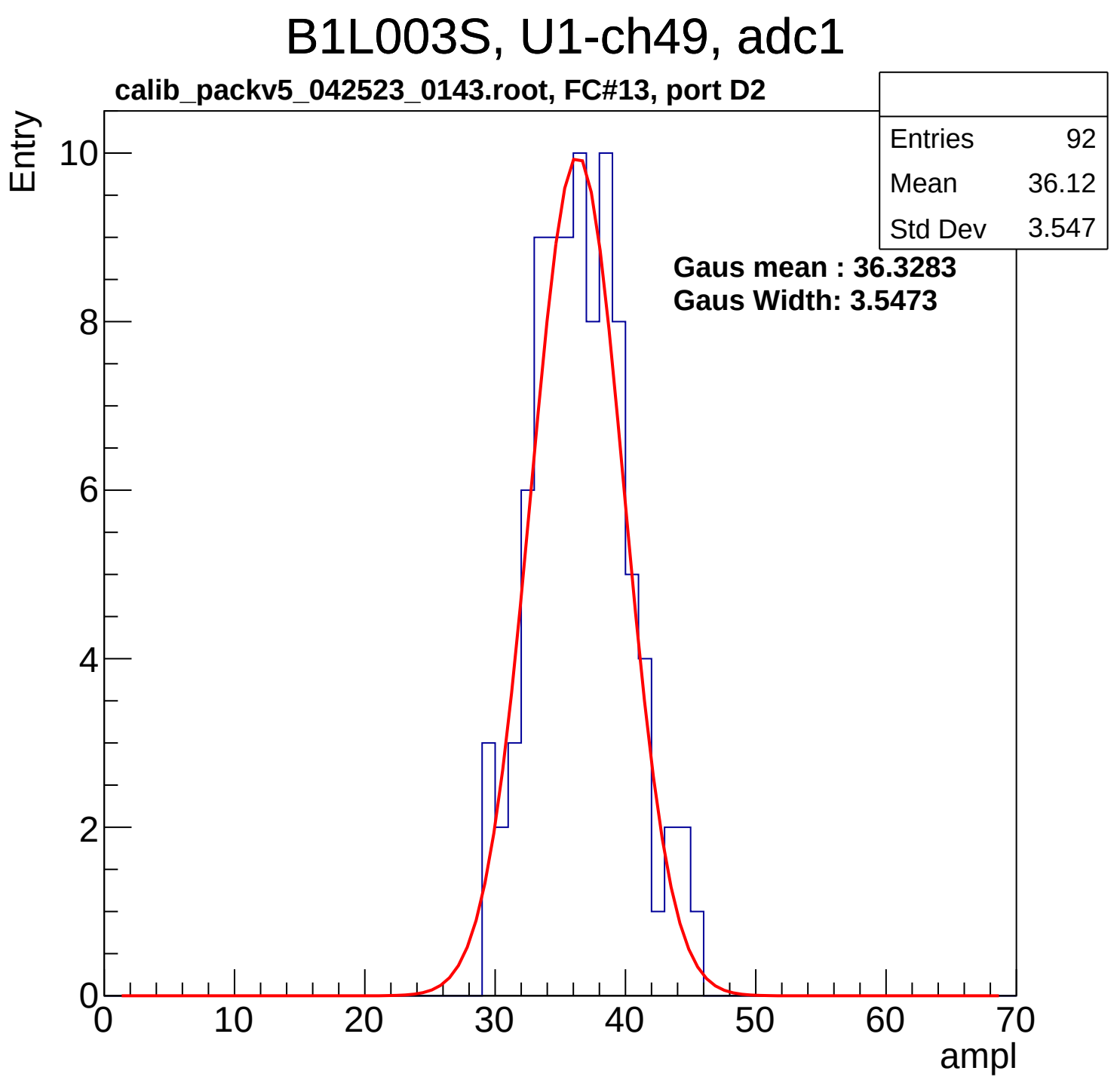
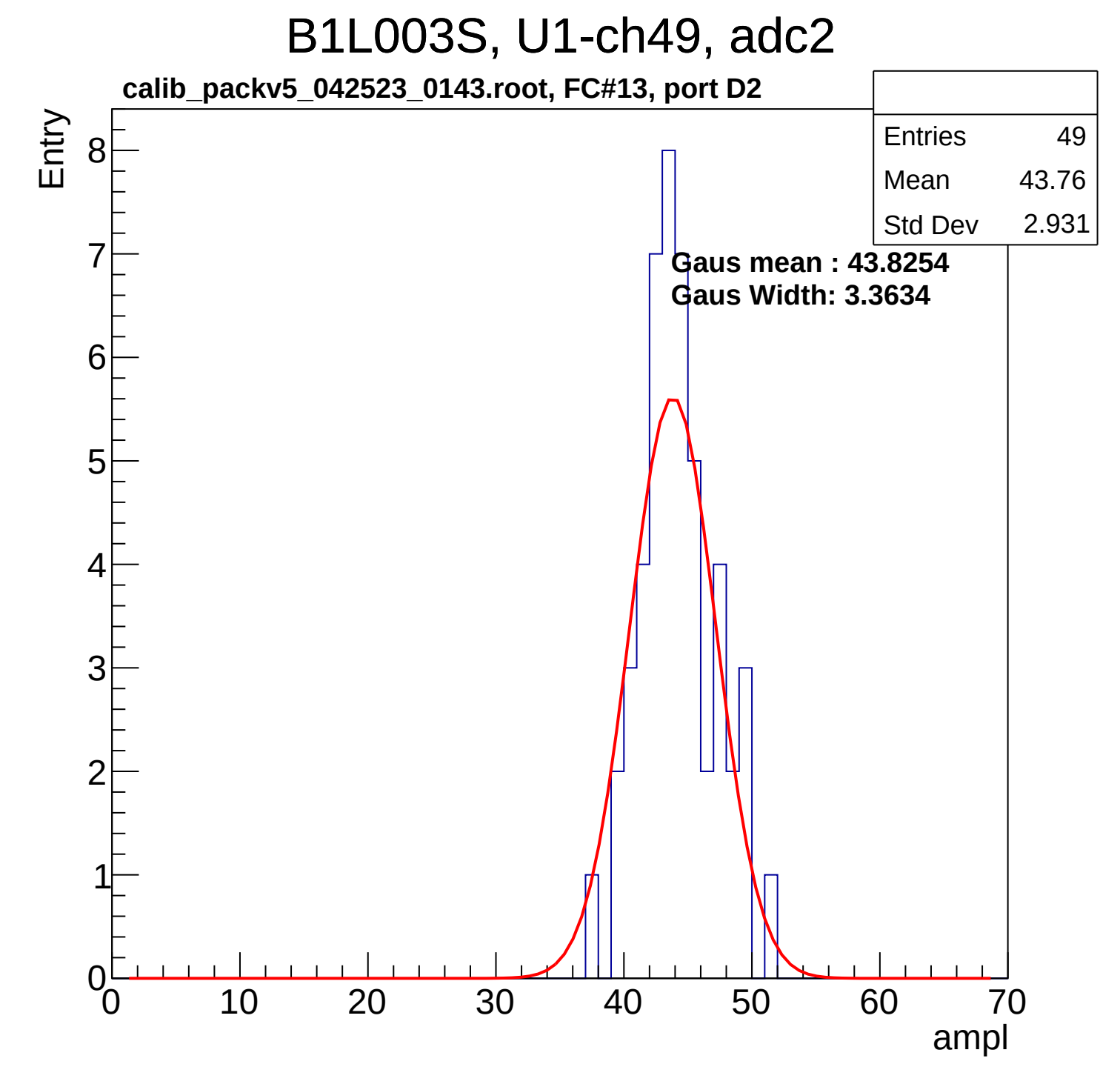

B1L003S, U1-ch49, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 92
Mean 36.12
Std Dev 3.547
Gaus mean : 36.3283
Gaus Width: 3.5473
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch49, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 43.76
Std Dev 2.931
Gaus mean : 43.8254
Gaus Width: 3.3634
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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60
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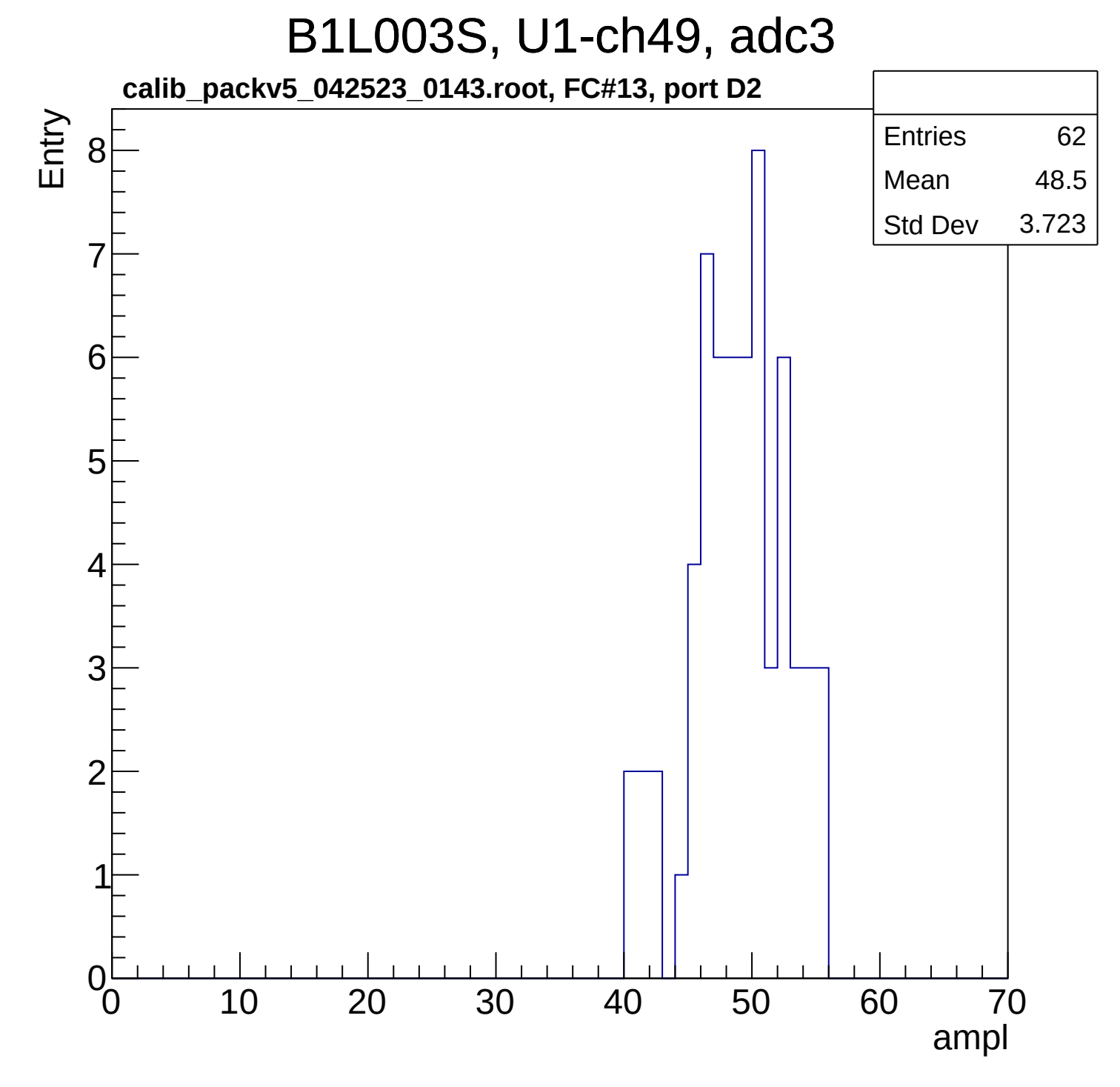
B1L003S, U1-ch49, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 48.5
Std Dev 3.723
Entry
ampl
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2
3
4
5
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7
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0
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60
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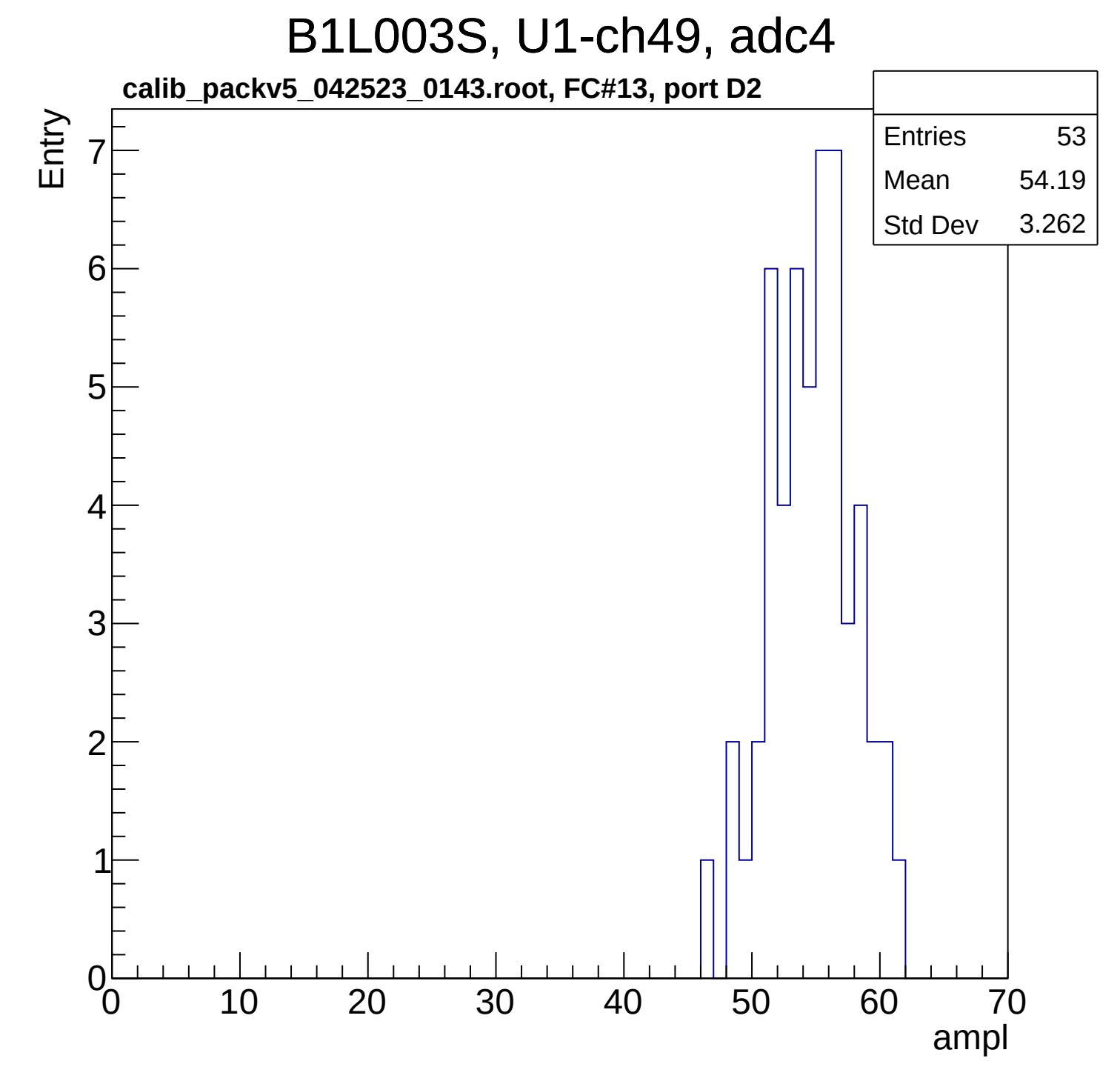
B1L003S, U1-ch49, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.19
Std Dev 3.262
Entry
ampl
0
1
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3
4
5
6
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0
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40
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60
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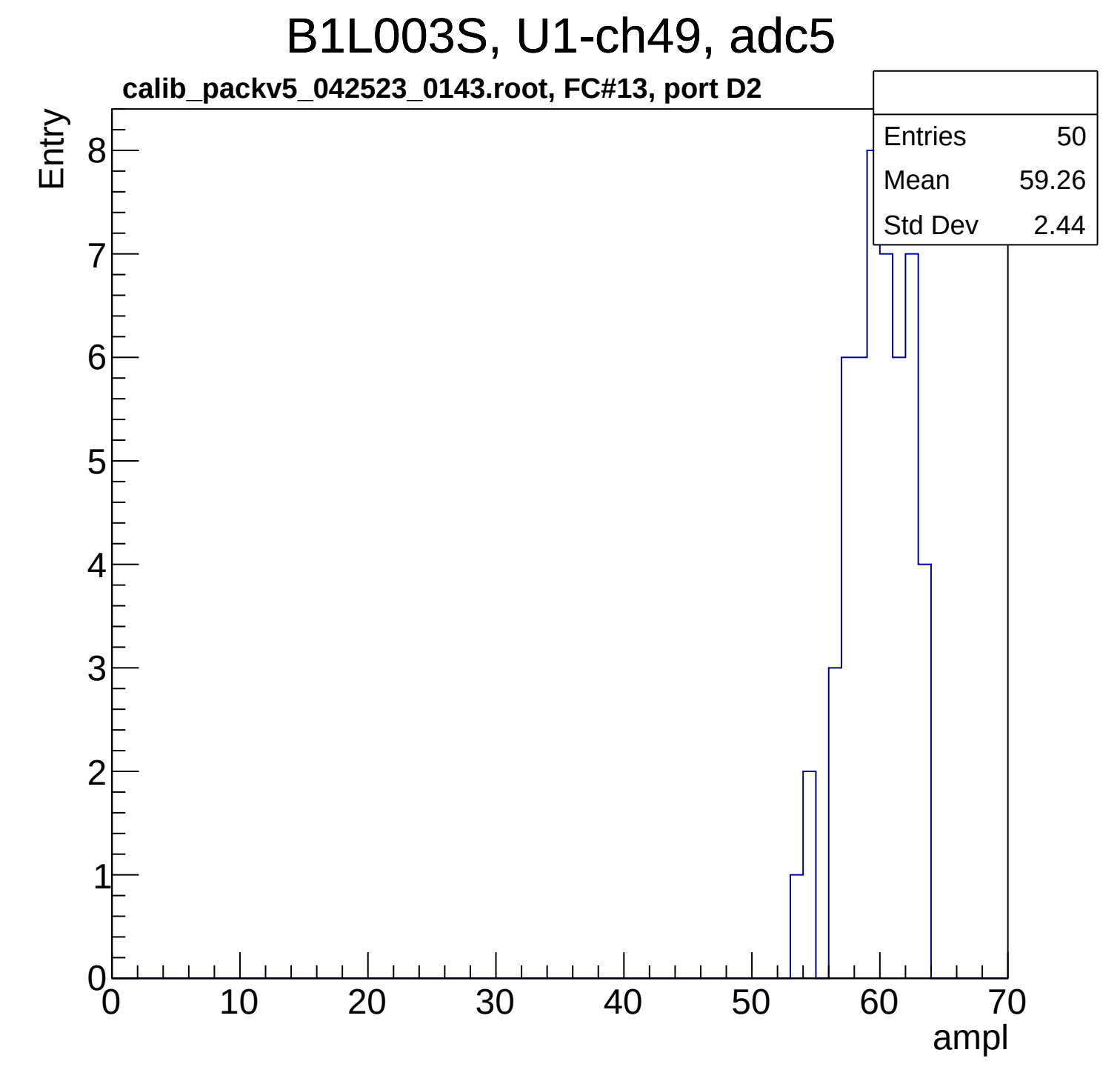
B1L003S, U1-ch49, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.26
Std Dev 2.44
Entry
ampl
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2
3
4
5
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7
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0
10
20
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40
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60
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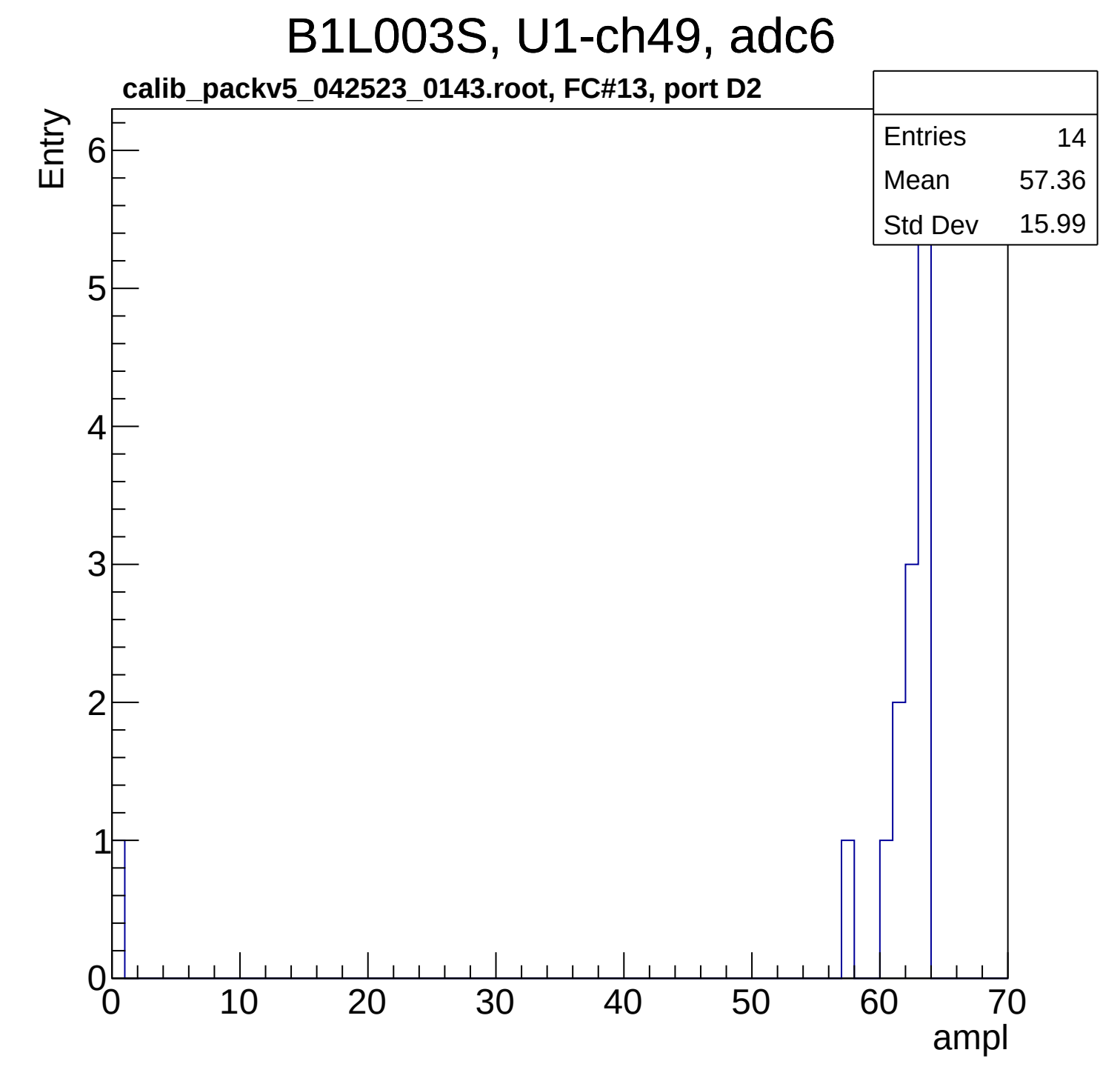
B1L003S, U1-ch49, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.36
Std Dev 15.99
Entry
0
1
2
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4
5
6
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10
20
30
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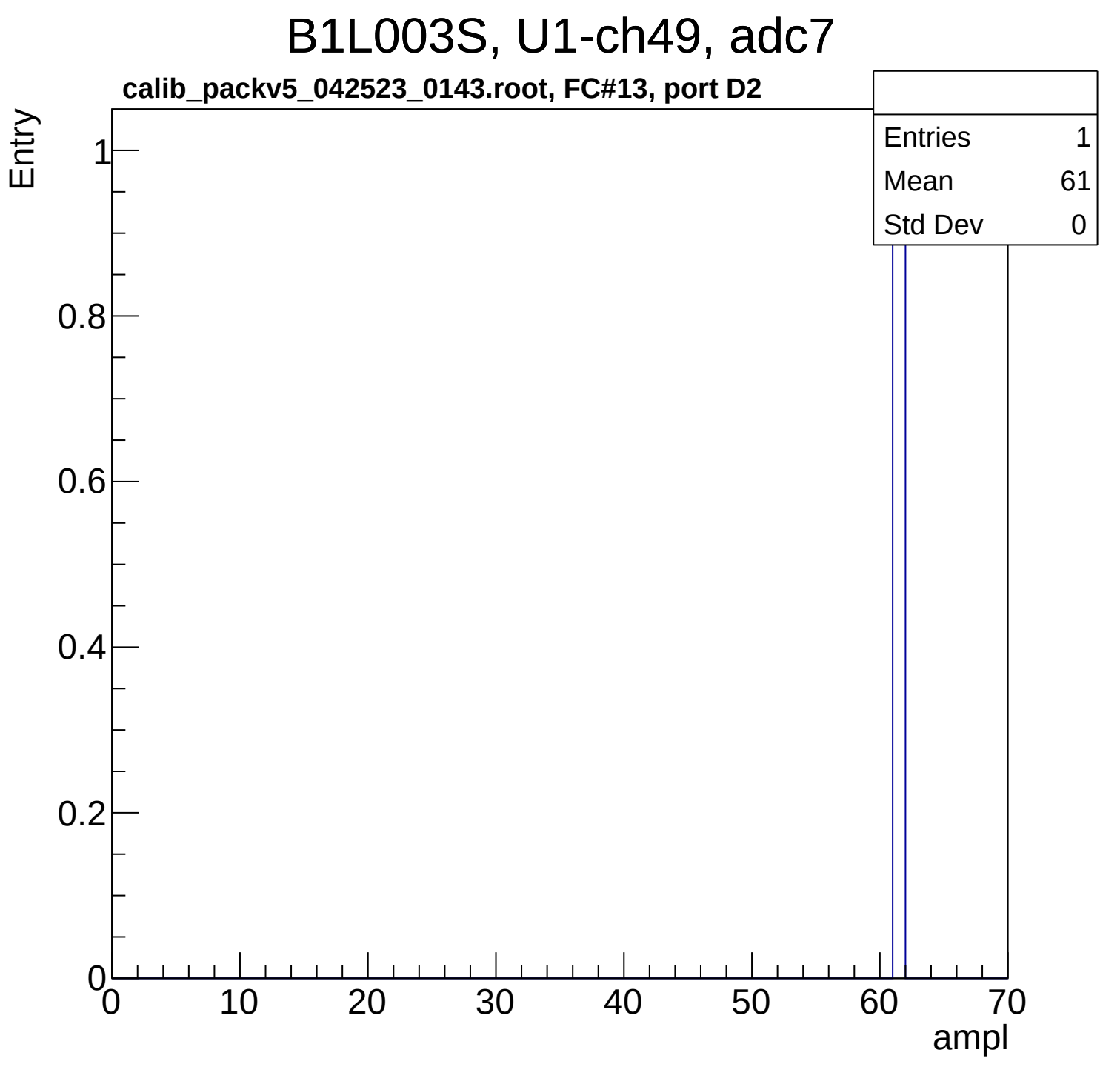
50
60
70
ampl


B1L003S, U1-ch49, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 61
Std Dev 0
Entry
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0.6
0.4
0.2
0
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10
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30
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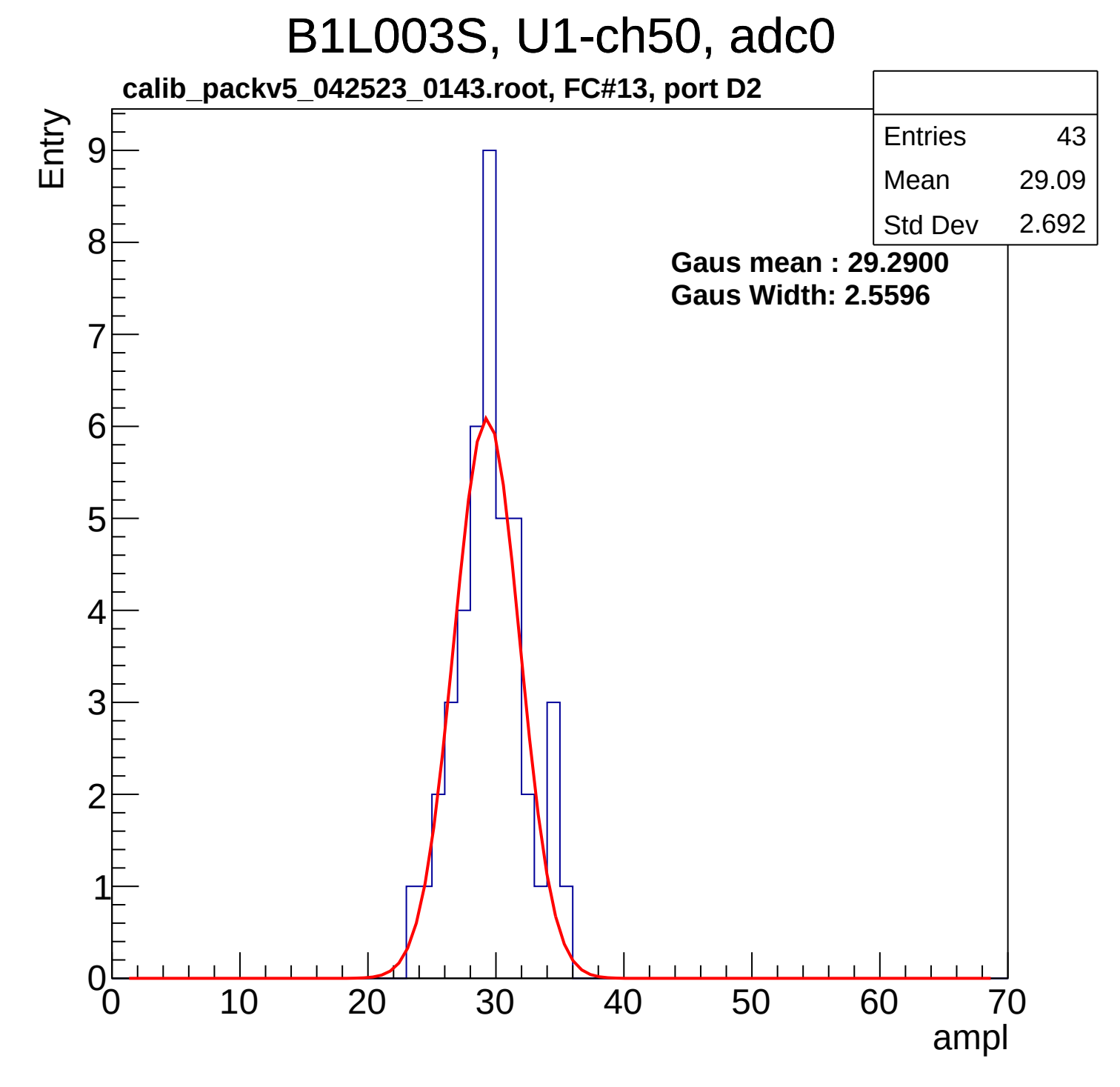
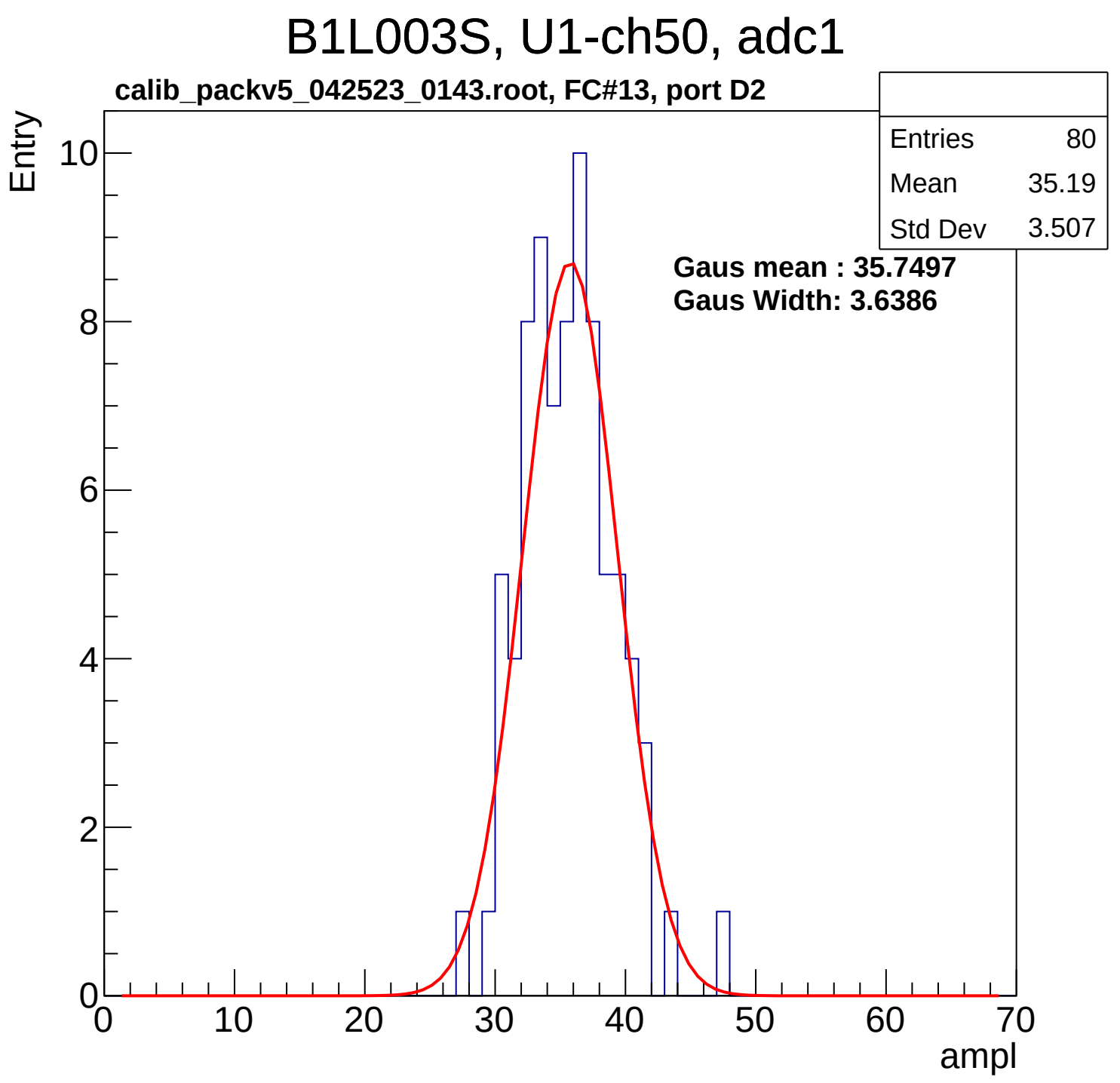
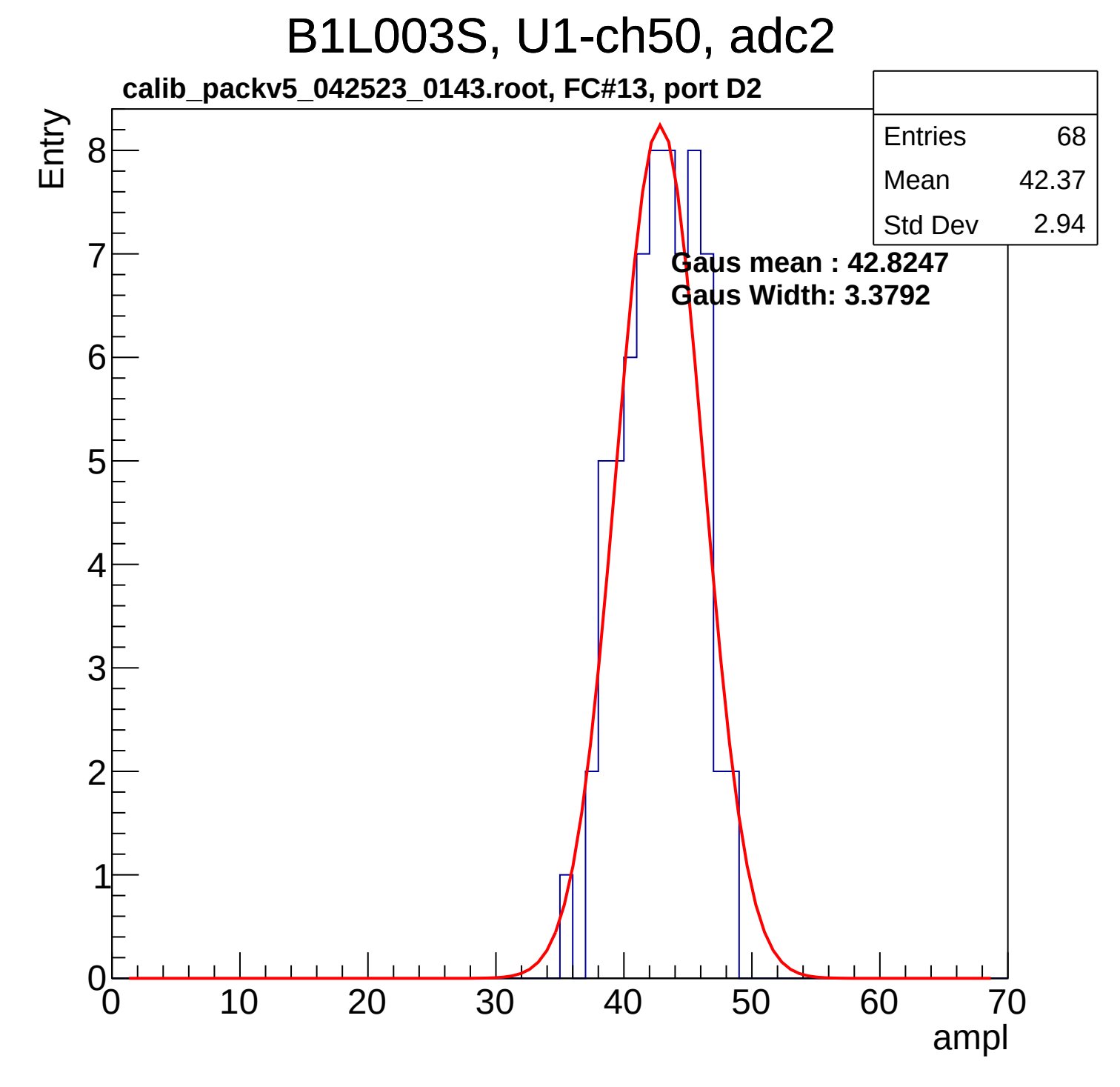
50
60
70
ampl


B1L003S, U1-ch50, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 29.09
Std Dev 2.692
Gaus mean : 29.2900
Gaus Width: 2.5596
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch50, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 80
Mean 35.19
Std Dev 3.507
Gaus mean : 35.7497
Gaus Width: 3.6386
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch50, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.37
Std Dev 2.94
Gaus mean : 42.8247
Gaus Width: 3.3792
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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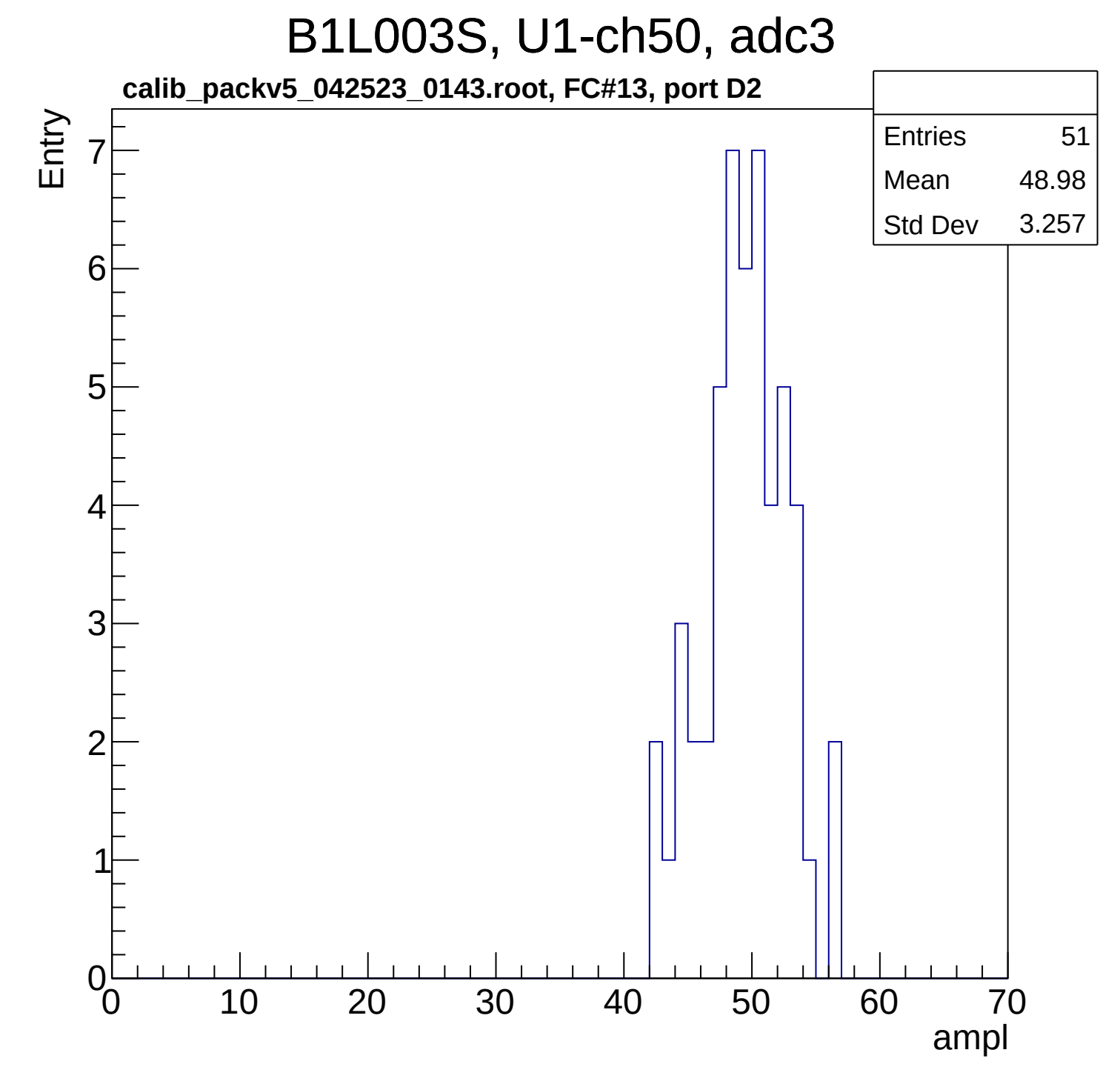
B1L003S, U1-ch50, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 48.98
Std Dev 3.257
Entry
ampl
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1
2
3
4
5
6
7
0
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40
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60
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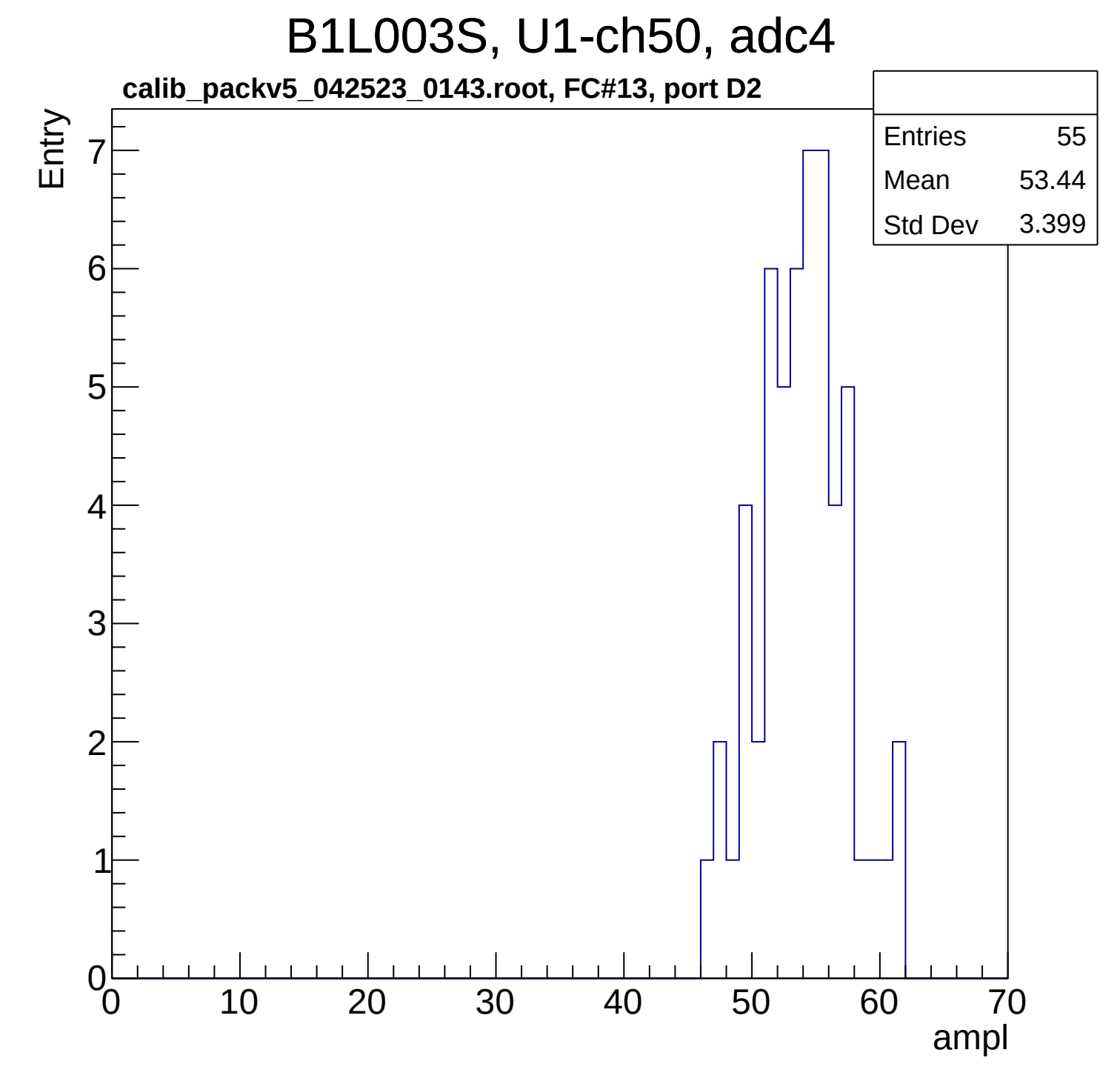
B1L003S, U1-ch50, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.44
Std Dev 3.399
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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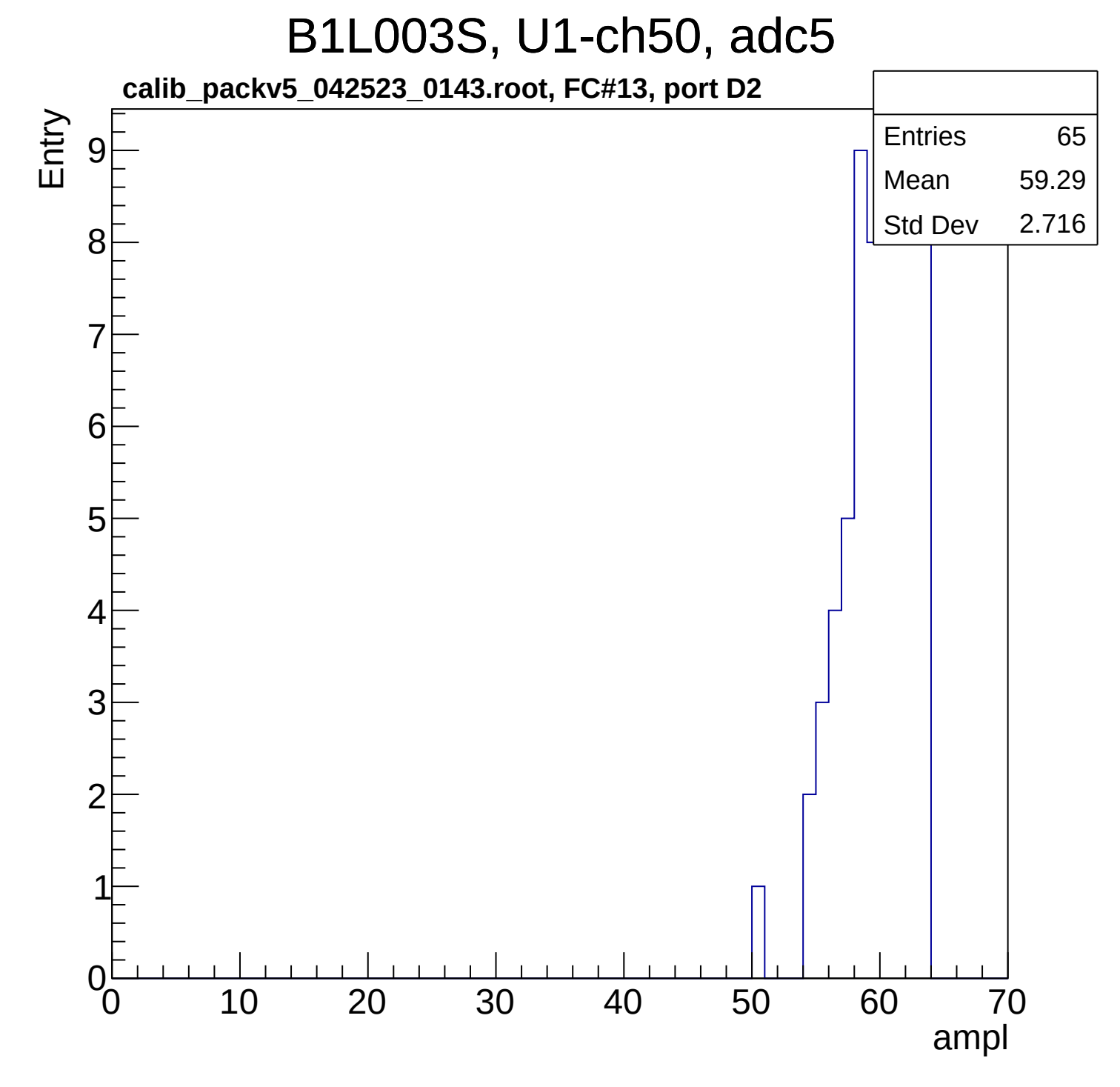
B1L003S, U1-ch50, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 59.29
Std Dev 2.716
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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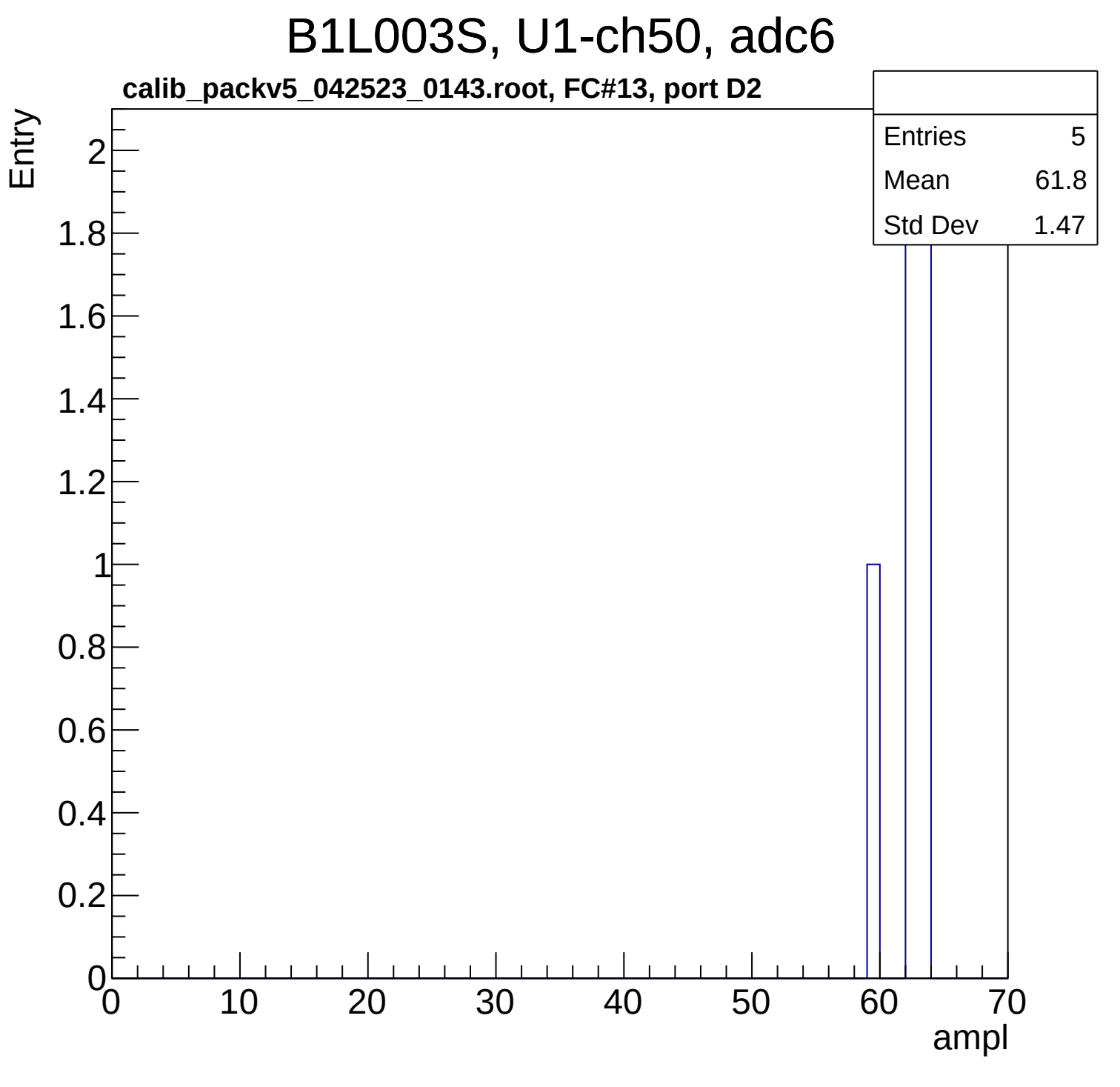
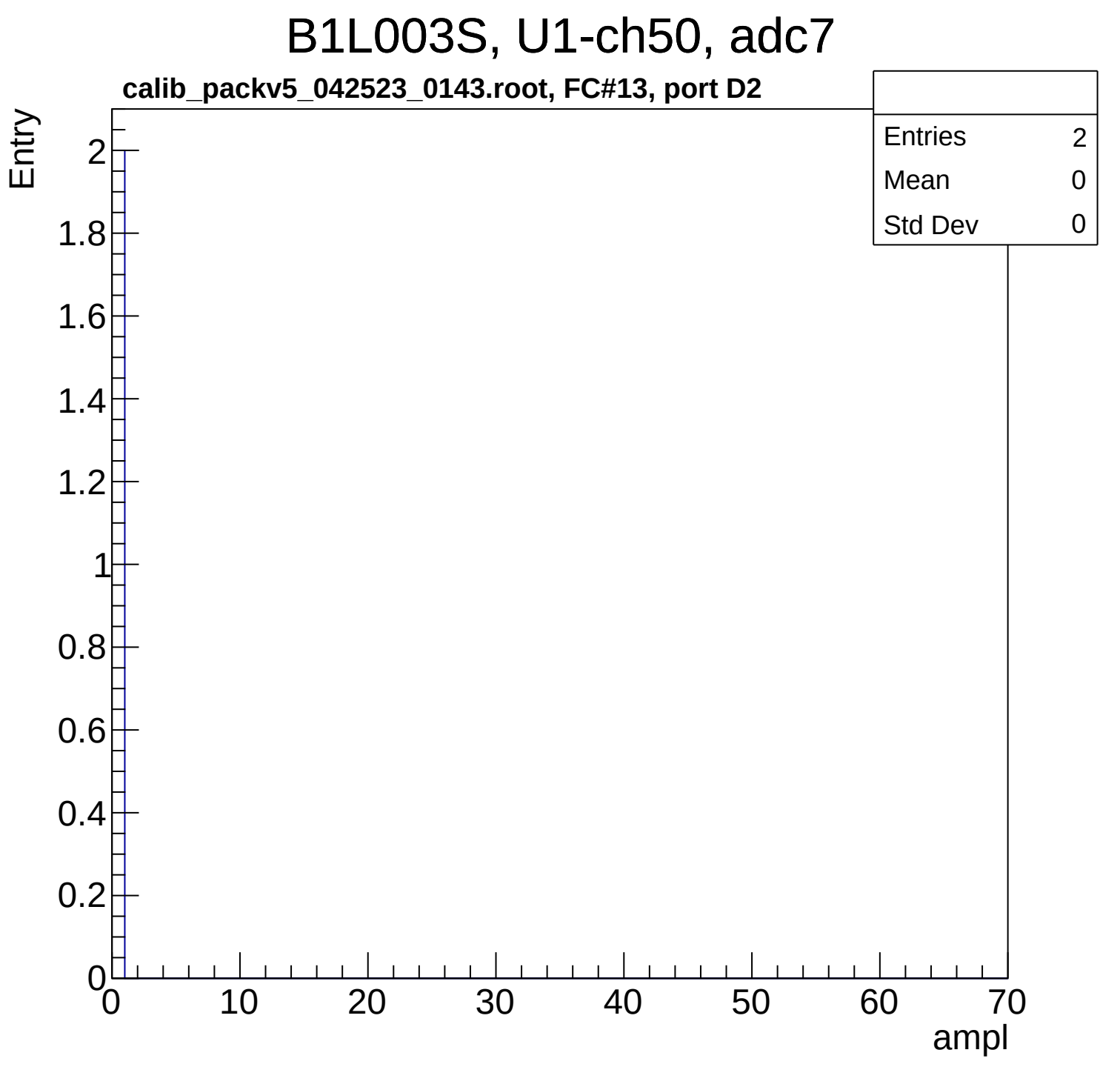
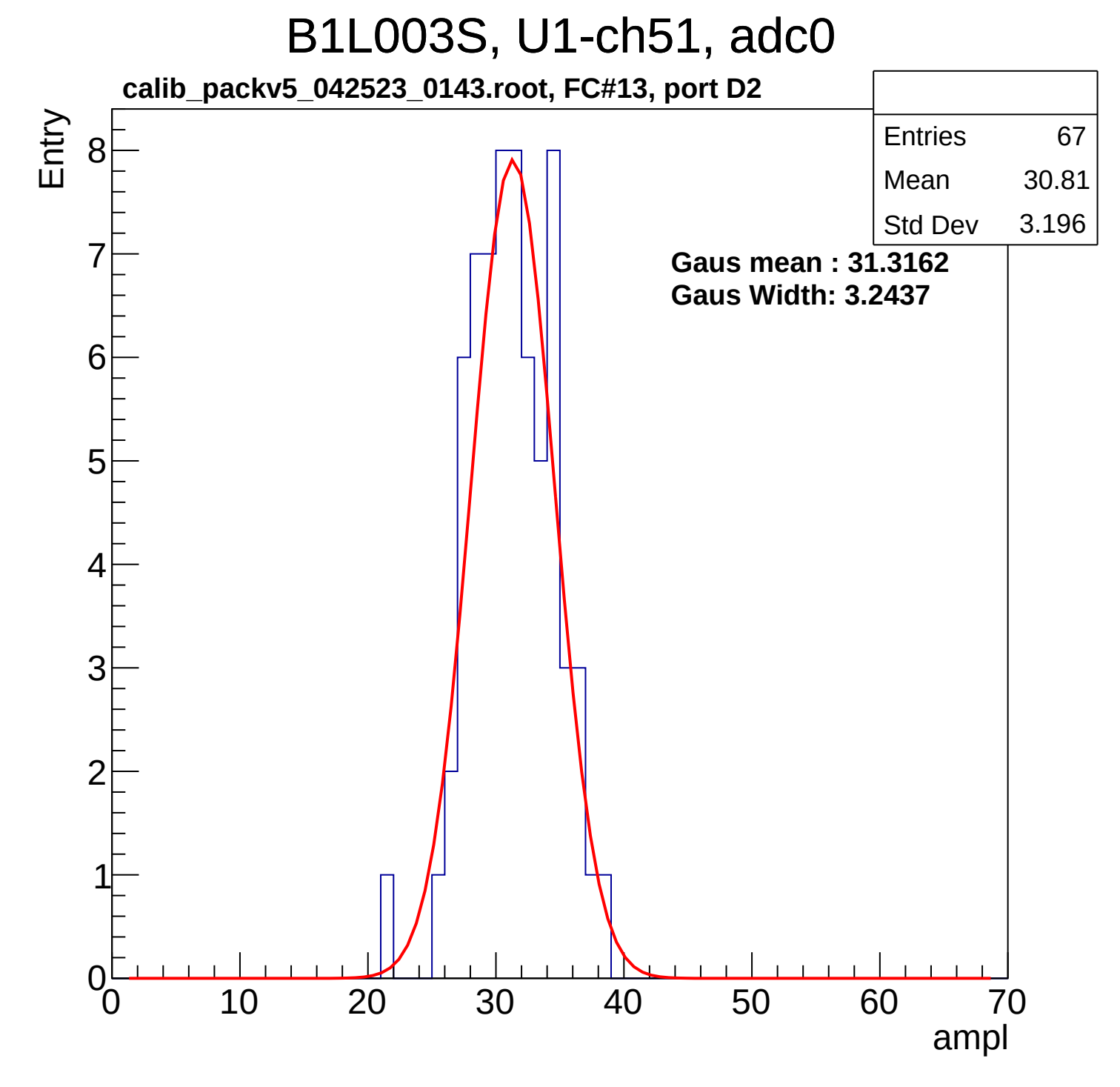
60
70


B1L003S, U1-ch50, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch50, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch51, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.81
Std Dev 3.196
Gaus mean : 31.3162
Gaus Width: 3.2437
Entry
ampl
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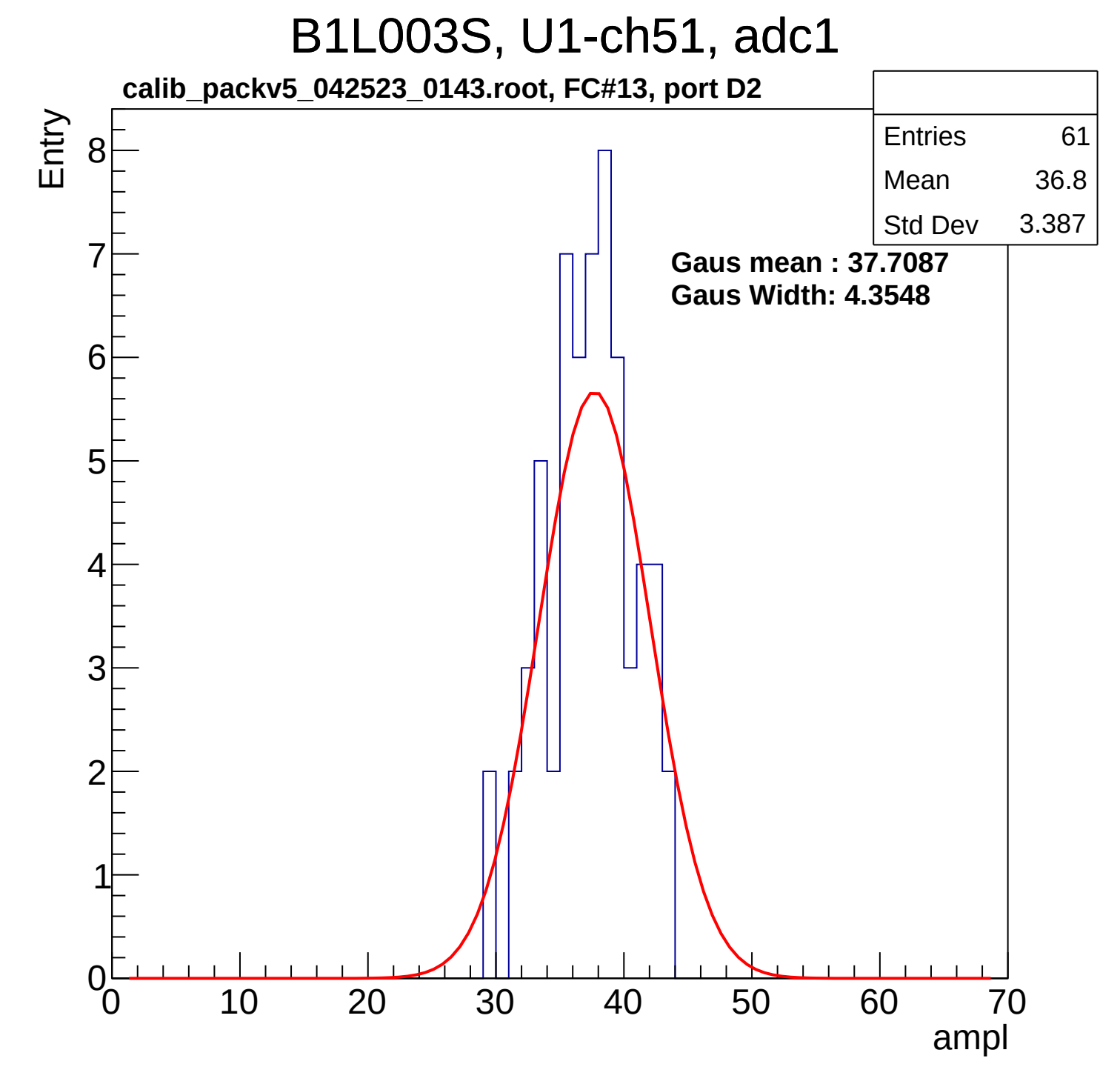
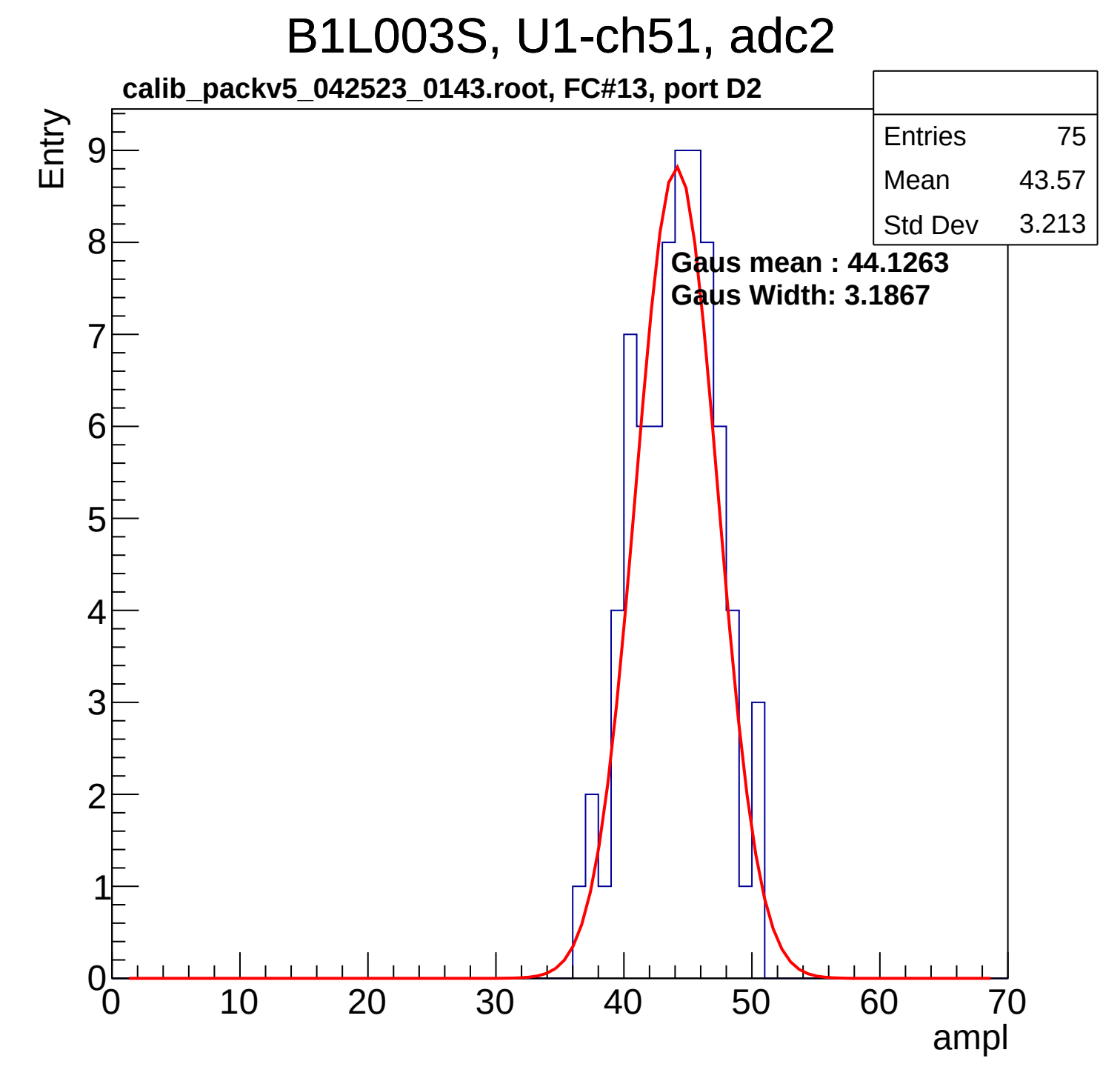
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch51, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.8
Std Dev 3.387
Gaus mean : 37.7087
Gaus Width: 4.3548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch51, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 43.57
Std Dev 3.213
Gaus mean : 44.1263
Gaus Width: 3.1867
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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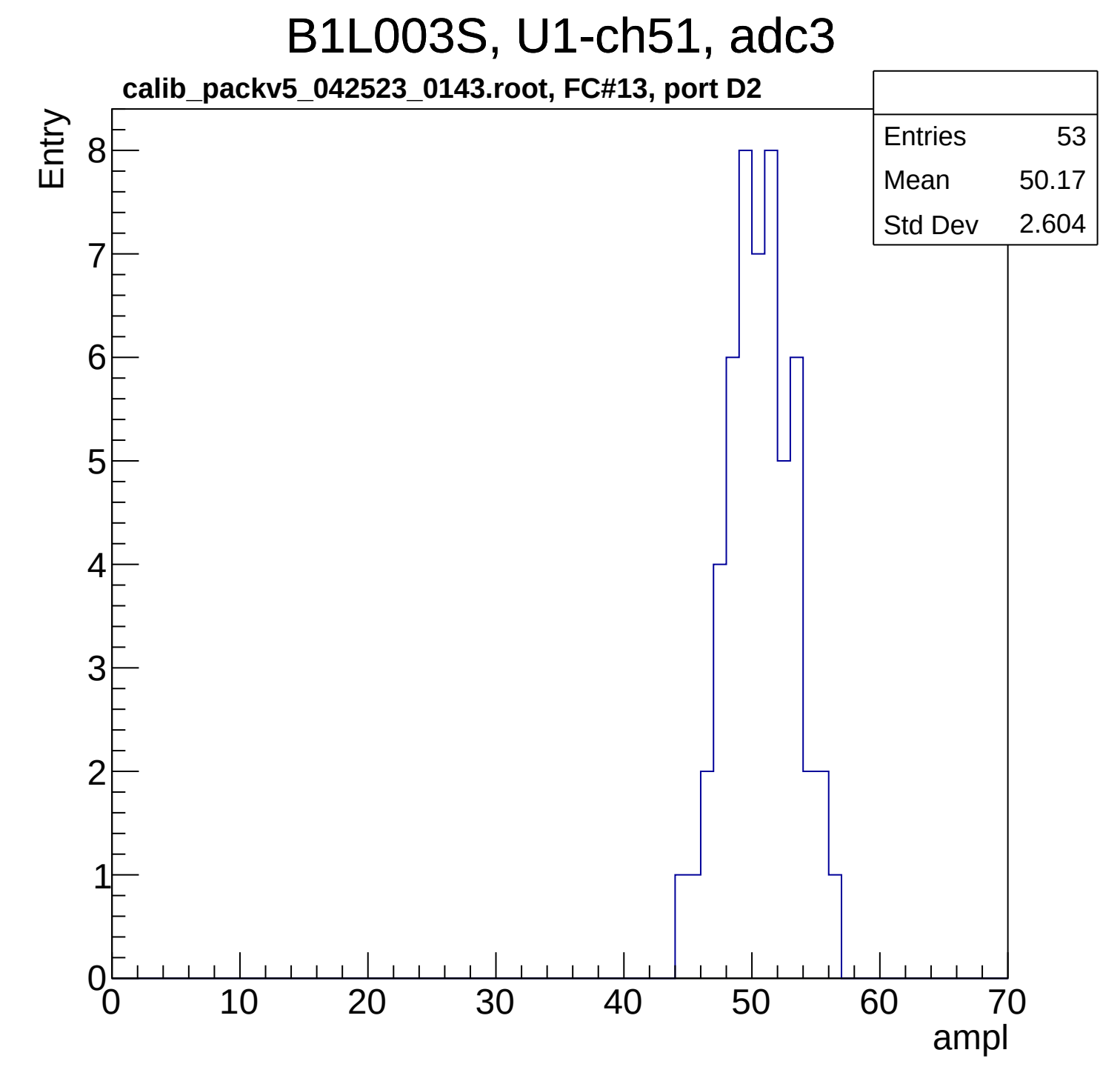
40
50
60
70


B1L003S, U1-ch51, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 50.17
Std Dev 2.604
Entry
ampl
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1
2
3
4
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0
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60
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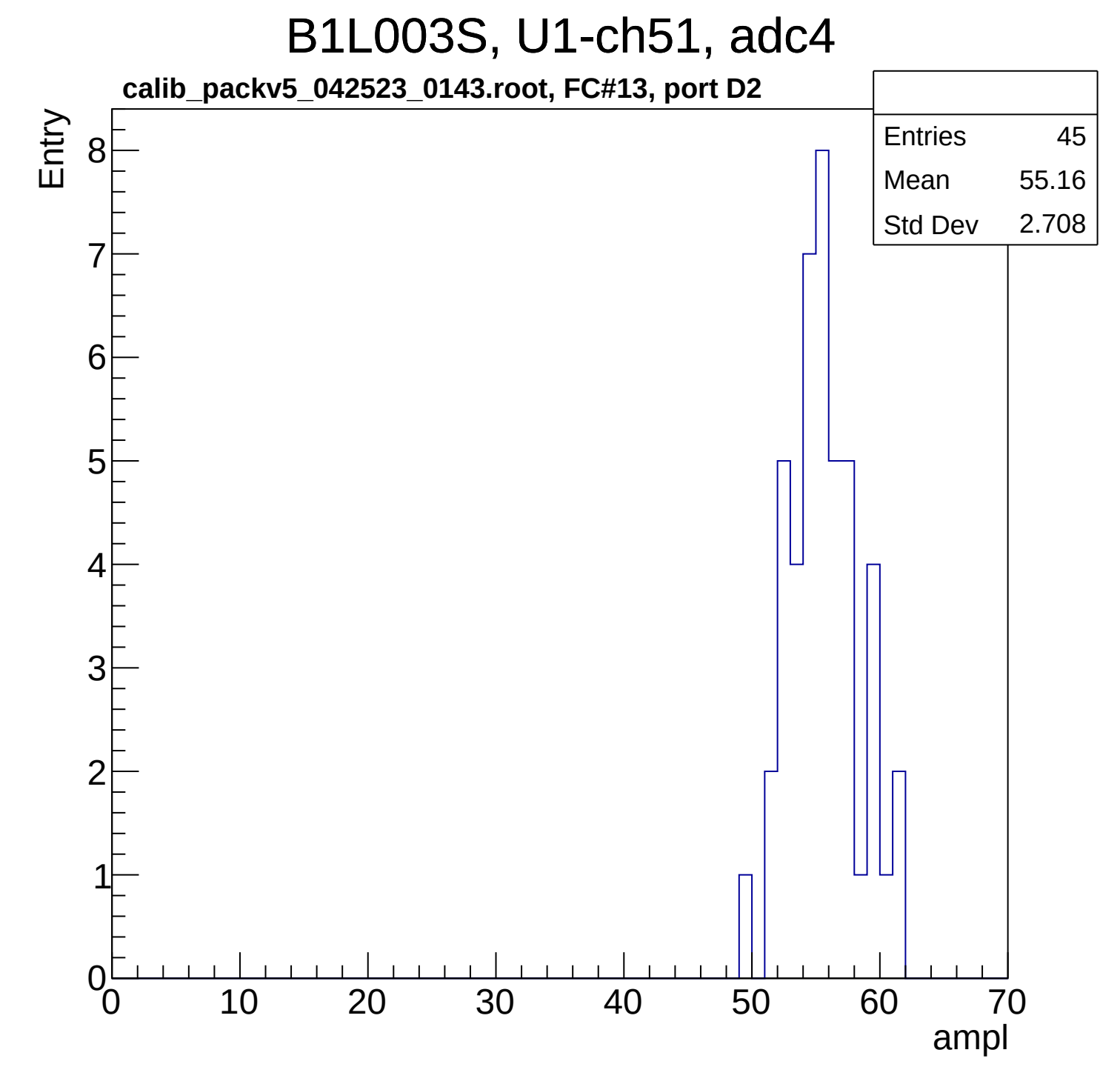
B1L003S, U1-ch51, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 55.16
Std Dev 2.708
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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60
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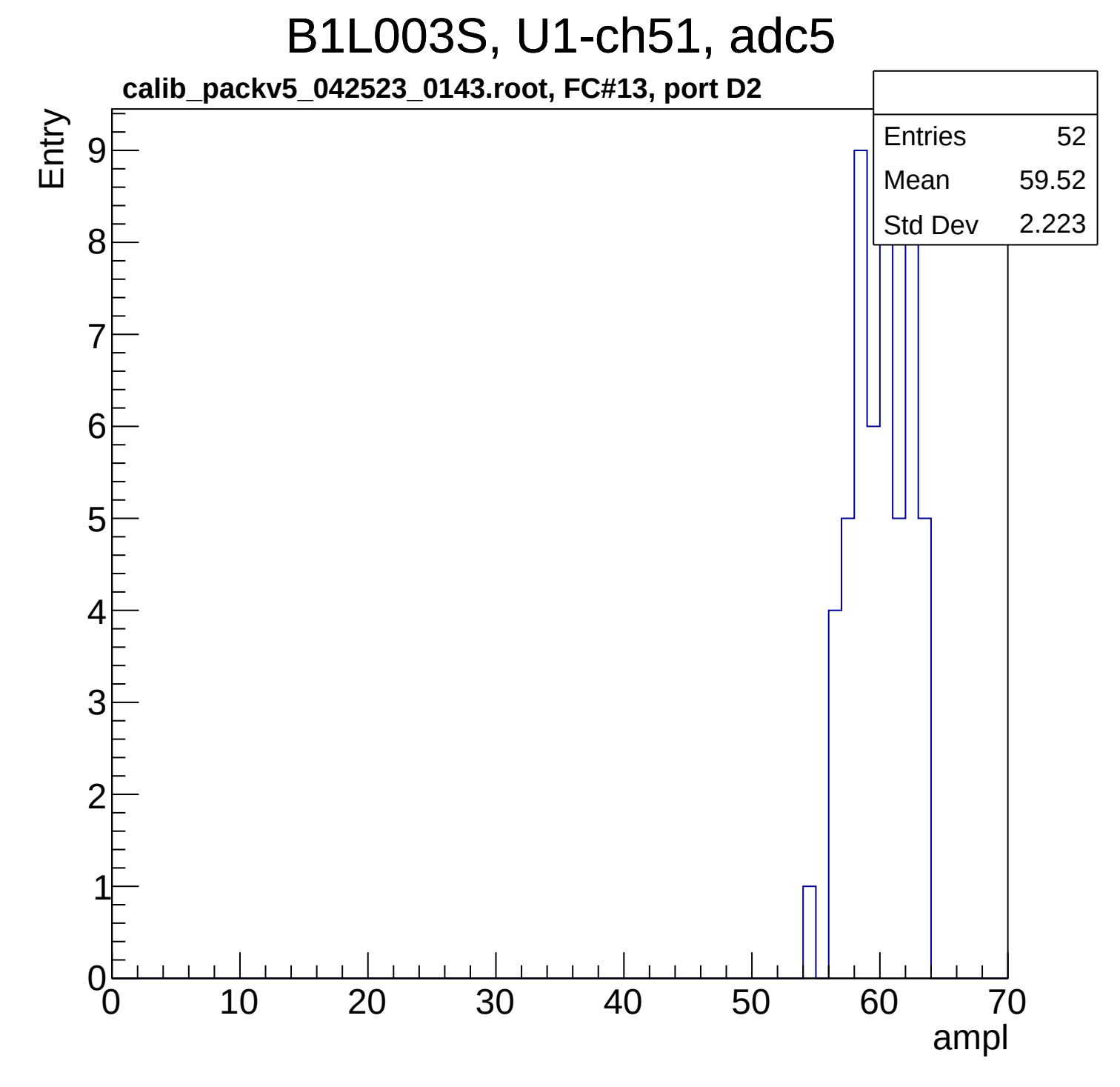
B1L003S, U1-ch51, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.52
Std Dev 2.223
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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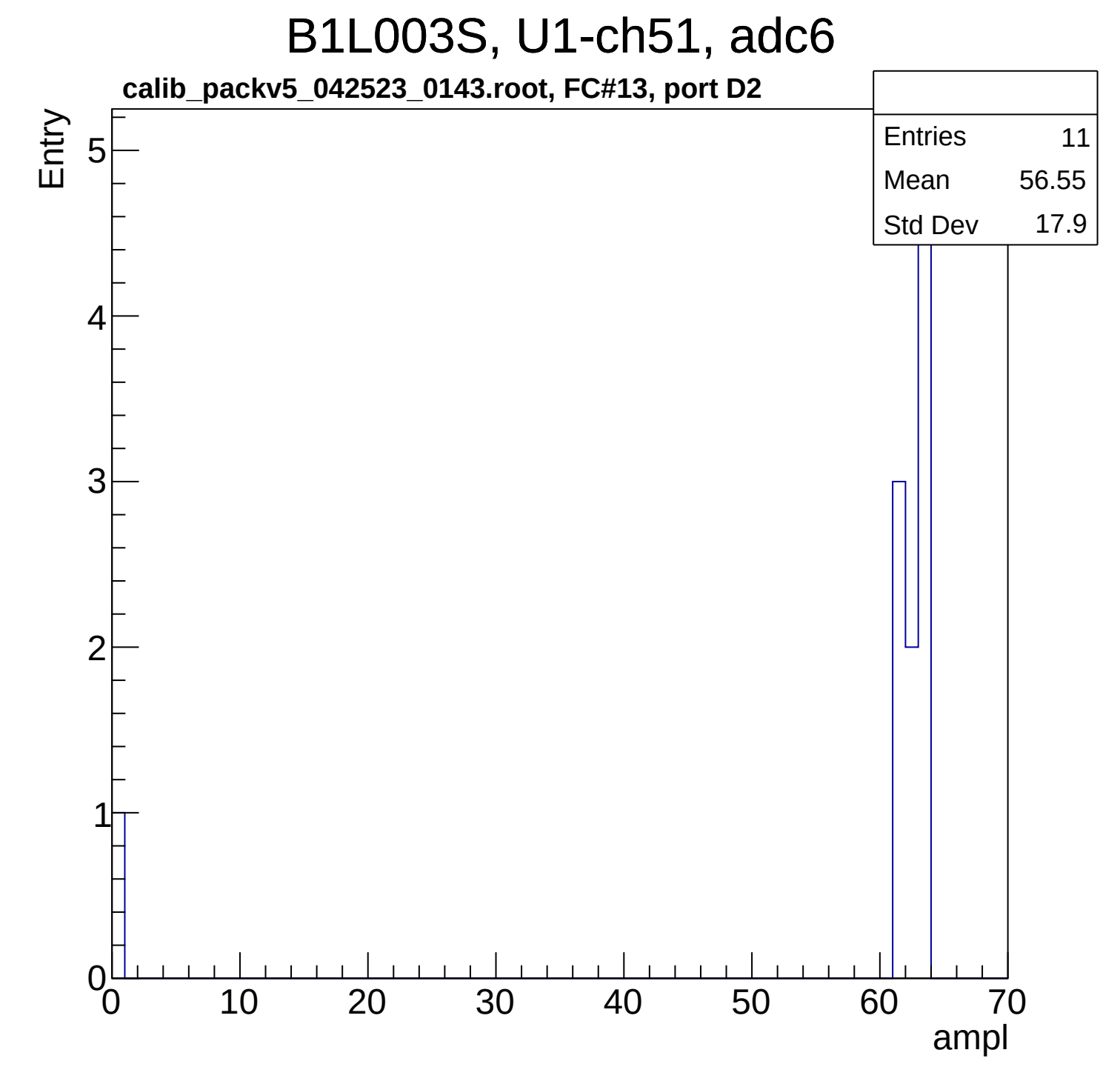
B1L003S, U1-ch51, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.55
Std Dev 17.9
Entry
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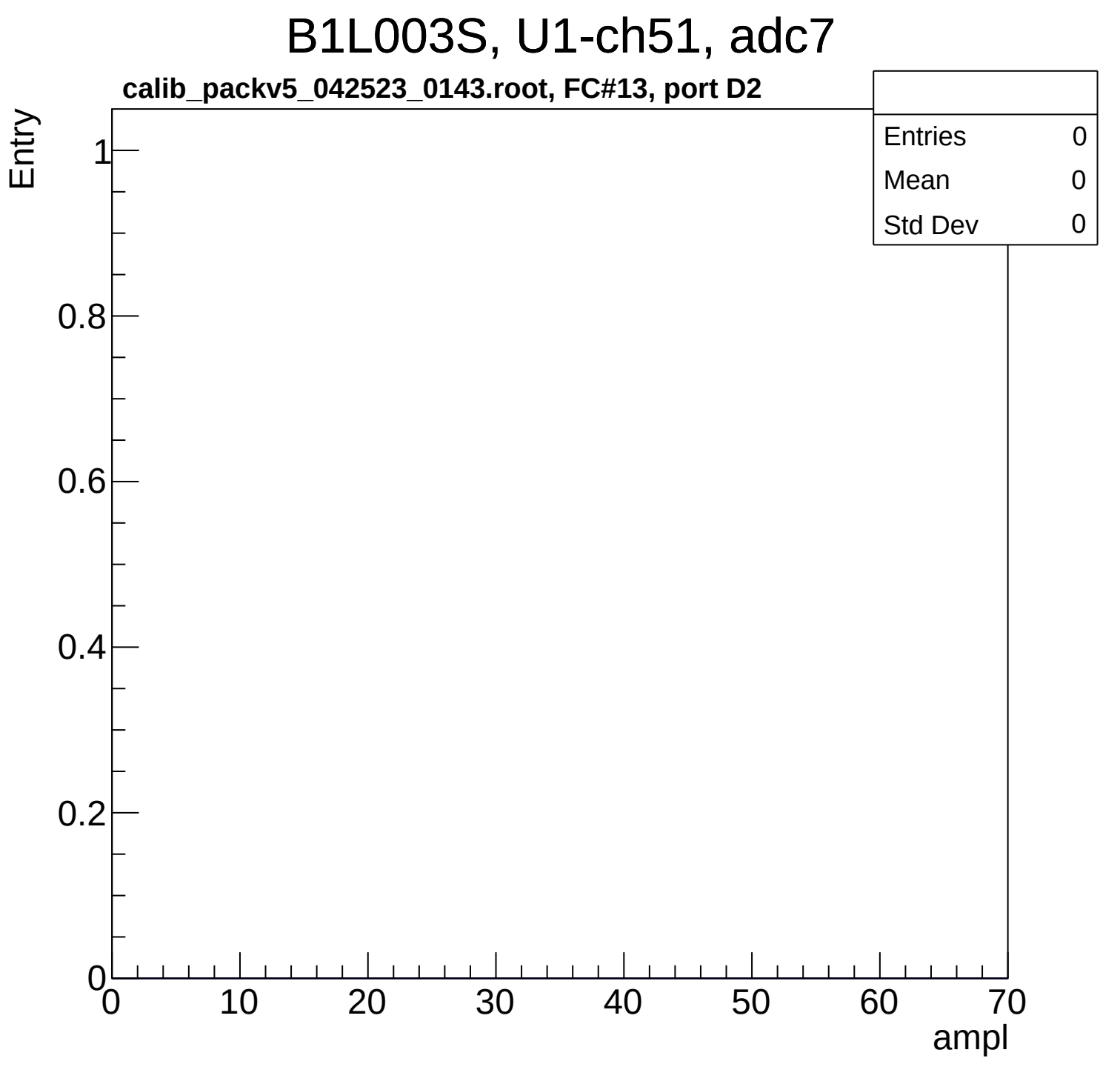
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch51, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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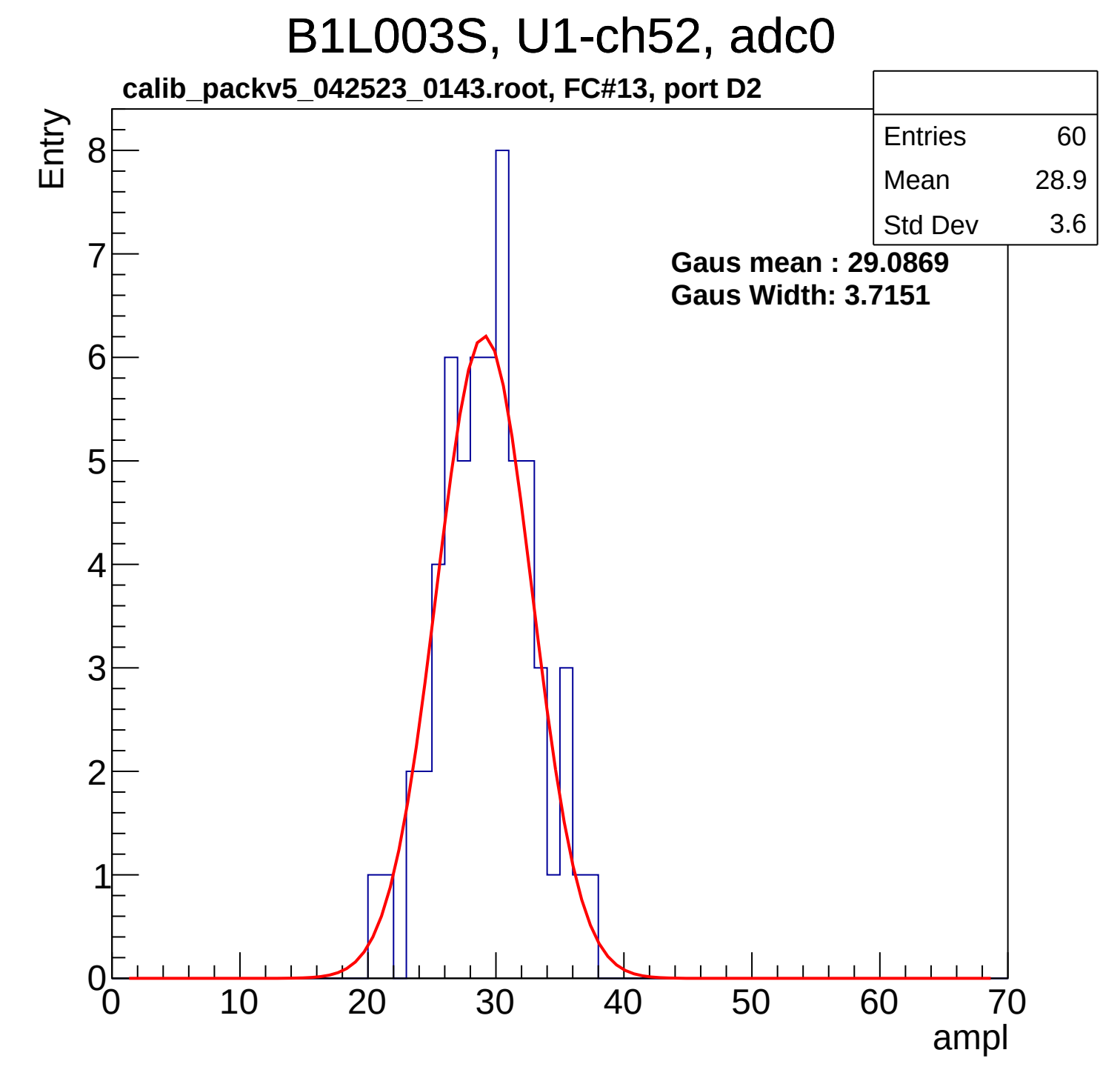
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch52, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.9
Std Dev 3.6
Gaus mean : 29.0869
Gaus Width: 3.7151
Entry
ampl
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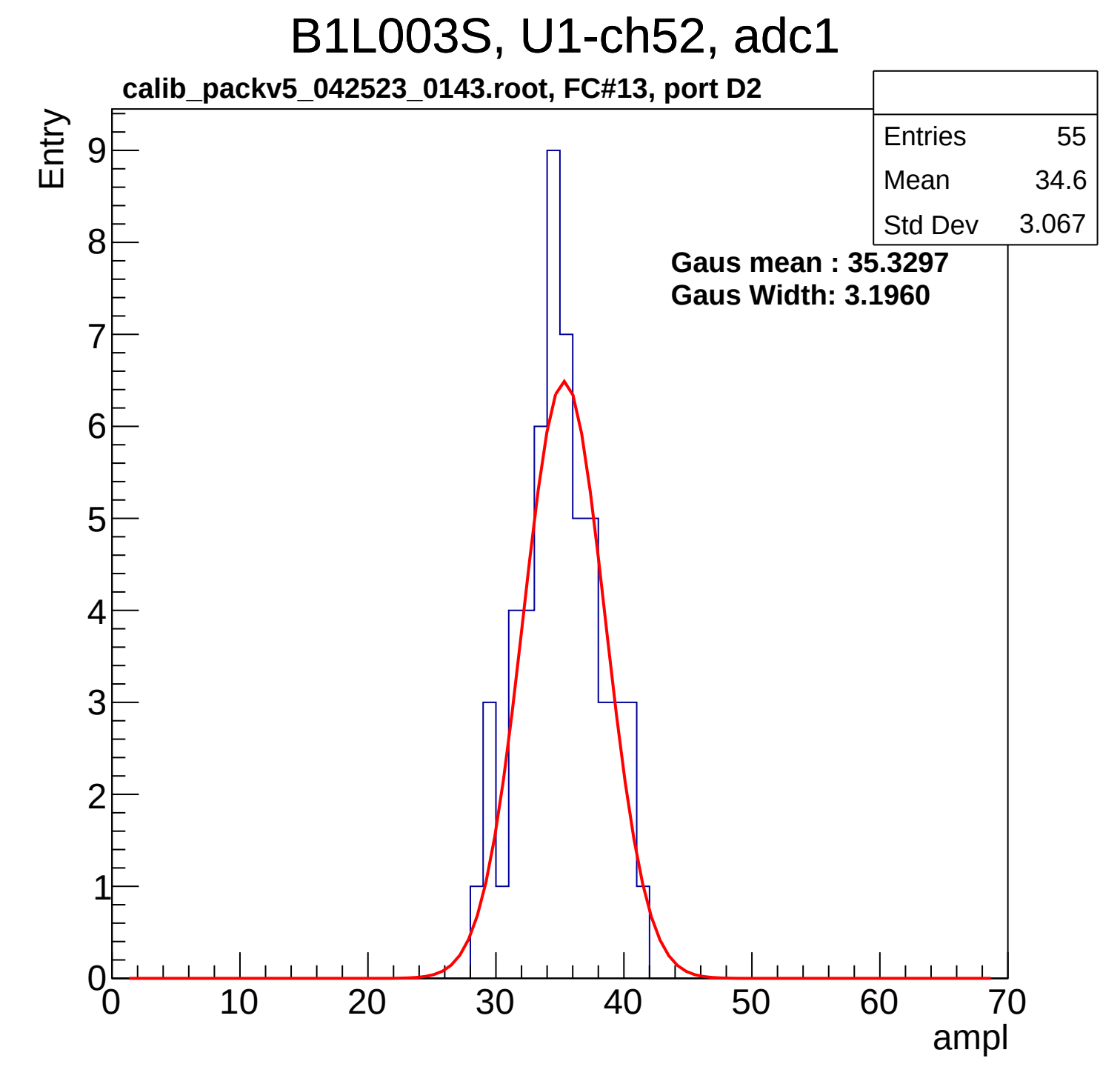
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch52, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 34.6
Std Dev 3.067
Gaus mean : 35.3297
Gaus Width: 3.1960
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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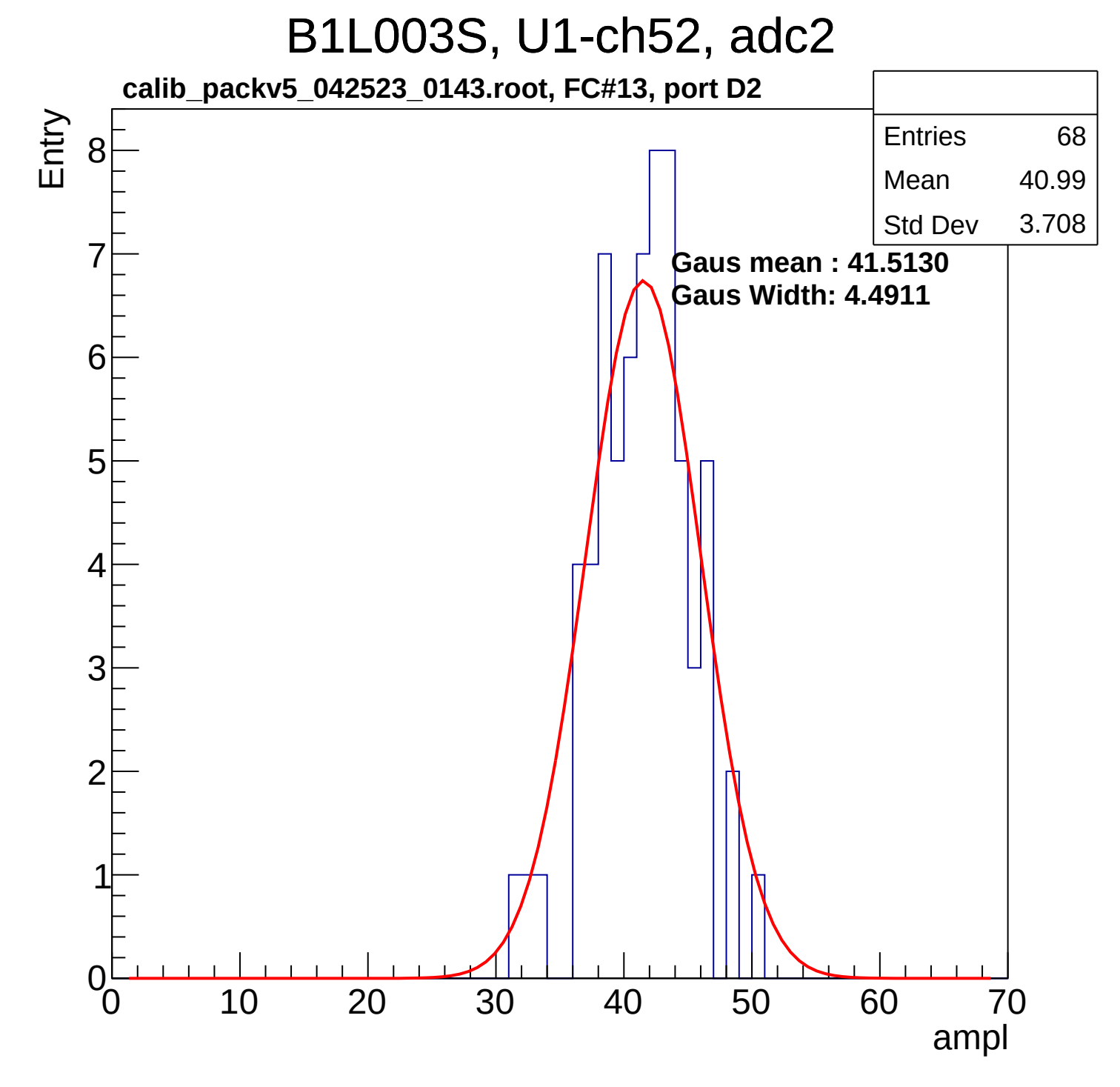
B1L003S, U1-ch52, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 40.99
Std Dev 3.708
Gaus mean : 41.5130
Gaus Width: 4.4911
Entry
ampl
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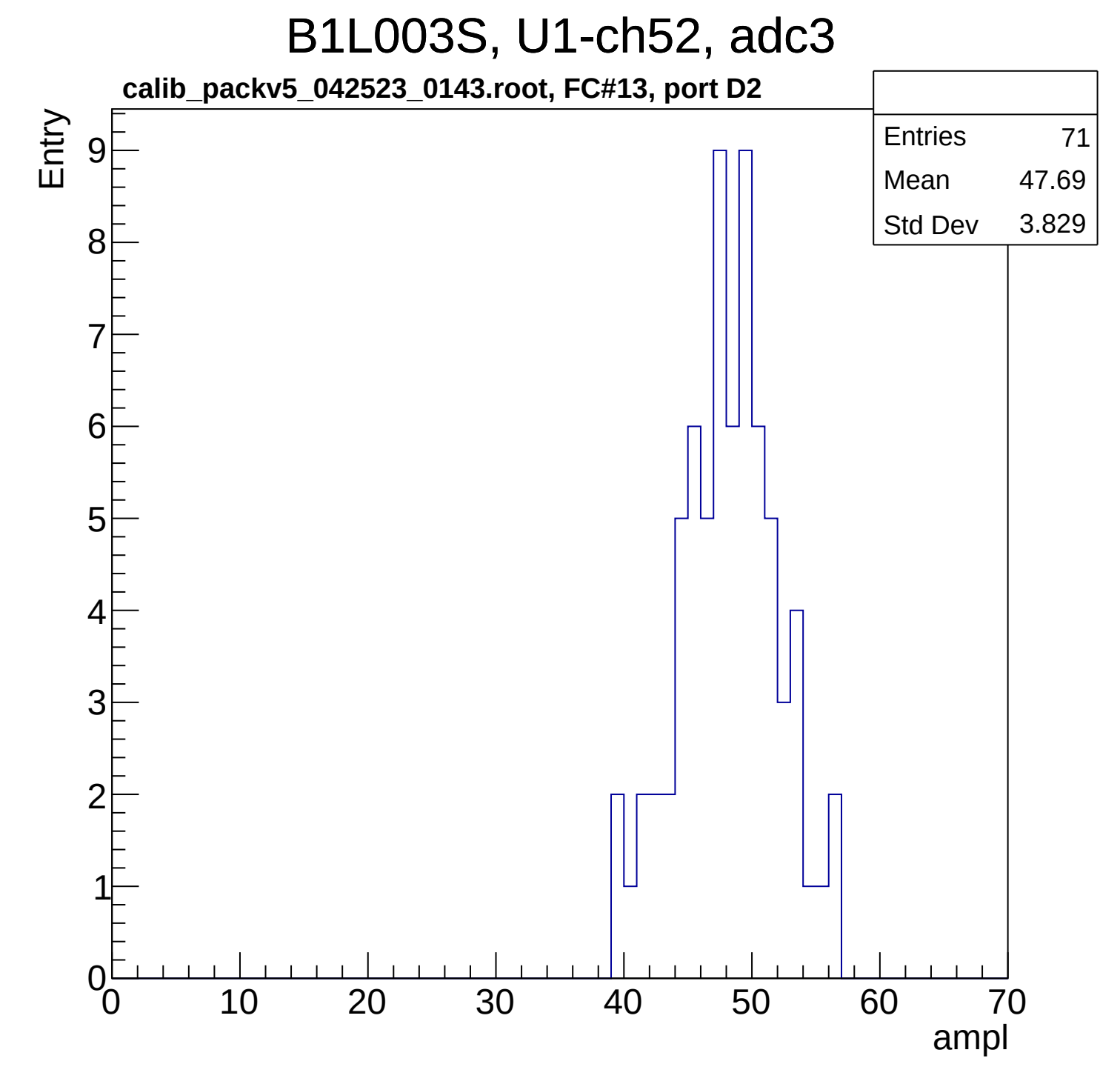
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch52, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.69
Std Dev 3.829
Entry
ampl
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5
6
7
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9
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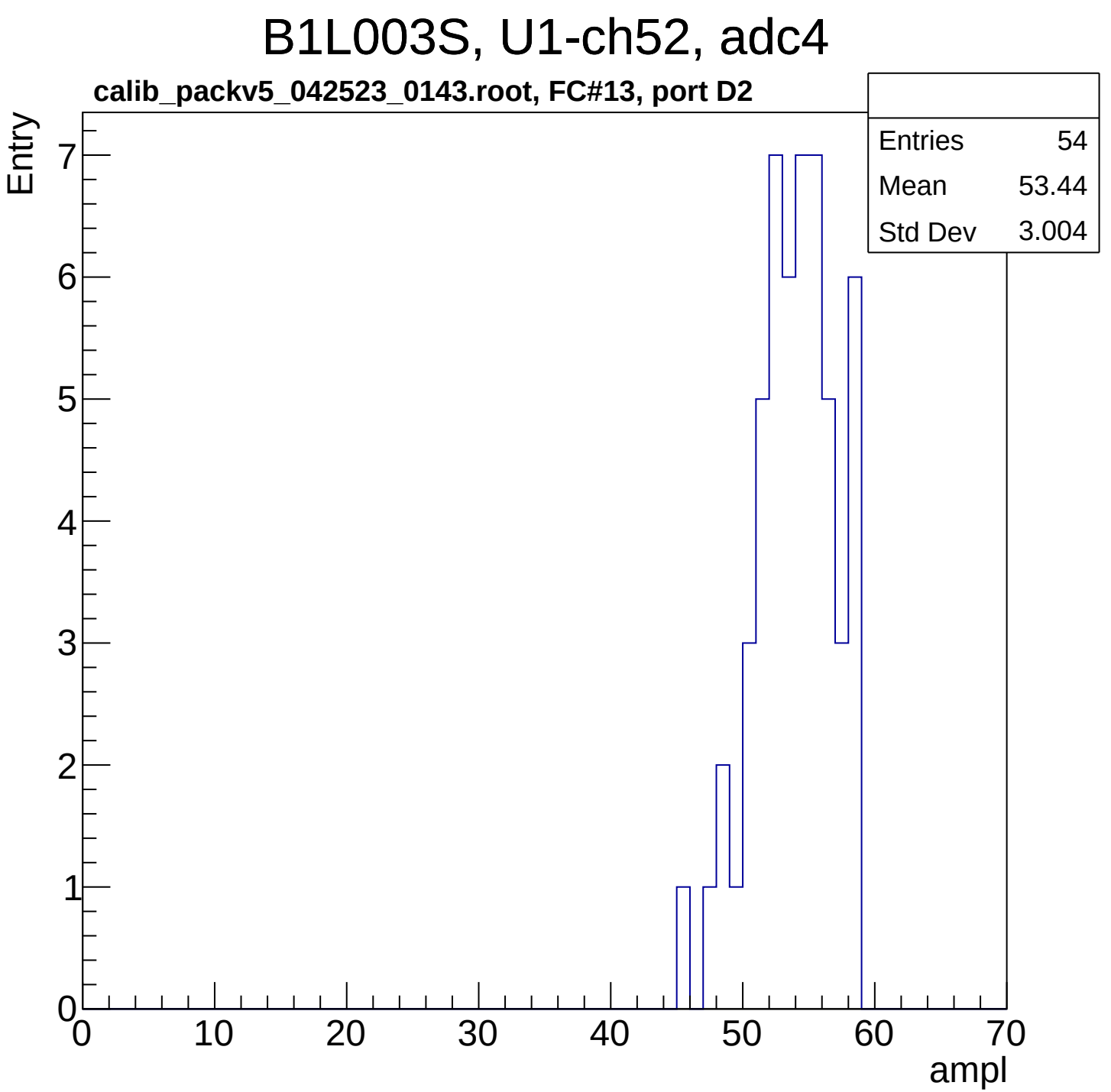
B1L003S, U1-ch52, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.44
Std Dev 3.004
Entry
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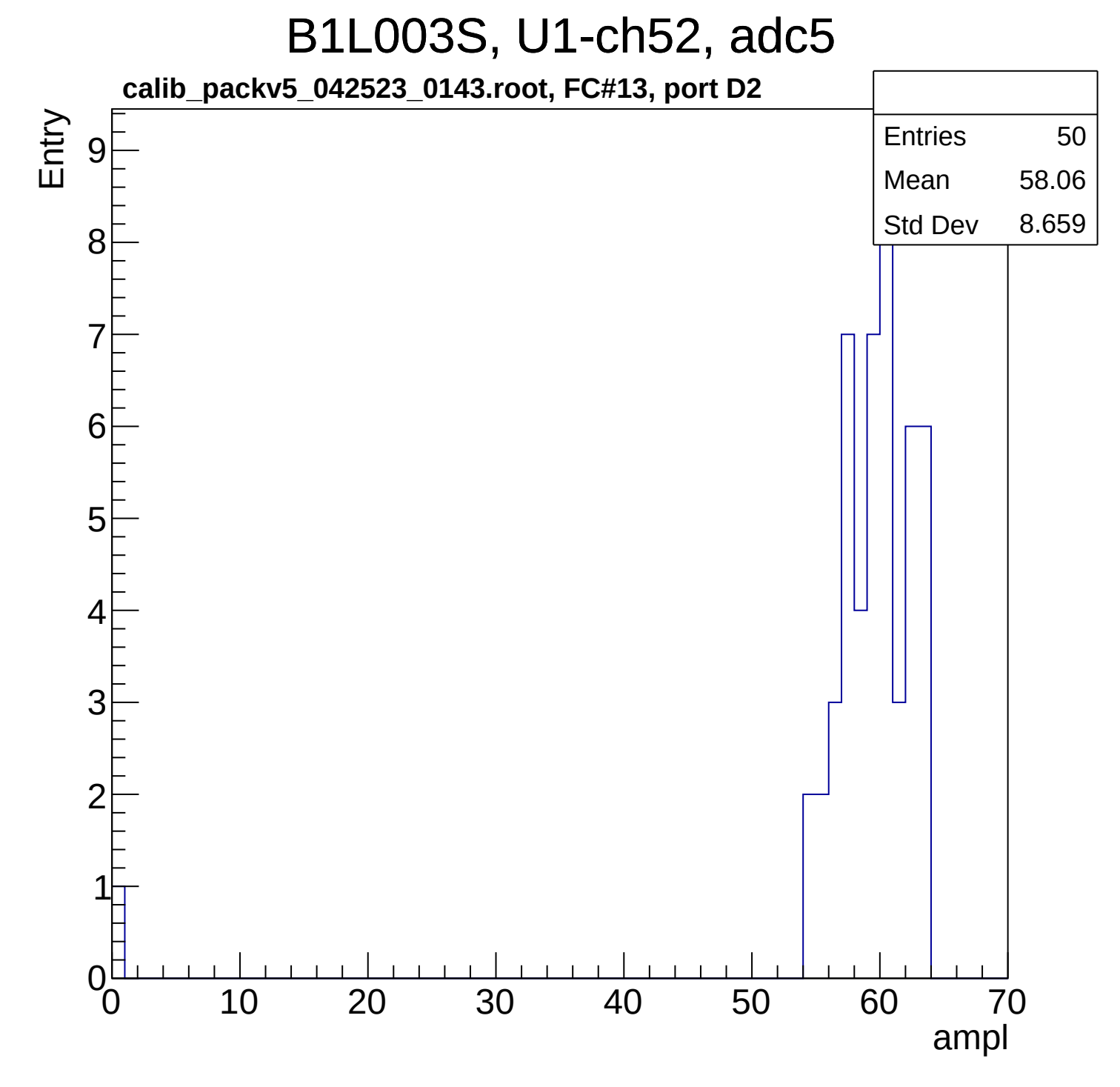
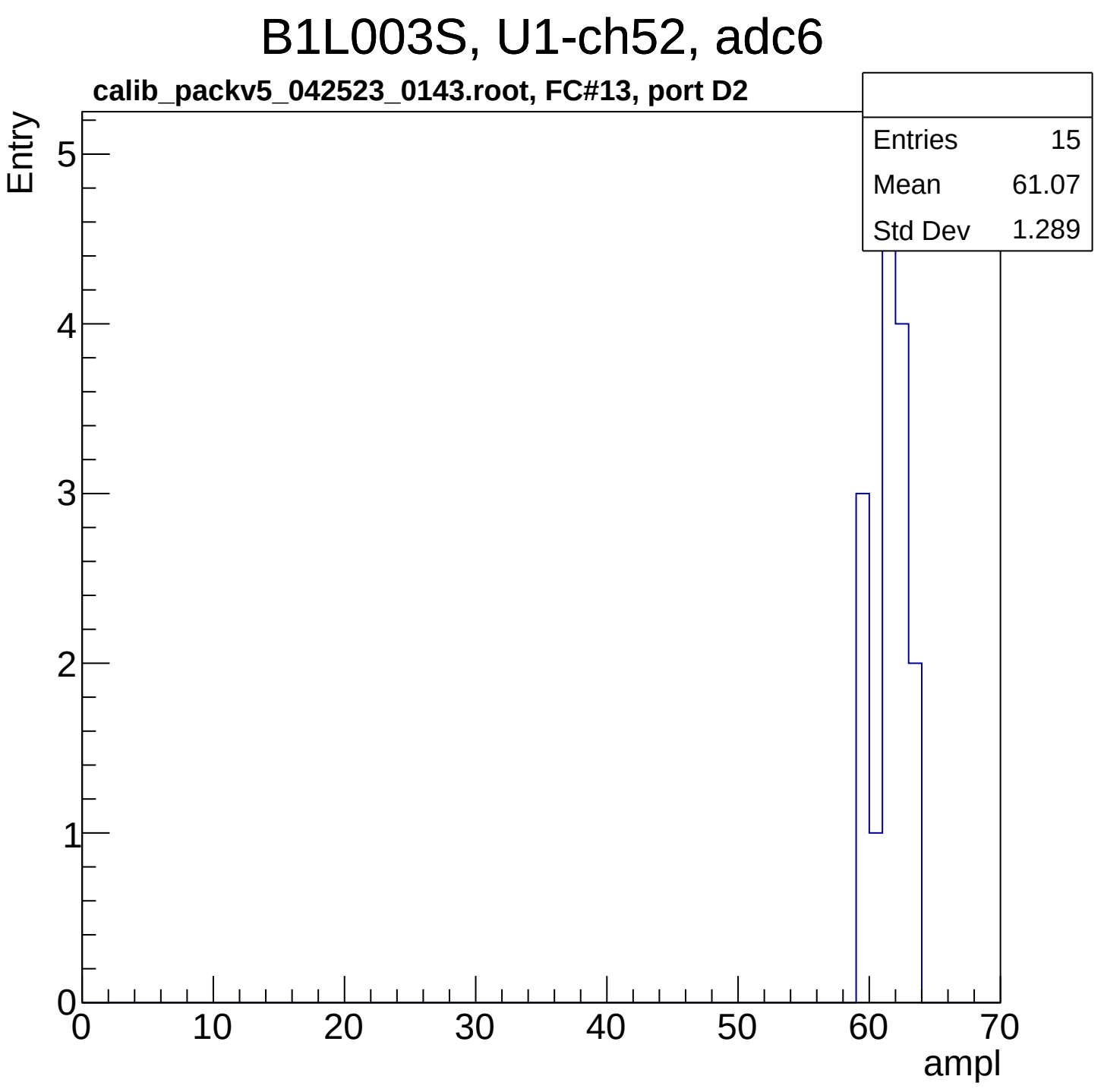
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch52, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.06
Std Dev 8.659
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch52, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 15
Mean 61.07
Std Dev 1.289
Entry
0
1
2
3
4
5
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10
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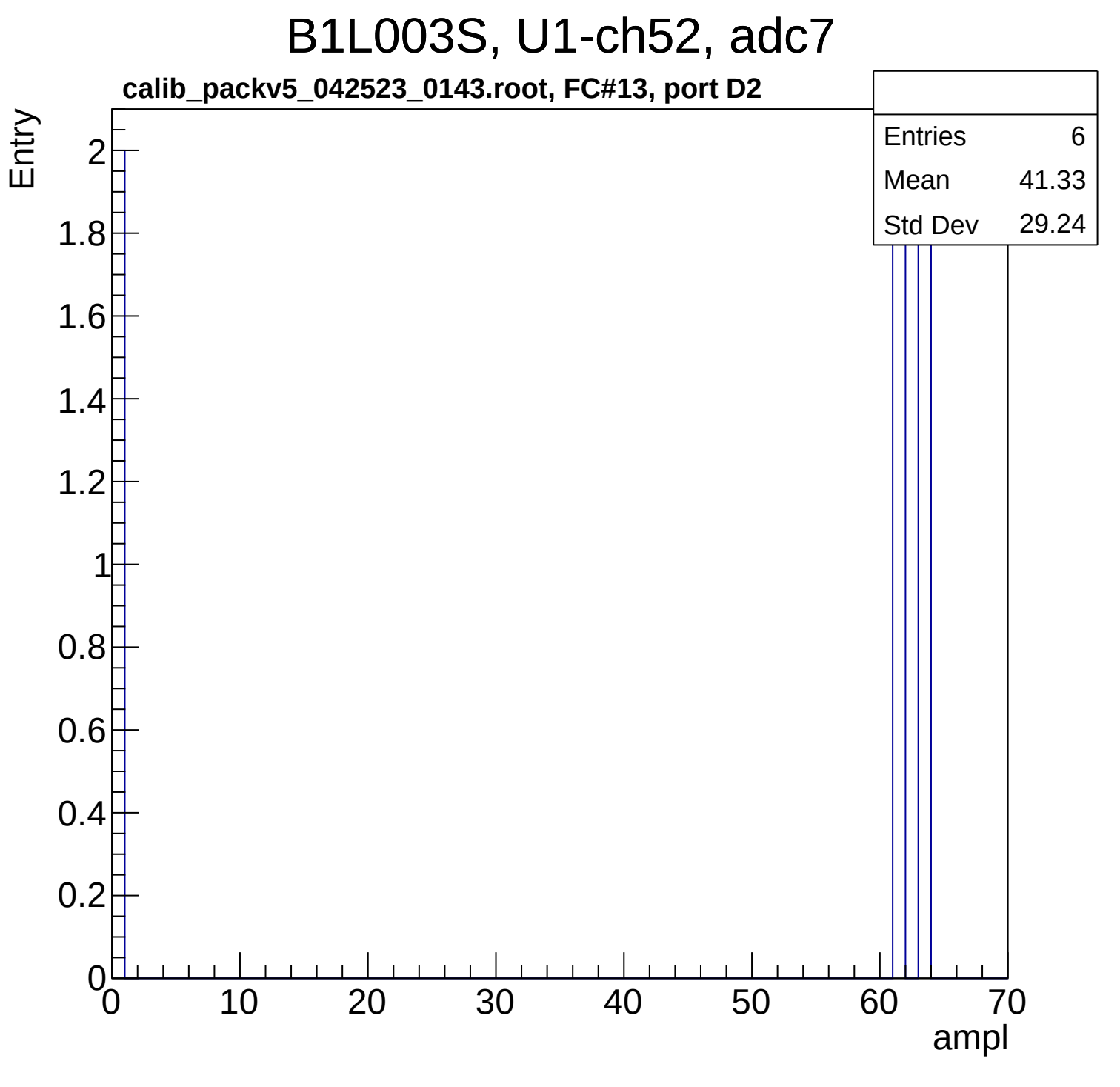
70
ampl


B1L003S, U1-ch52, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 41.33
Std Dev 29.24
Entry
0
0.2
0.4
0.6
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1.4
1.6
1.8
2
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10
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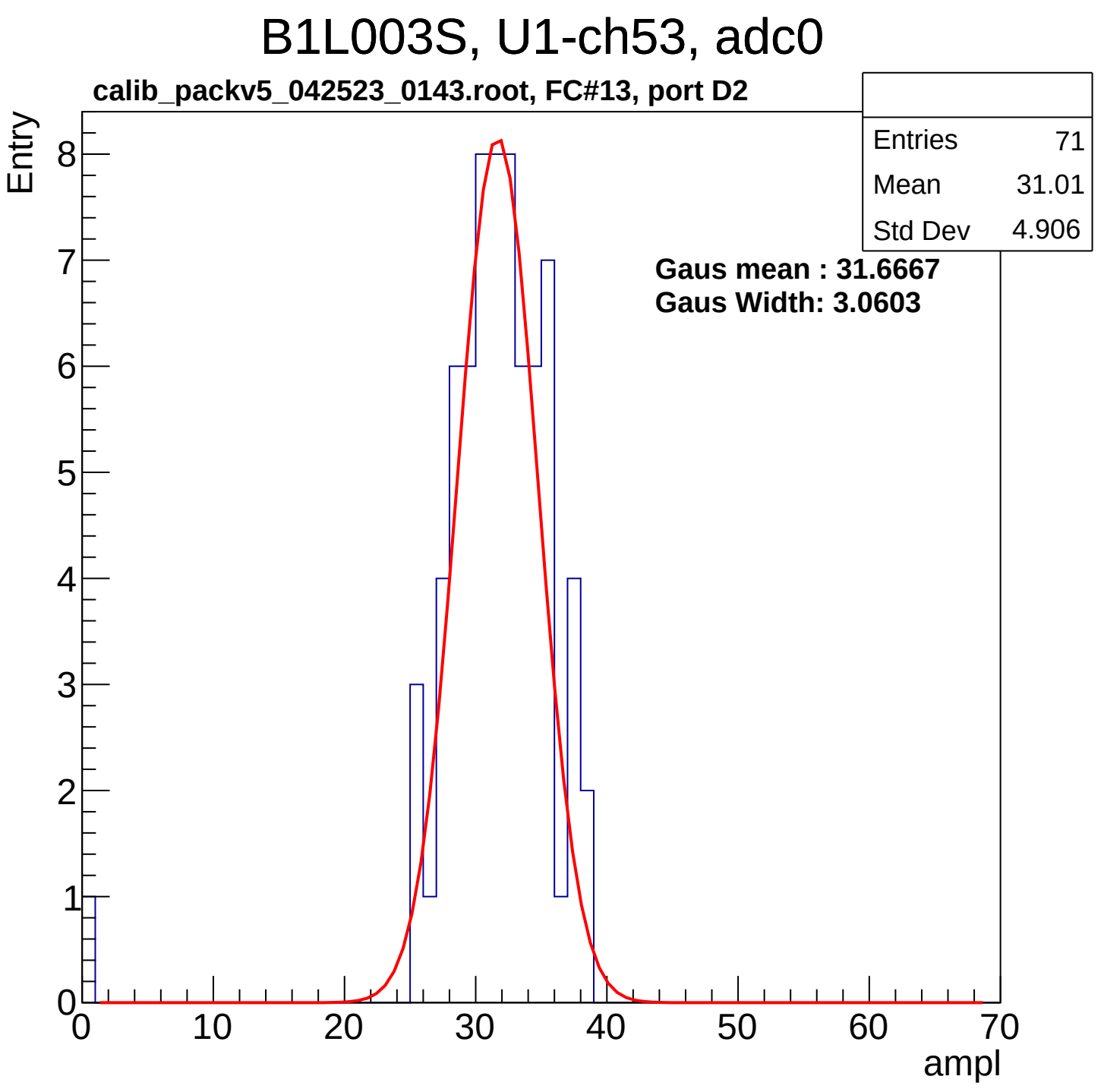
60
70
ampl


B1L003S, U1-ch53, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 31.01
Std Dev 4.906
Gaus mean : 31.6667
Gaus Width: 3.0603
Entry
ampl
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3
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5
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0
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60
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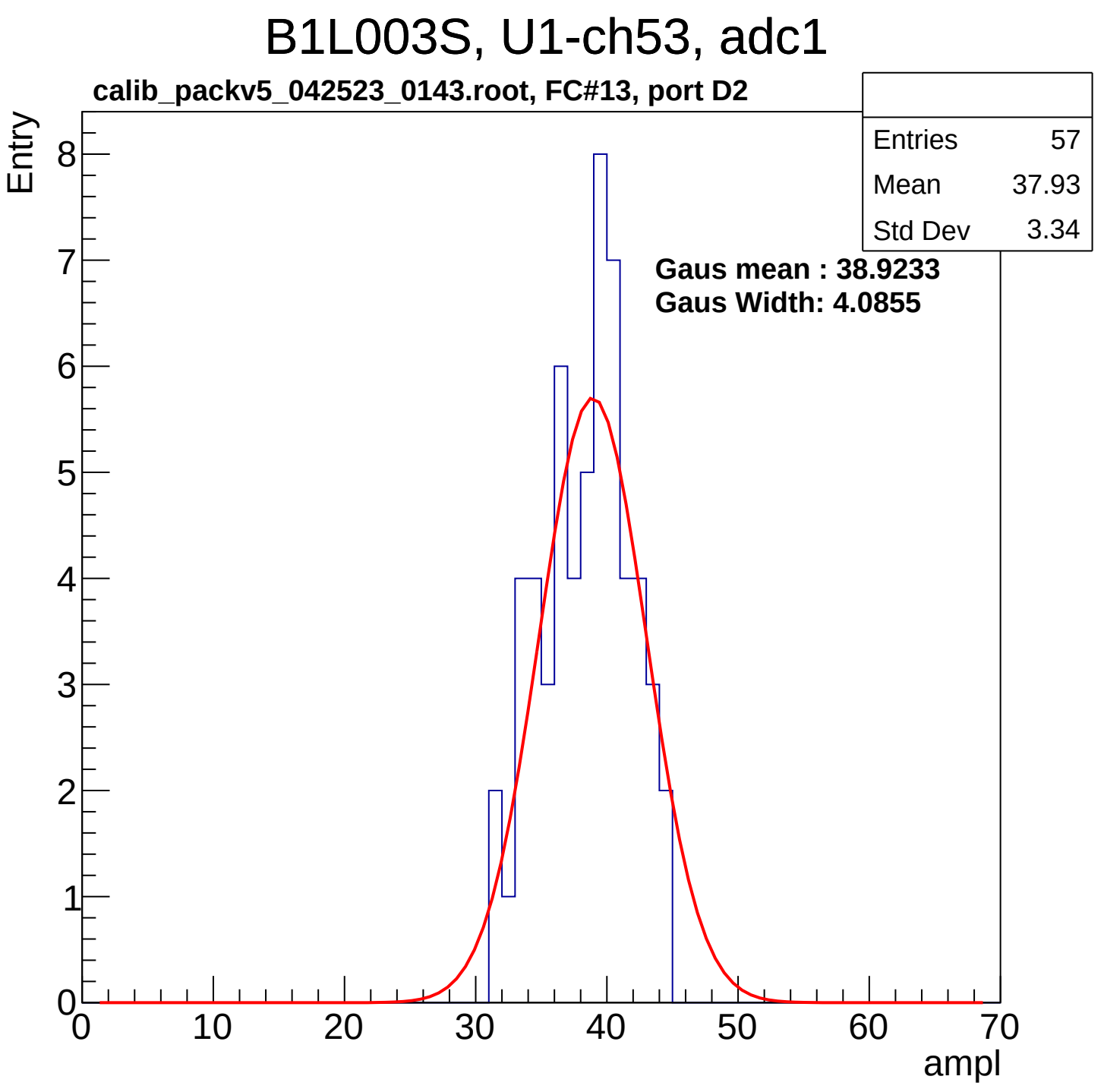
B1L003S, U1-ch53, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 37.93
Std Dev 3.34
Gaus mean : 38.9233
Gaus Width: 4.0855
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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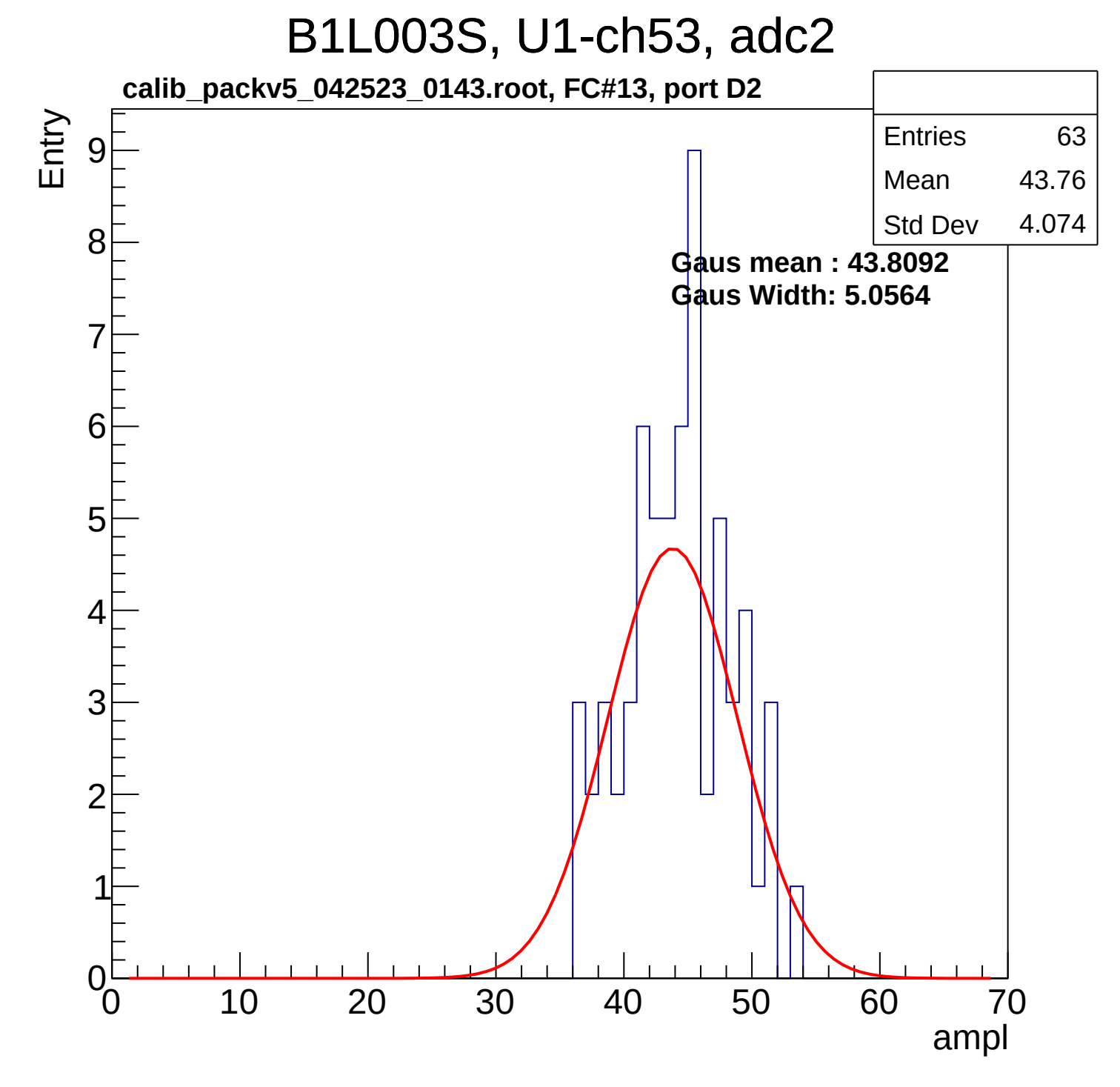
B1L003S, U1-ch53, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 43.76
Std Dev 4.074
Gaus mean : 43.8092
Gaus Width: 5.0564
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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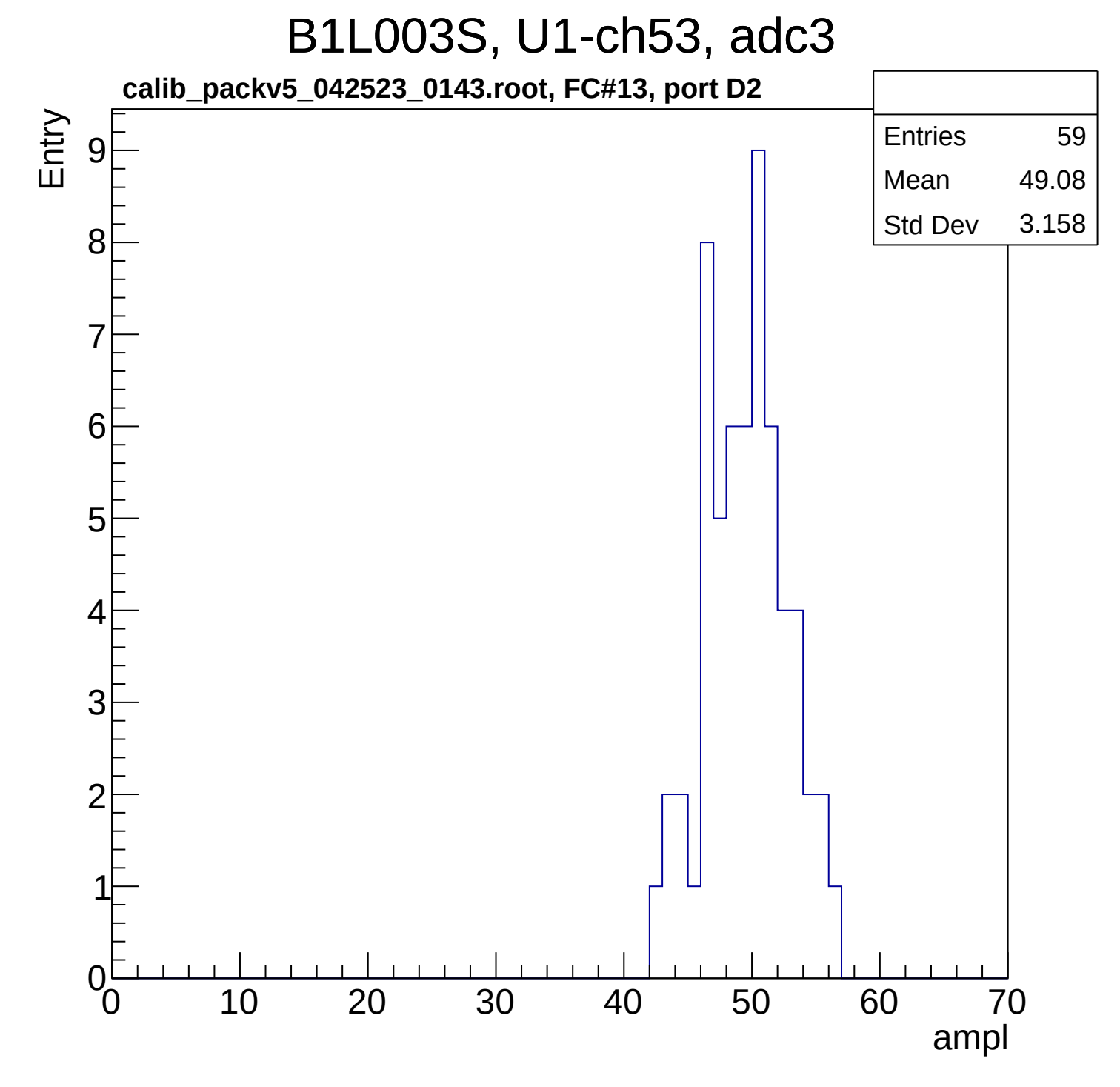
B1L003S, U1-ch53, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.08
Std Dev 3.158
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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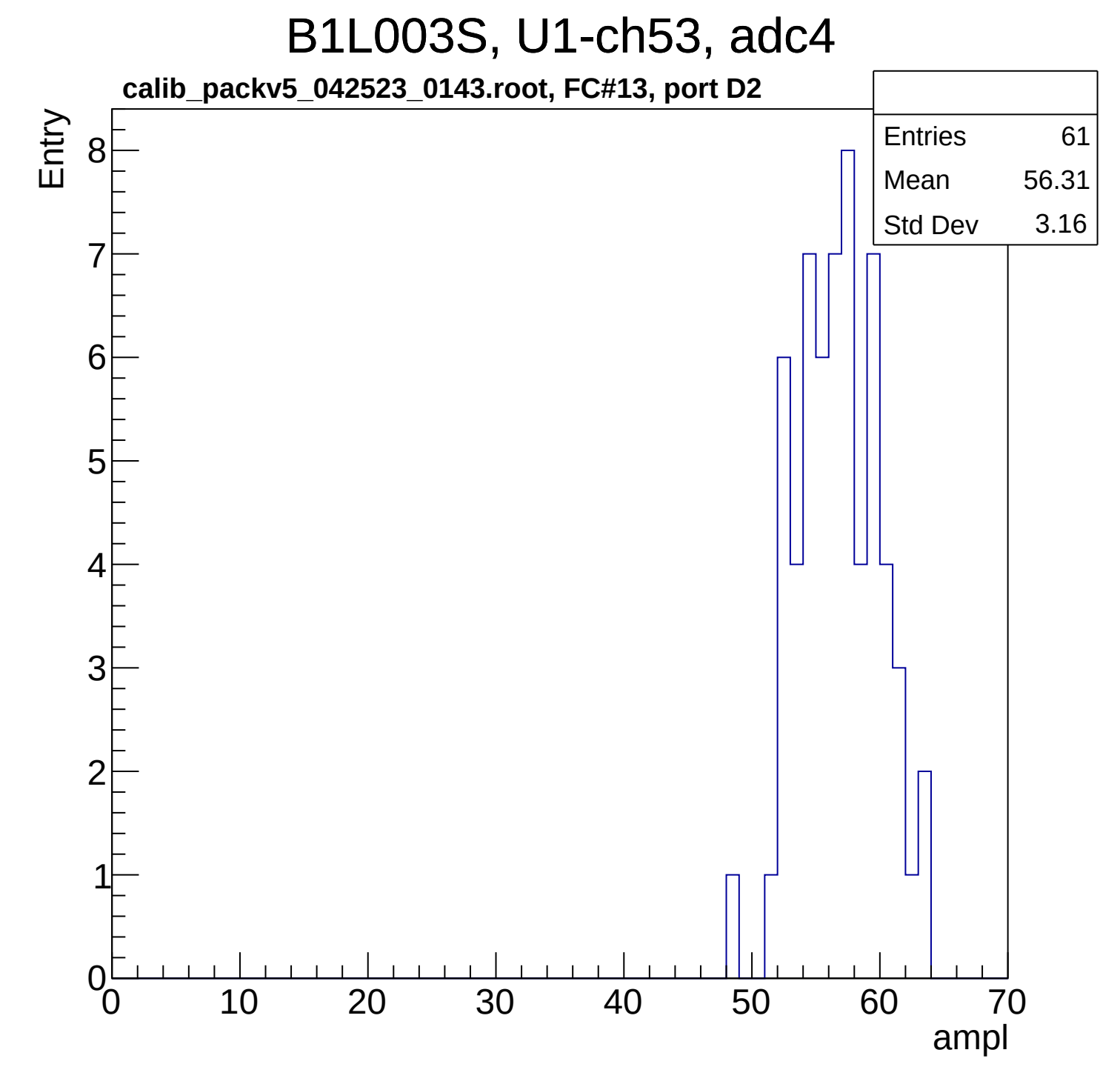
B1L003S, U1-ch53, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 56.31
Std Dev 3.16
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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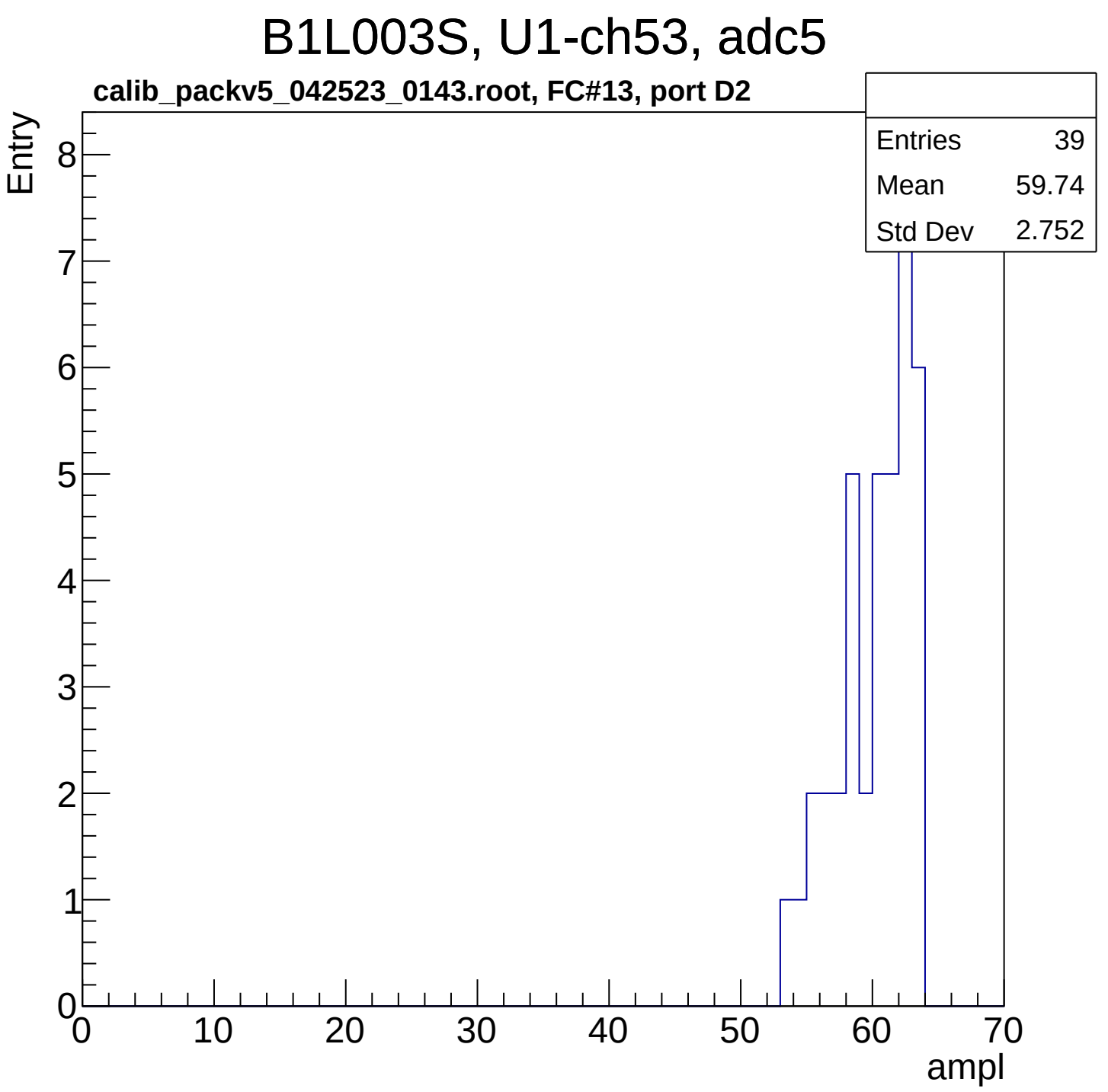
B1L003S, U1-ch53, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 59.74
Std Dev 2.752
Entry
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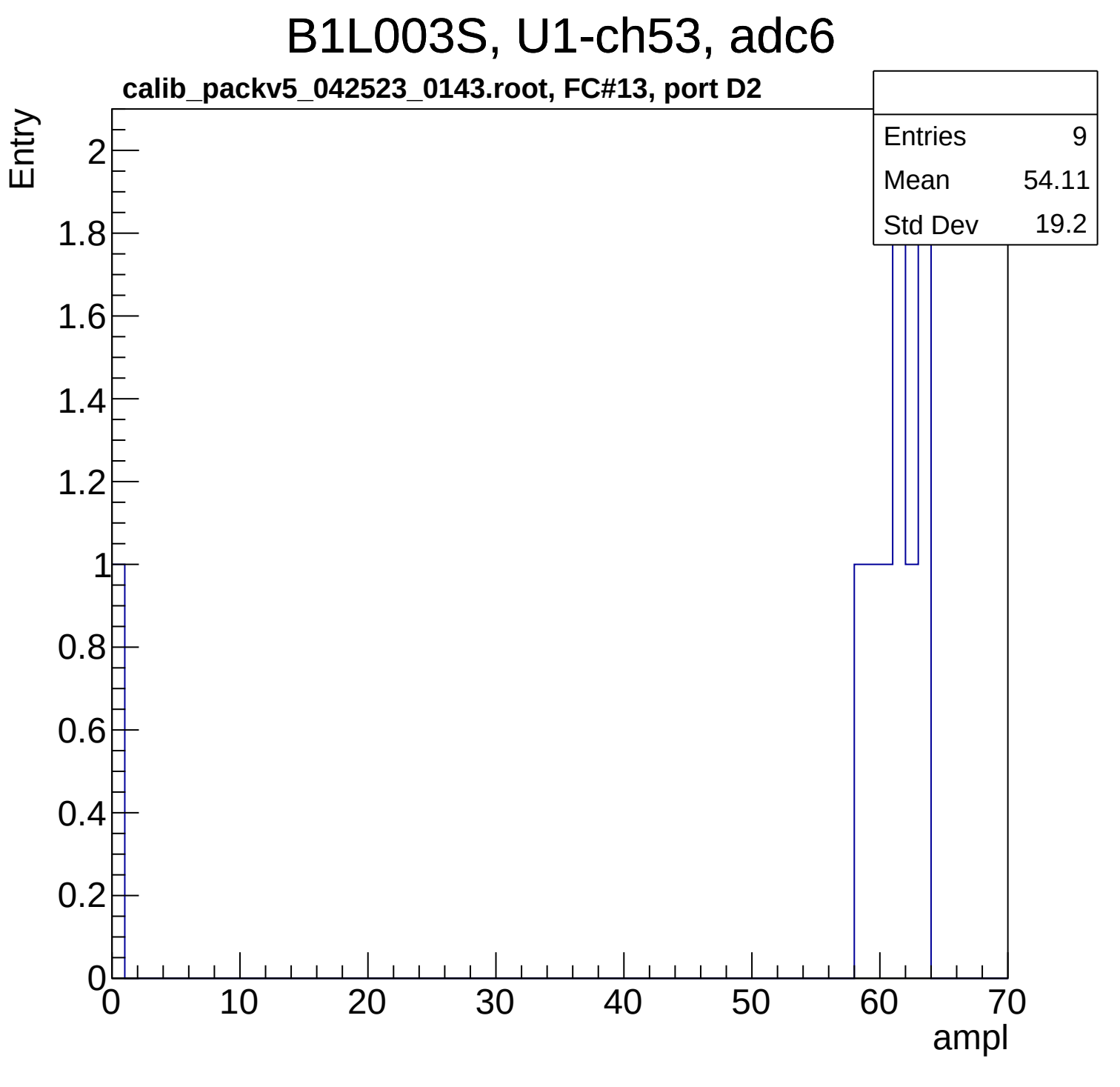
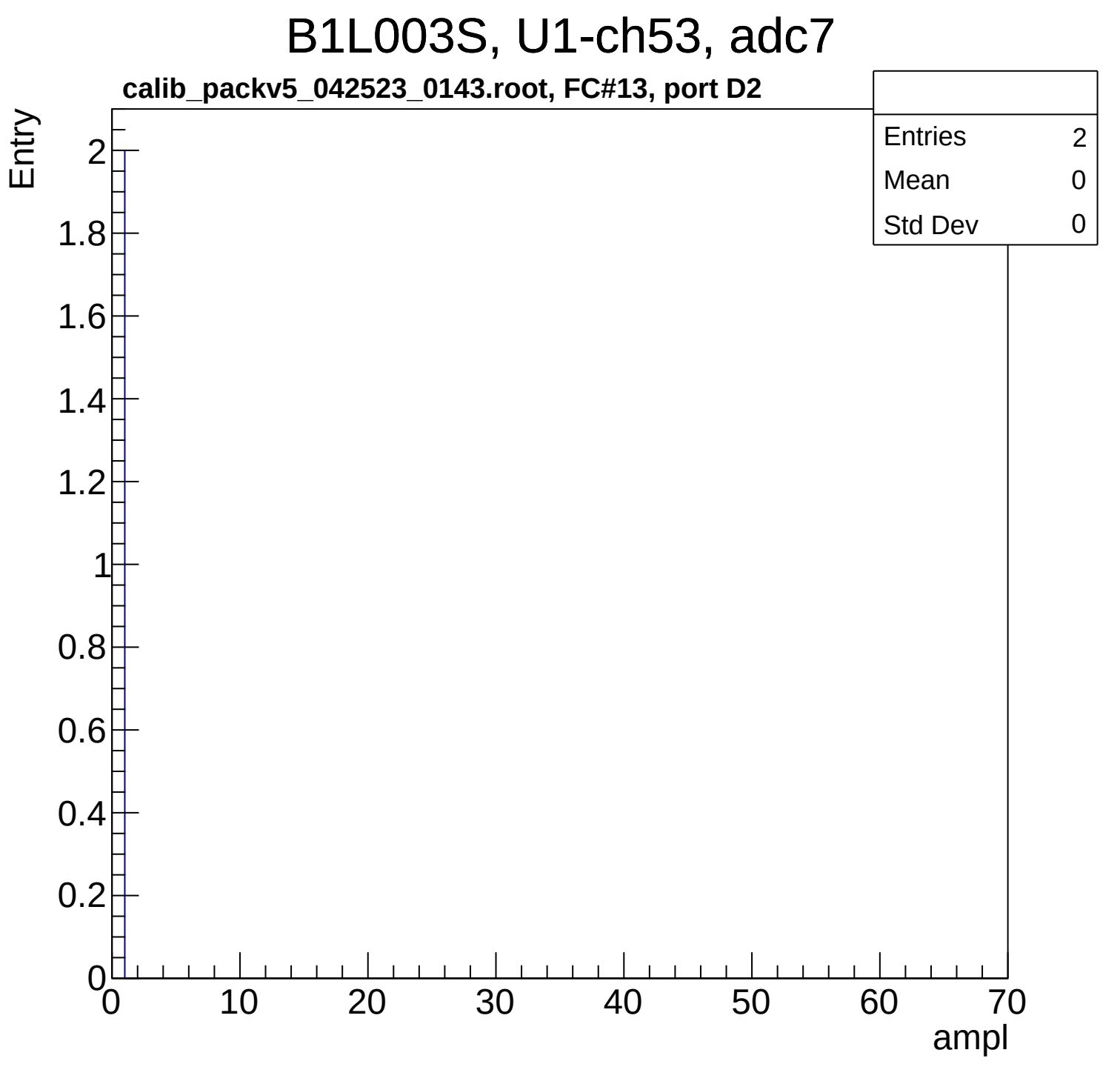
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch53, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 54.11
Std Dev 19.2
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch53, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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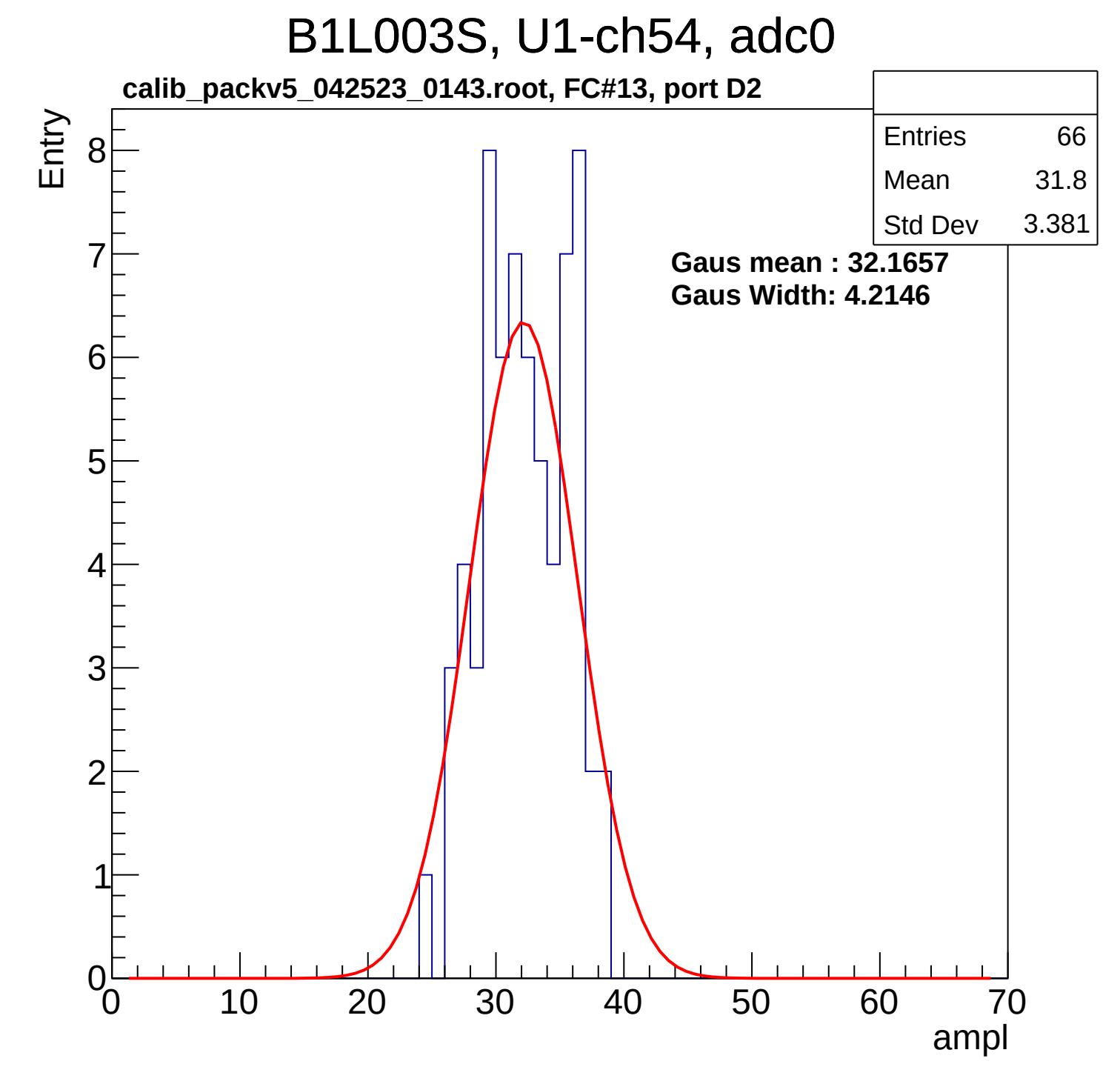
B1L003S, U1-ch54, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 31.8
Std Dev 3.381
Gaus mean : 32.1657
Gaus Width: 4.2146
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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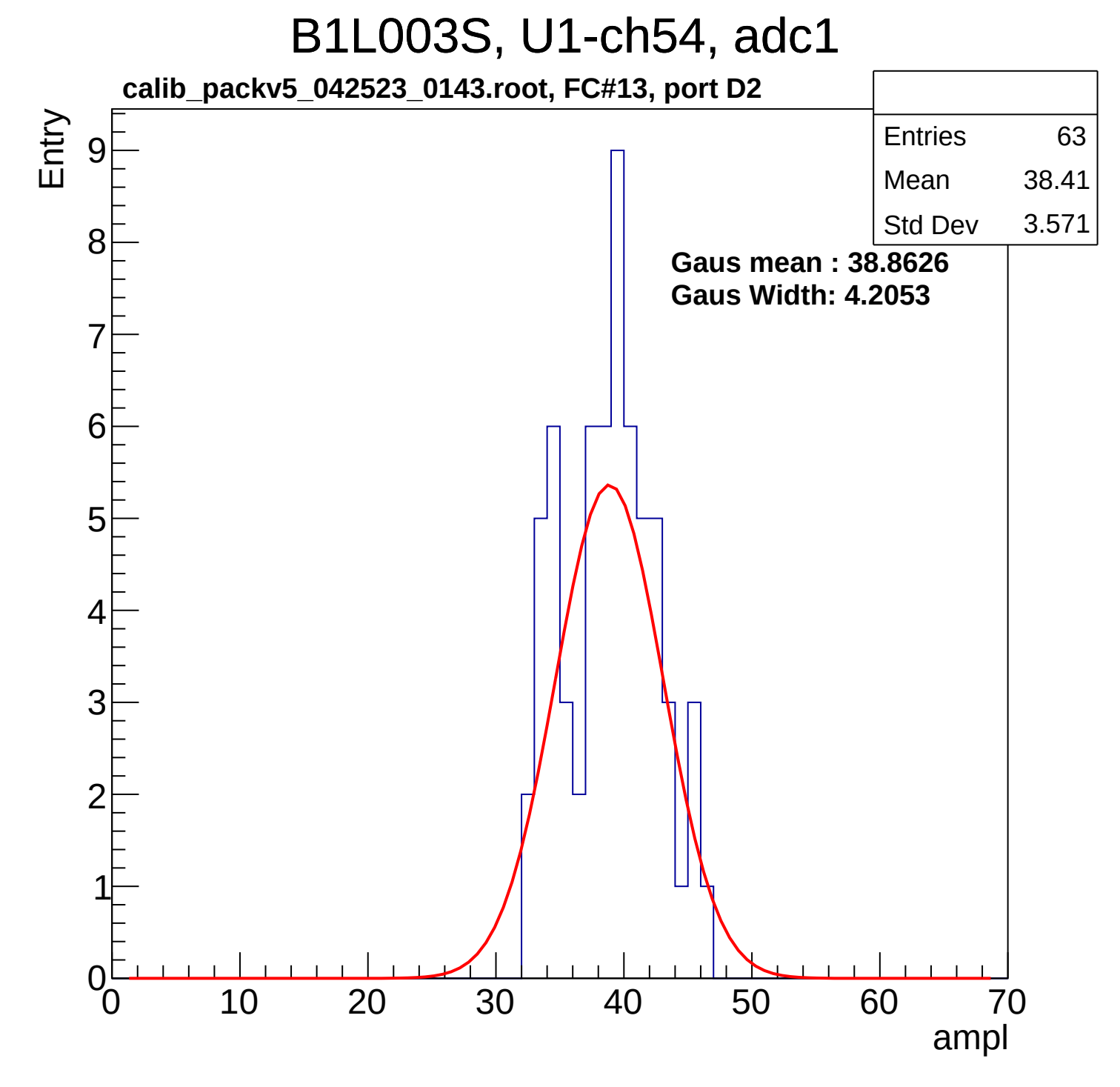
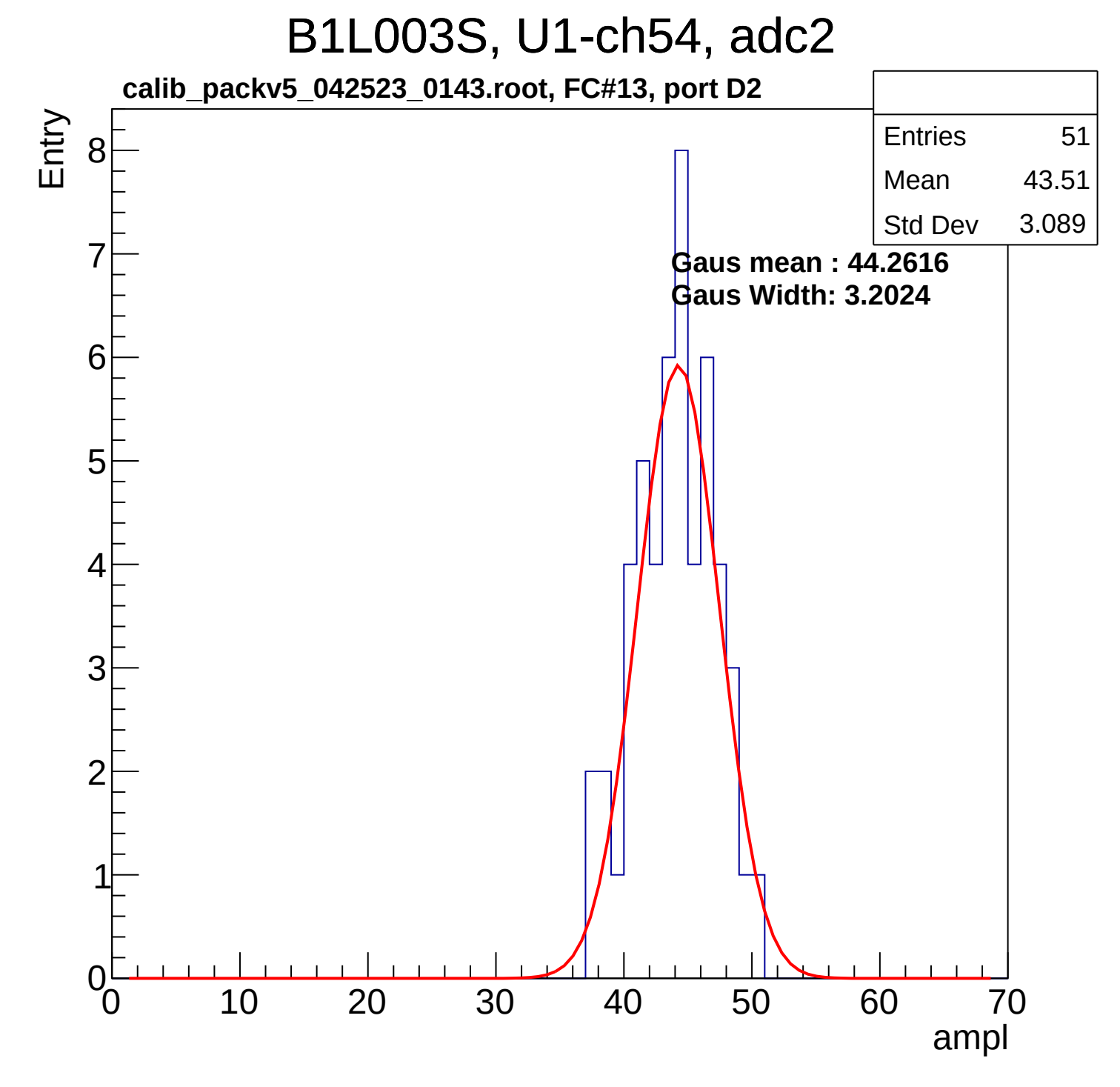
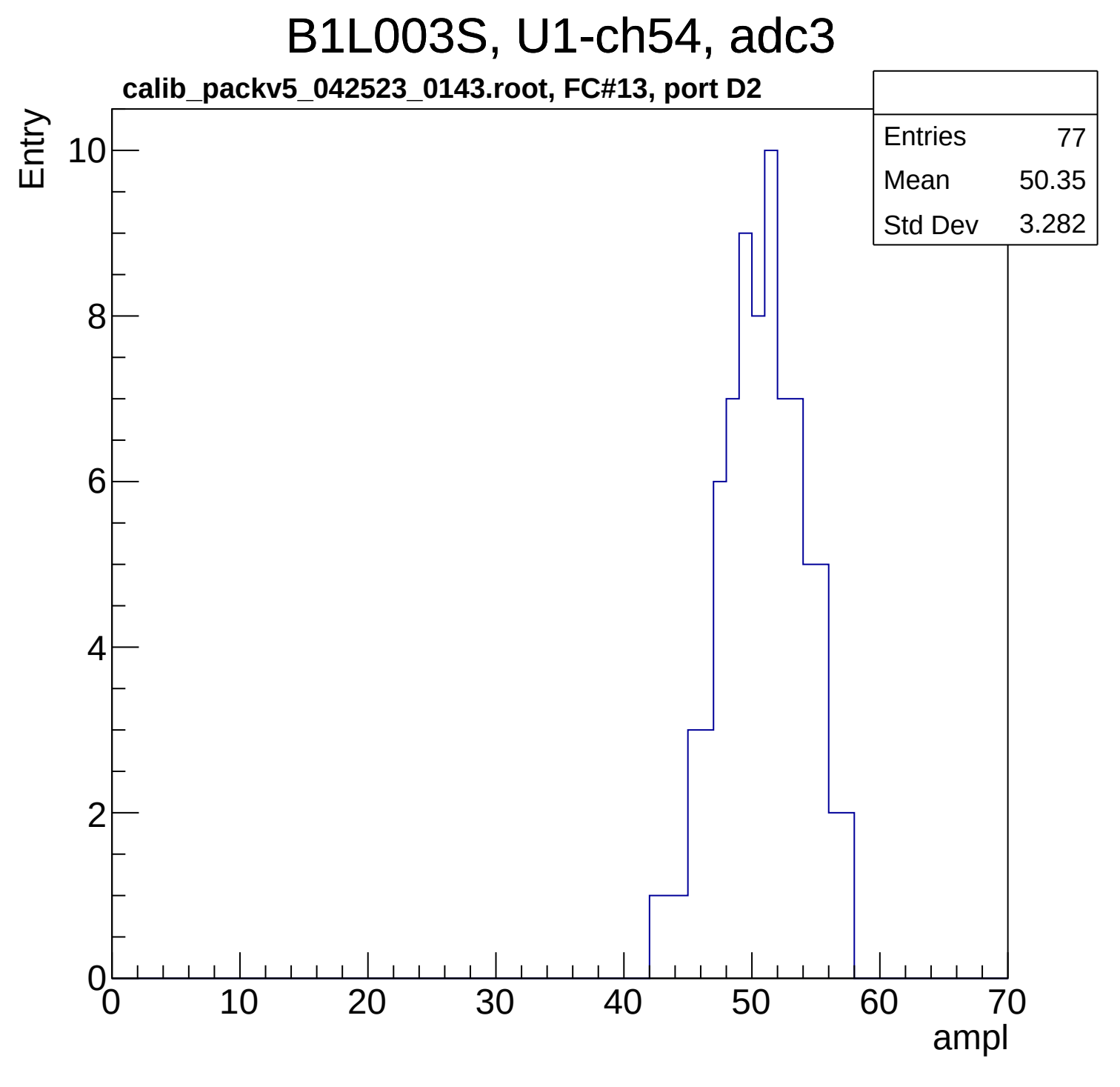
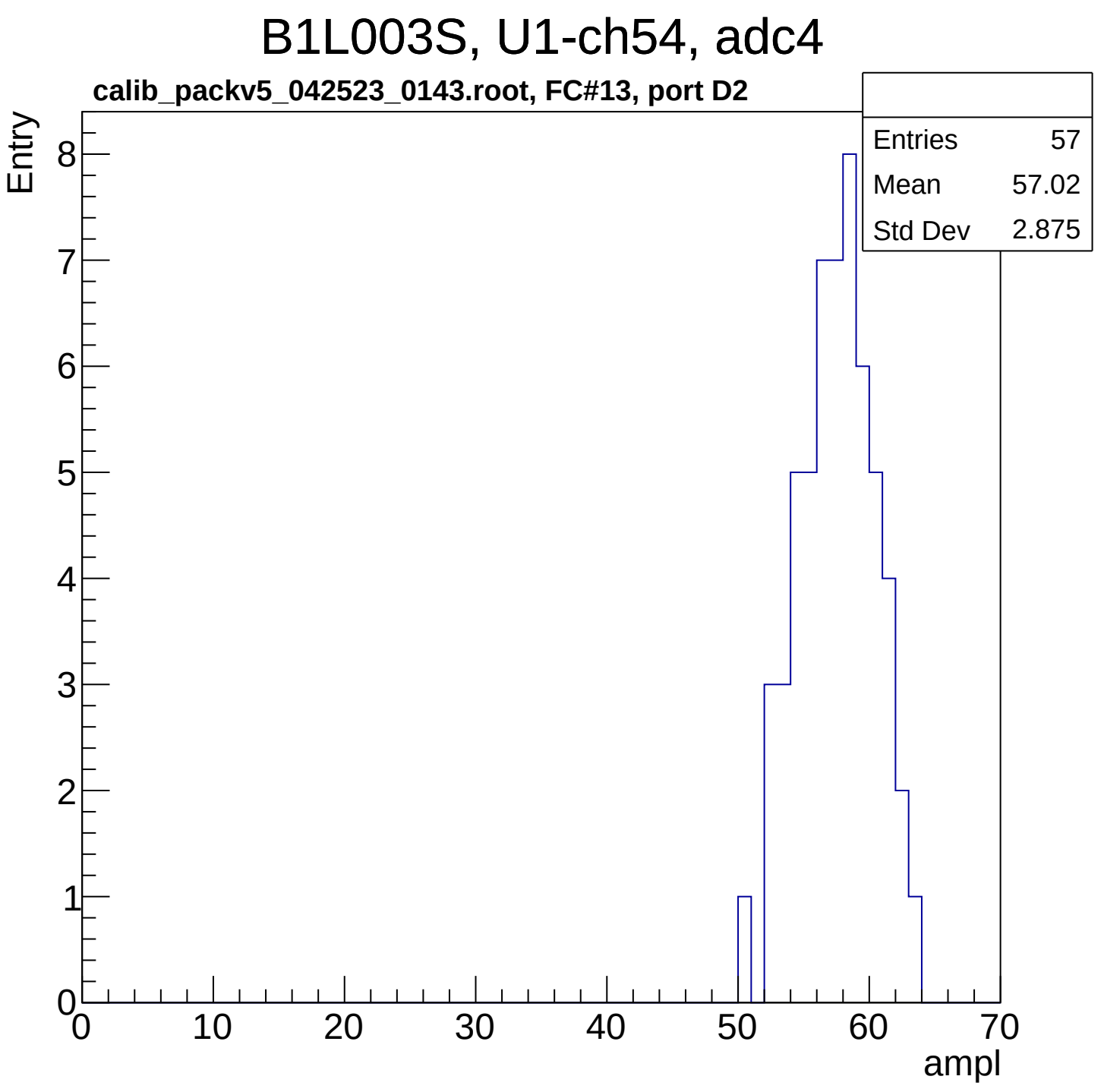

B1L003S, U1-ch54, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 38.41
Std Dev 3.571
Gaus mean : 38.8626
Gaus Width: 4.2053
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch54, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 43.51
Std Dev 3.089
Gaus mean : 44.2616
Gaus Width: 3.2024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch54, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 50.35
Std Dev 3.282
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch54, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.02
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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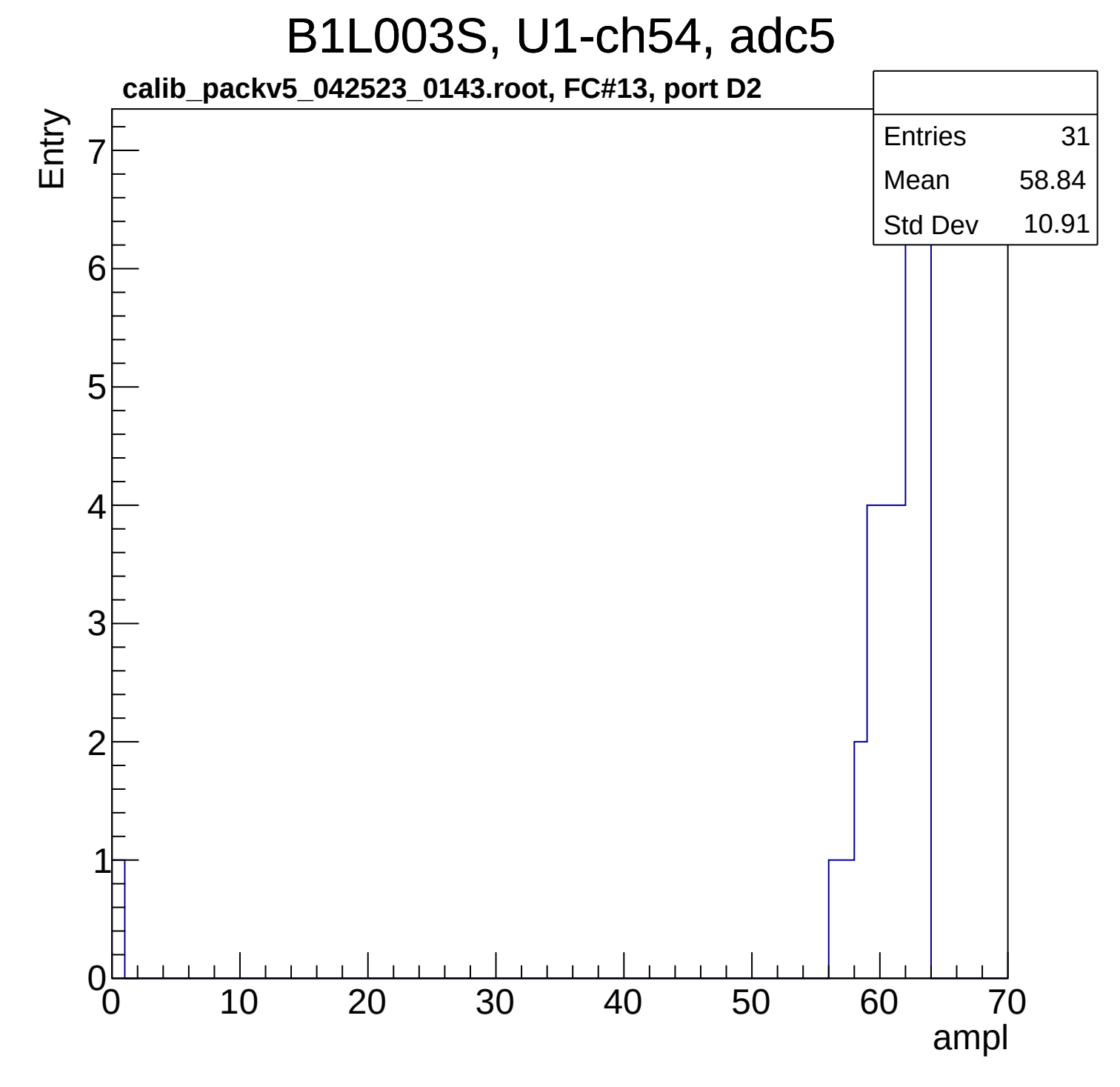
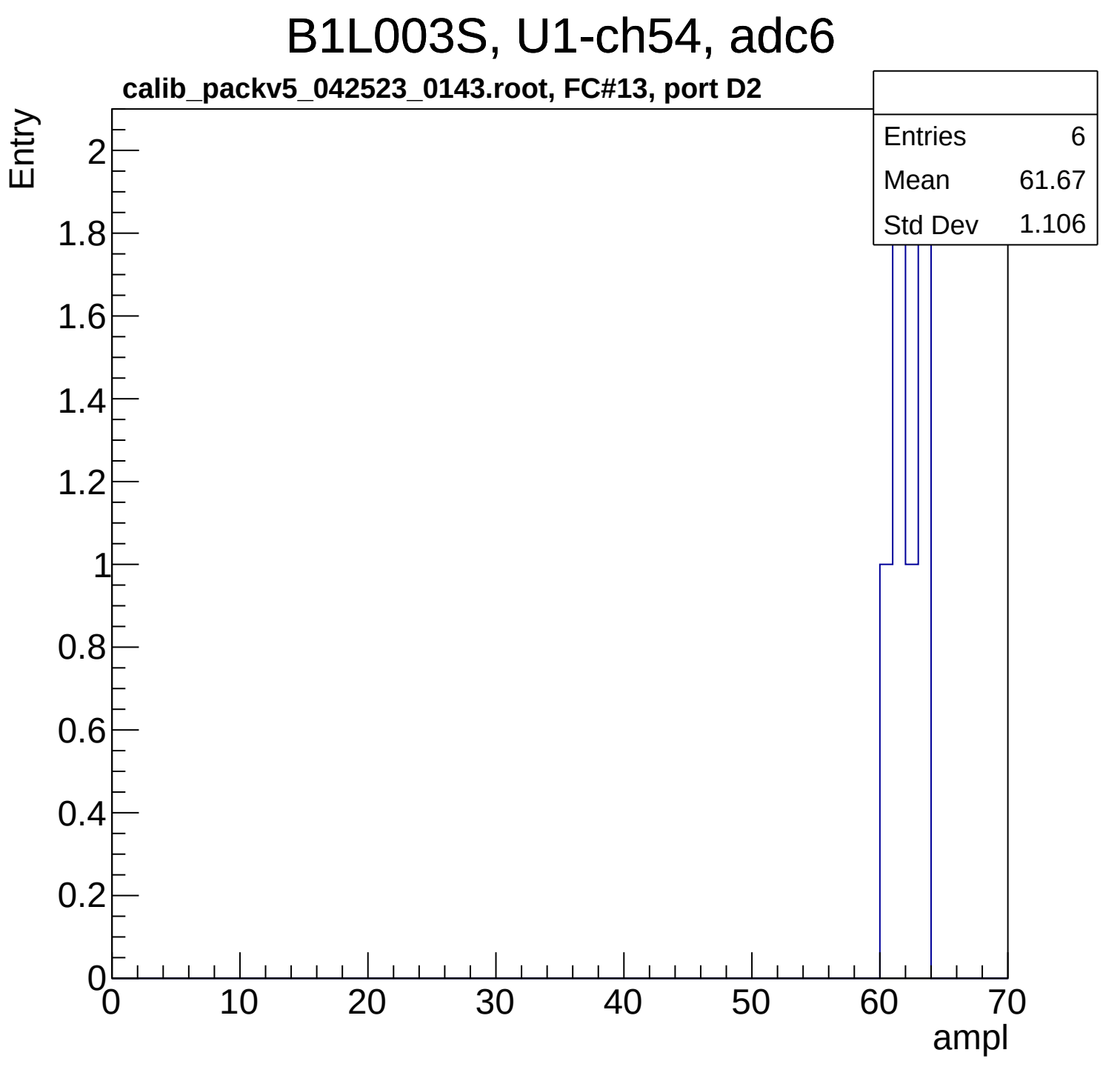
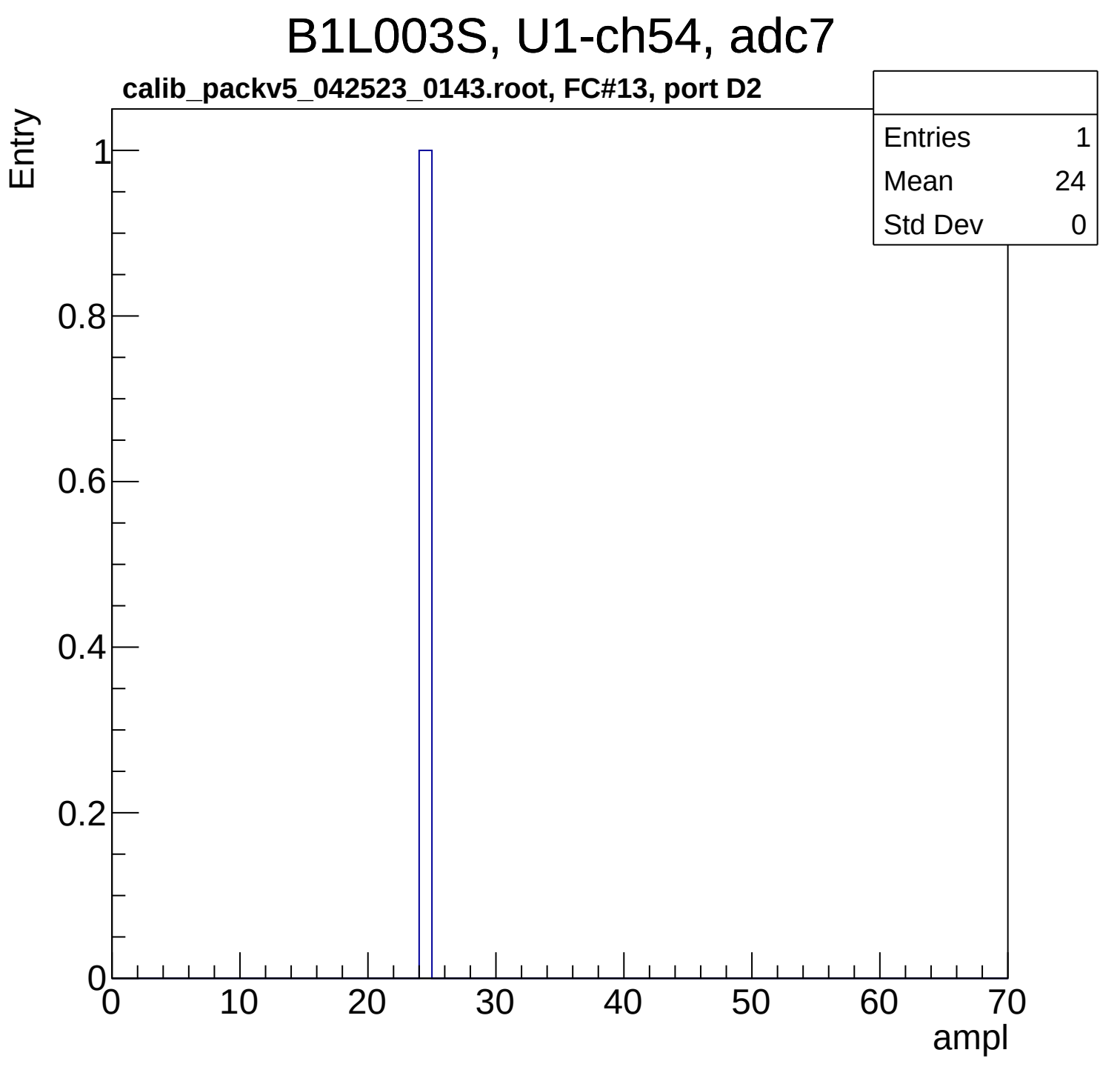
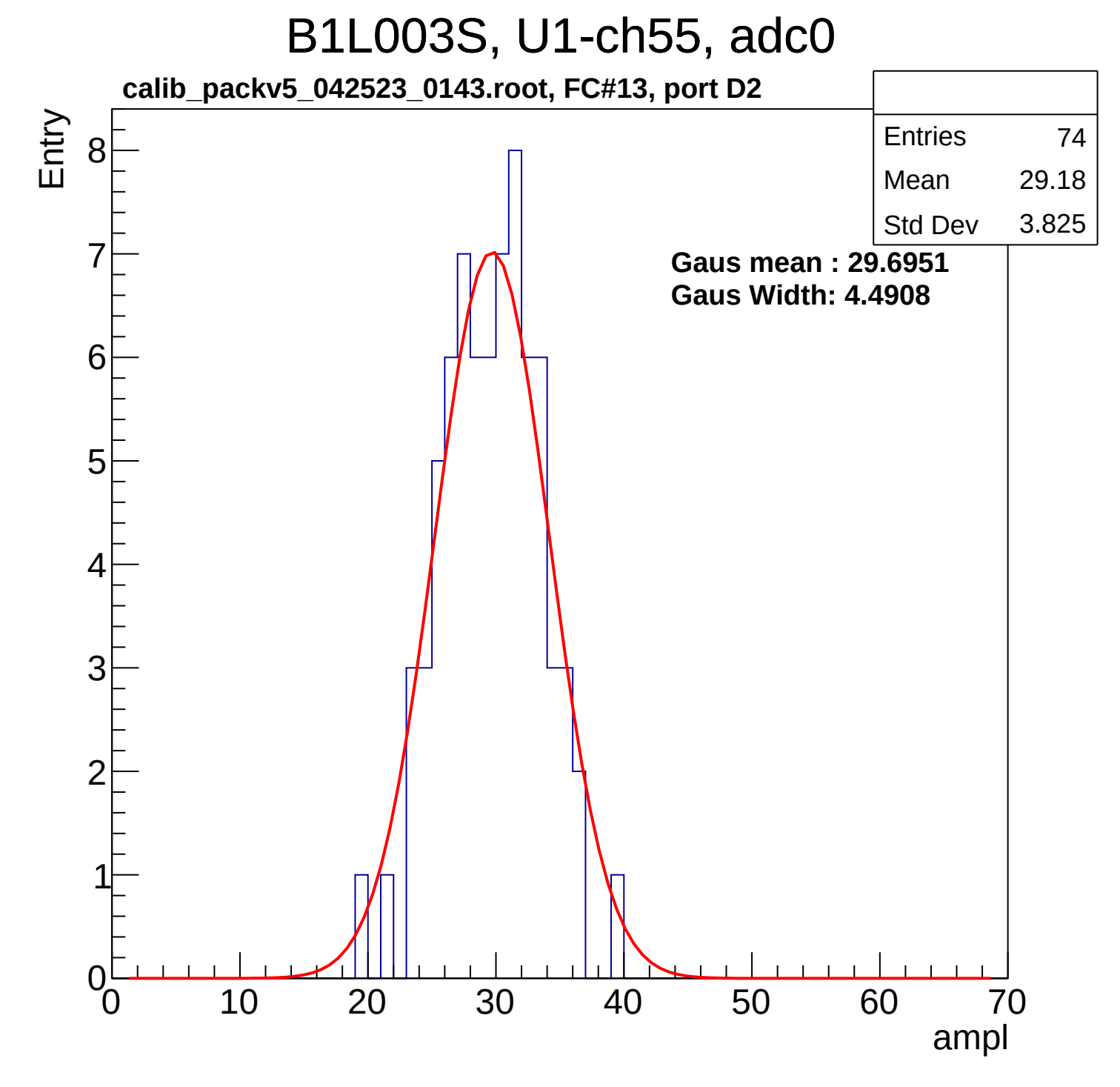
40
50
60
70


B1L003S, U1-ch54, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 58.84
Std Dev 10.91
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch54, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61.67
Std Dev 1.106
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch54, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch55, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.18
Std Dev 3.825
Gaus mean : 29.6951
Gaus Width: 4.4908
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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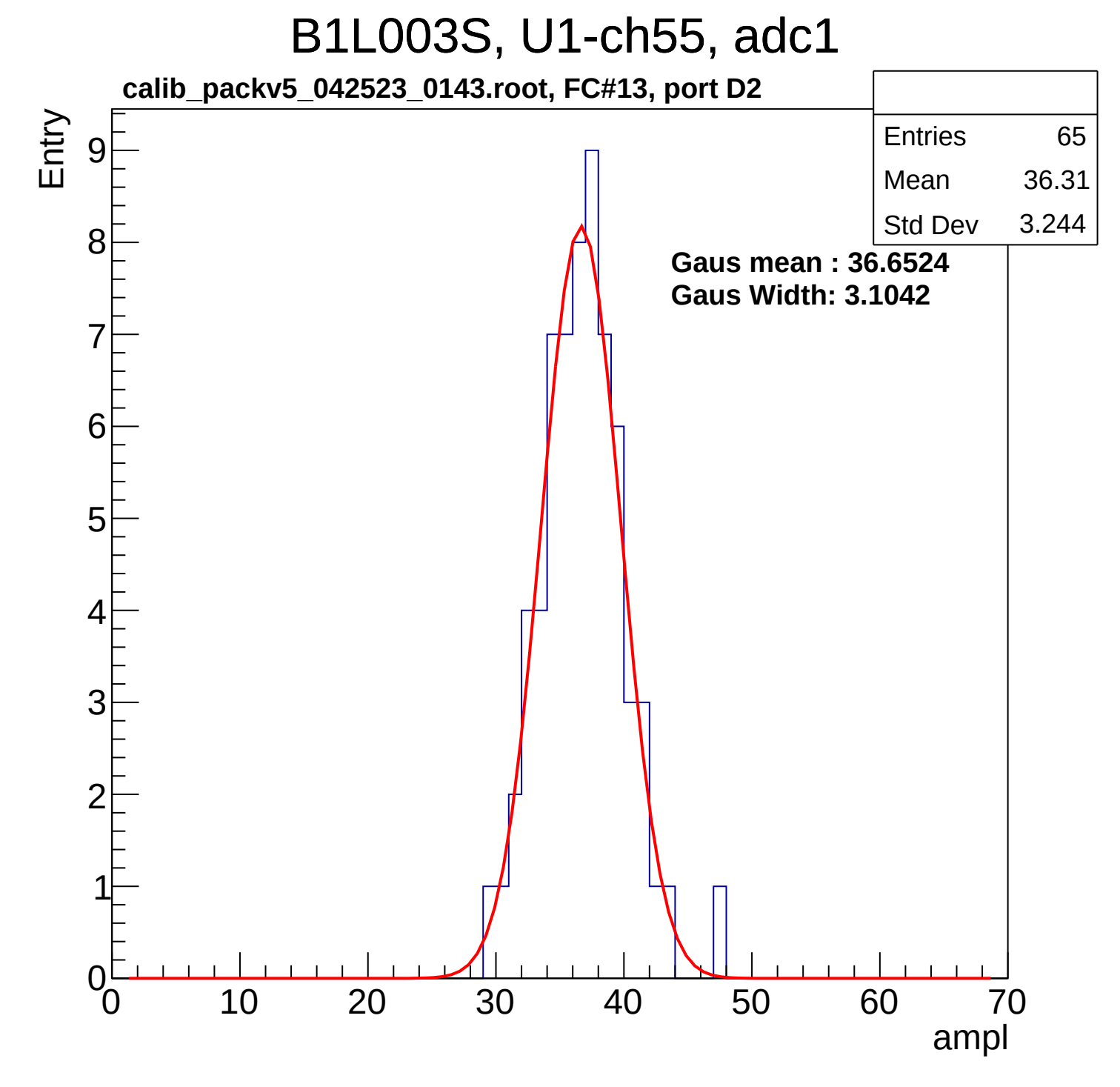
40
50
60
70


B1L003S, U1-ch55, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 36.31
Std Dev 3.244
Gaus mean : 36.6524
Gaus Width: 3.1042
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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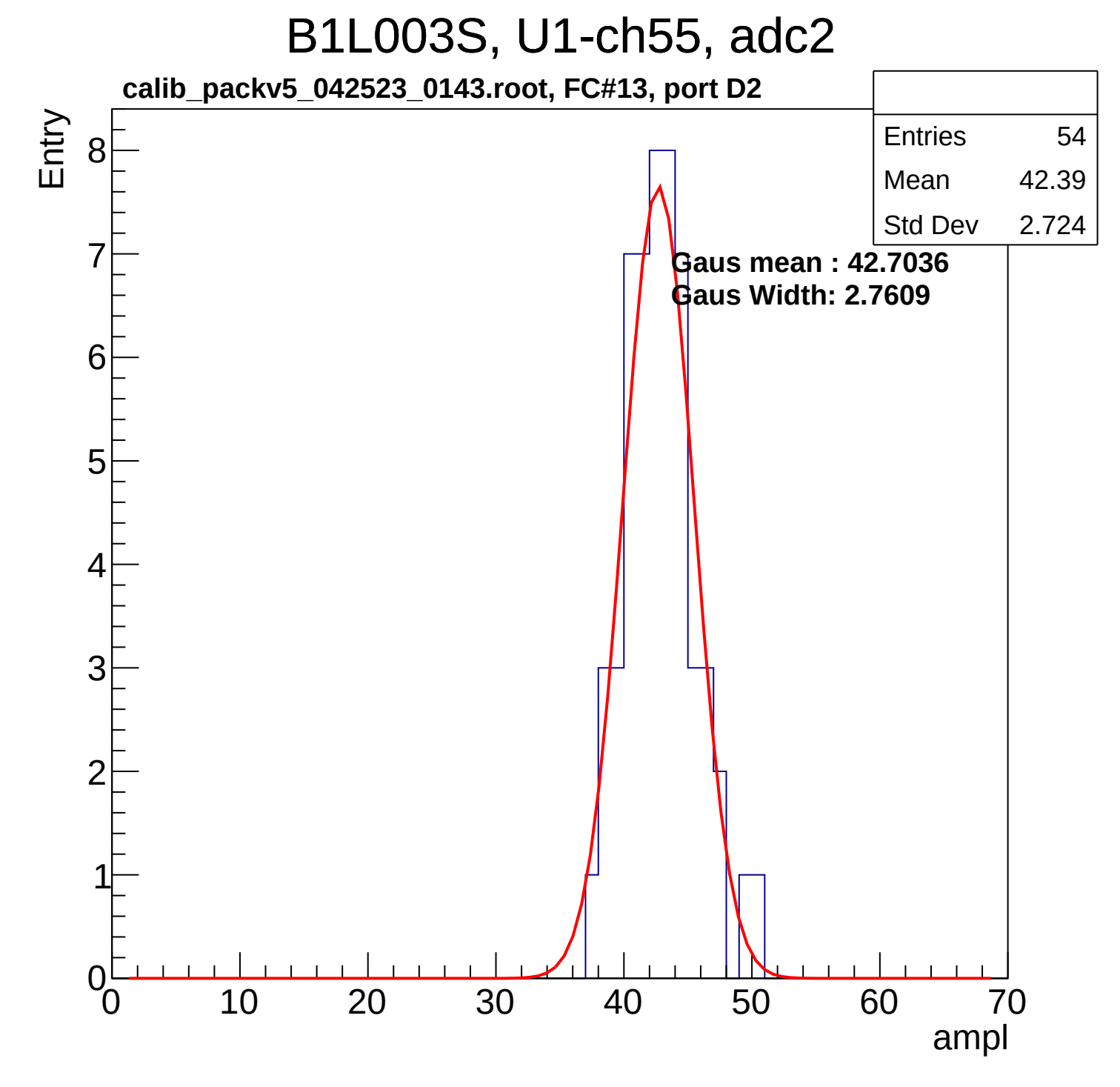
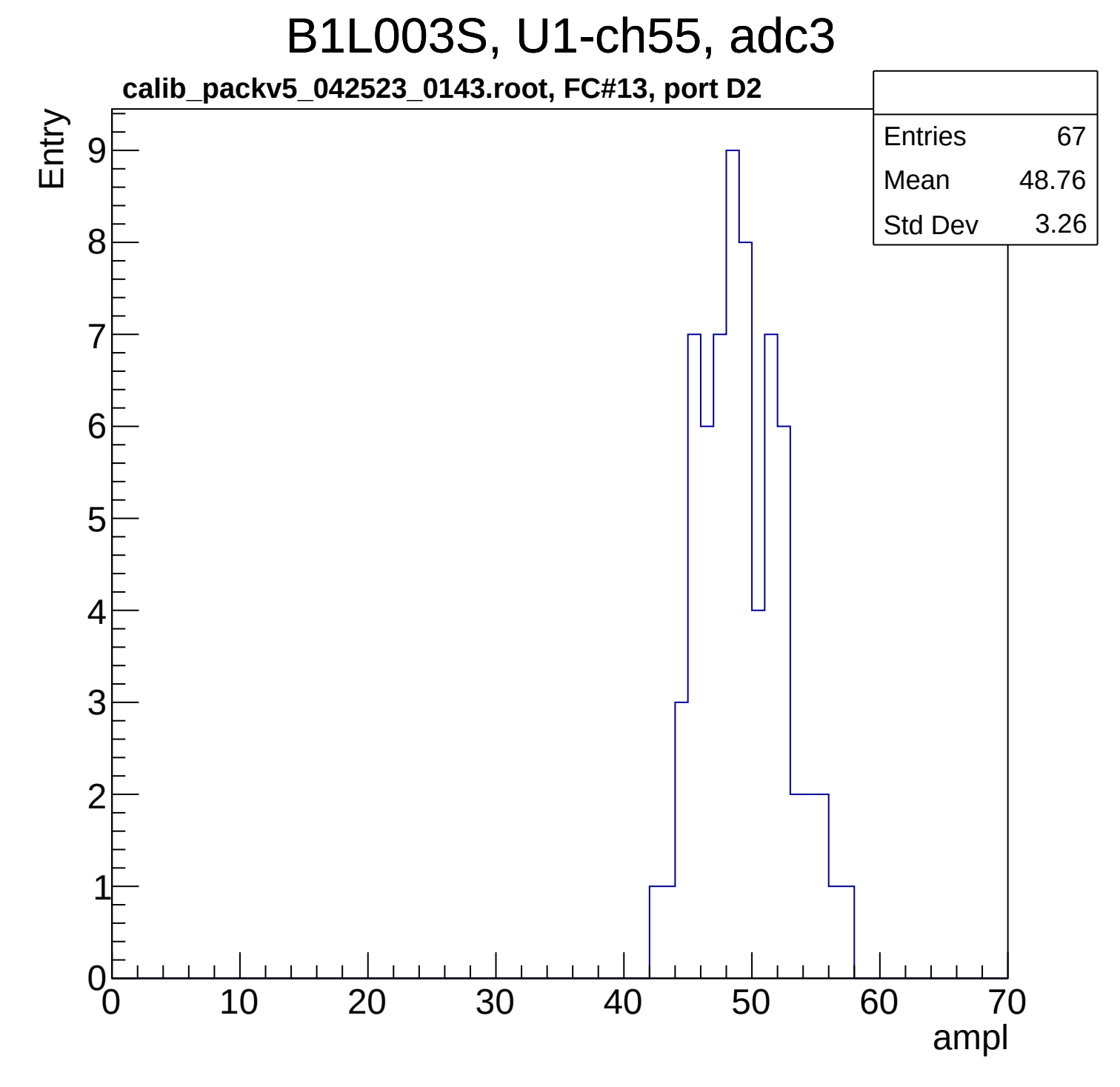
50
60
70


B1L003S, U1-ch55, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.39
Std Dev 2.724
Gaus mean : 42.7036
Gaus Width: 2.7609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch55, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.76
Std Dev 3.26
Entry
ampl
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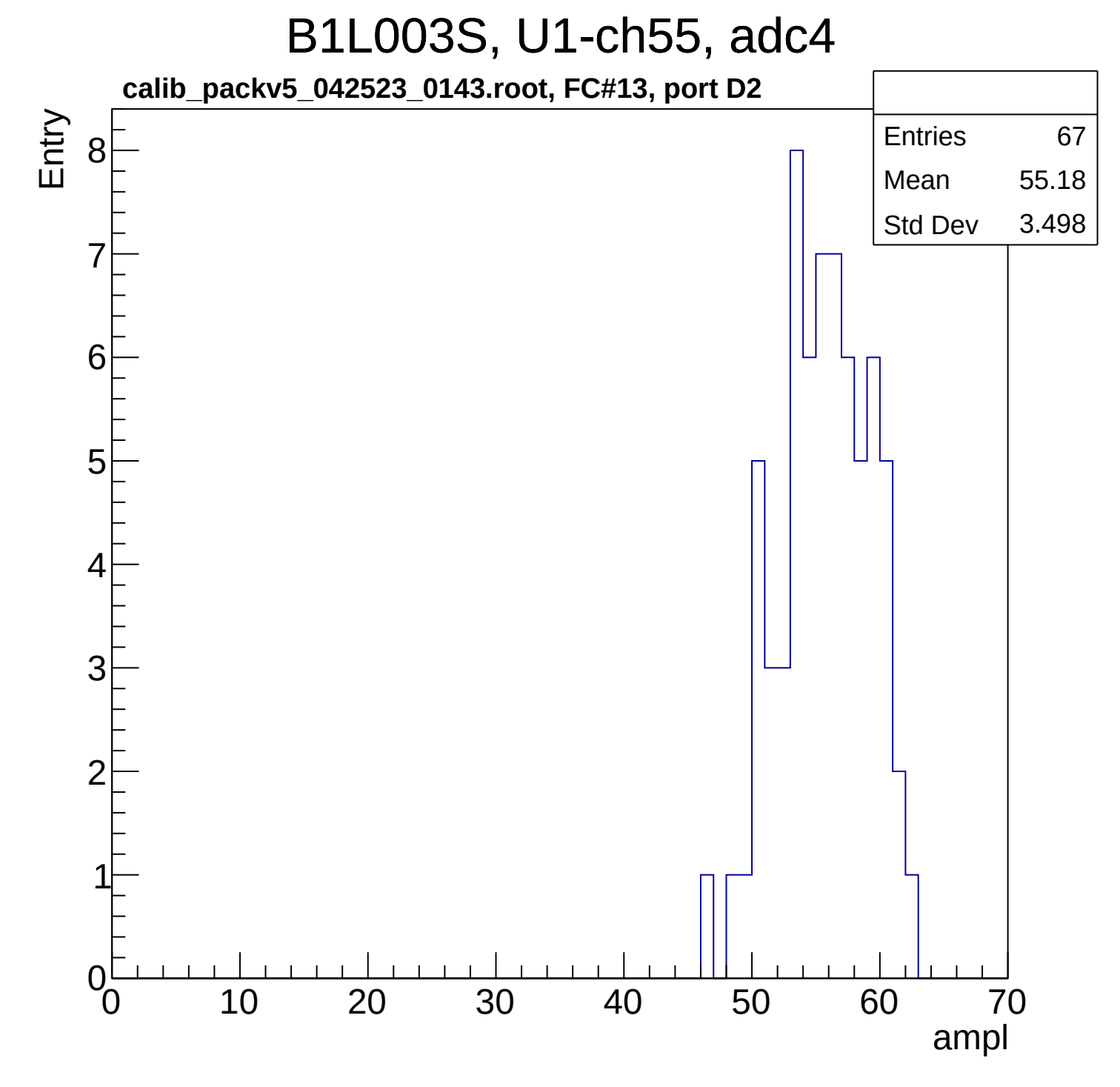
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch55, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 55.18
Std Dev 3.498
Entry
ampl
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2
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4
5
6
7
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0
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60
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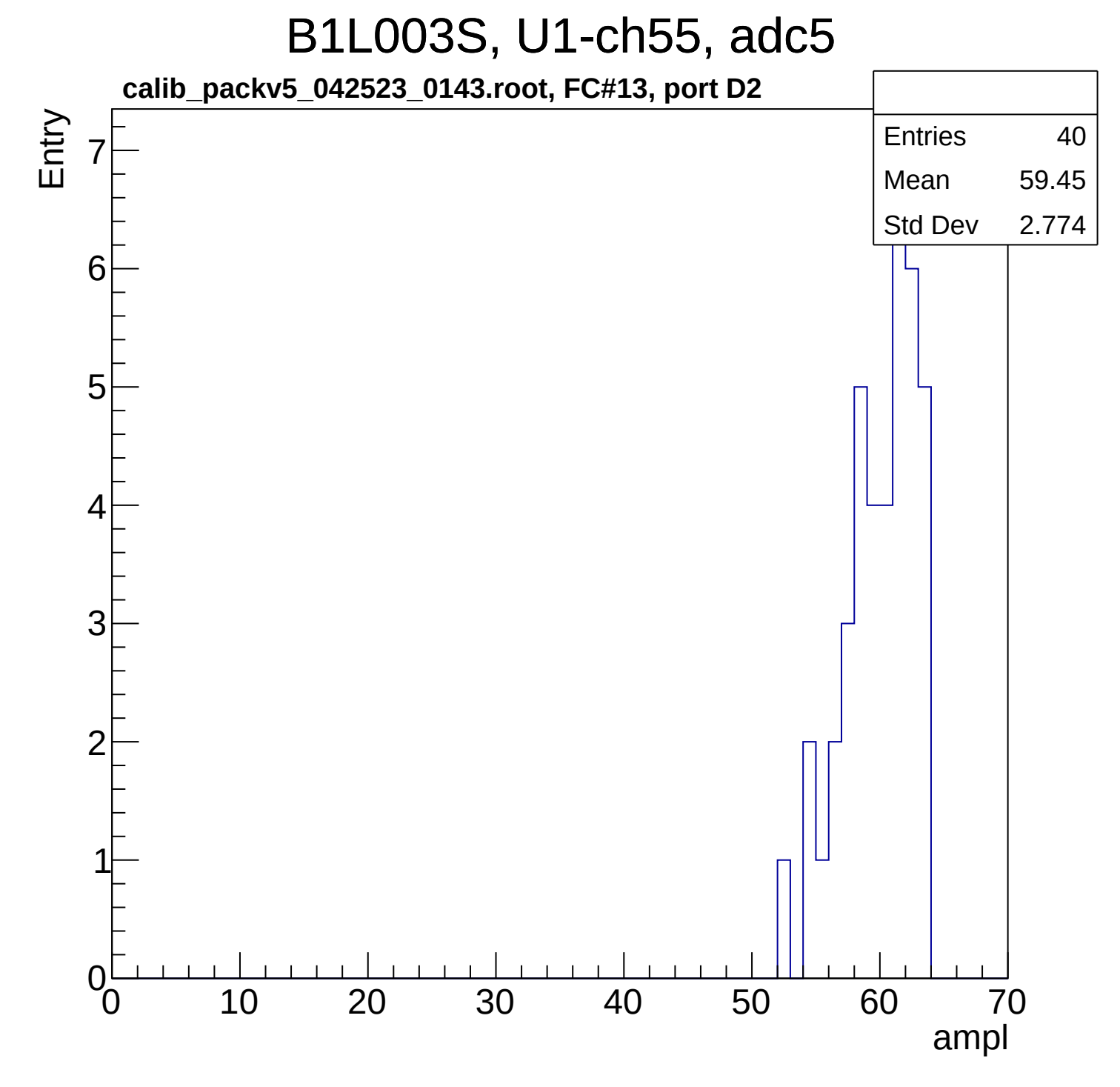
B1L003S, U1-ch55, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 40
Mean 59.45
Std Dev 2.774
Entry
ampl
0
1
2
3
4
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6
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0
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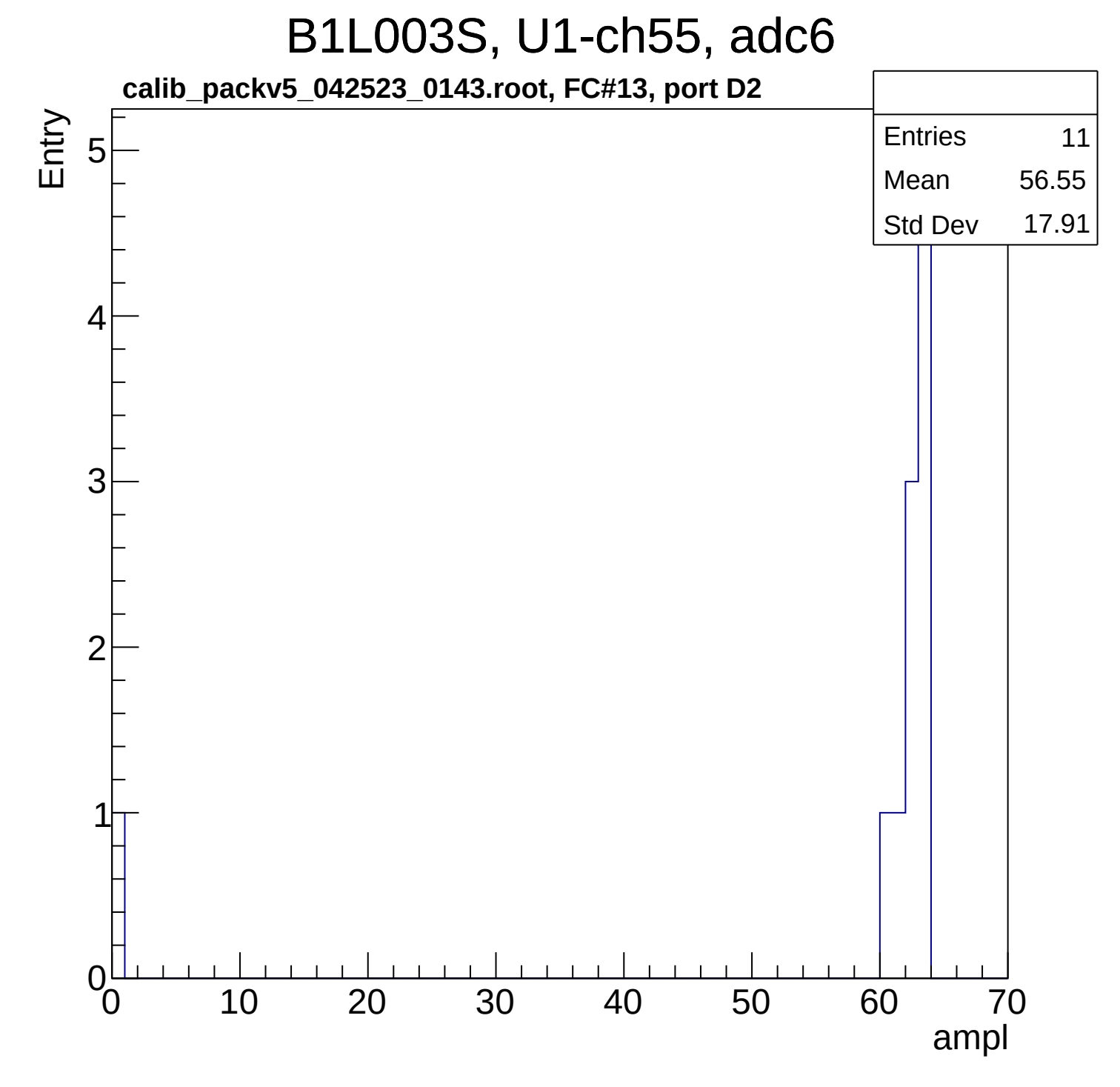
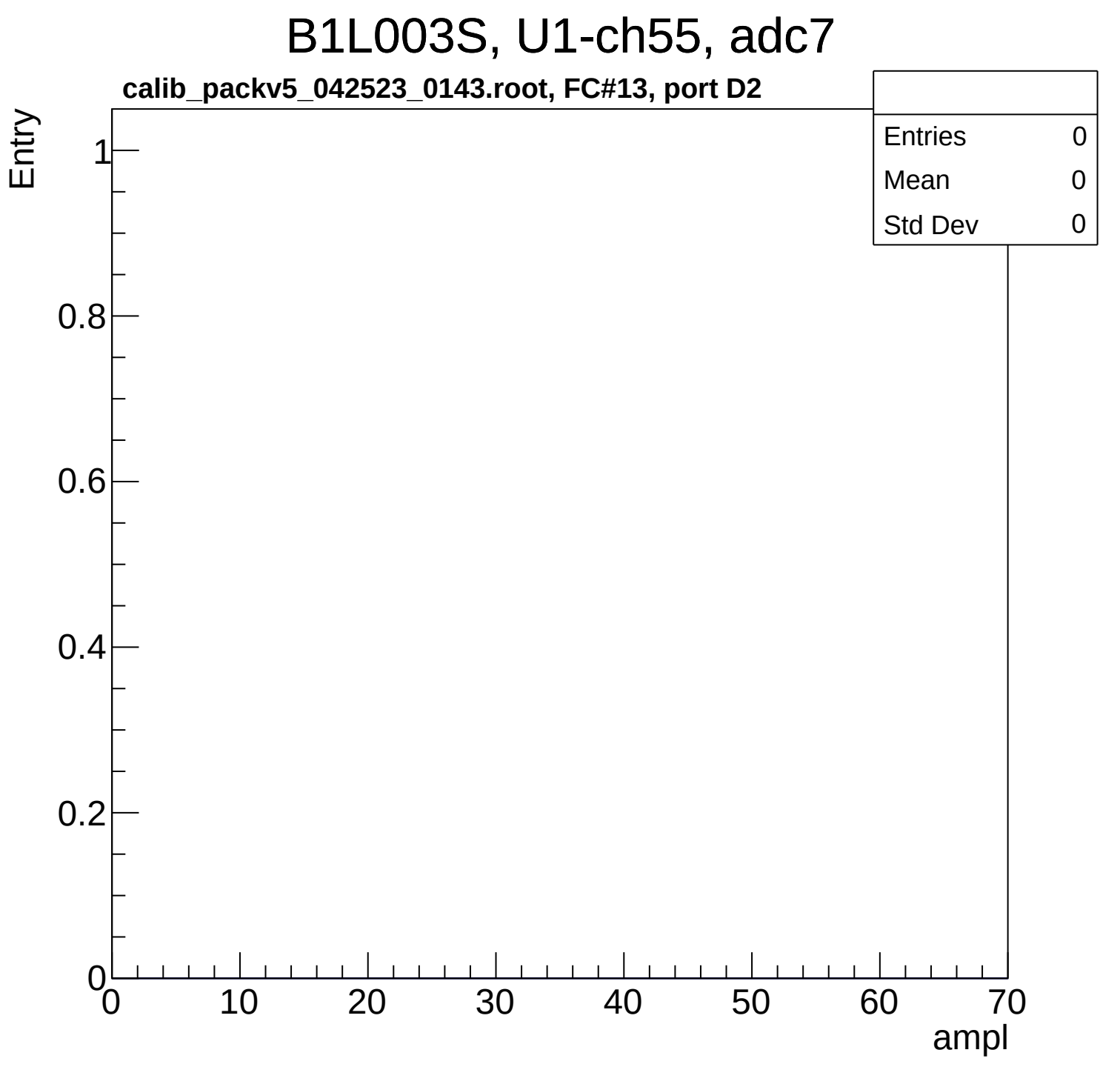
40
50
60
70


B1L003S, U1-ch55, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.55
Std Dev 17.91
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch55, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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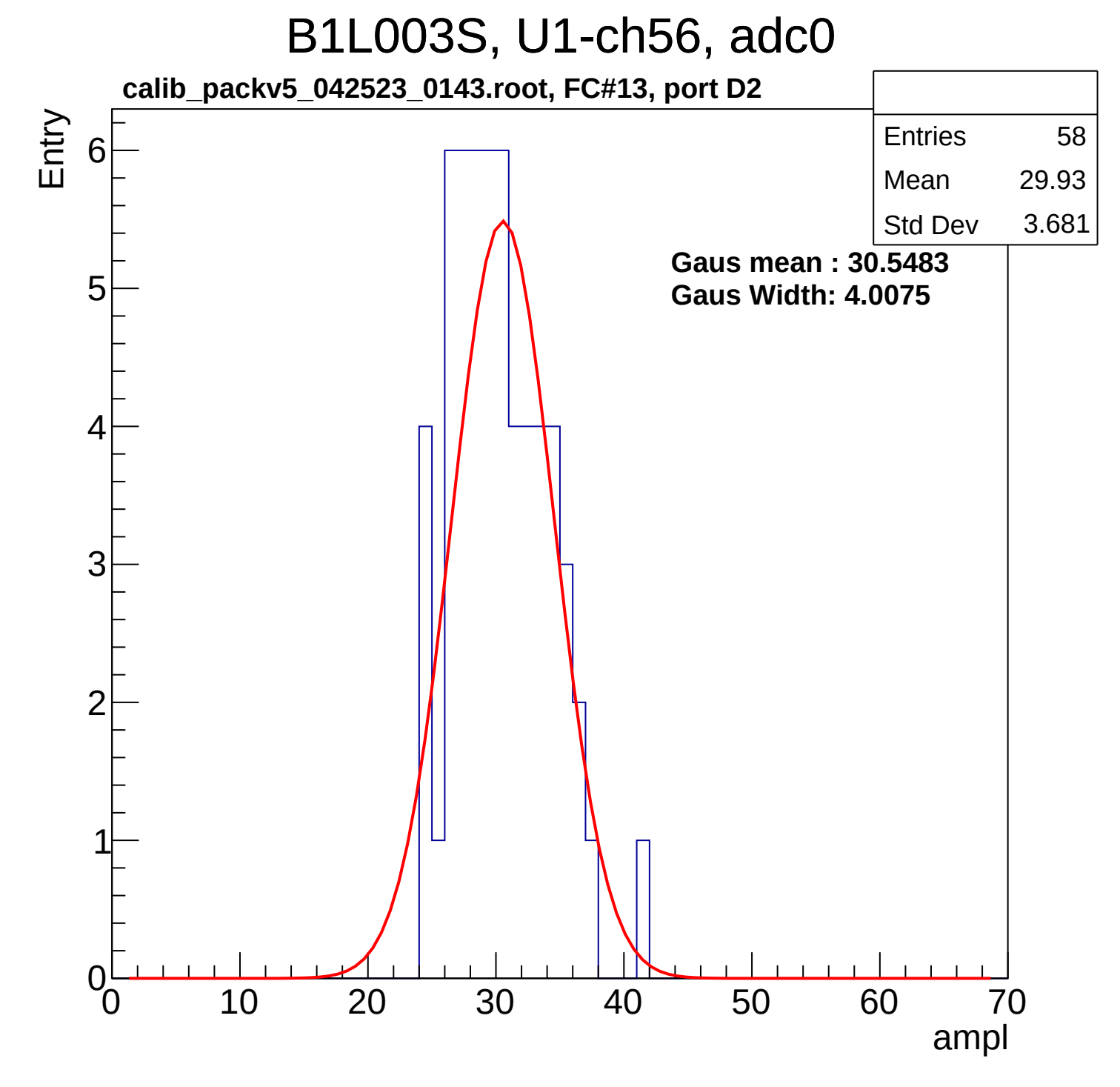
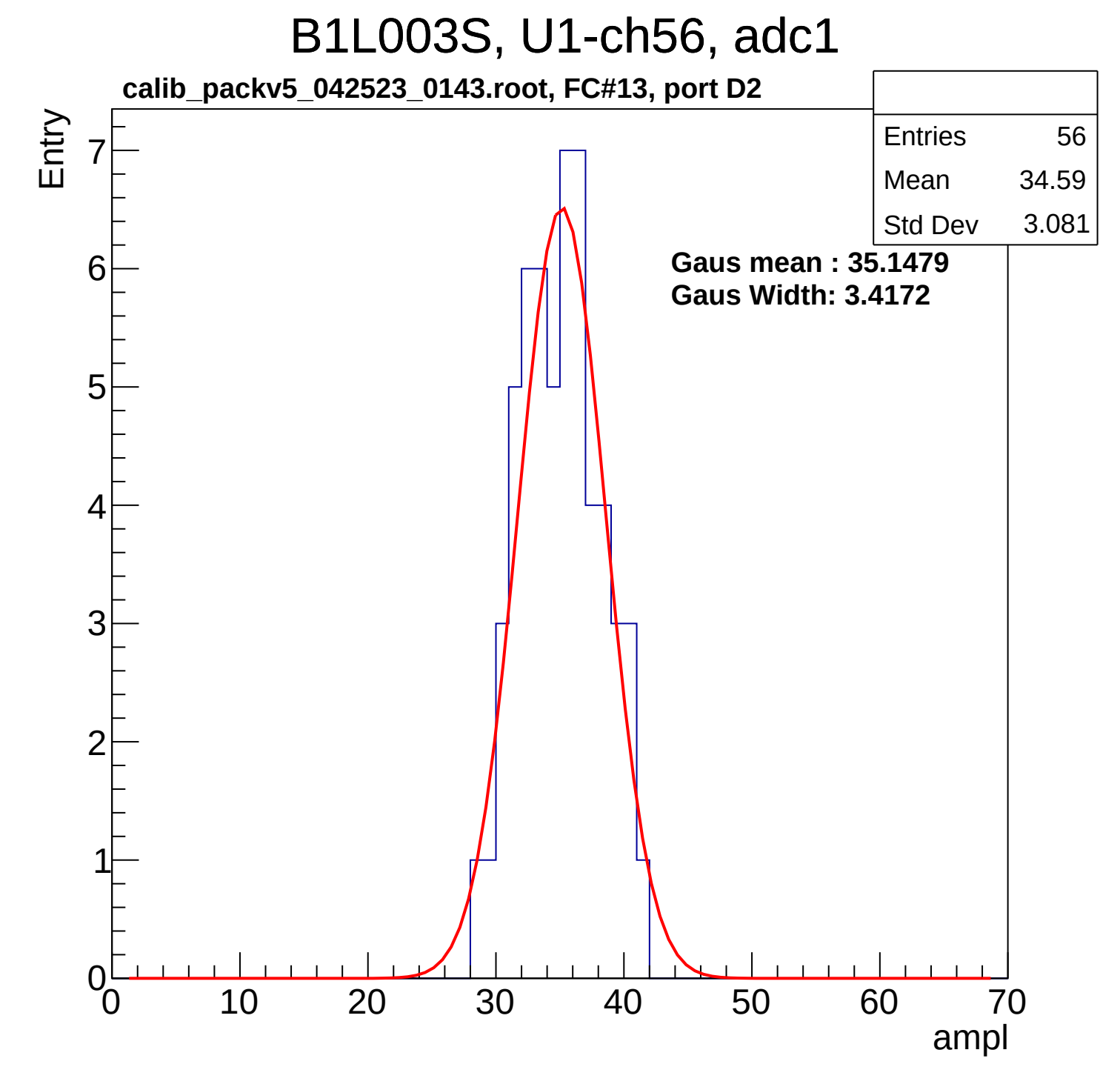
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch56, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.93
Std Dev 3.681
Gaus mean : 30.5483
Gaus Width: 4.0075
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch56, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 34.59
Std Dev 3.081
Gaus mean : 35.1479
Gaus Width: 3.4172
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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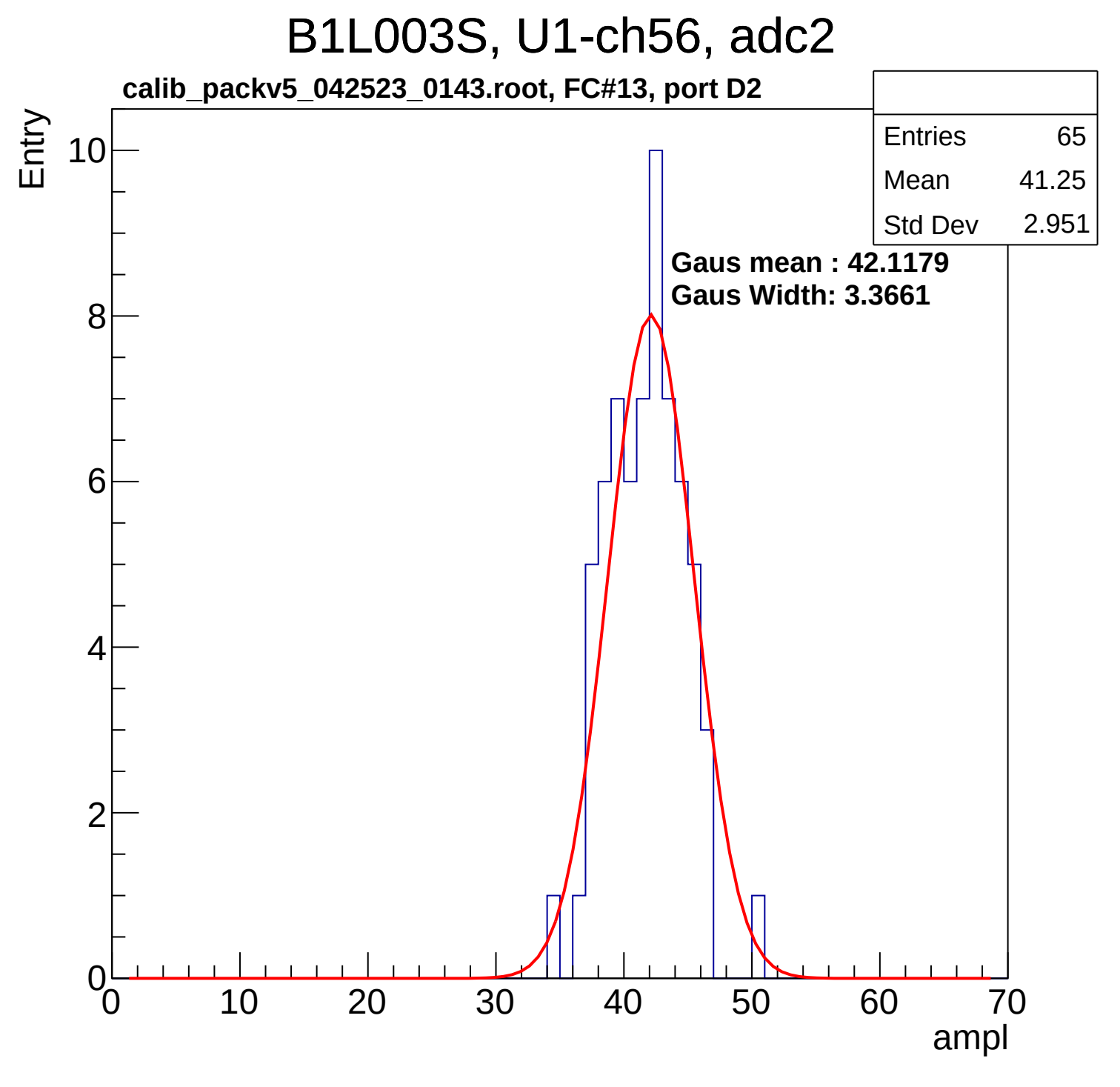
B1L003S, U1-ch56, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.25
Std Dev 2.951
Gaus mean : 42.1179
Gaus Width: 3.3661
Entry
ampl
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4
6
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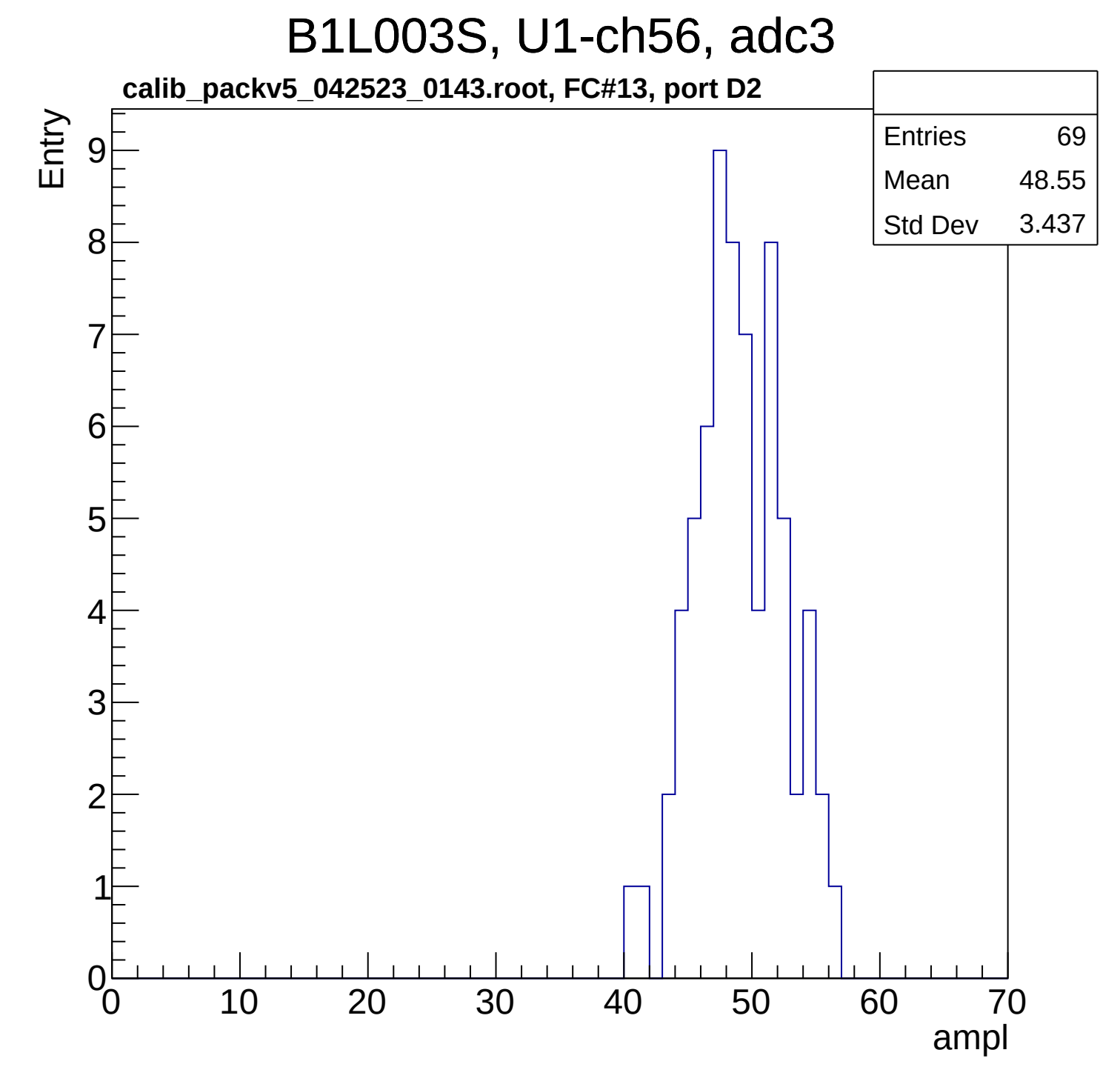
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch56, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 48.55
Std Dev 3.437
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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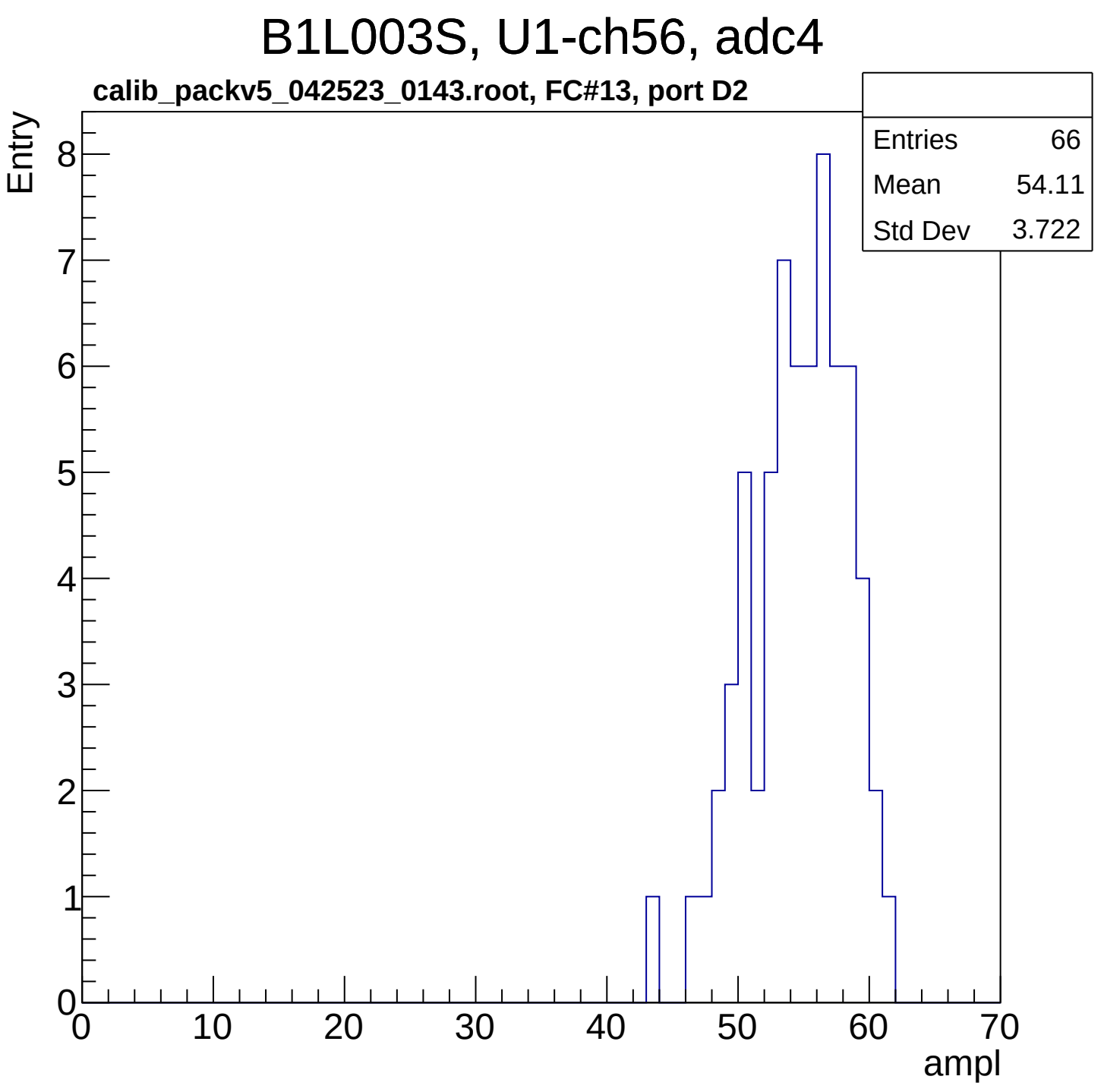
B1L003S, U1-ch56, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.11
Std Dev 3.722
Entry
ampl
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1
2
3
4
5
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7
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0
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60
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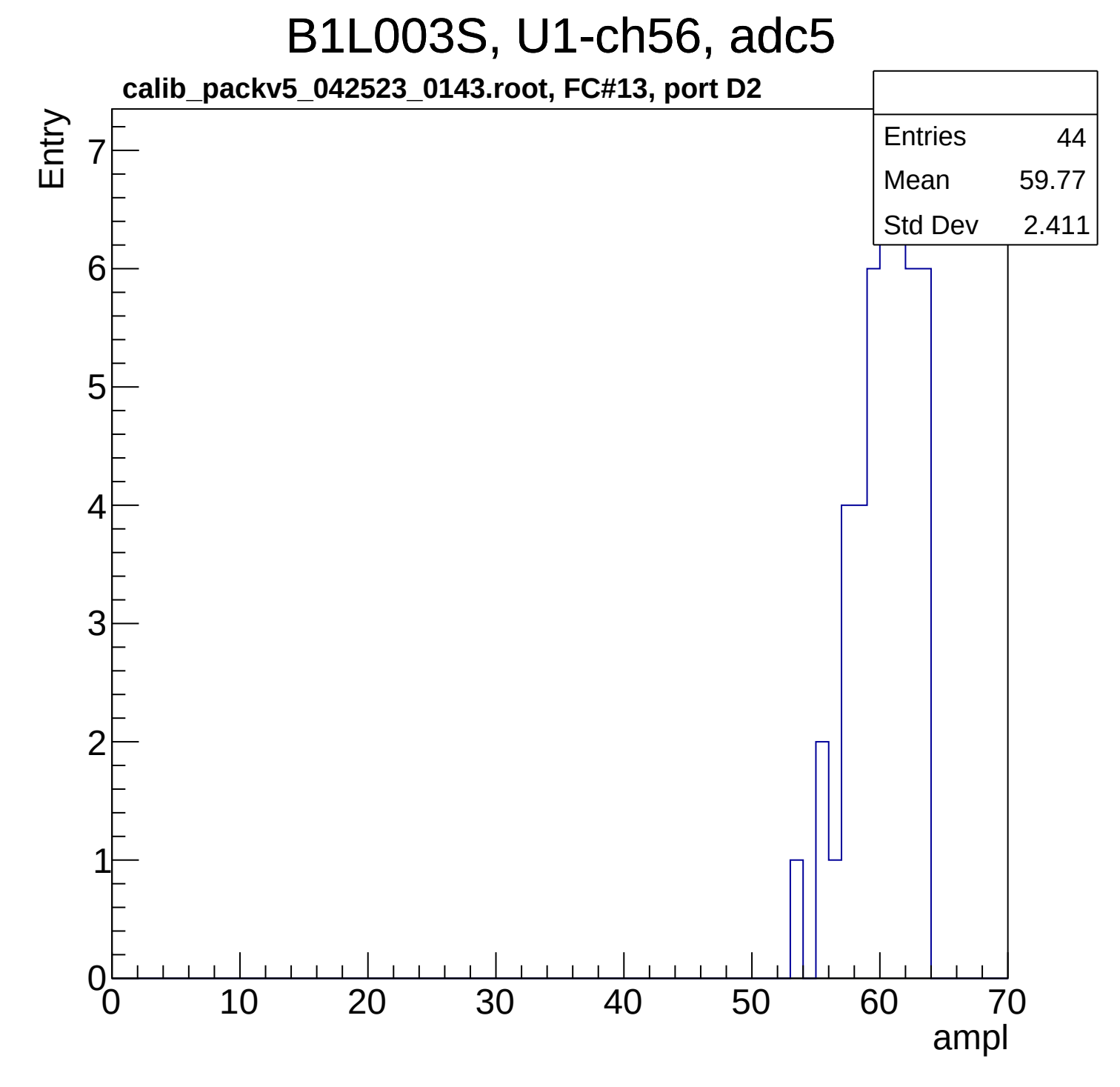
B1L003S, U1-ch56, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.77
Std Dev 2.411
Entry
ampl
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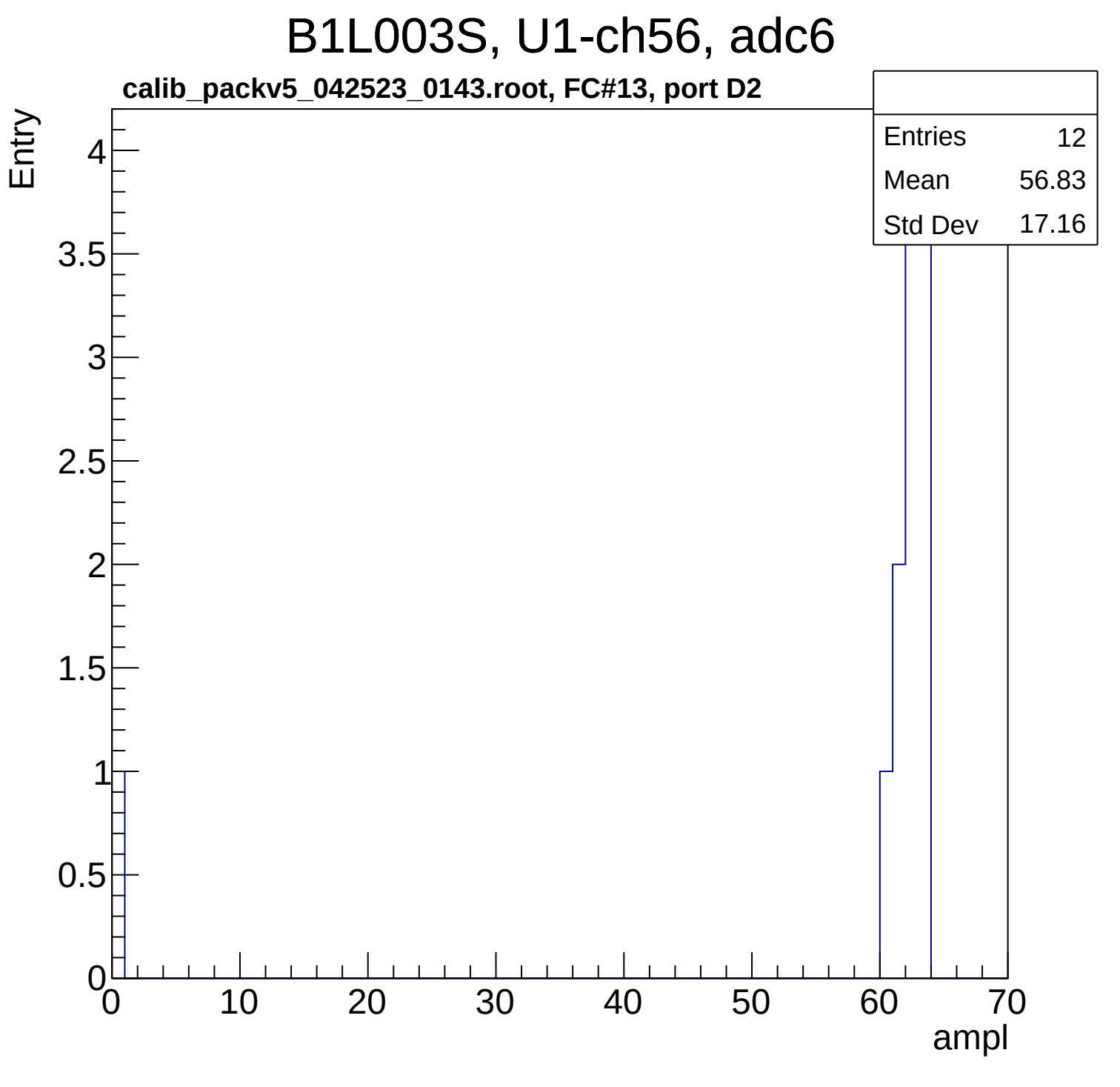
B1L003S, U1-ch56, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.83
Std Dev 17.16
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
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0
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60
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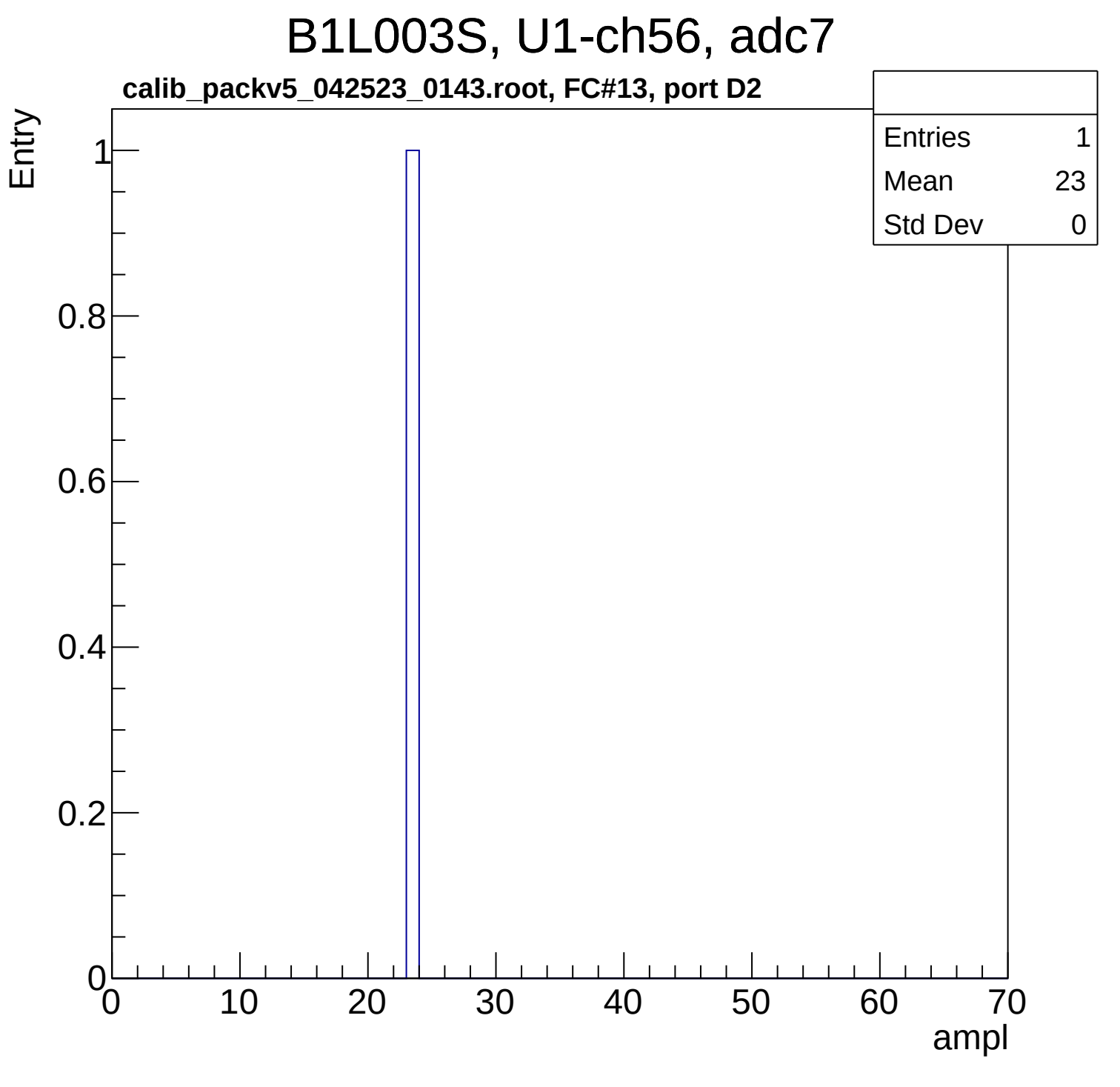
B1L003S, U1-ch56, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
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0.8
0.6
0.4
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10
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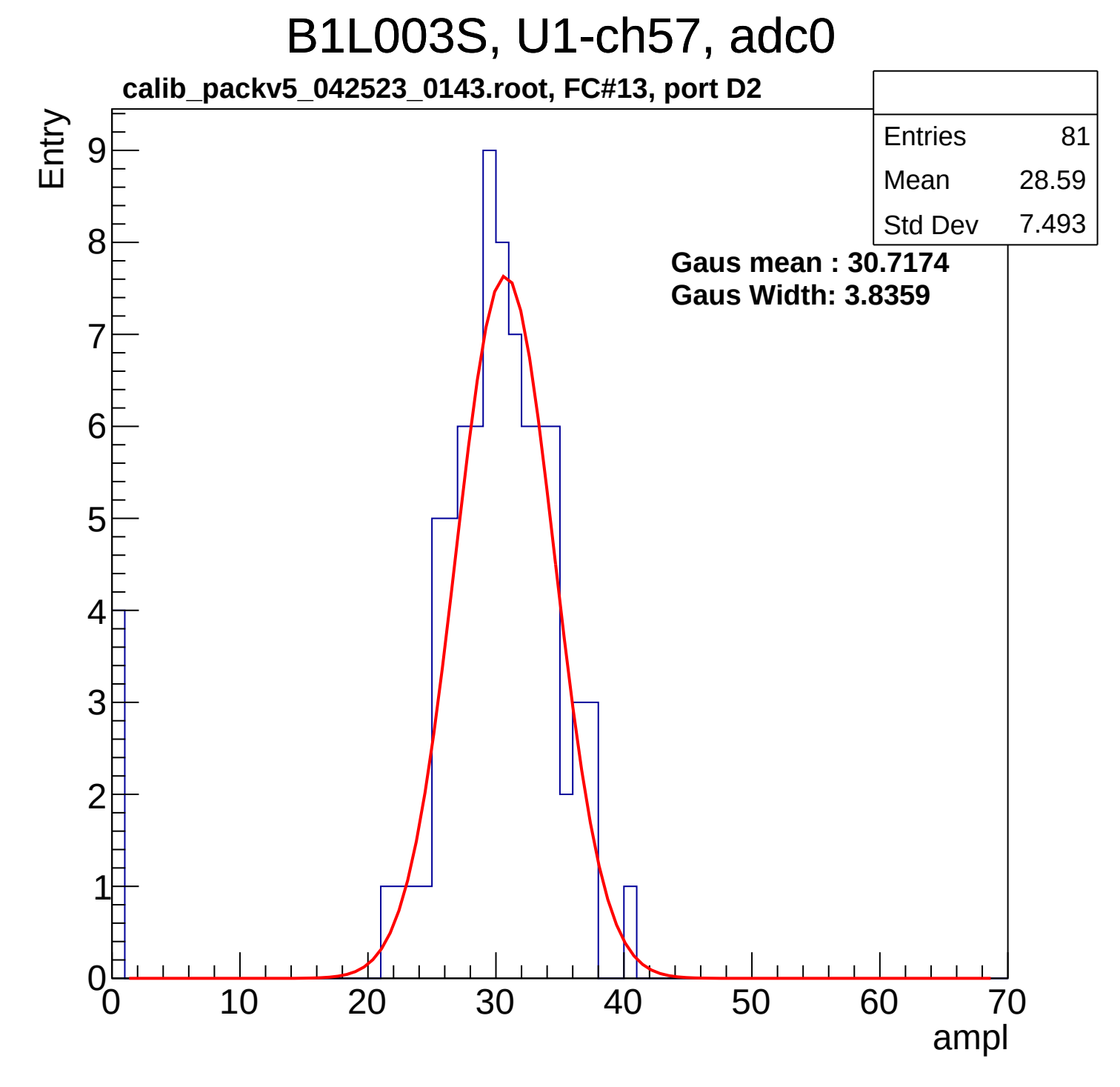
40
50
60
70
ampl


B1L003S, U1-ch57, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.59
Std Dev 7.493
Gaus mean : 30.7174
Gaus Width: 3.8359
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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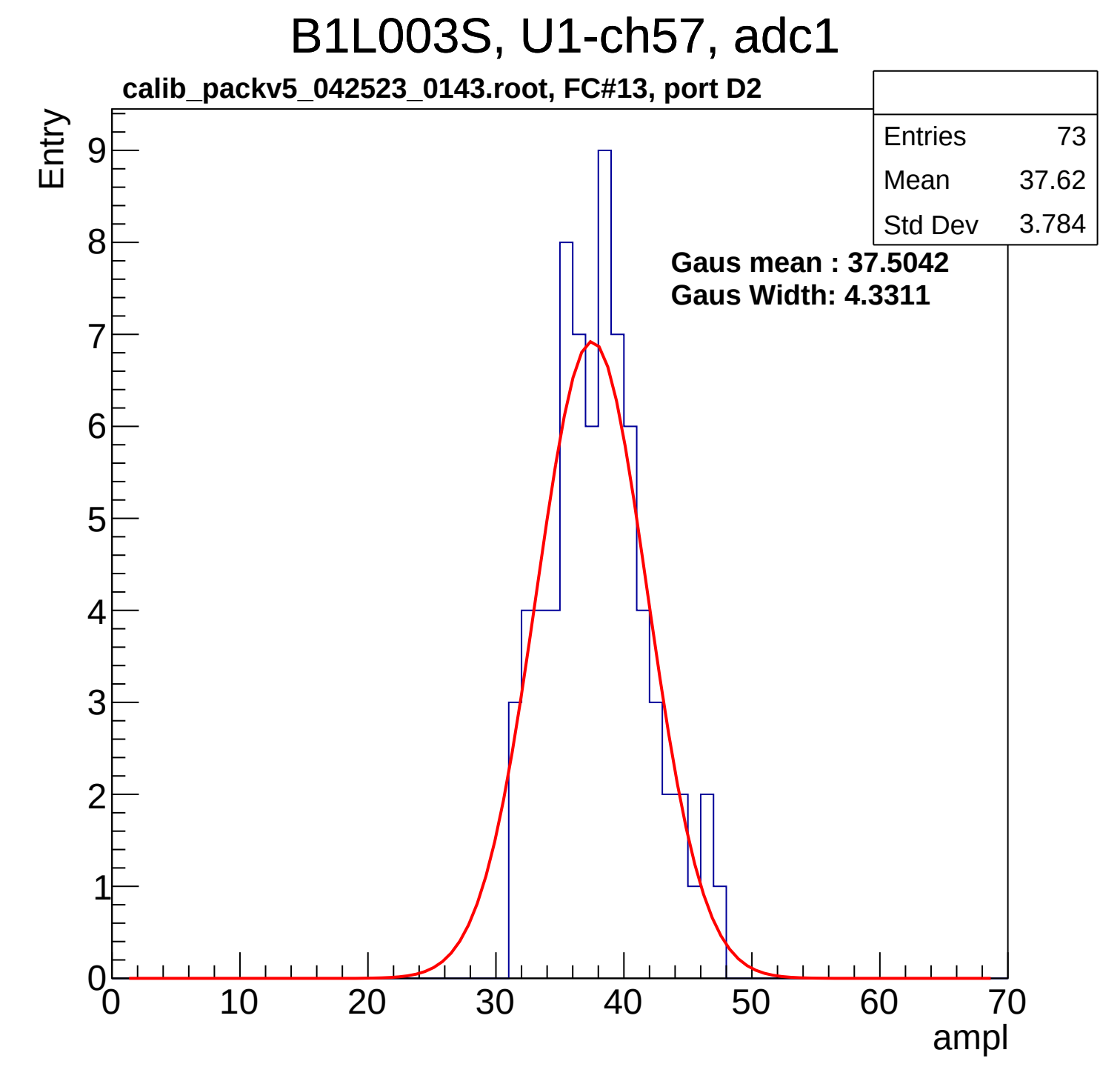
B1L003S, U1-ch57, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 37.62
Std Dev 3.784
Gaus mean : 37.5042
Gaus Width: 4.3311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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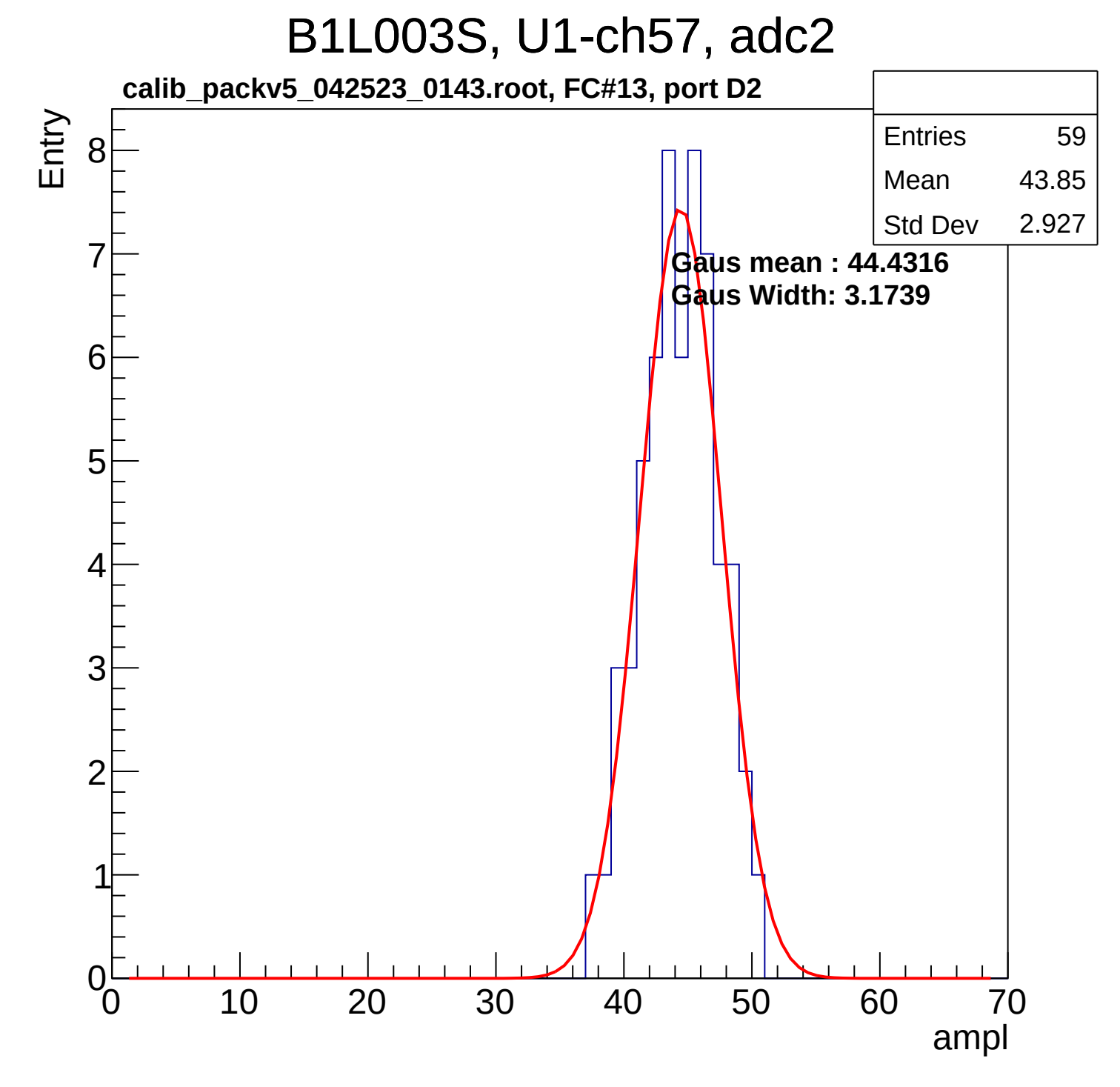
B1L003S, U1-ch57, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 43.85
Std Dev 2.927
Gaus mean : 44.4316
Gaus Width: 3.1739
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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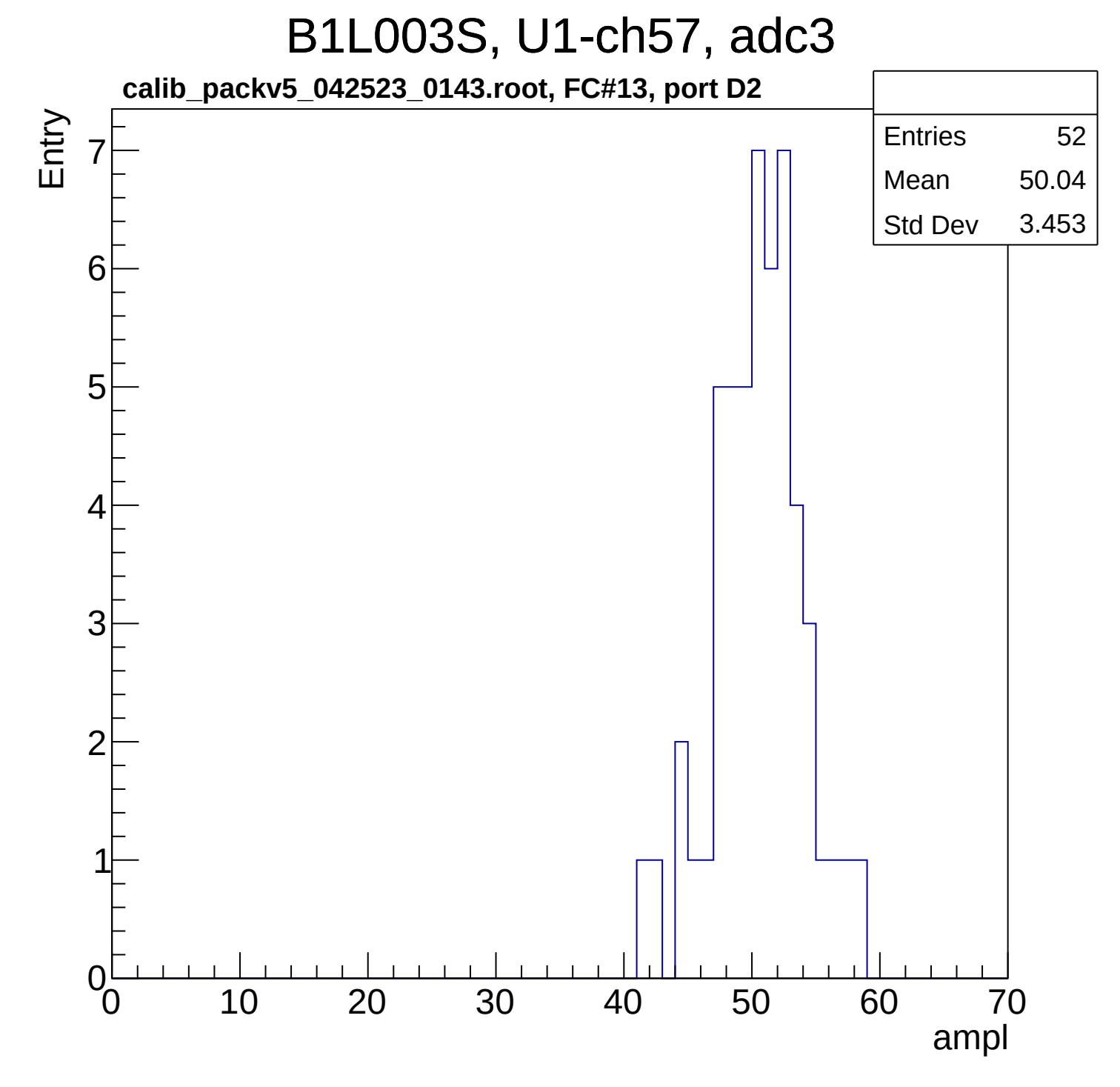
B1L003S, U1-ch57, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 50.04
Std Dev 3.453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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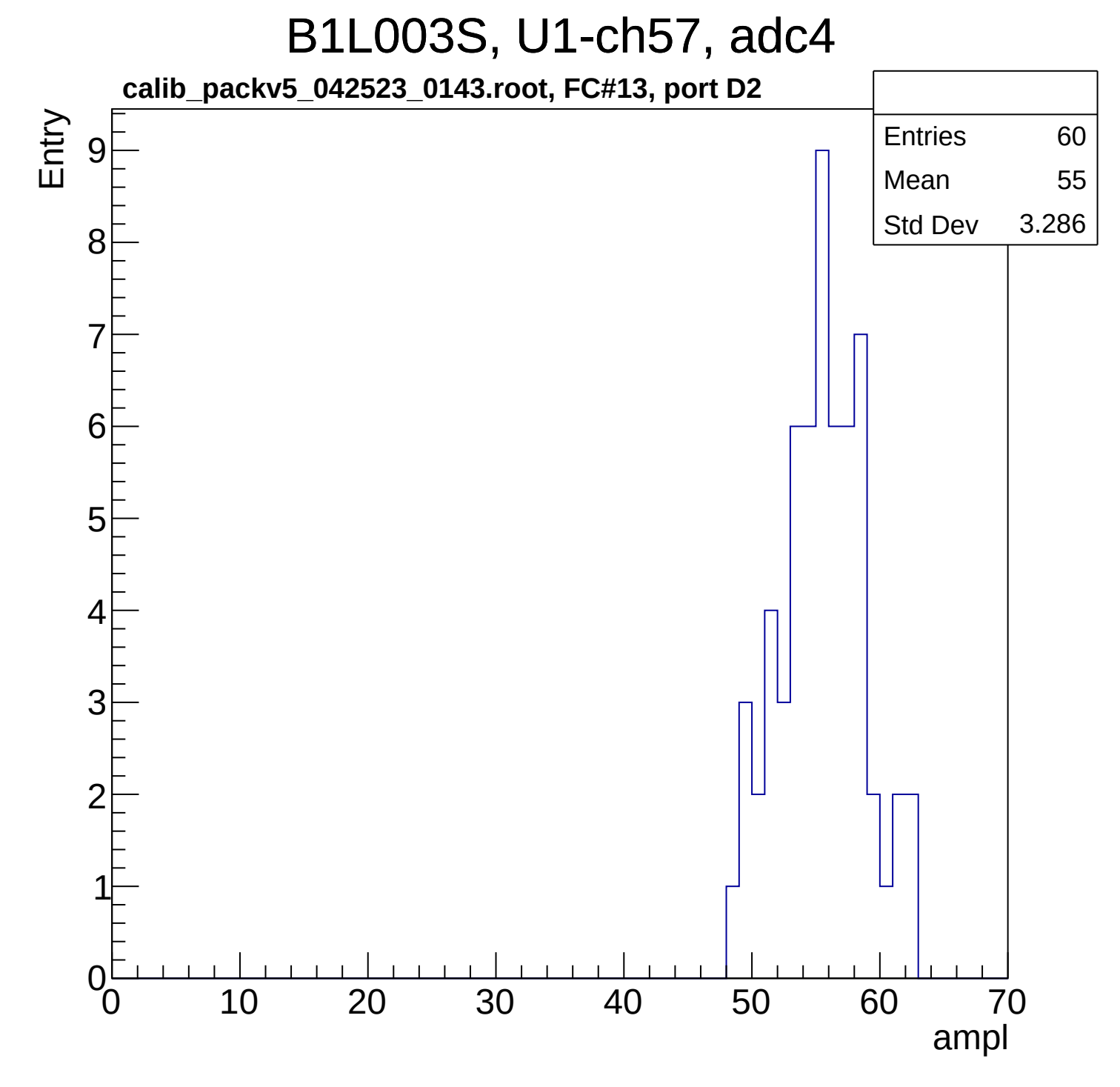
B1L003S, U1-ch57, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 55
Std Dev 3.286
Entry
ampl
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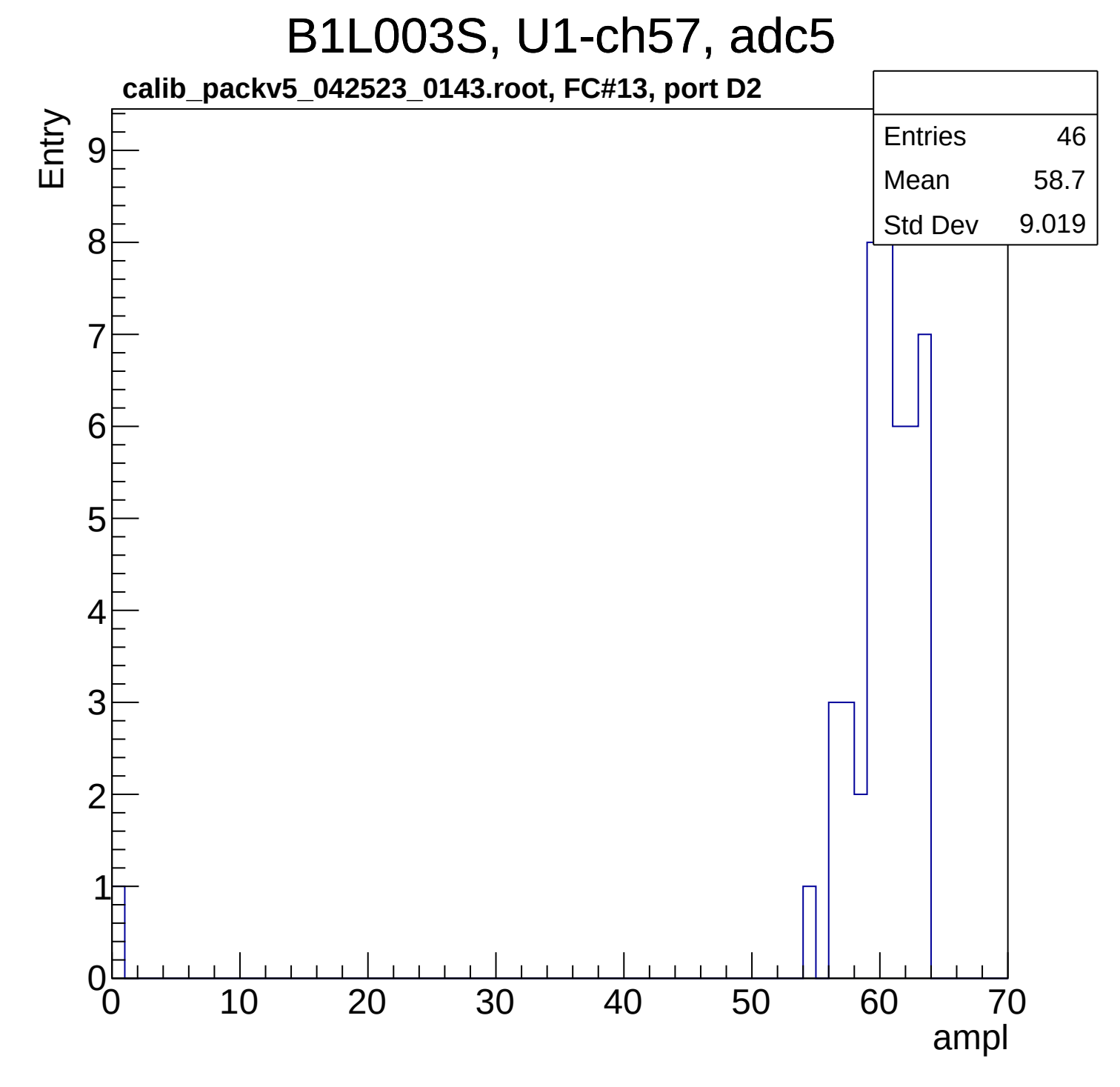
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch57, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 58.7
Std Dev 9.019
Entry
ampl
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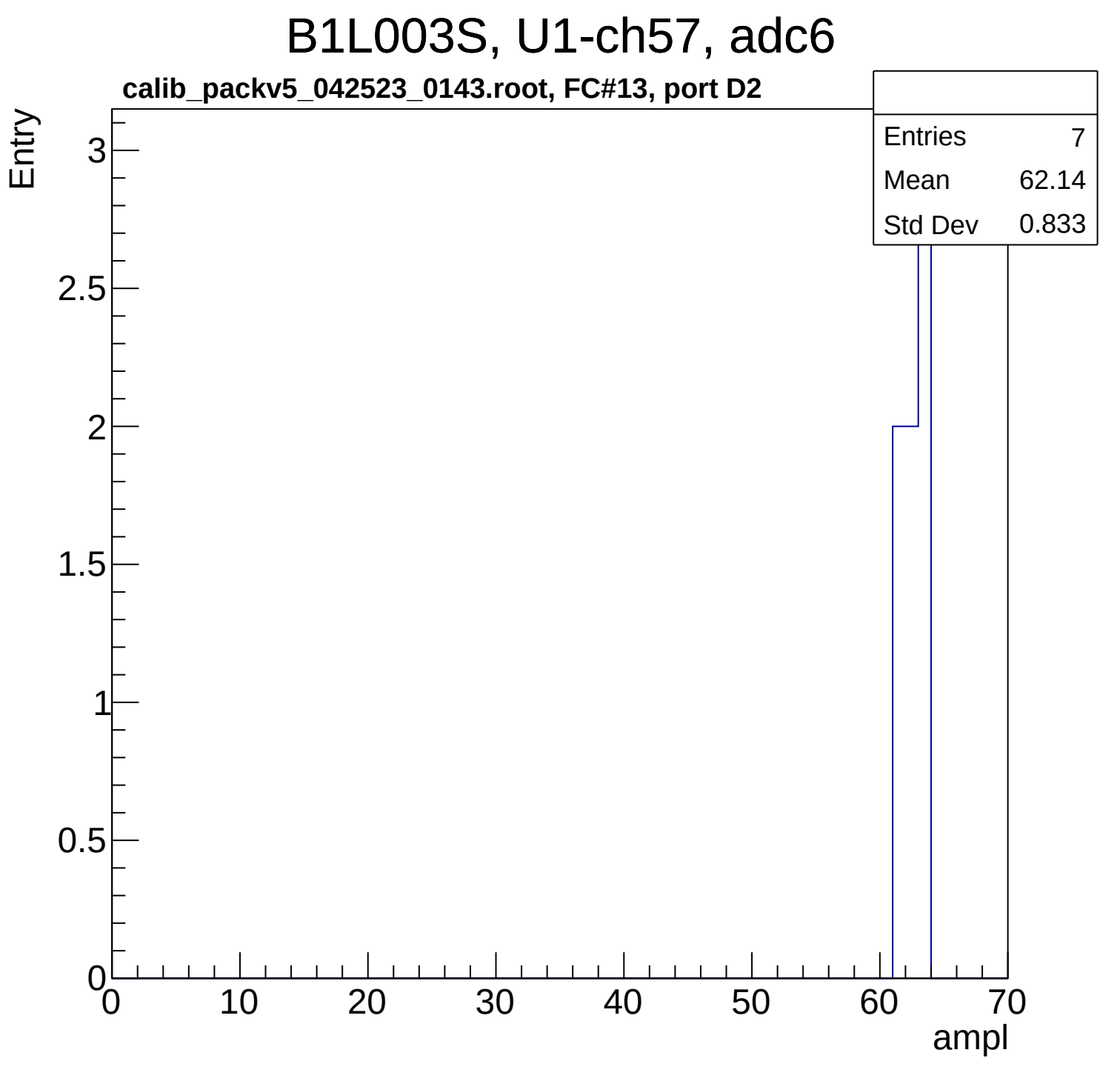
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch57, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
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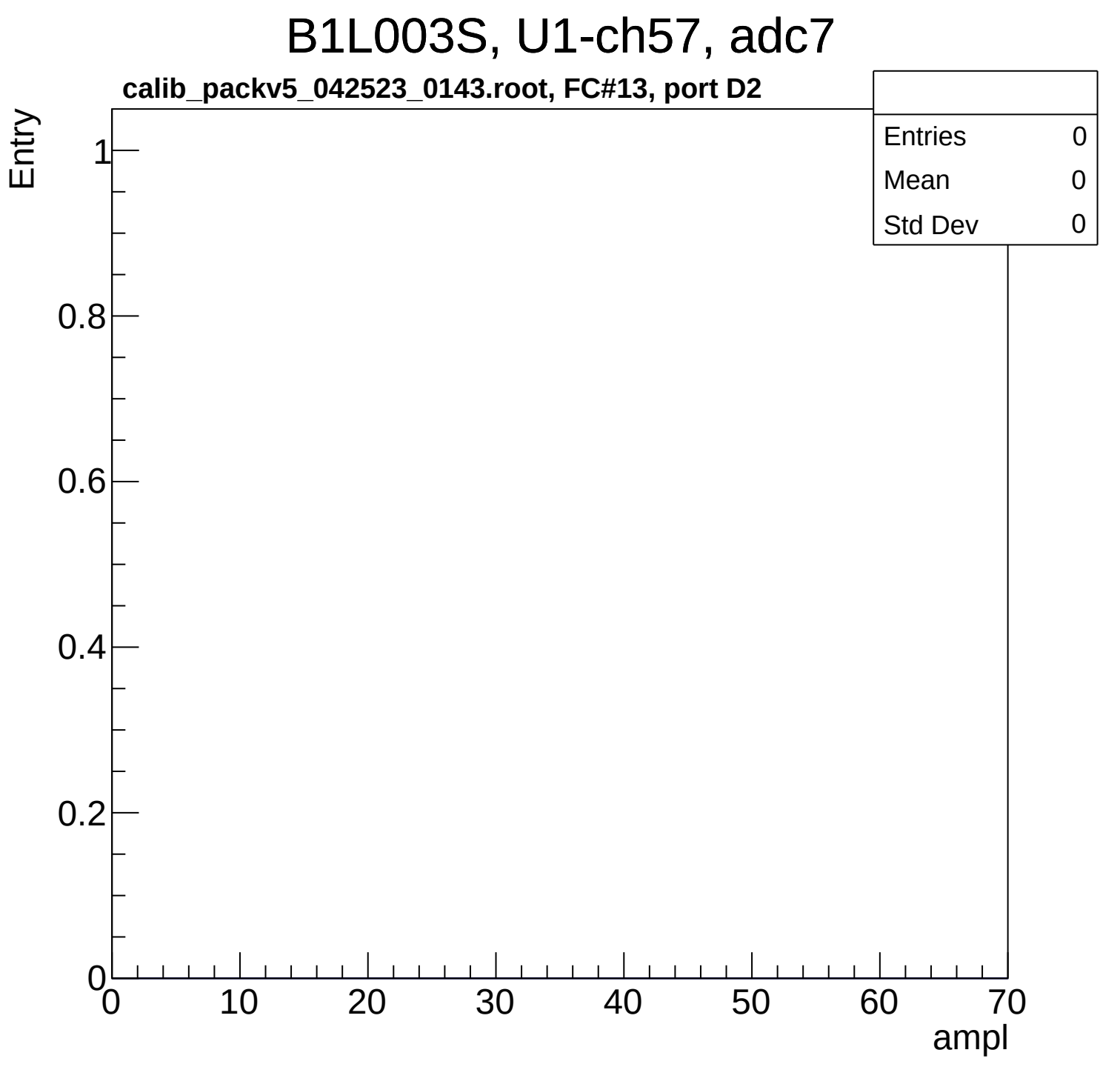
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U1-ch57, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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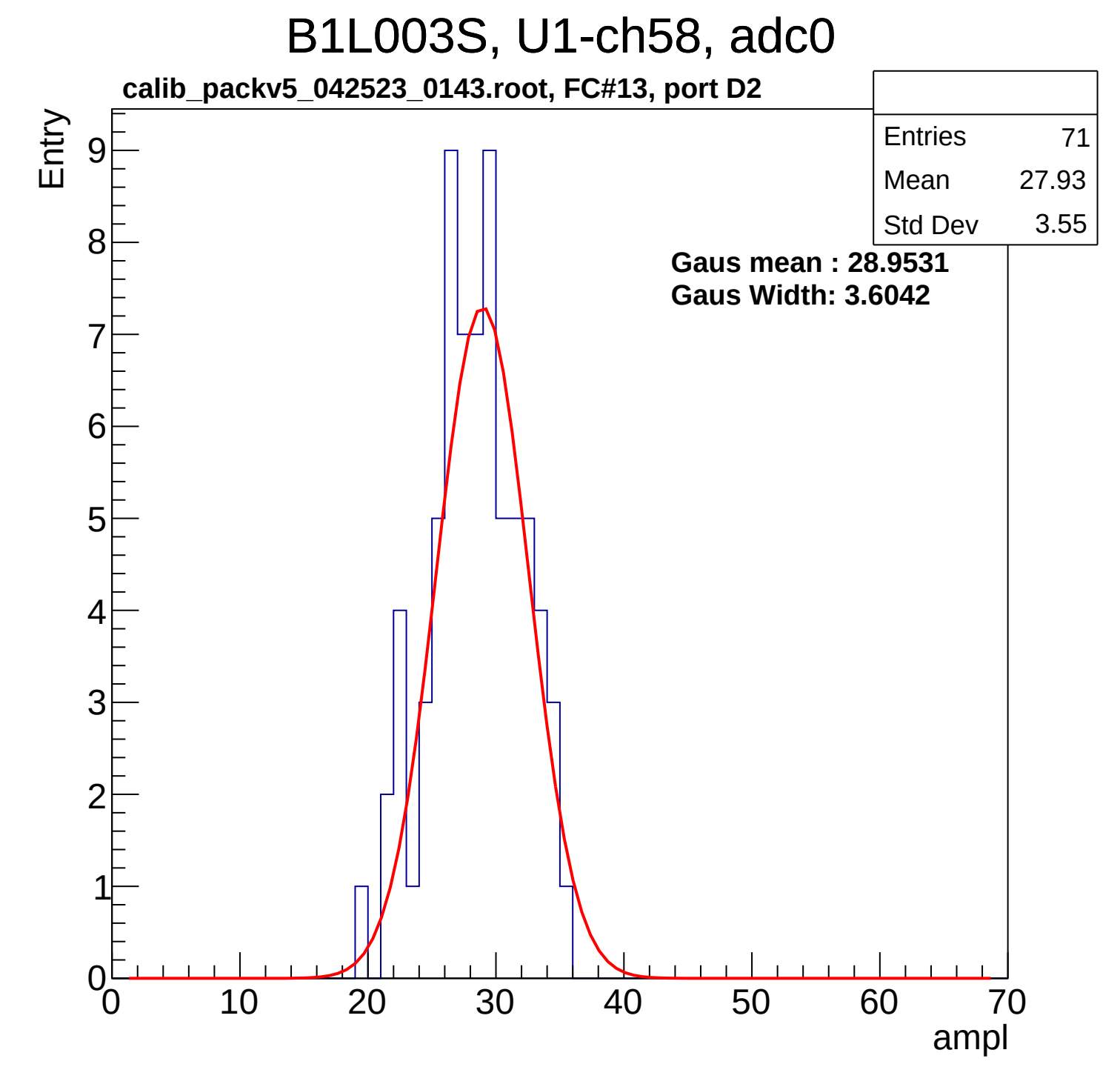
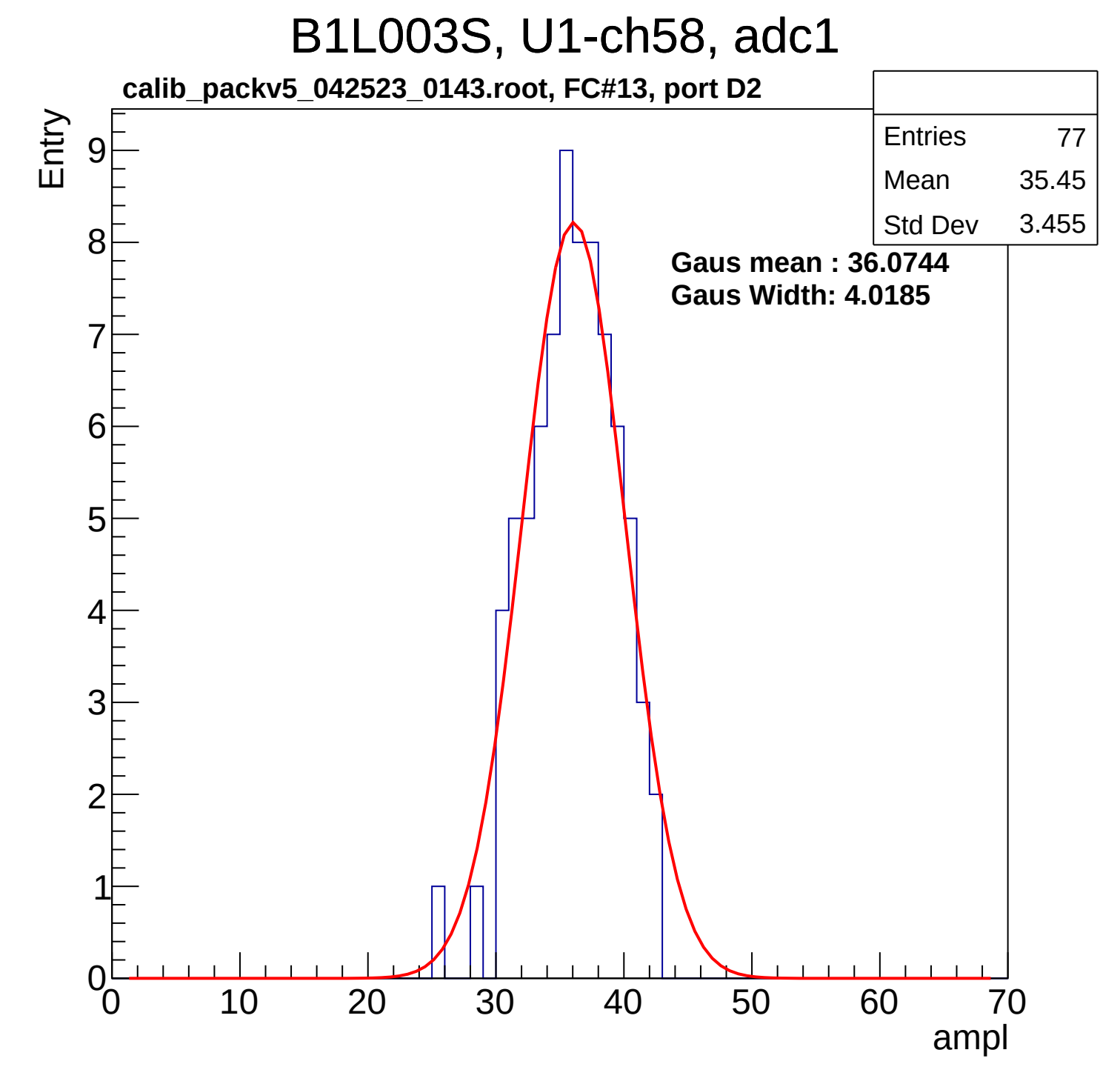
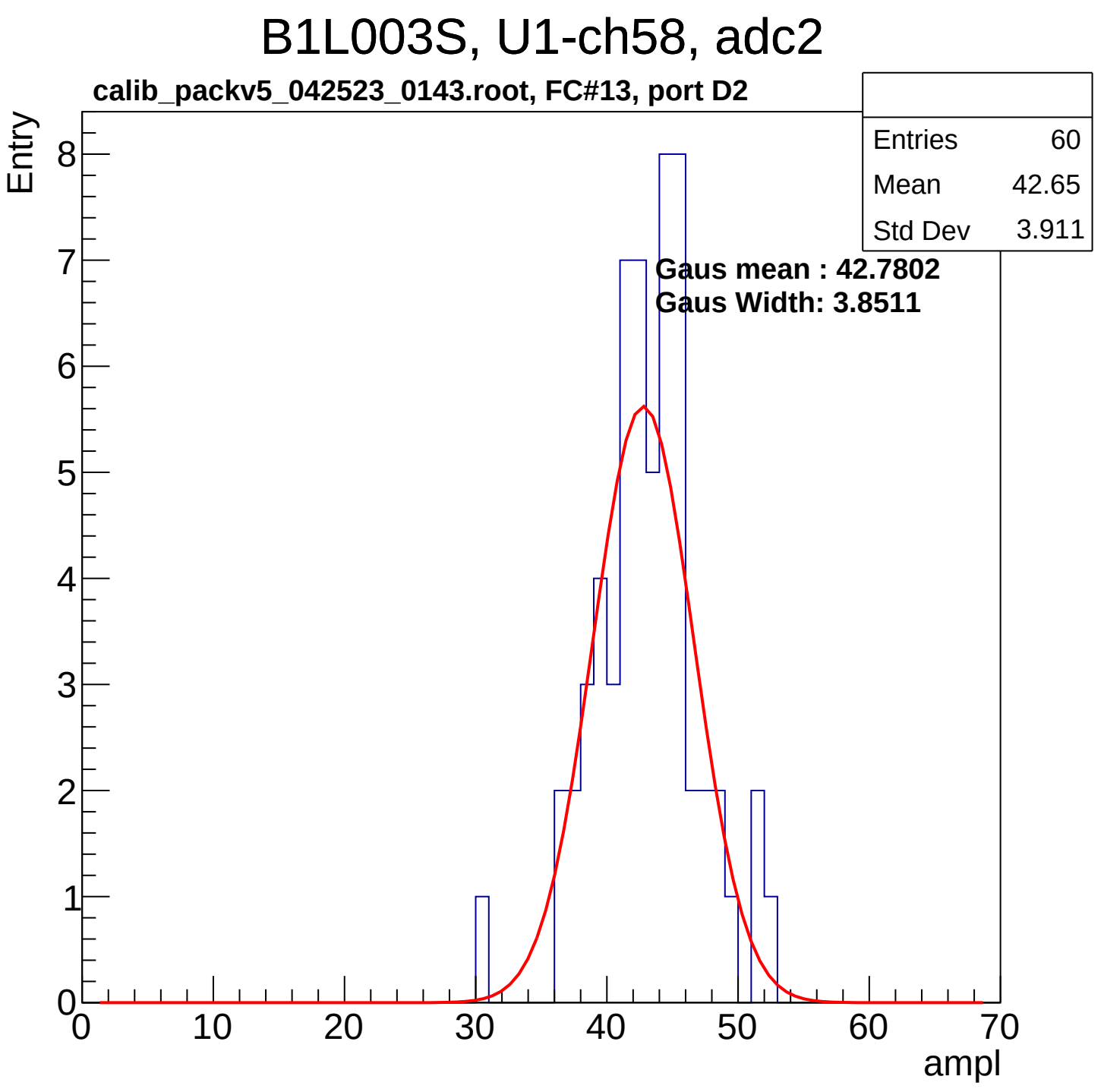
40
50
60
70
ampl


B1L003S, U1-ch58, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 27.93
Std Dev 3.55
Gaus mean : 28.9531
Gaus Width: 3.6042
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch58, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.45
Std Dev 3.455
Gaus mean : 36.0744
Gaus Width: 4.0185
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch58, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.65
Std Dev 3.911
Gaus mean : 42.7802
Gaus Width: 3.8511
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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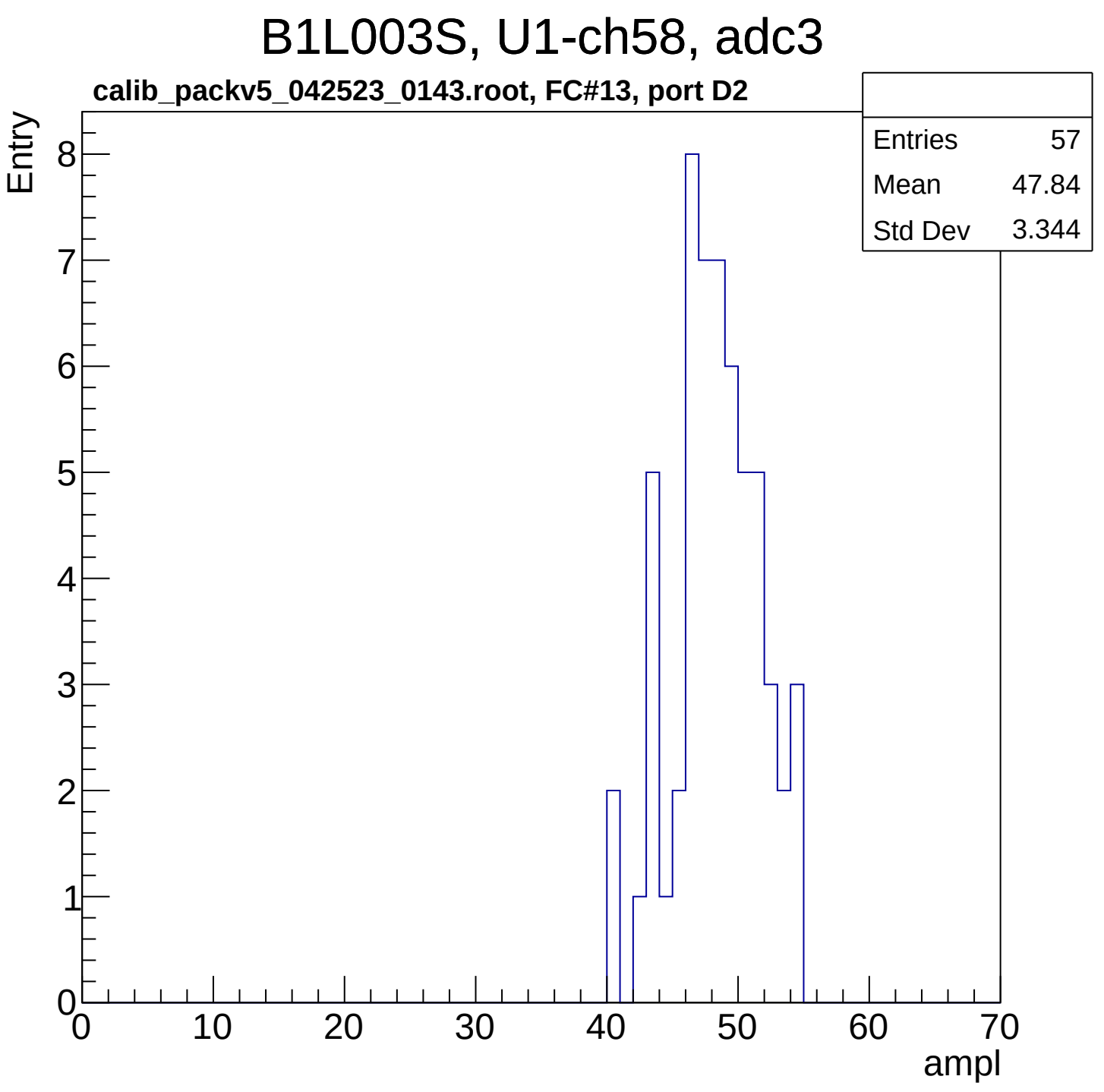
B1L003S, U1-ch58, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 47.84
Std Dev 3.344
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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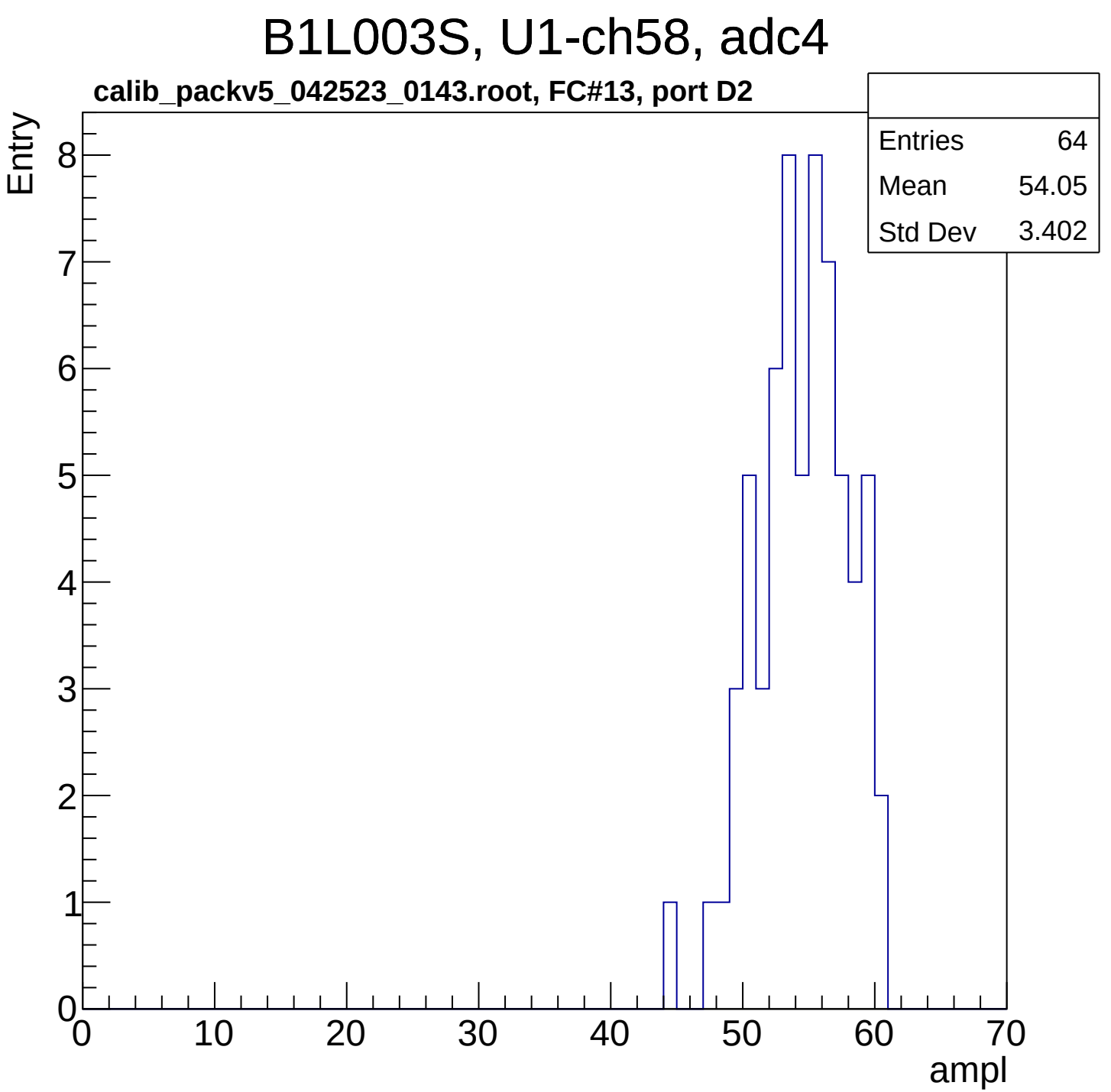
B1L003S, U1-ch58, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 54.05
Std Dev 3.402
Entry
ampl
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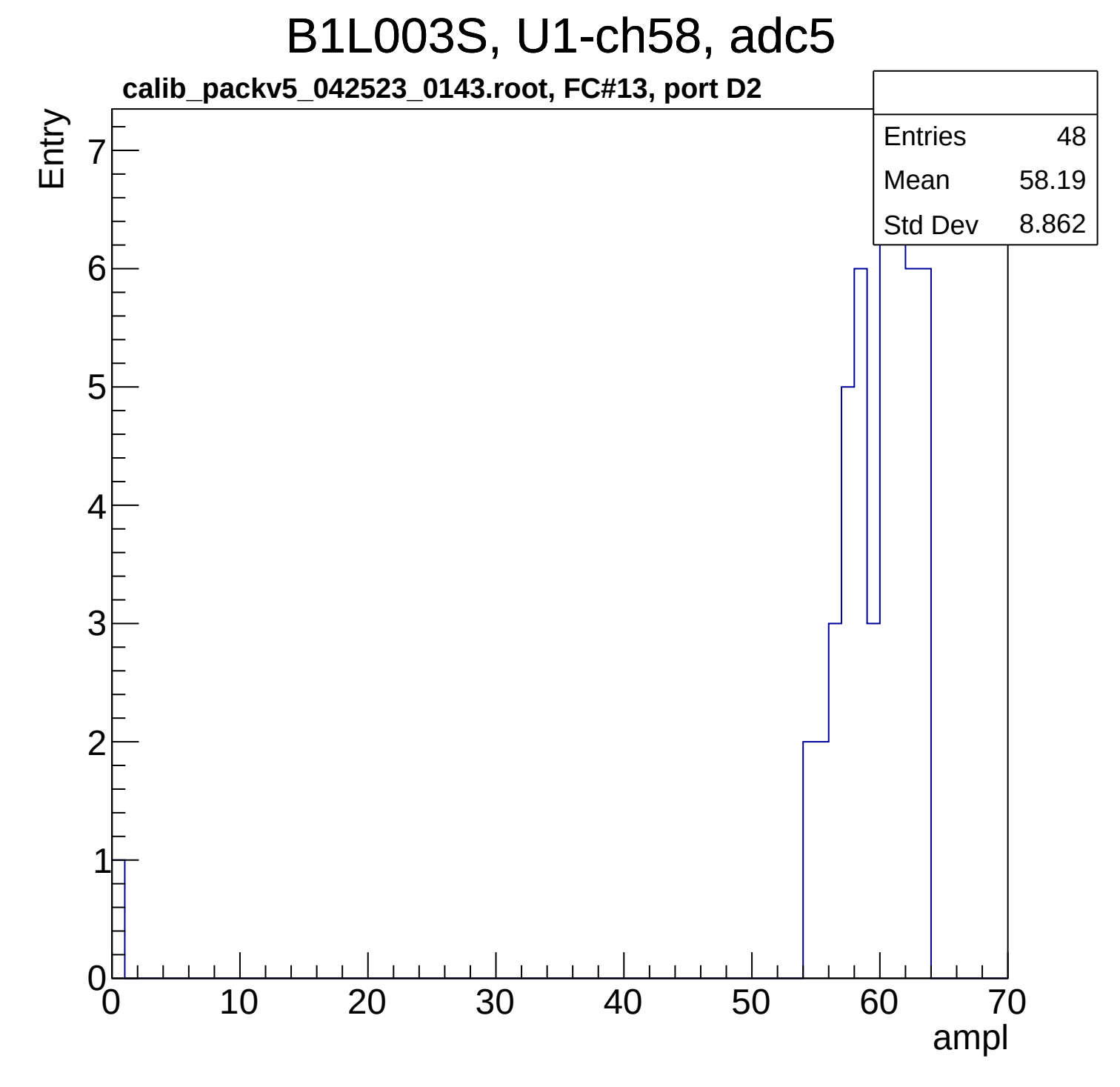
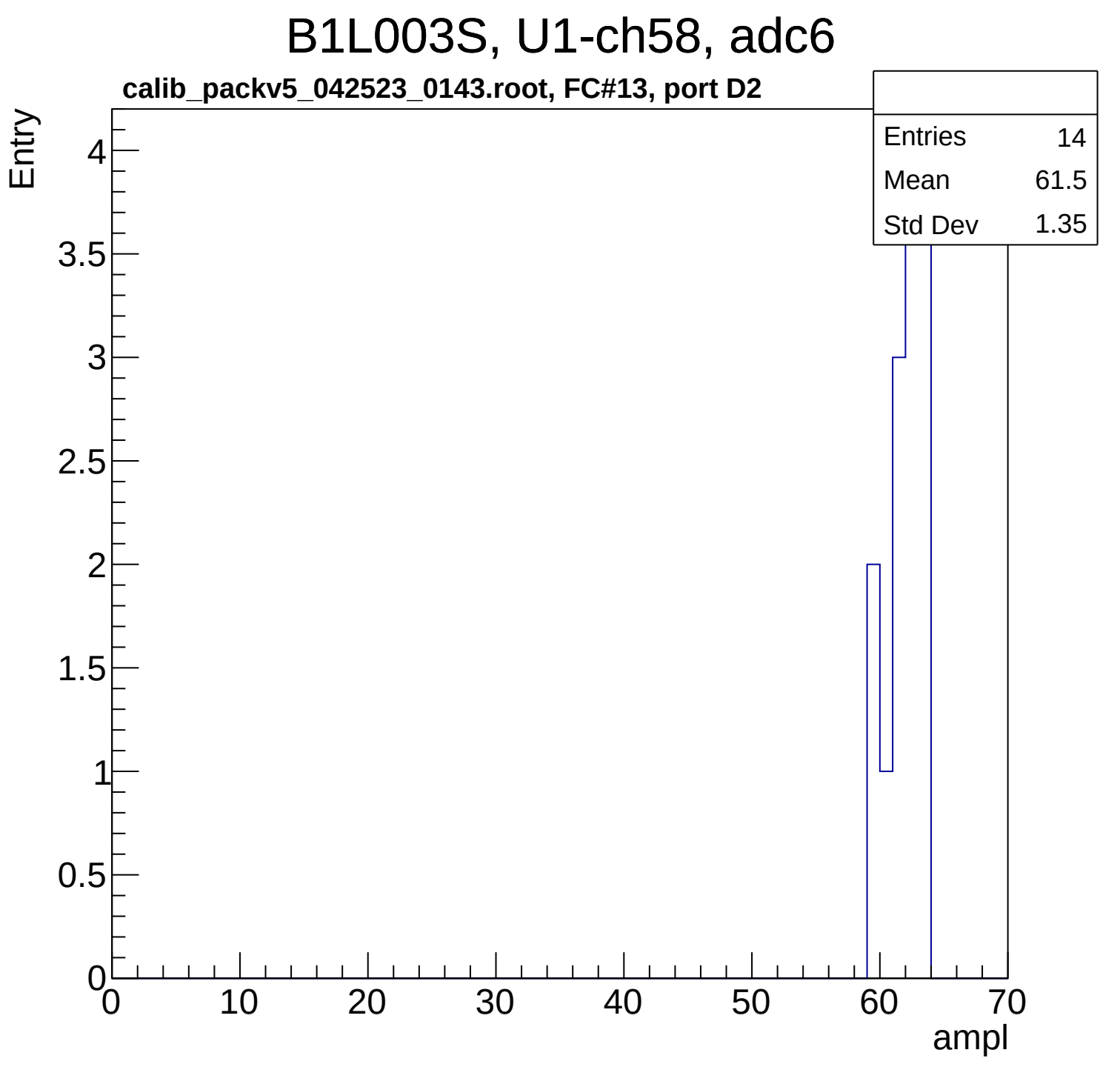
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch58, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.19
Std Dev 8.862
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch58, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 61.5
Std Dev 1.35
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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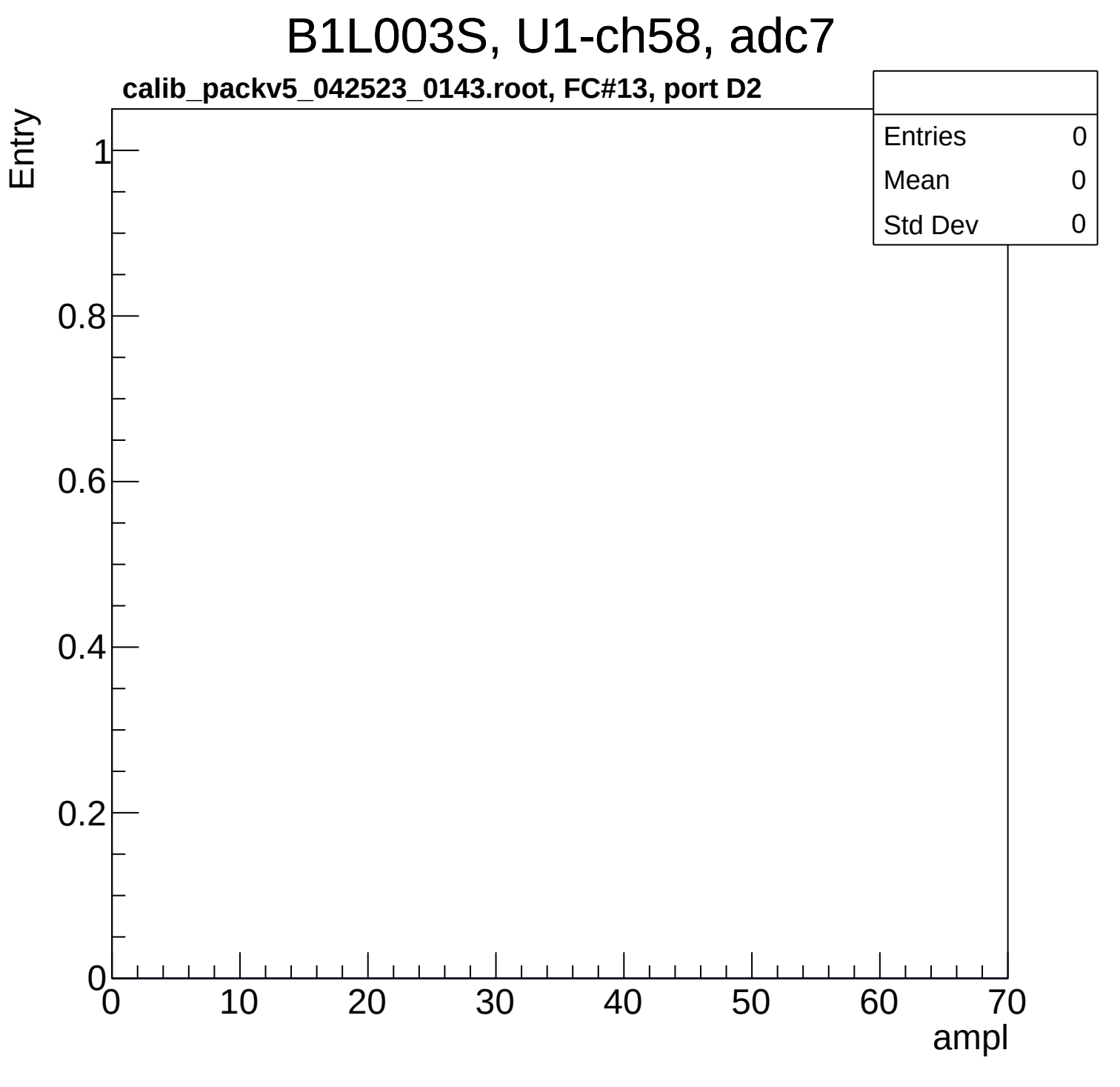
50
60
70


B1L003S, U1-ch58, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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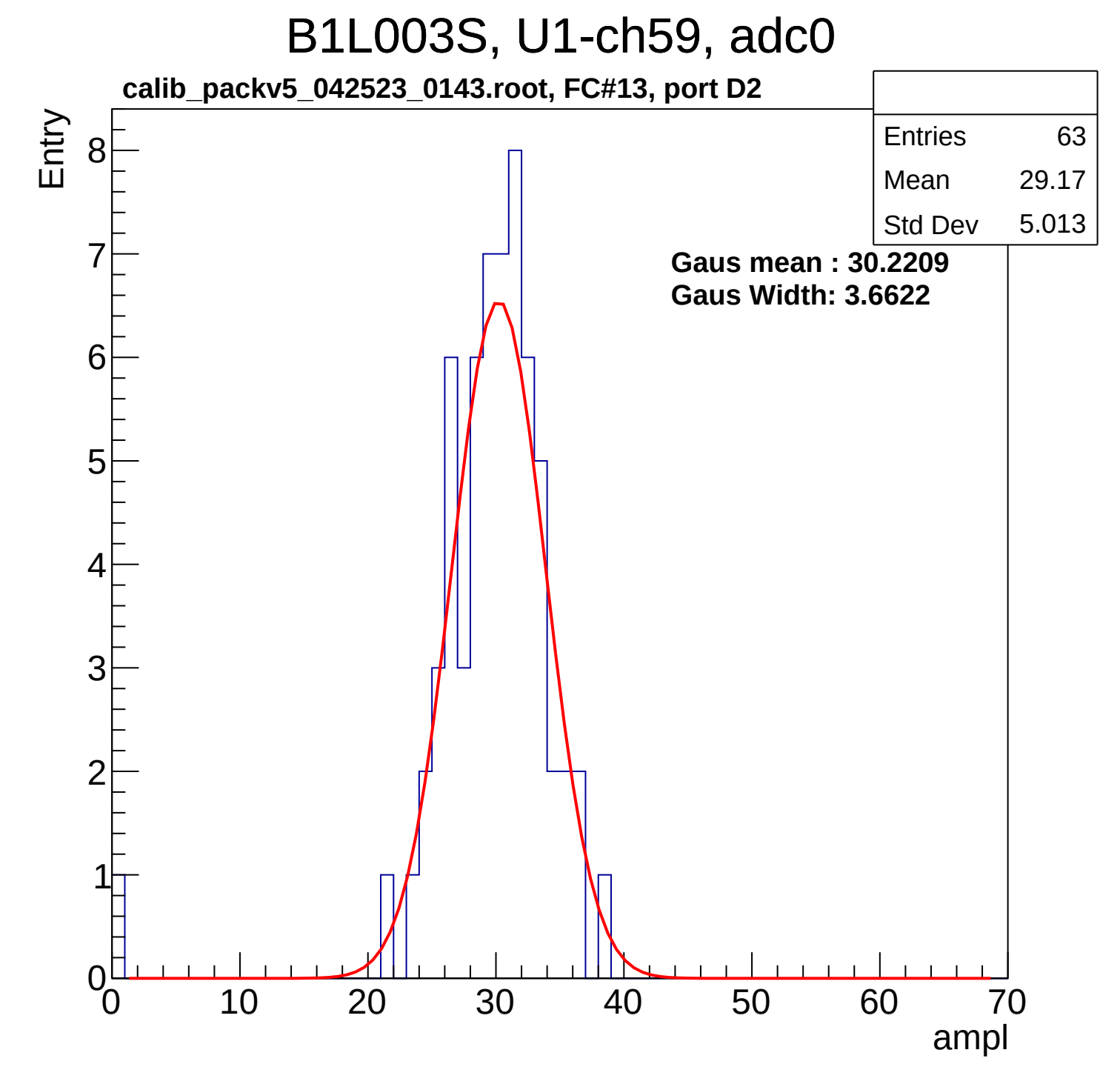
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch59, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.17
Std Dev 5.013
Gaus mean : 30.2209
Gaus Width: 3.6622
Entry
ampl
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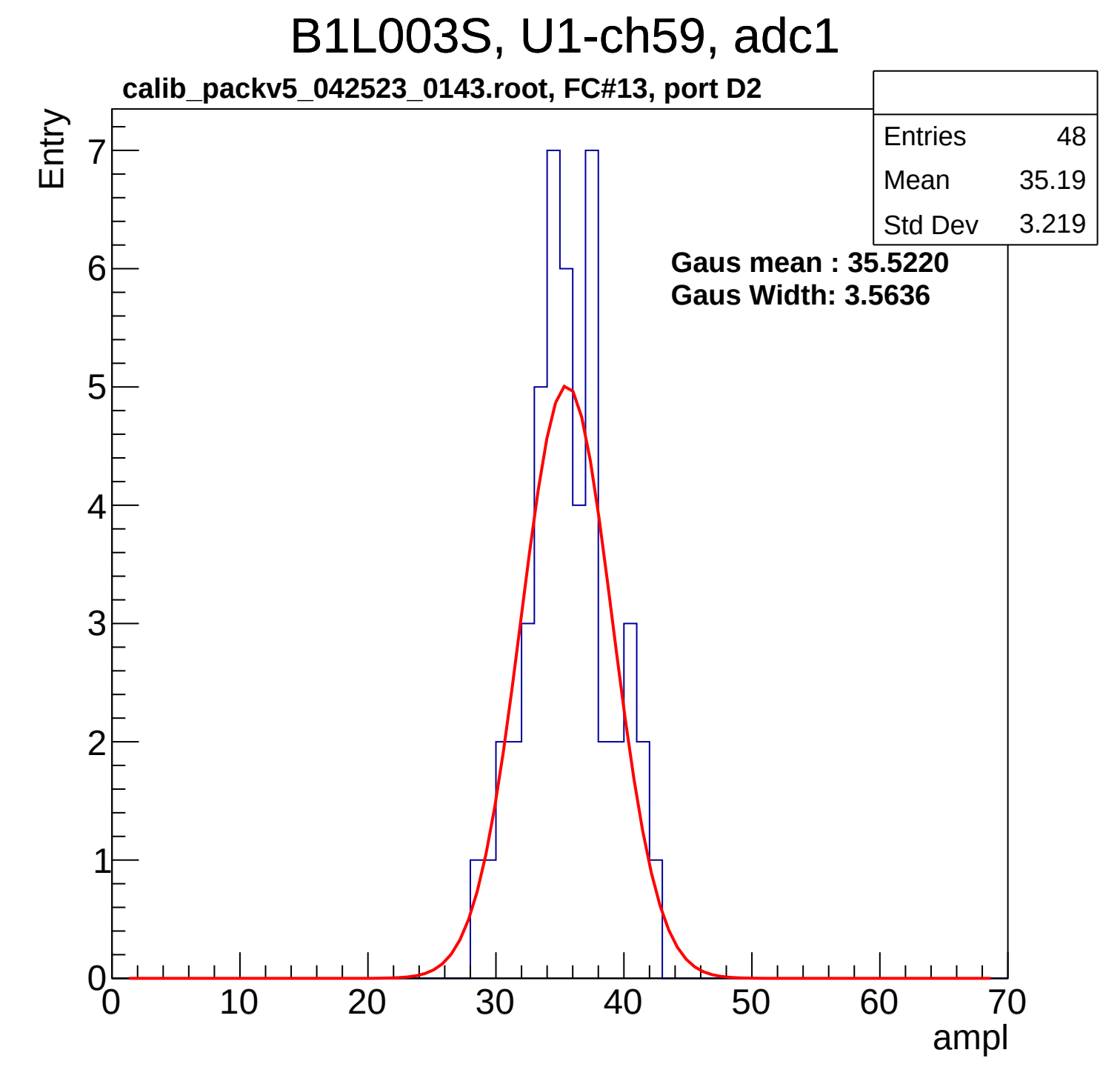
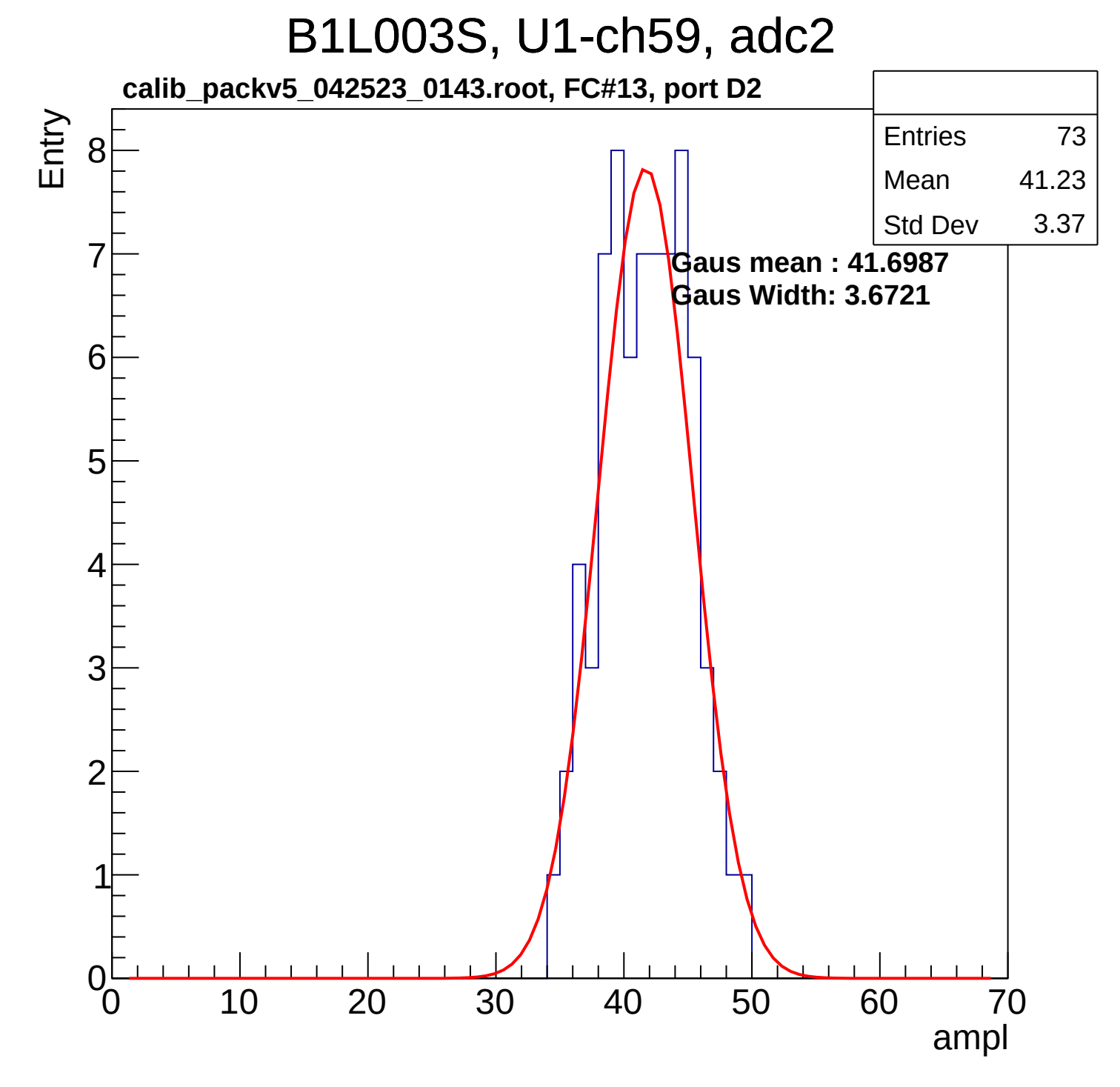
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch59, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 35.19
Std Dev 3.219
Gaus mean : 35.5220
Gaus Width: 3.5636
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch59, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.23
Std Dev 3.37
Gaus mean : 41.6987
Gaus Width: 3.6721
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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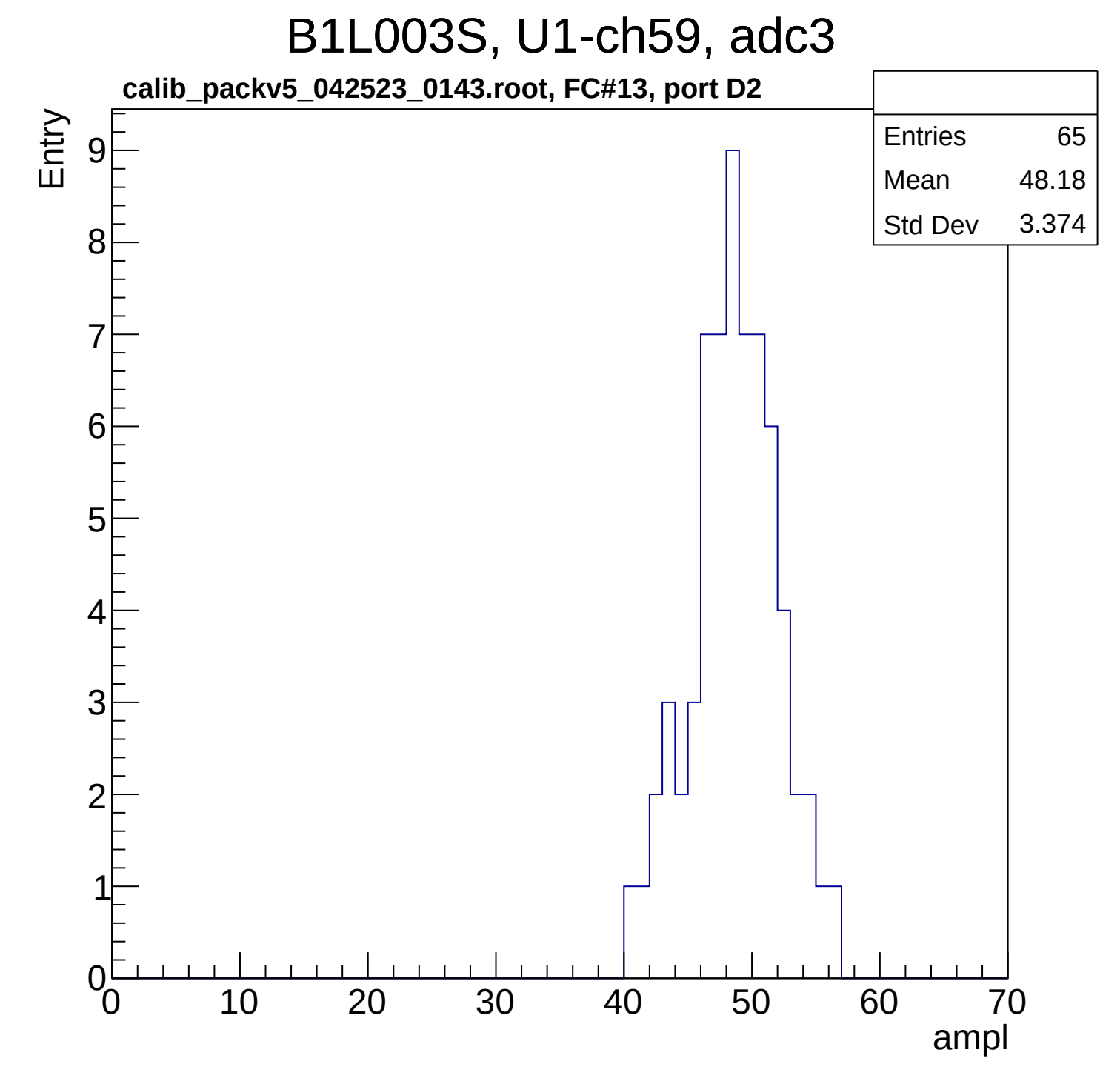
B1L003S, U1-ch59, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.18
Std Dev 3.374
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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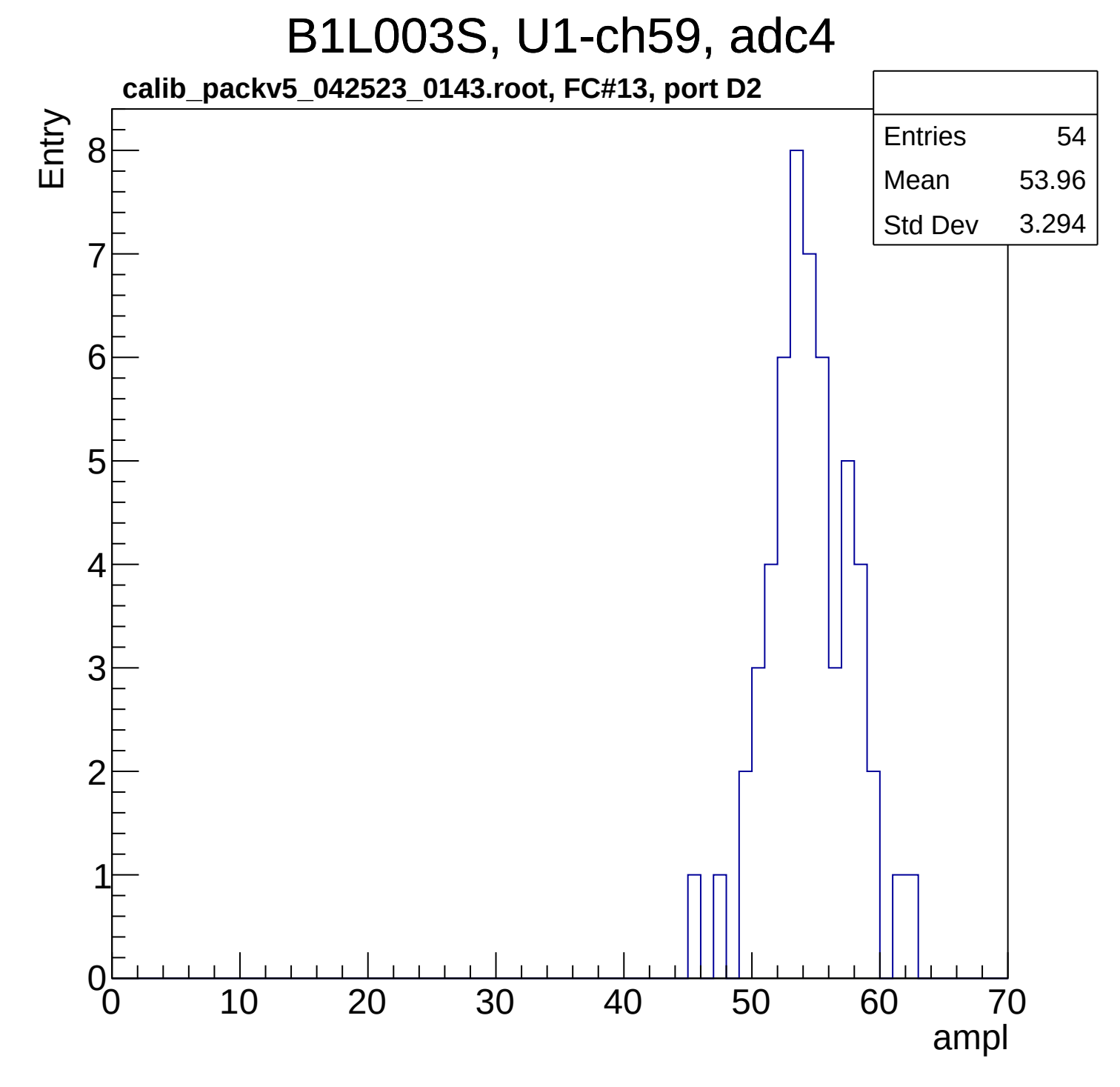
B1L003S, U1-ch59, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.96
Std Dev 3.294
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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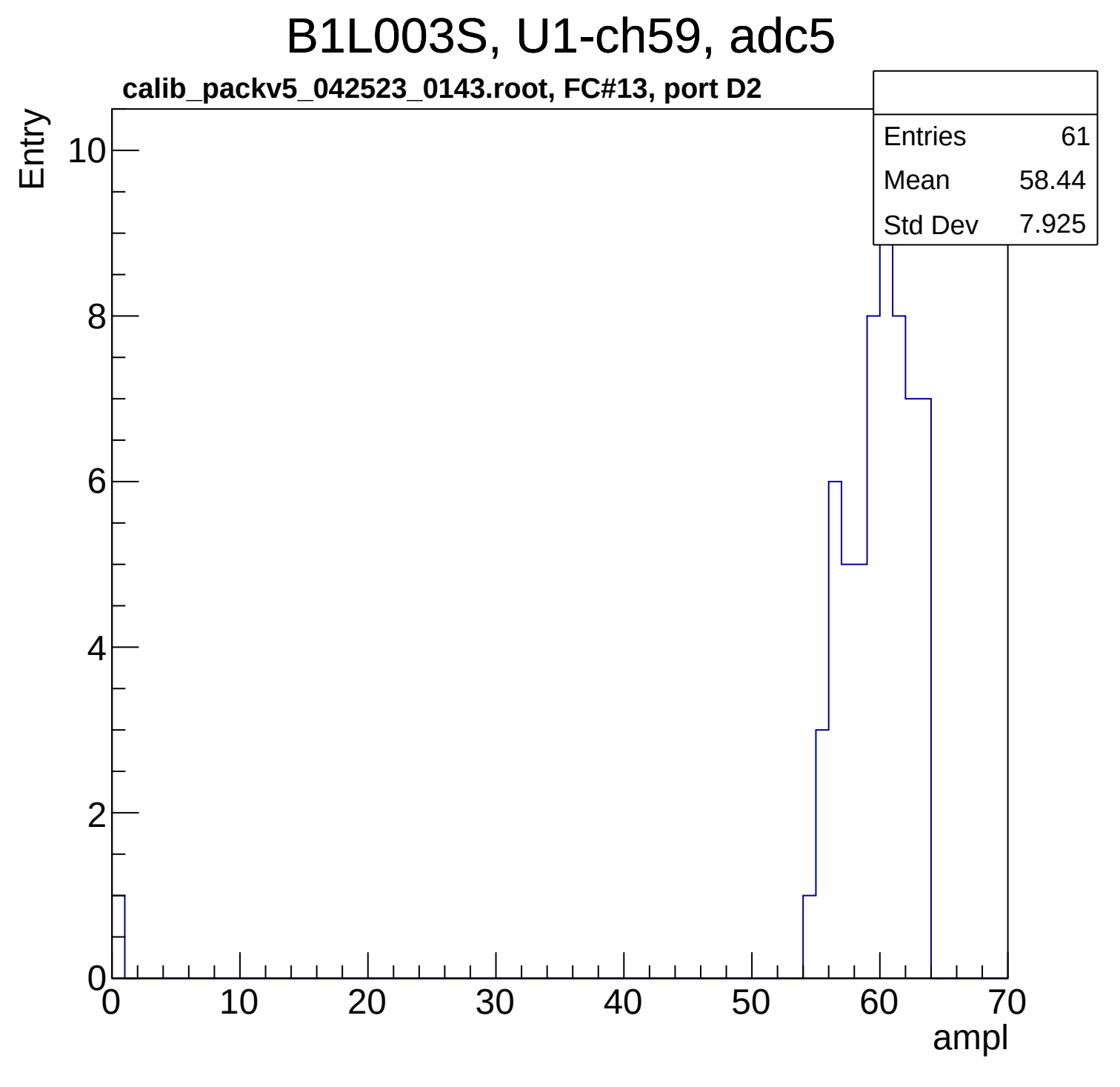
B1L003S, U1-ch59, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.44
Std Dev 7.925
Entry
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2
4
6
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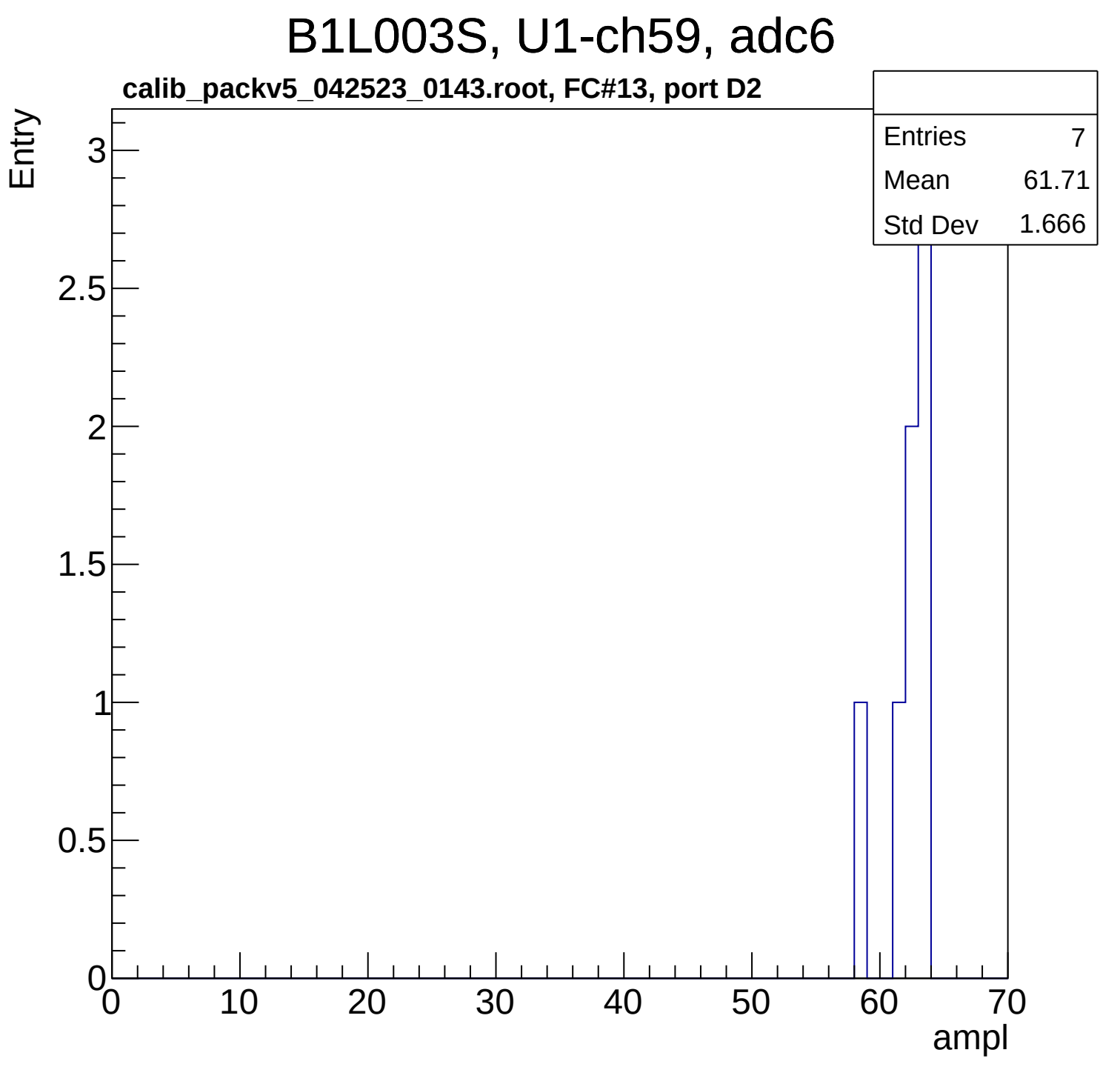
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch59, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.71
Std Dev 1.666
Entry
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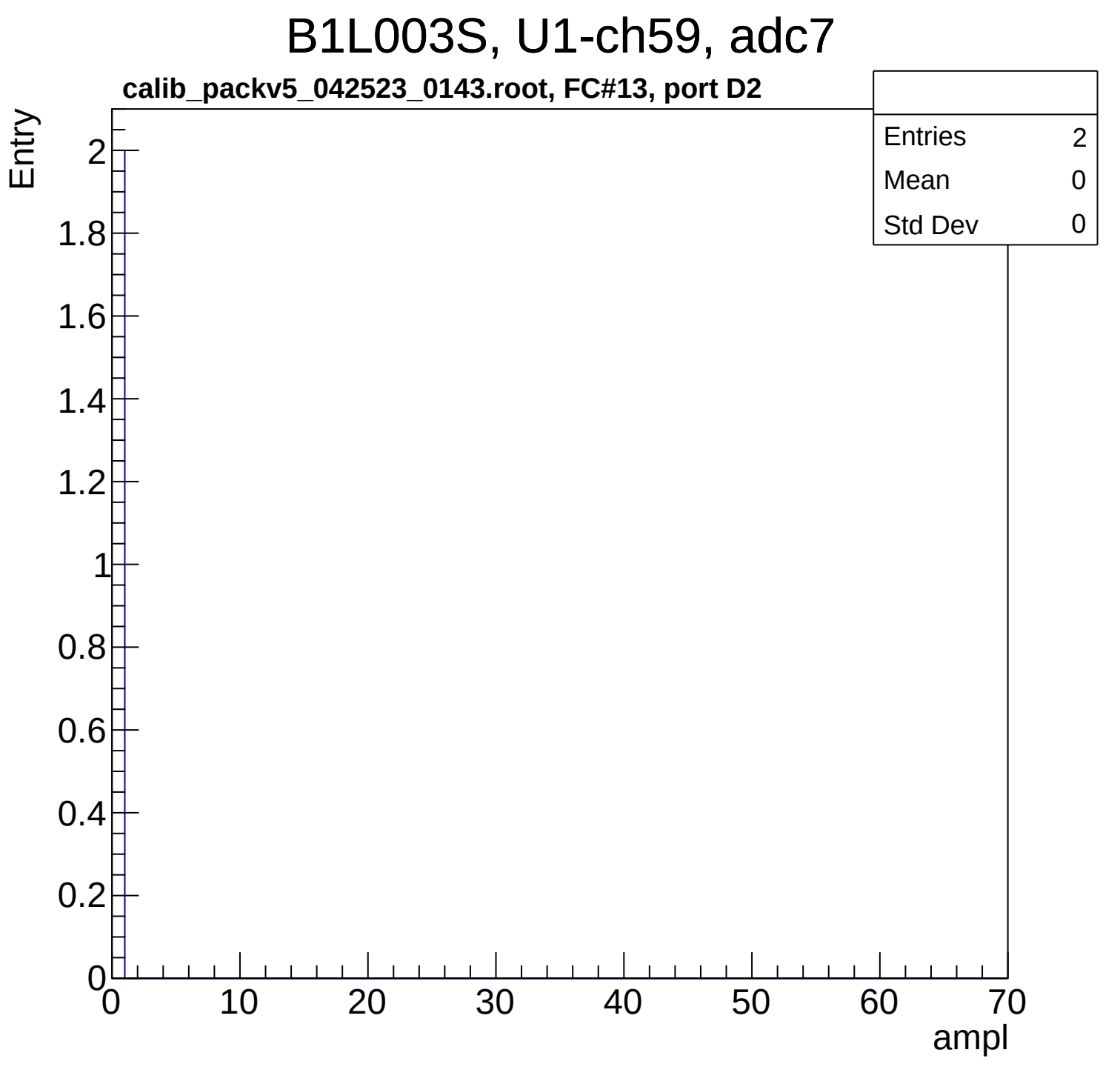
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch59, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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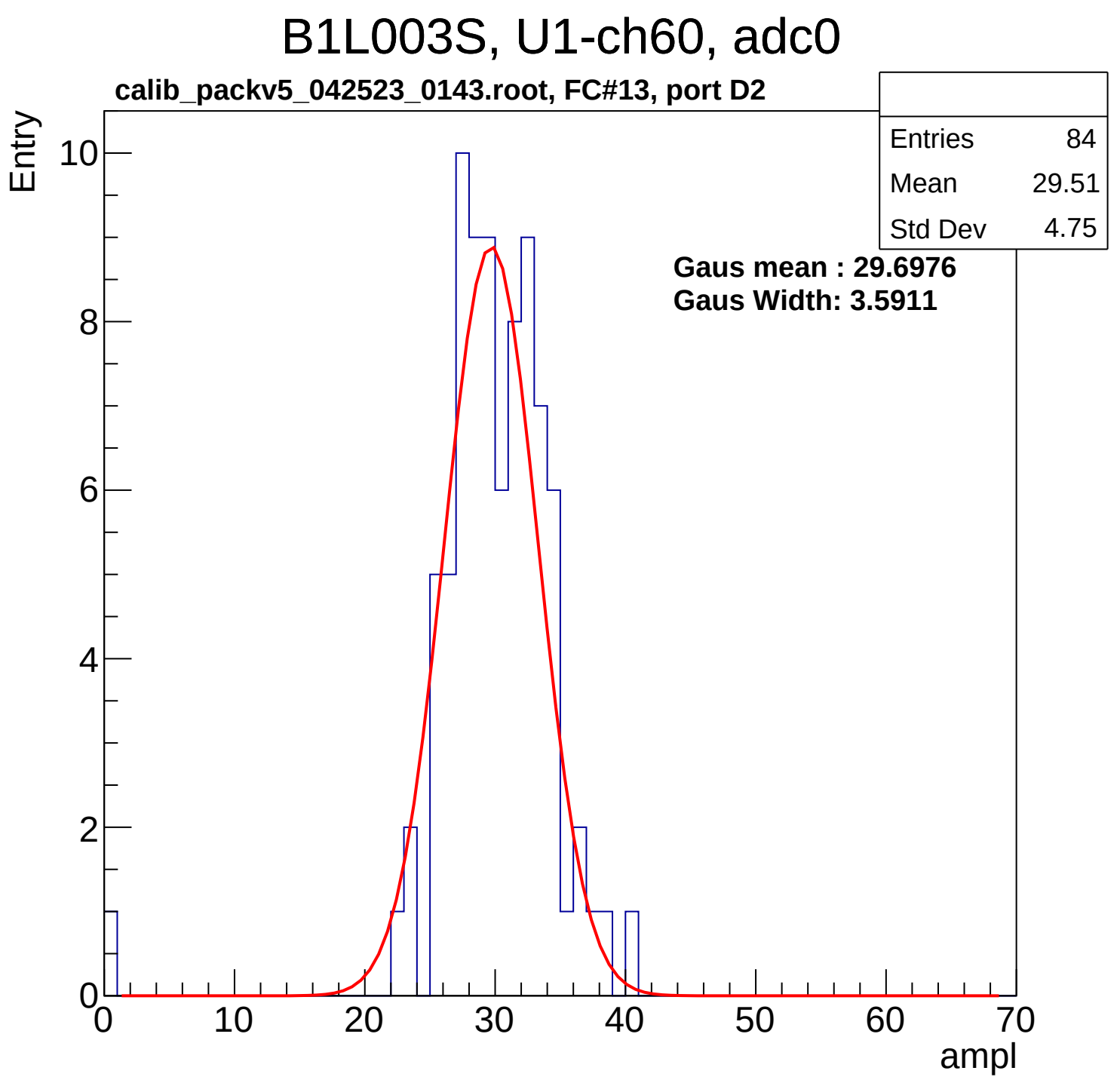
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch60, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 29.51
Std Dev 4.75
Gaus mean : 29.6976
Gaus Width: 3.5911
Entry
ampl
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4
6
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10
0
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40
50
60
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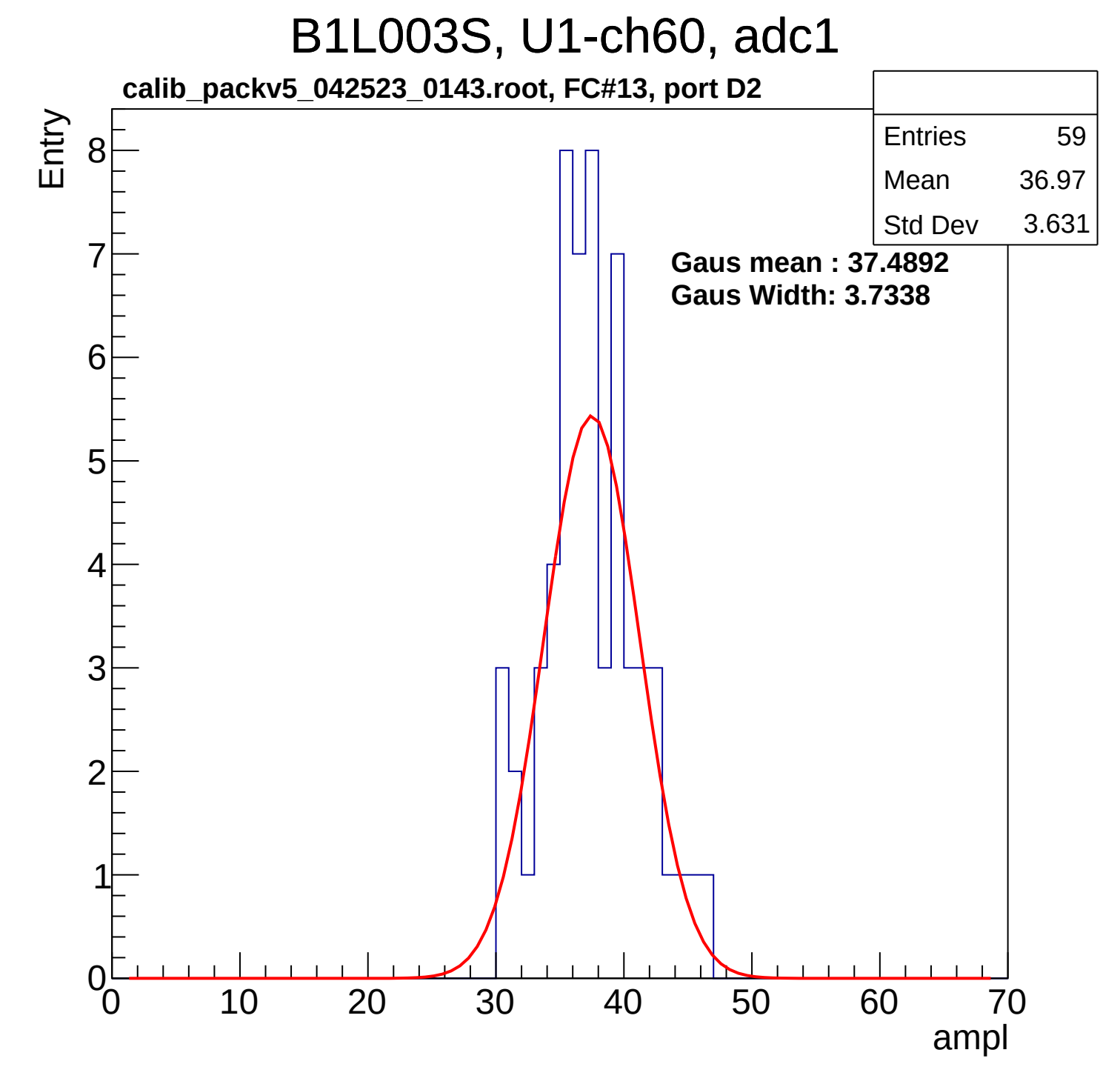
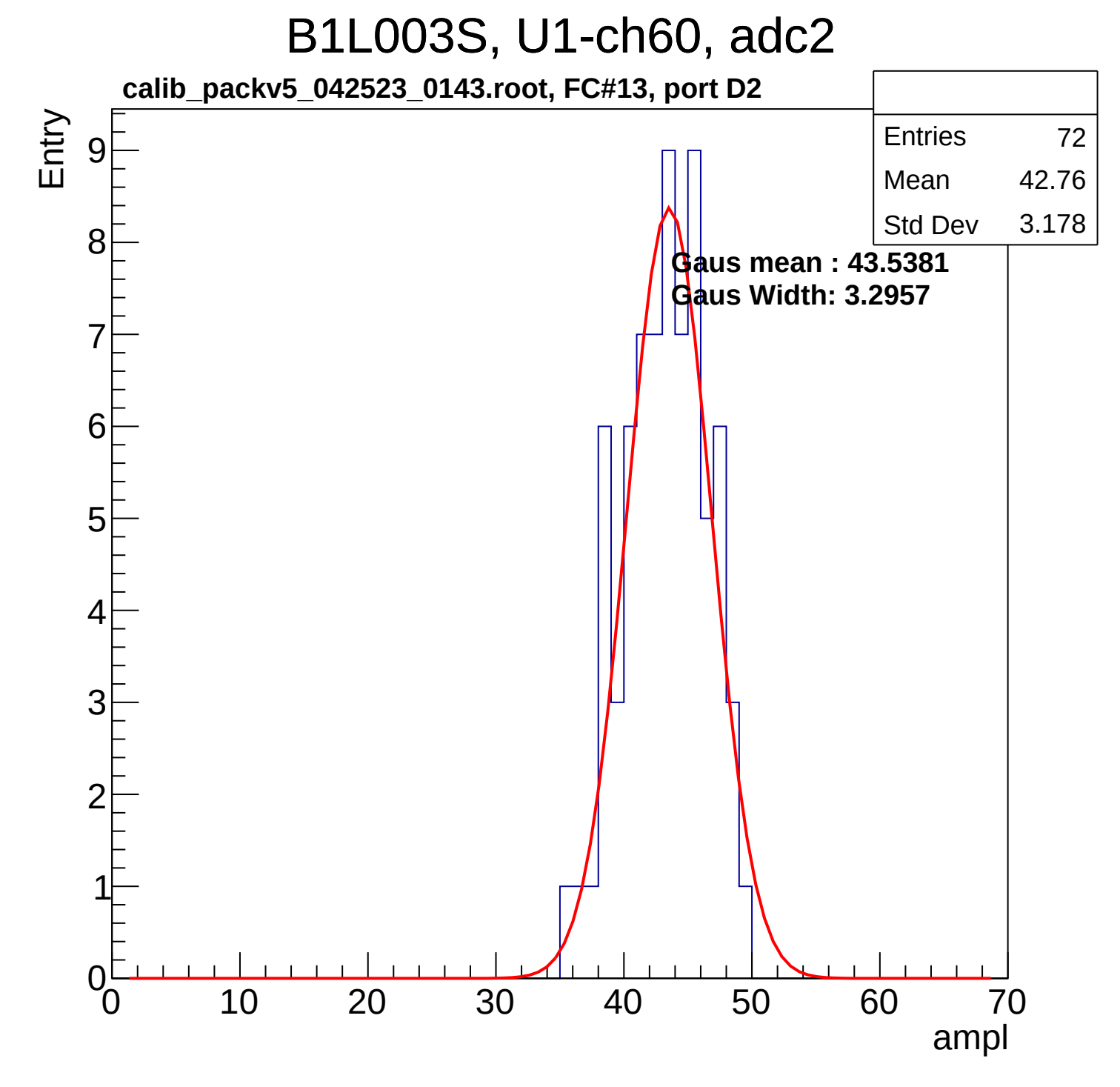

B1L003S, U1-ch60, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.97
Std Dev 3.631
Gaus mean : 37.4892
Gaus Width: 3.7338
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch60, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 42.76
Std Dev 3.178
Gaus mean : 43.5381
Gaus Width: 3.2957
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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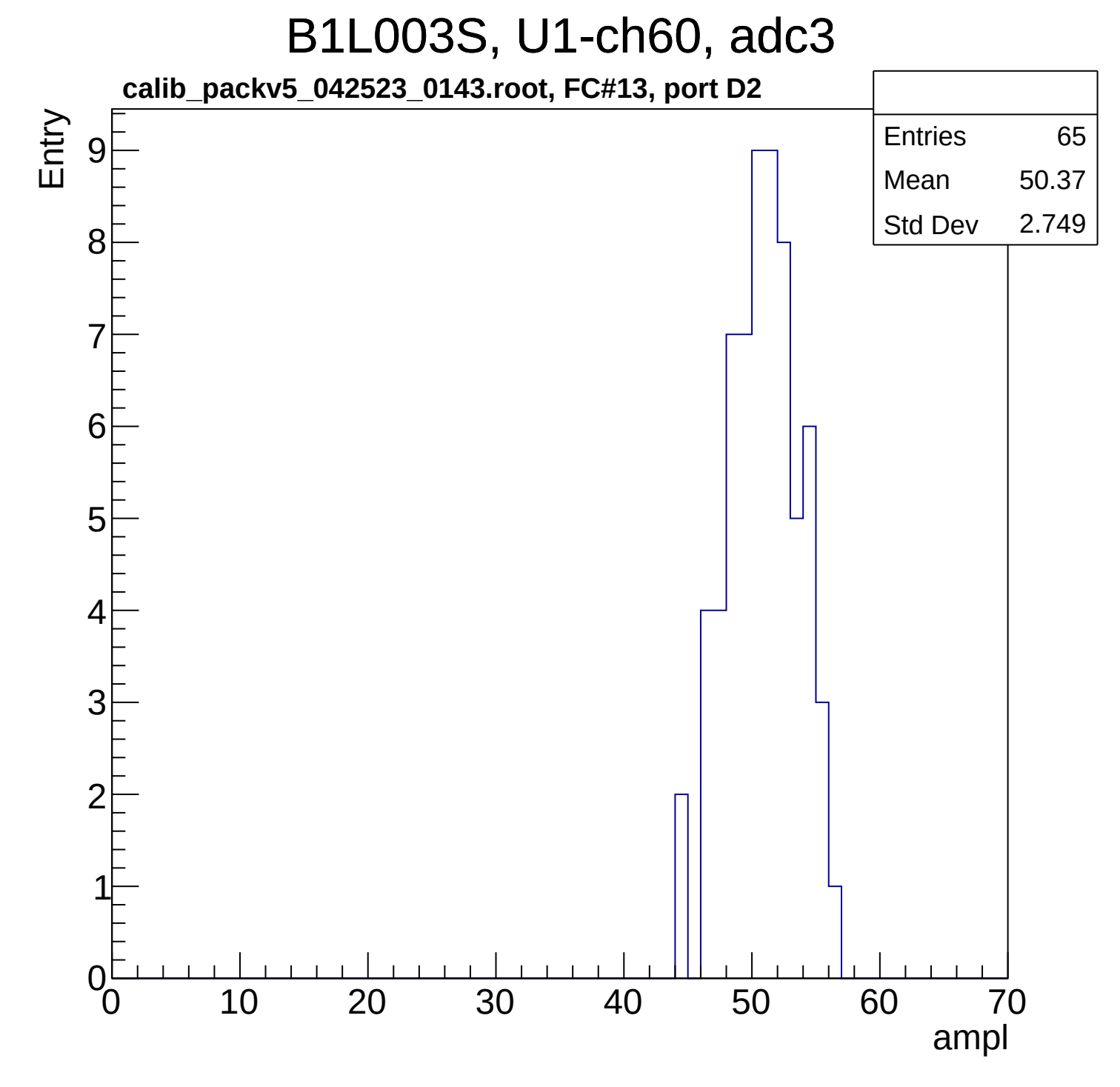
B1L003S, U1-ch60, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.37
Std Dev 2.749
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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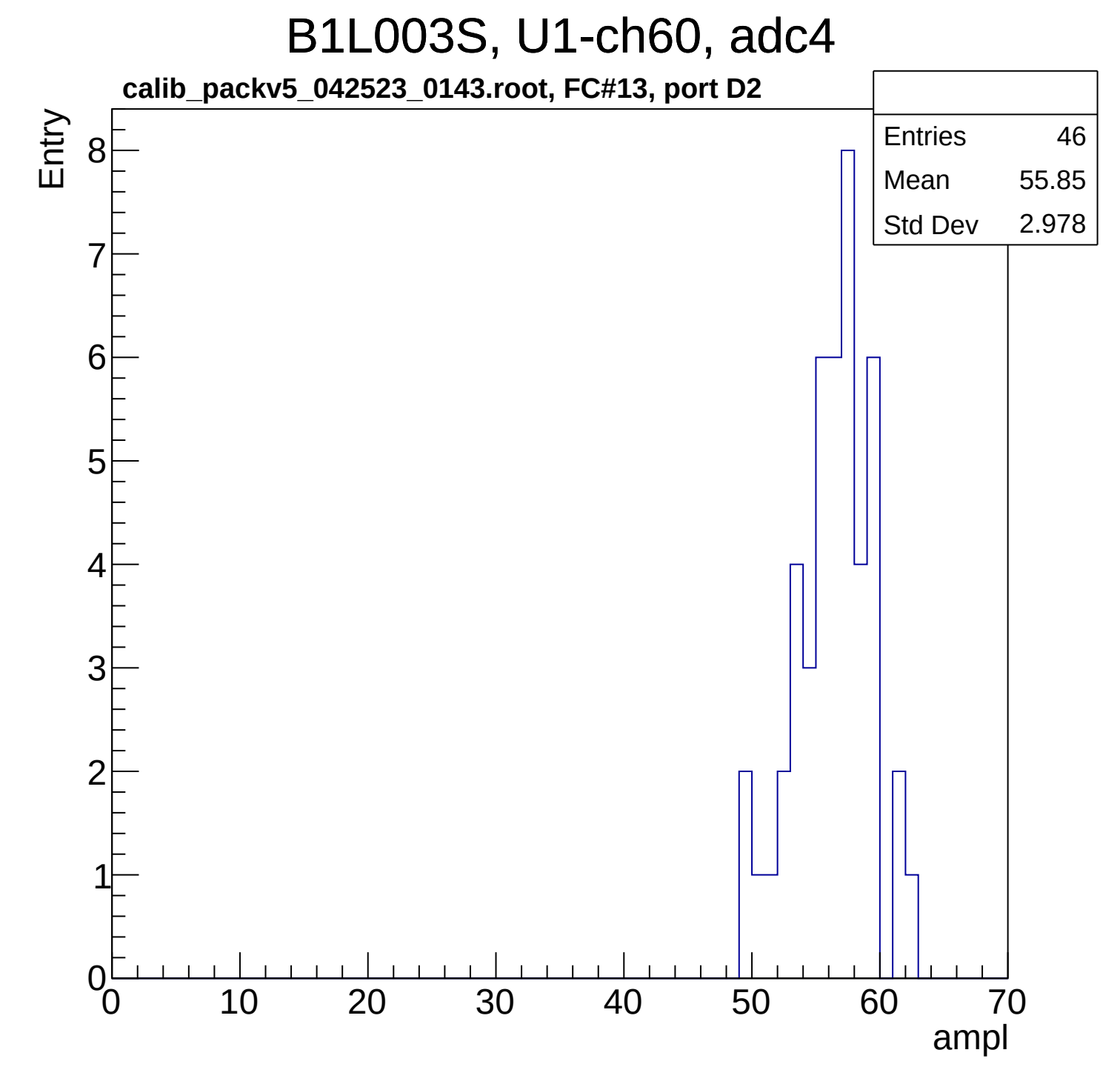
B1L003S, U1-ch60, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.85
Std Dev 2.978
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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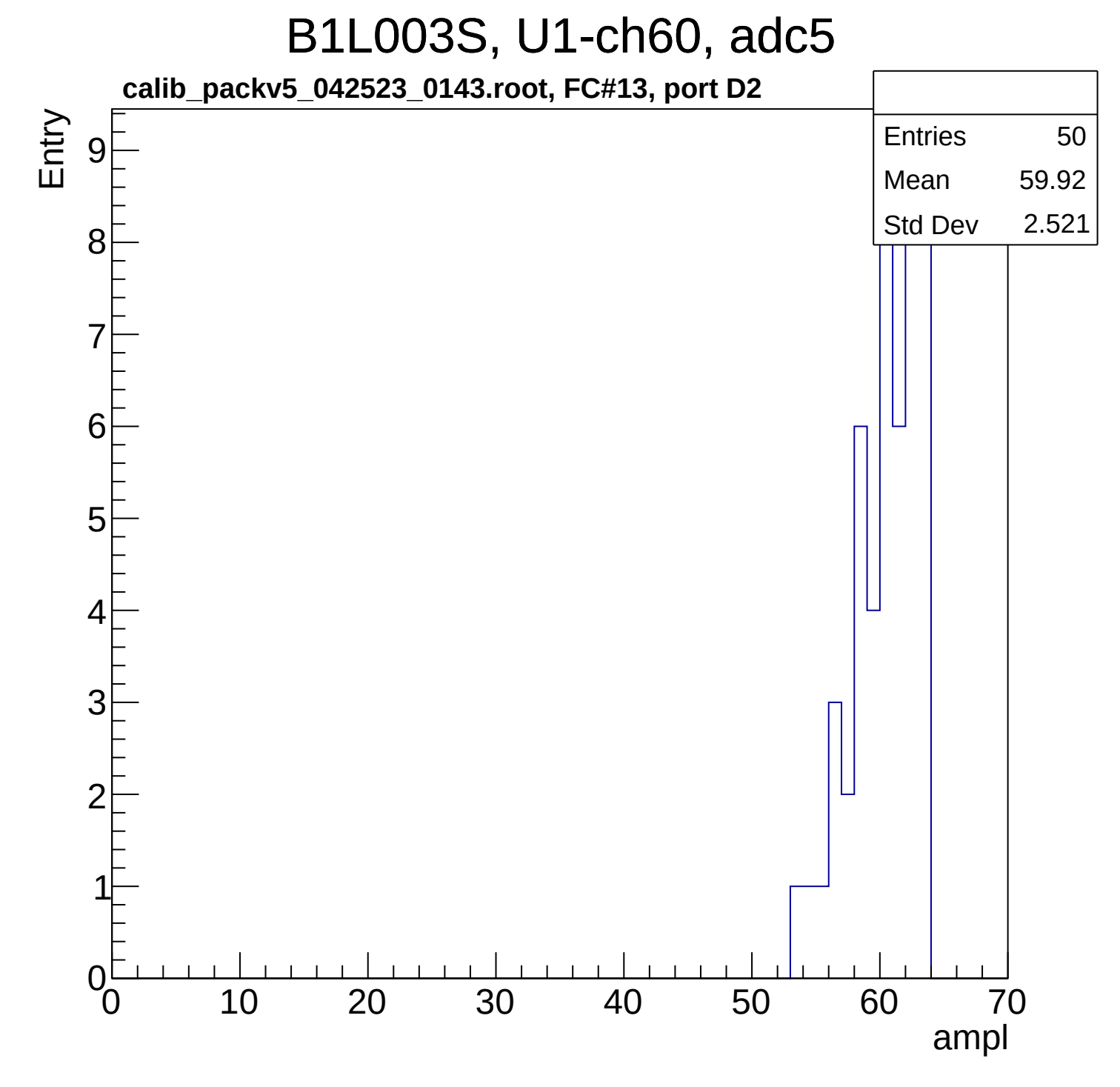
B1L003S, U1-ch60, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.92
Std Dev 2.521
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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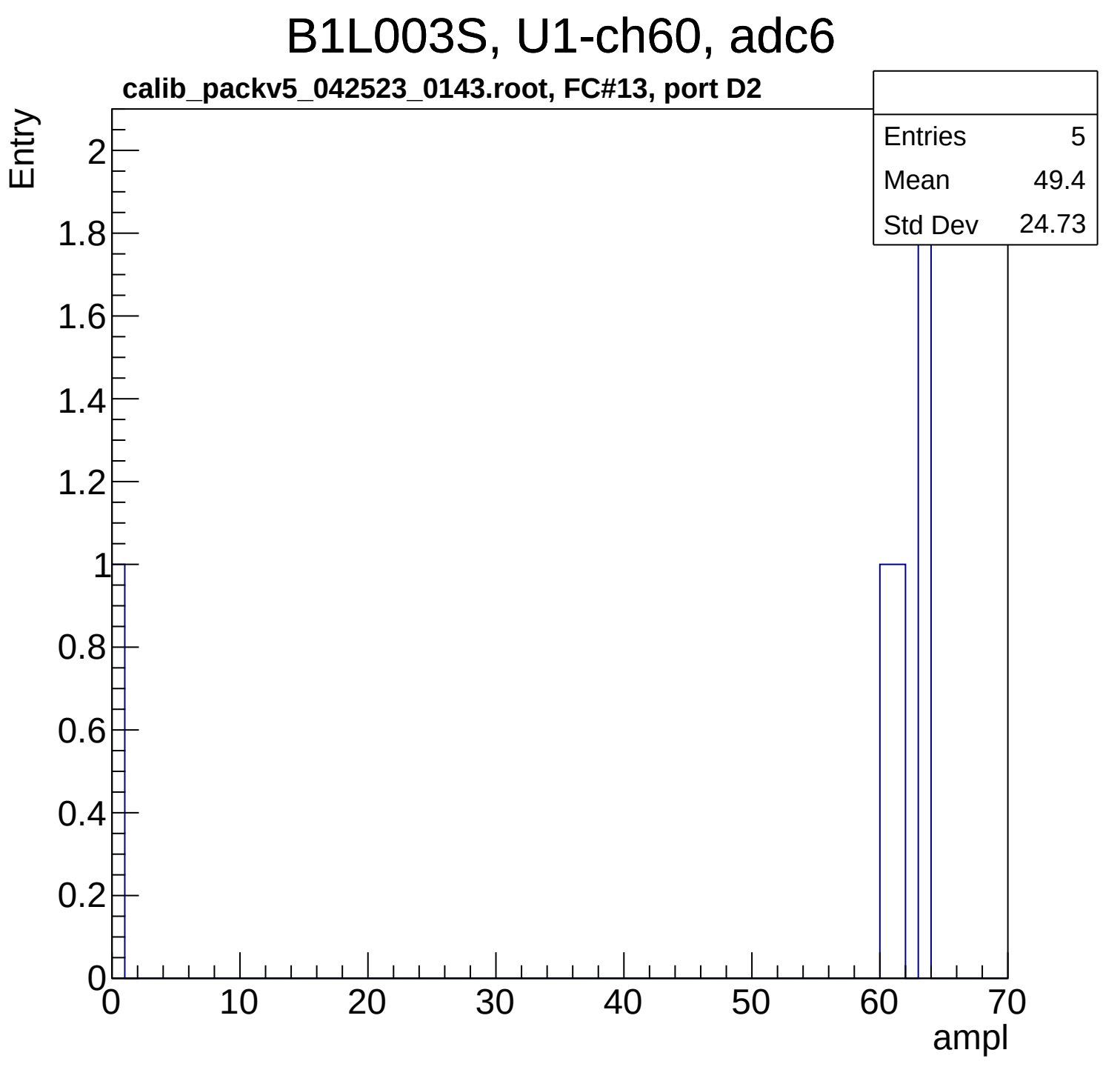
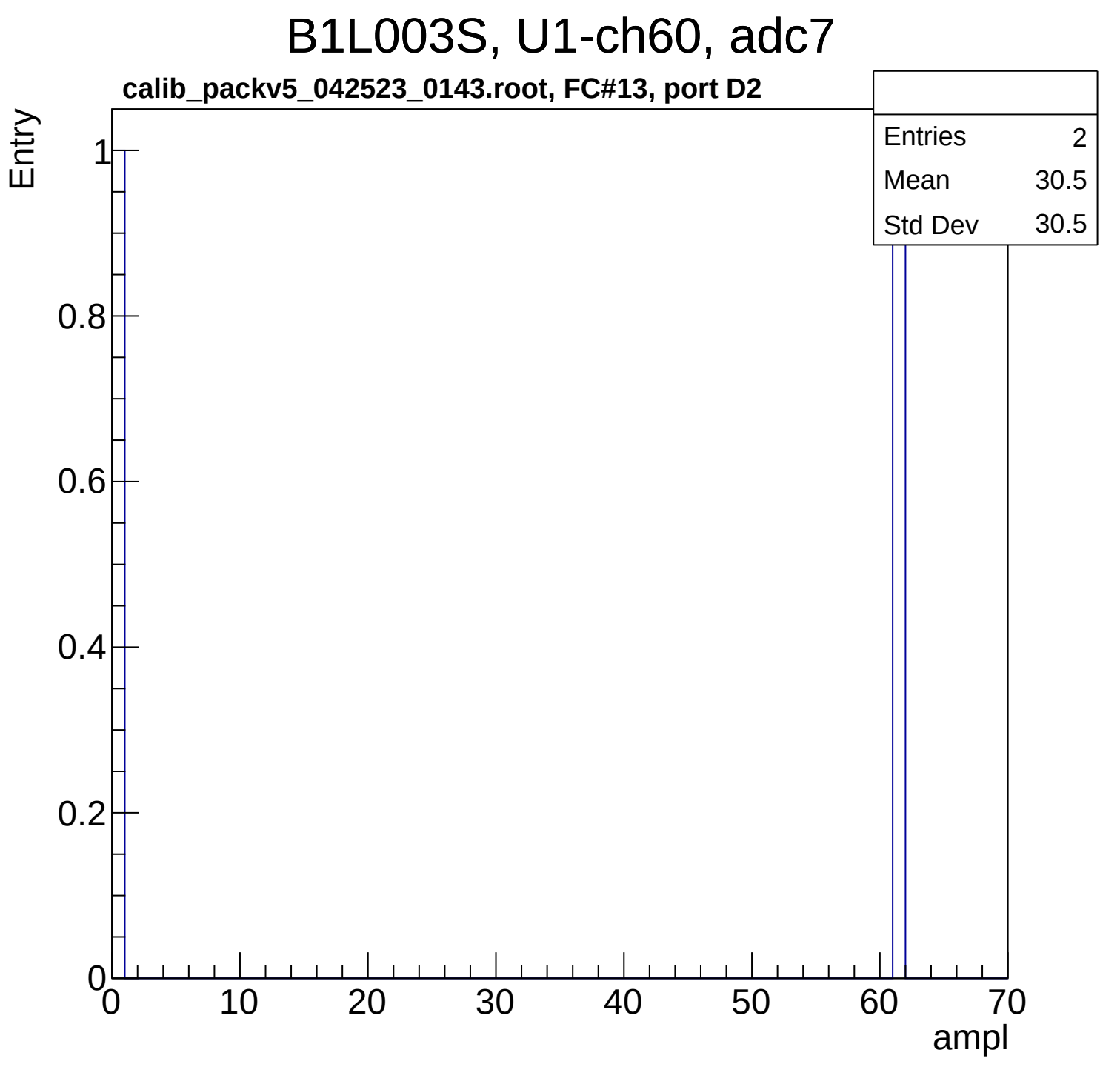
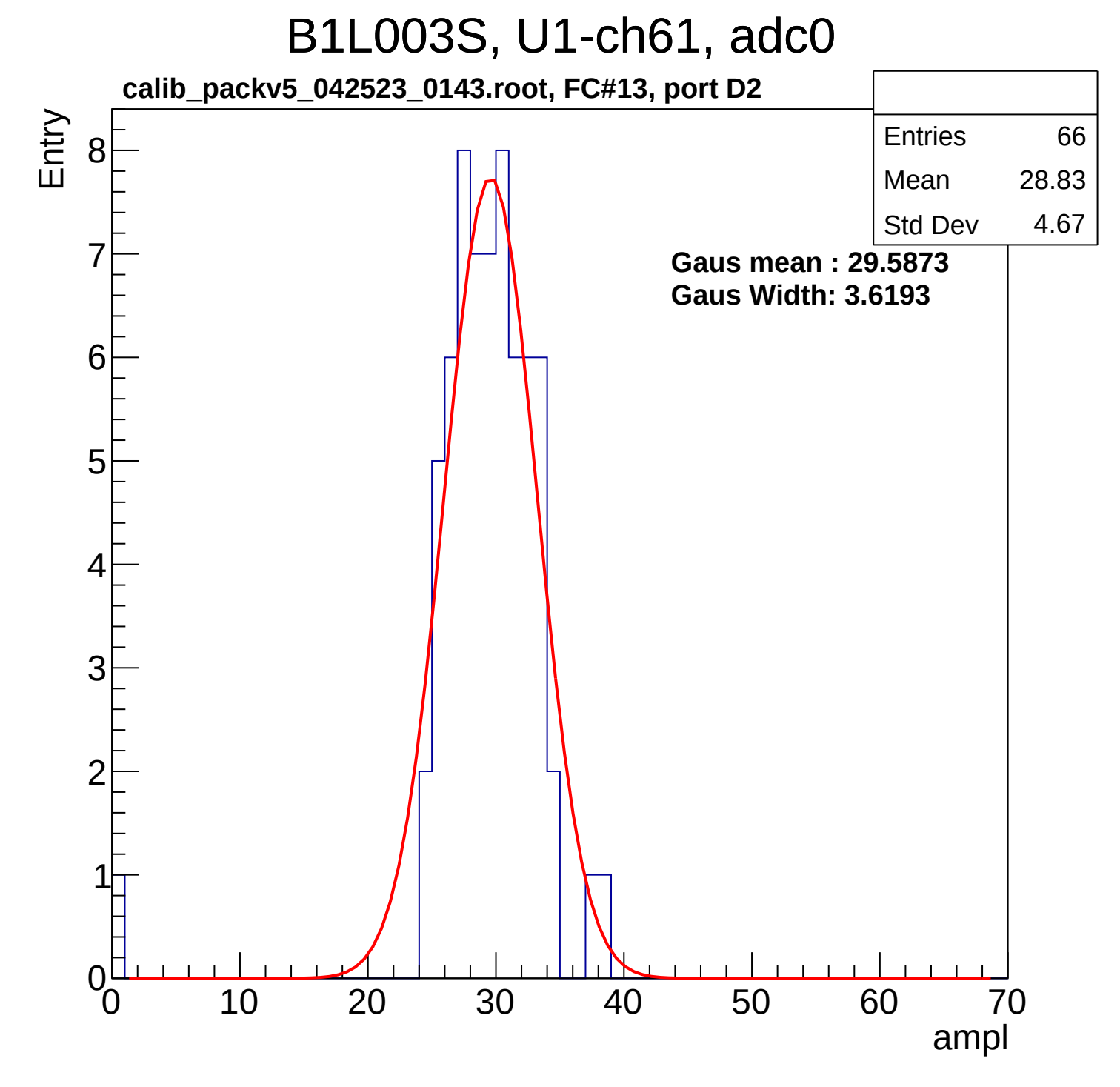

B1L003S, U1-ch60, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 49.4
Std Dev 24.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch60, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 30.5
Std Dev 30.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch61, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.83
Std Dev 4.67
Gaus mean : 29.5873
Gaus Width: 3.6193
Entry
ampl
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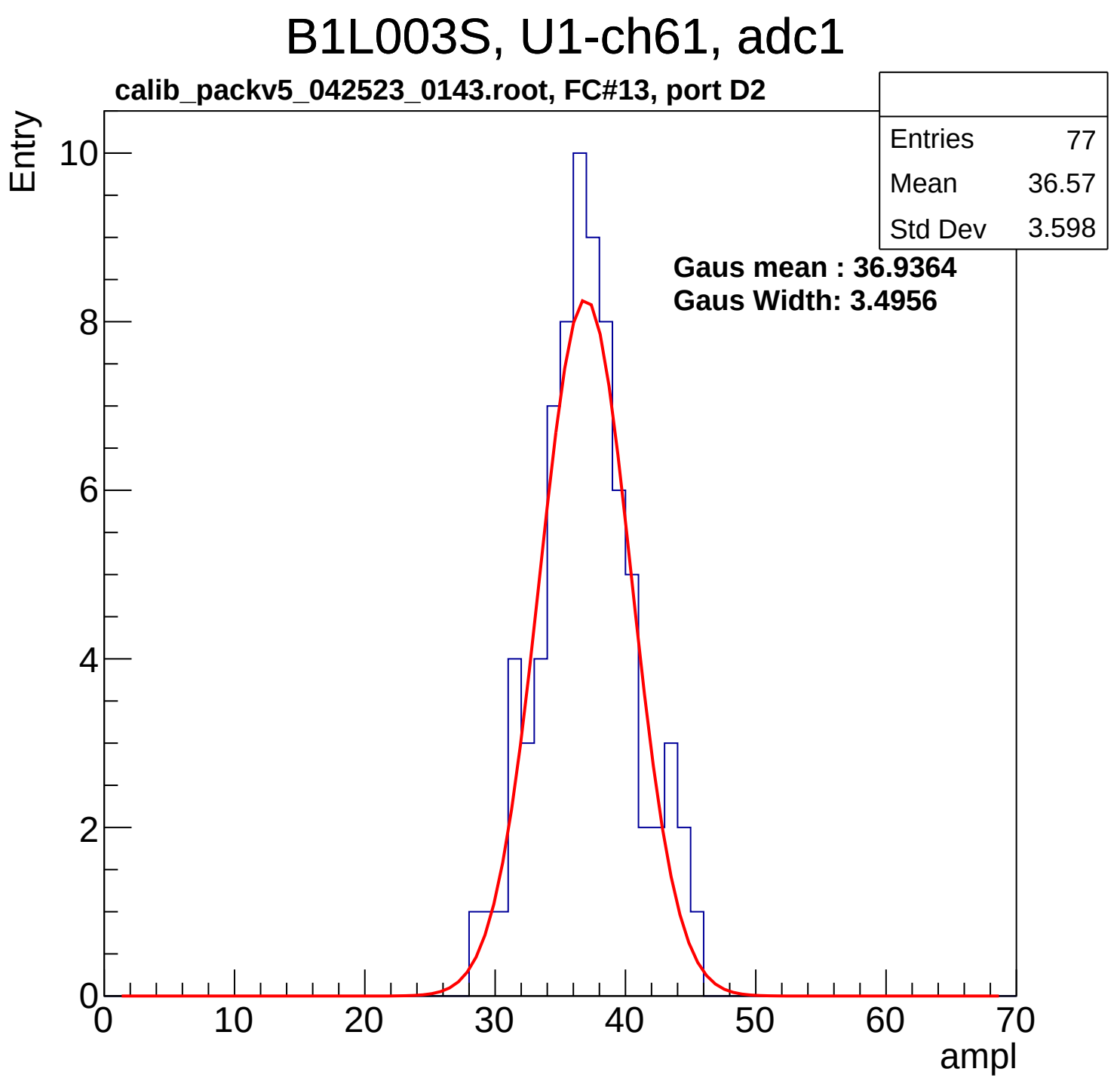
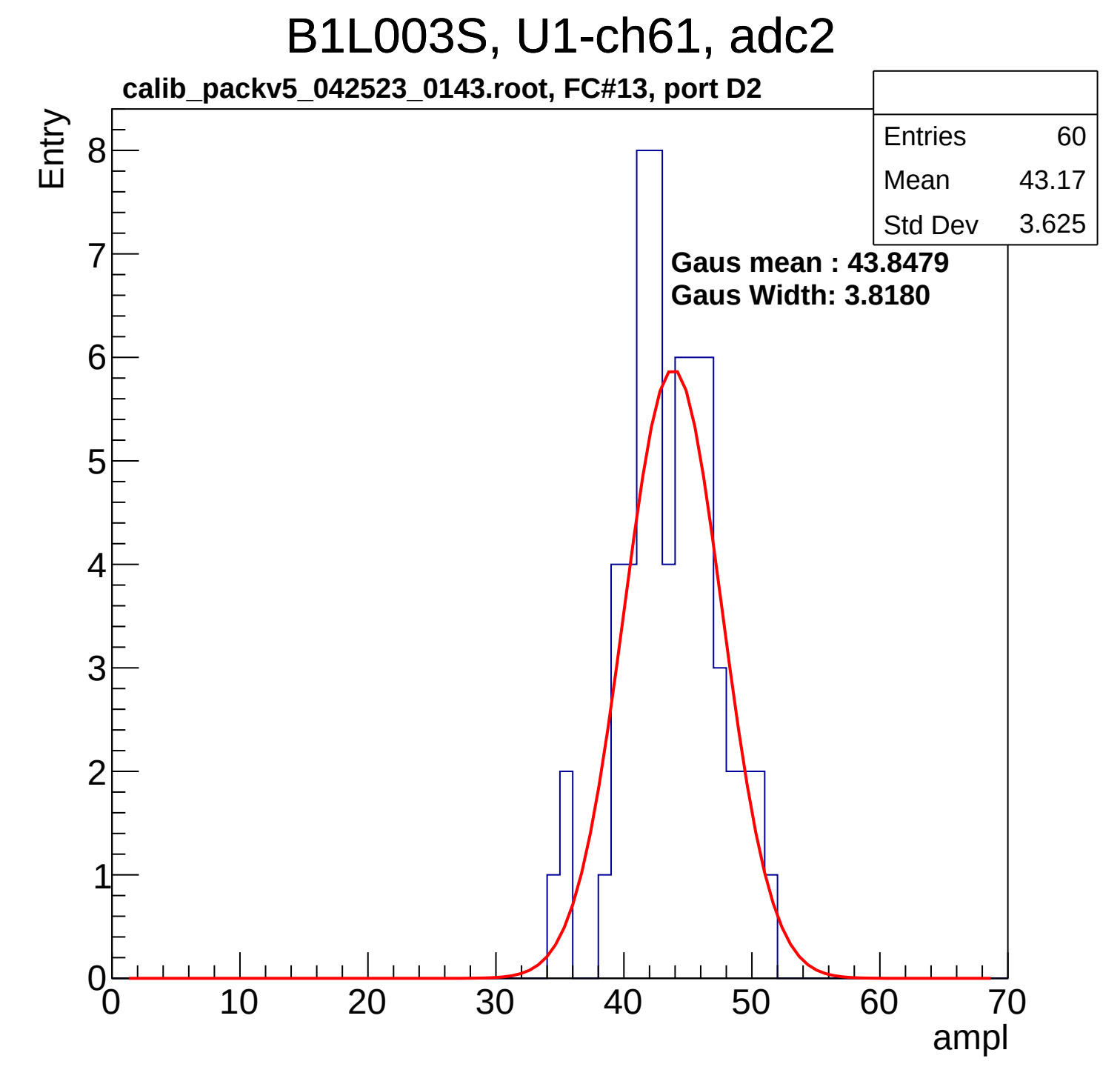
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch61, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 36.57
Std Dev 3.598
Gaus mean : 36.9364
Gaus Width: 3.4956
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch61, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 43.17
Std Dev 3.625
Gaus mean : 43.8479
Gaus Width: 3.8180
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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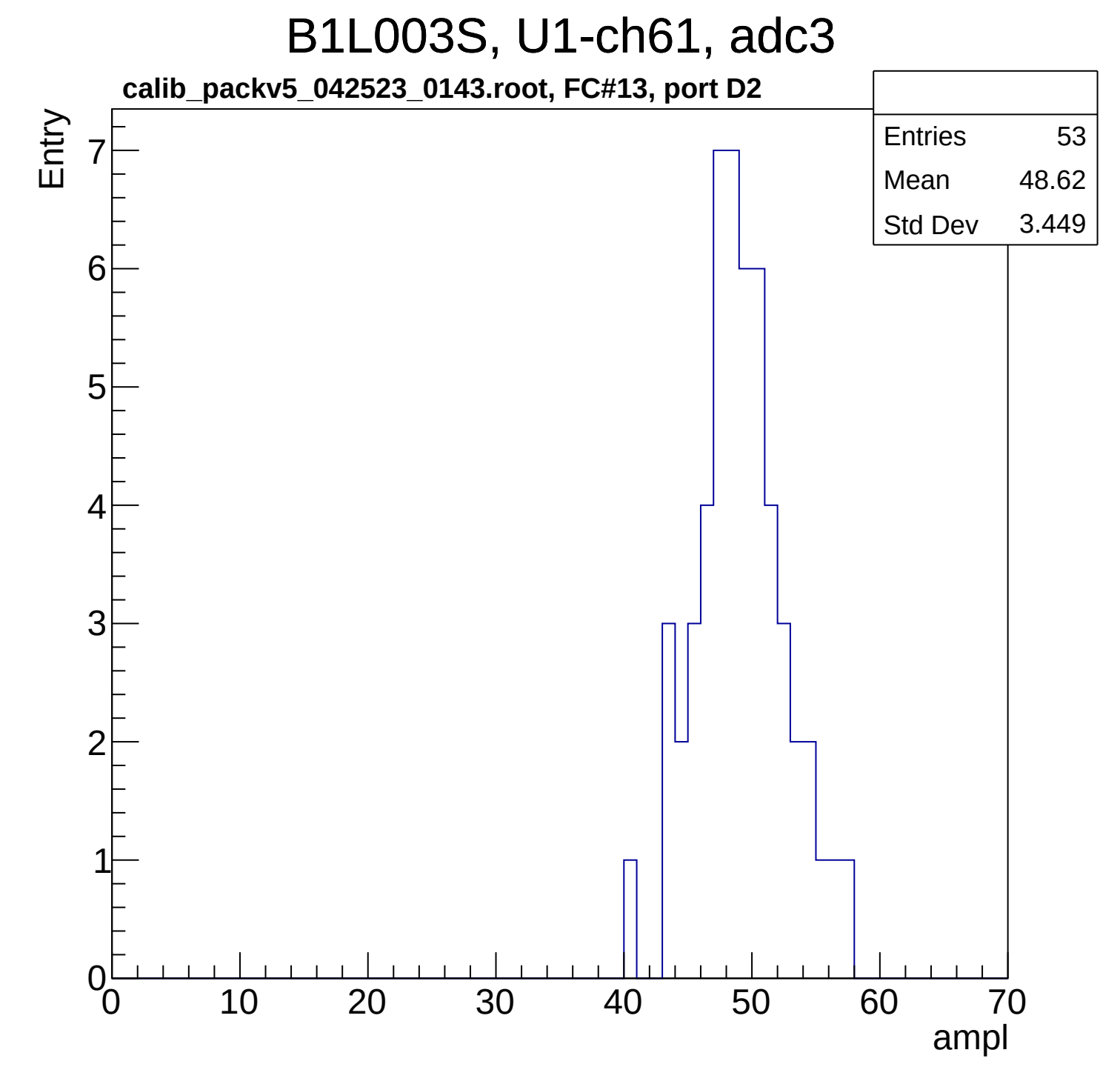
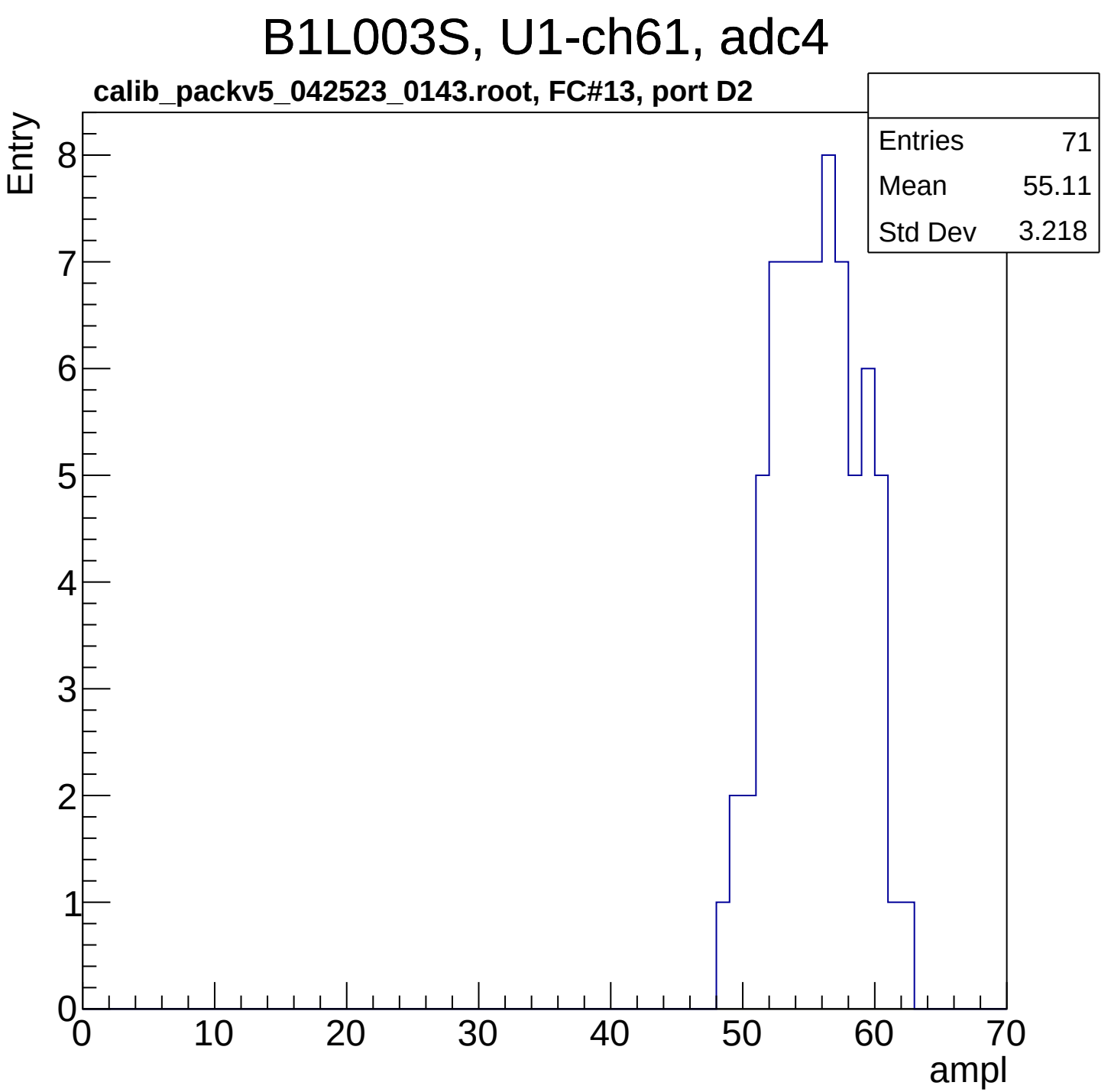
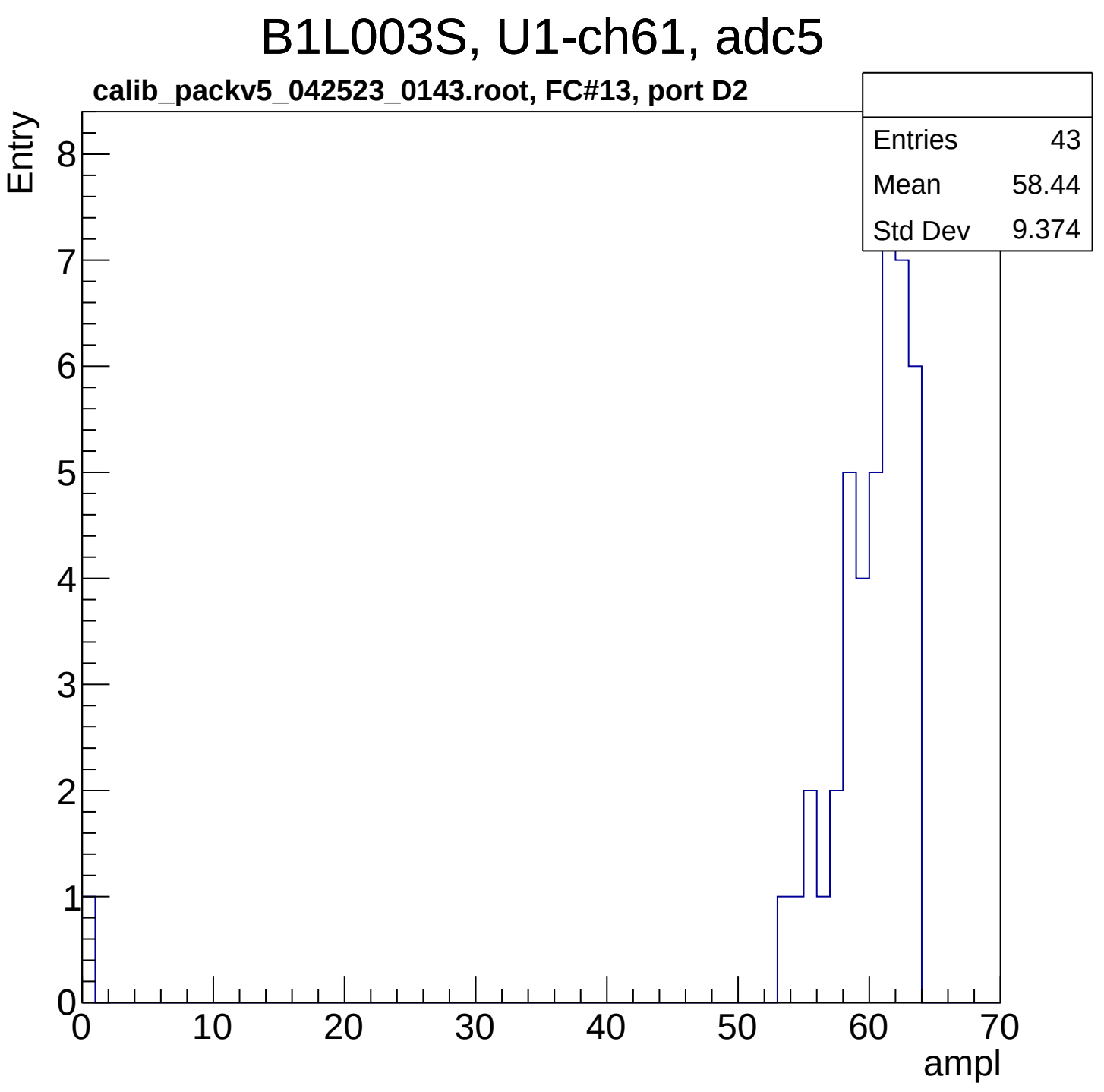
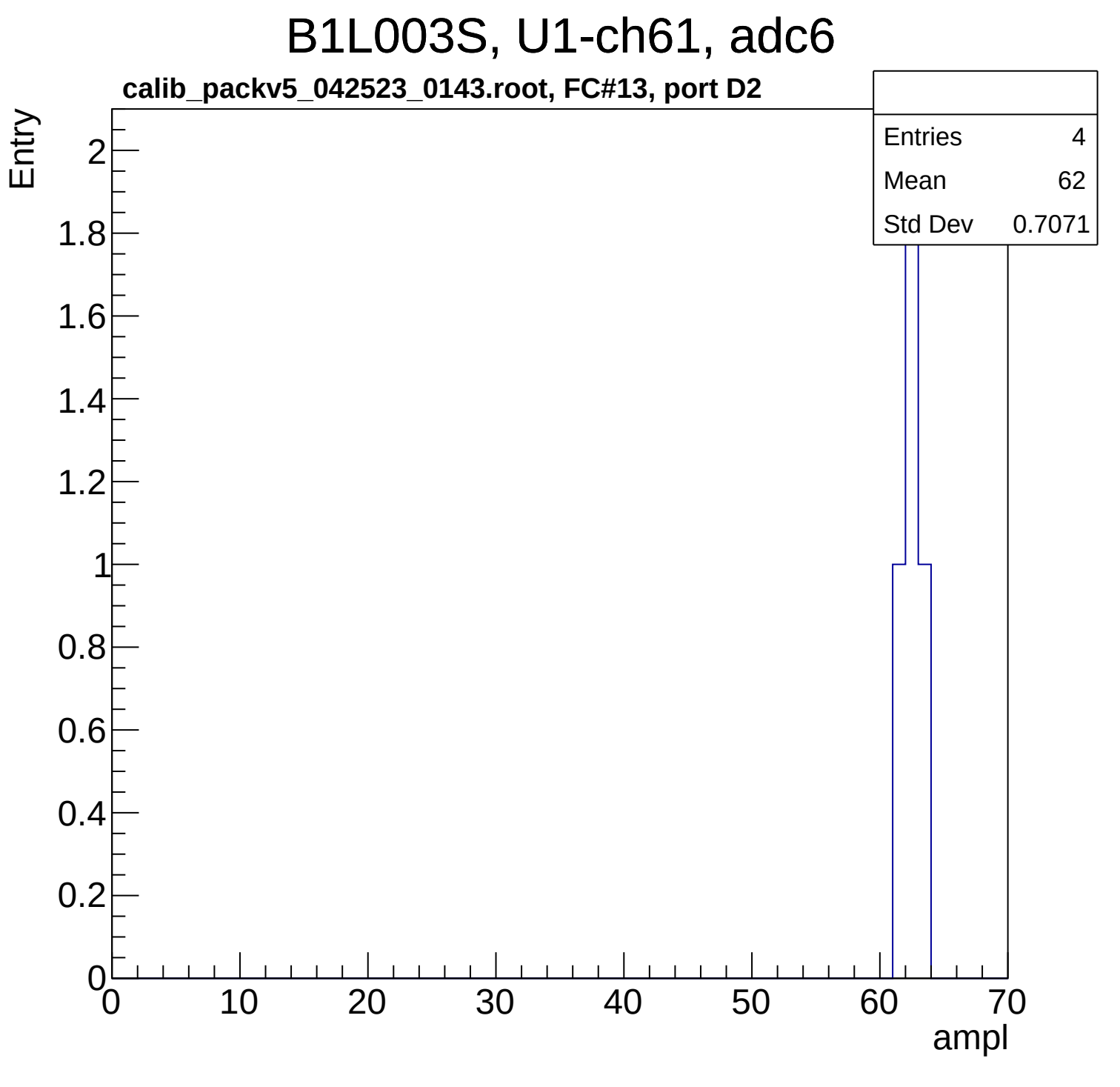
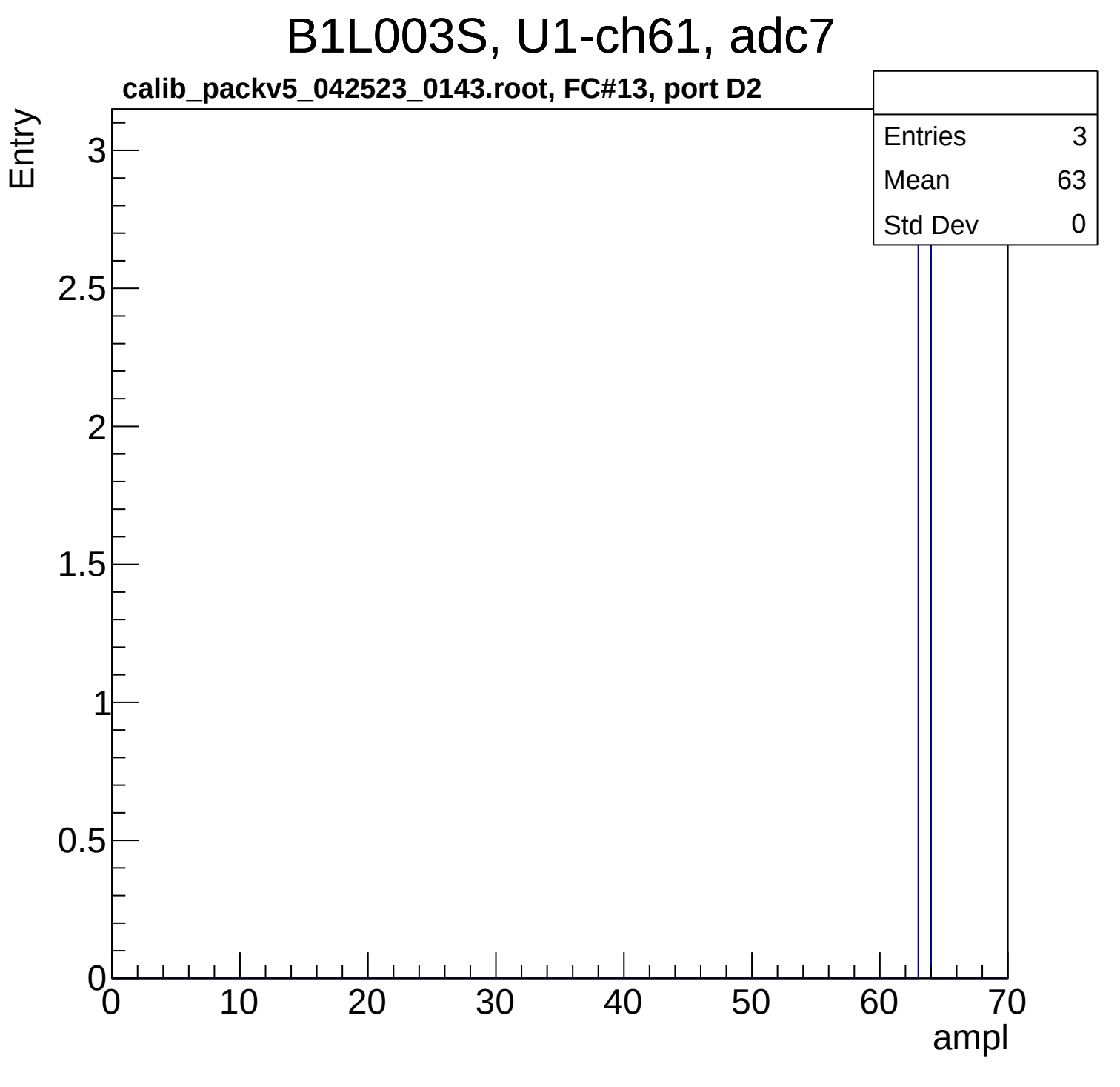
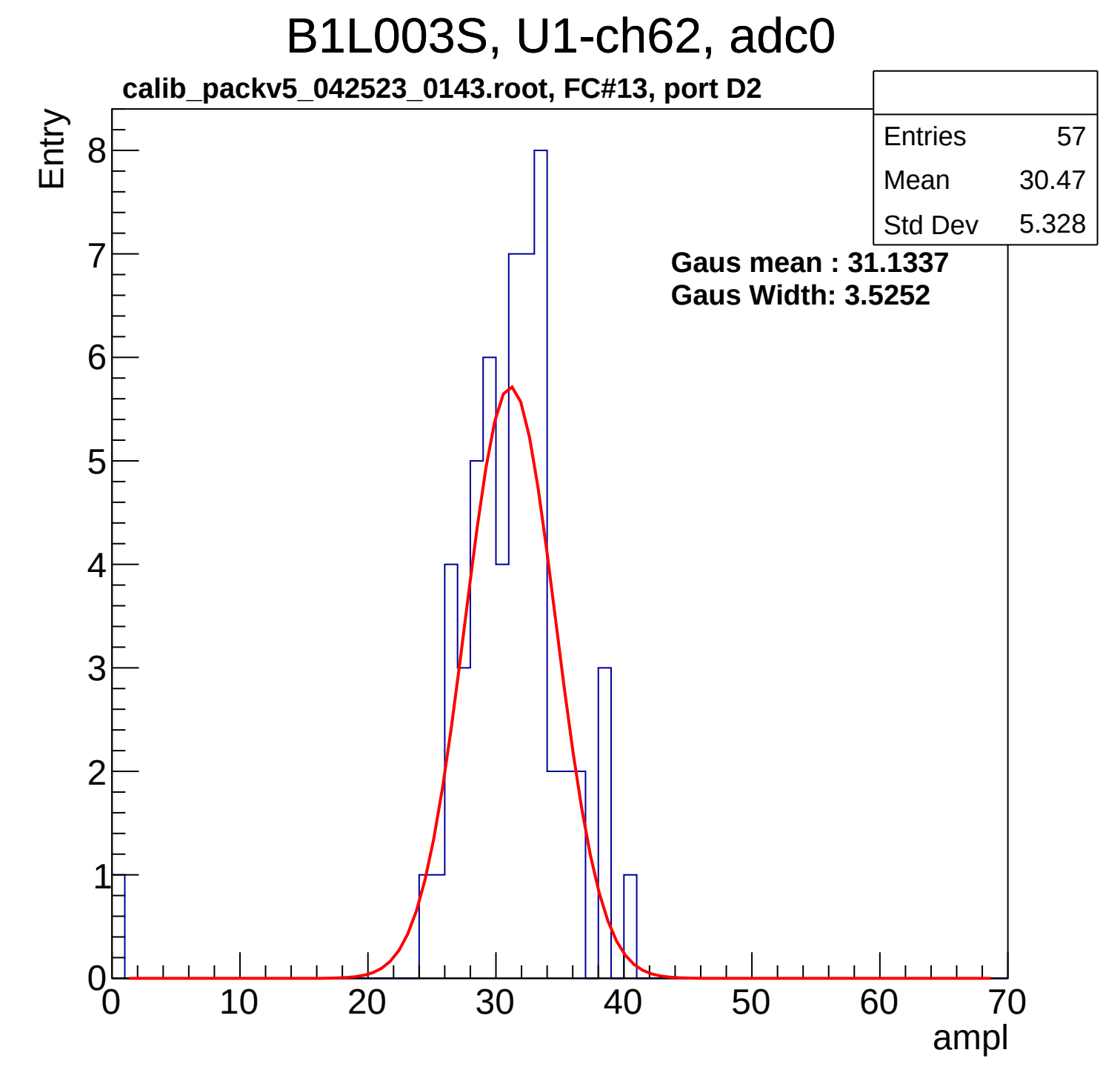

B1L003S, U1-ch61, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.62
Std Dev 3.449
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

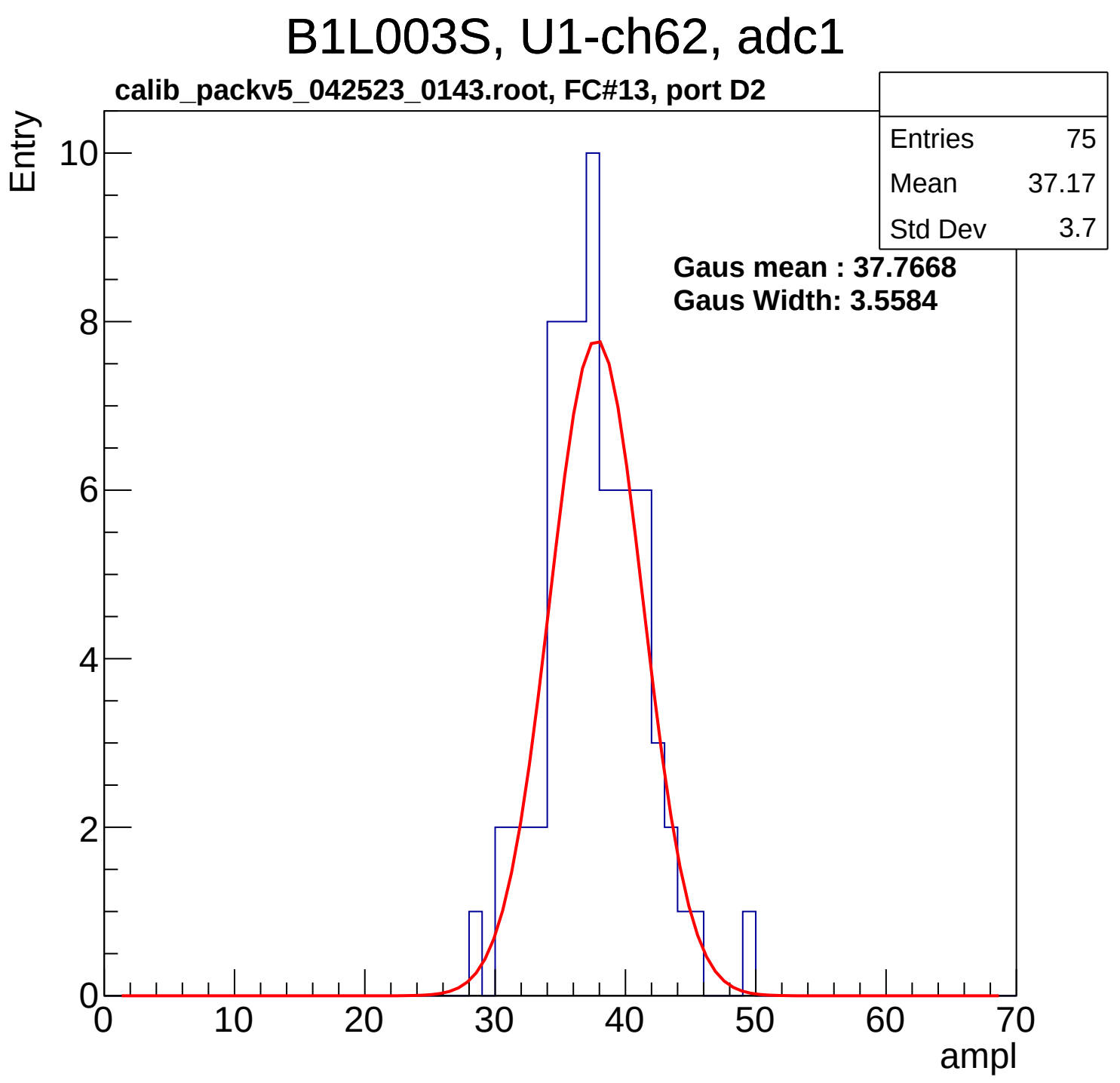

B1L003S, U1-ch61, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 55.11
Std Dev 3.218
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

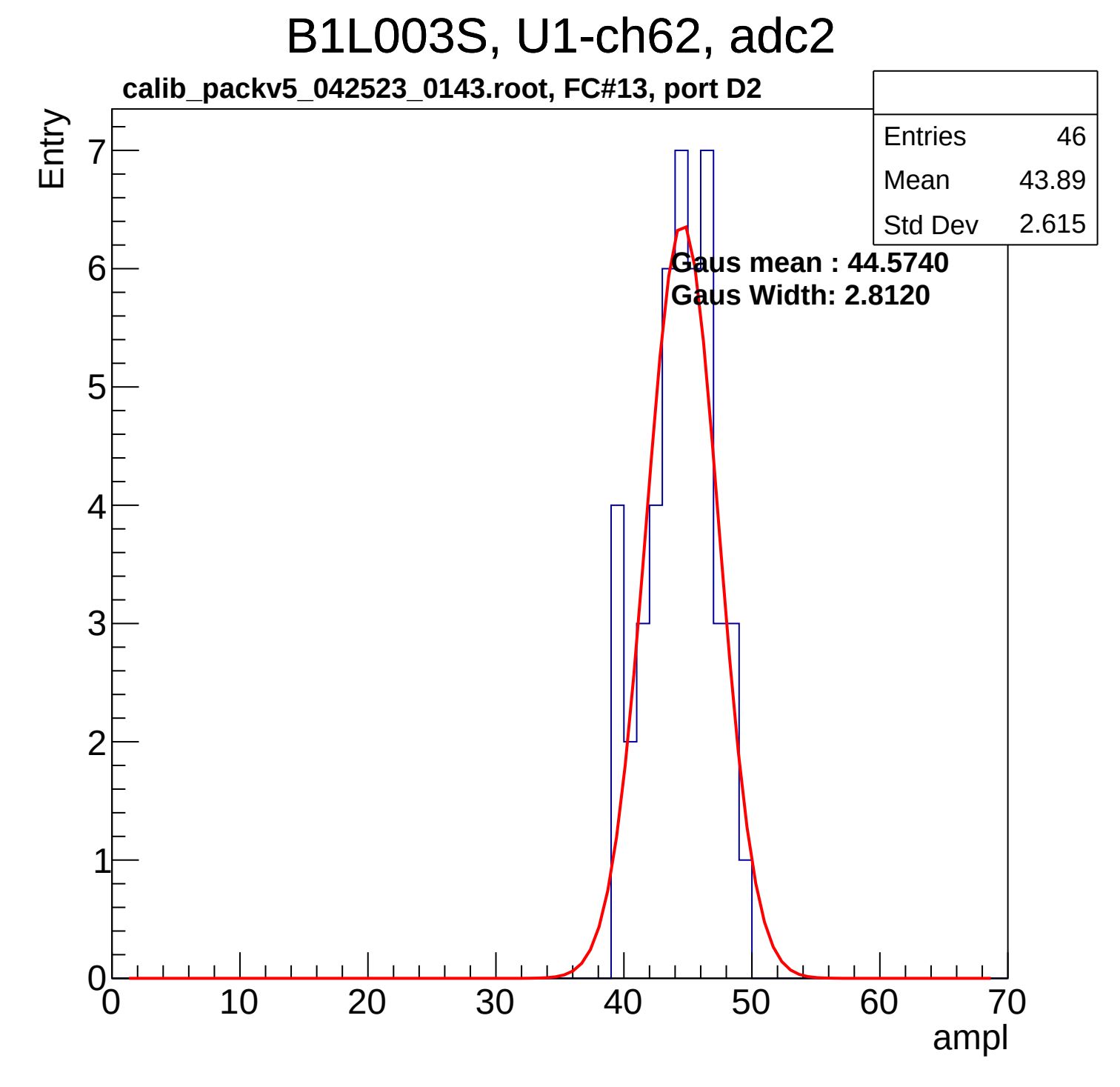

B1L003S, U1-ch61, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.44
Std Dev 9.374
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

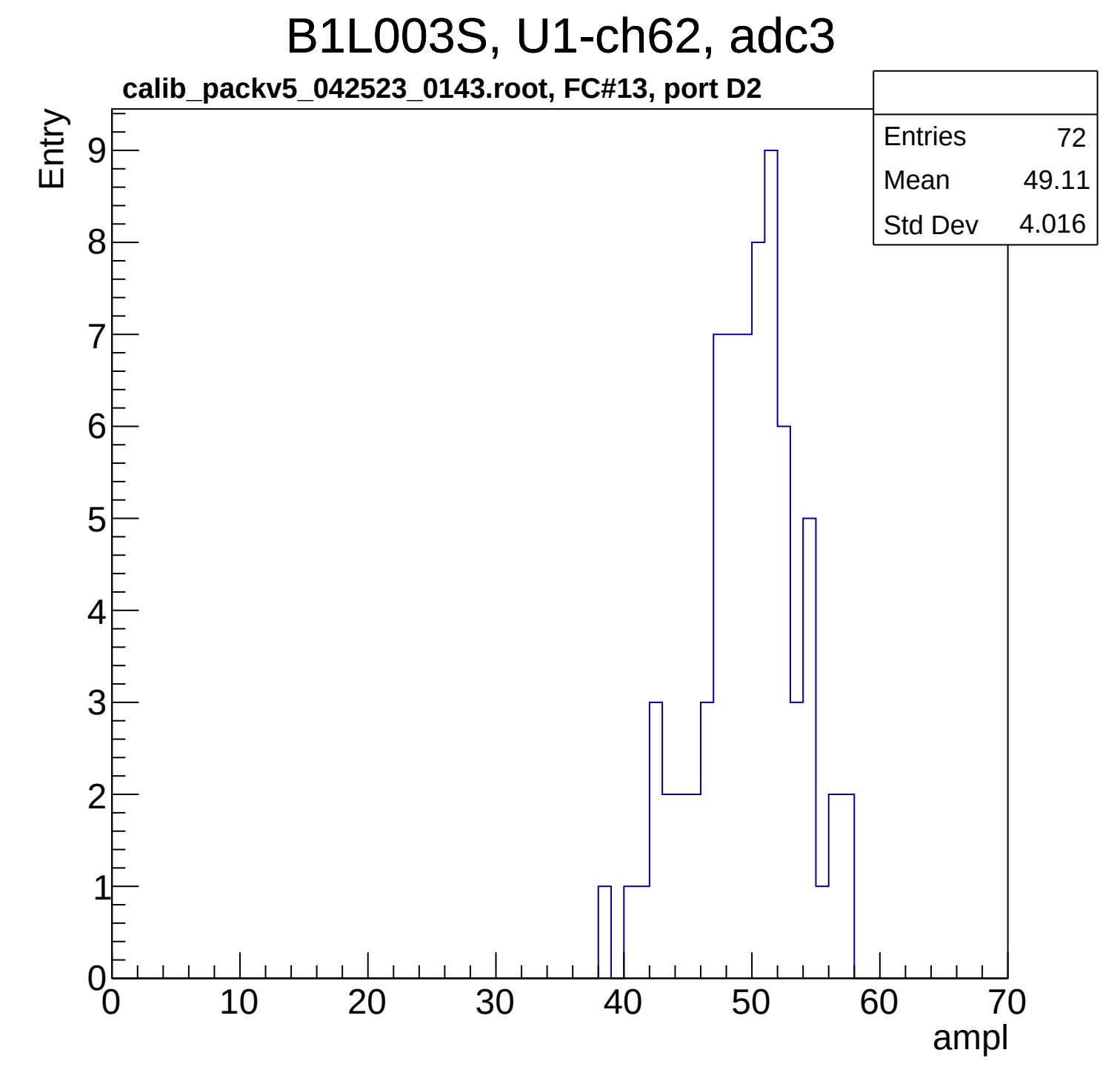

B1L003S, U1-ch61, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch61, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 30.47
Std Dev 5.328
Gaus mean : 31.1337
Gaus Width: 3.5252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 37.17
Std Dev 3.7
Gaus mean : 37.7668
Gaus Width: 3.5584
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 43.89
Std Dev 2.615
Gaus mean : 44.5740
Gaus Width: 2.8120
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 49.11
Std Dev 4.016
Entry
ampl
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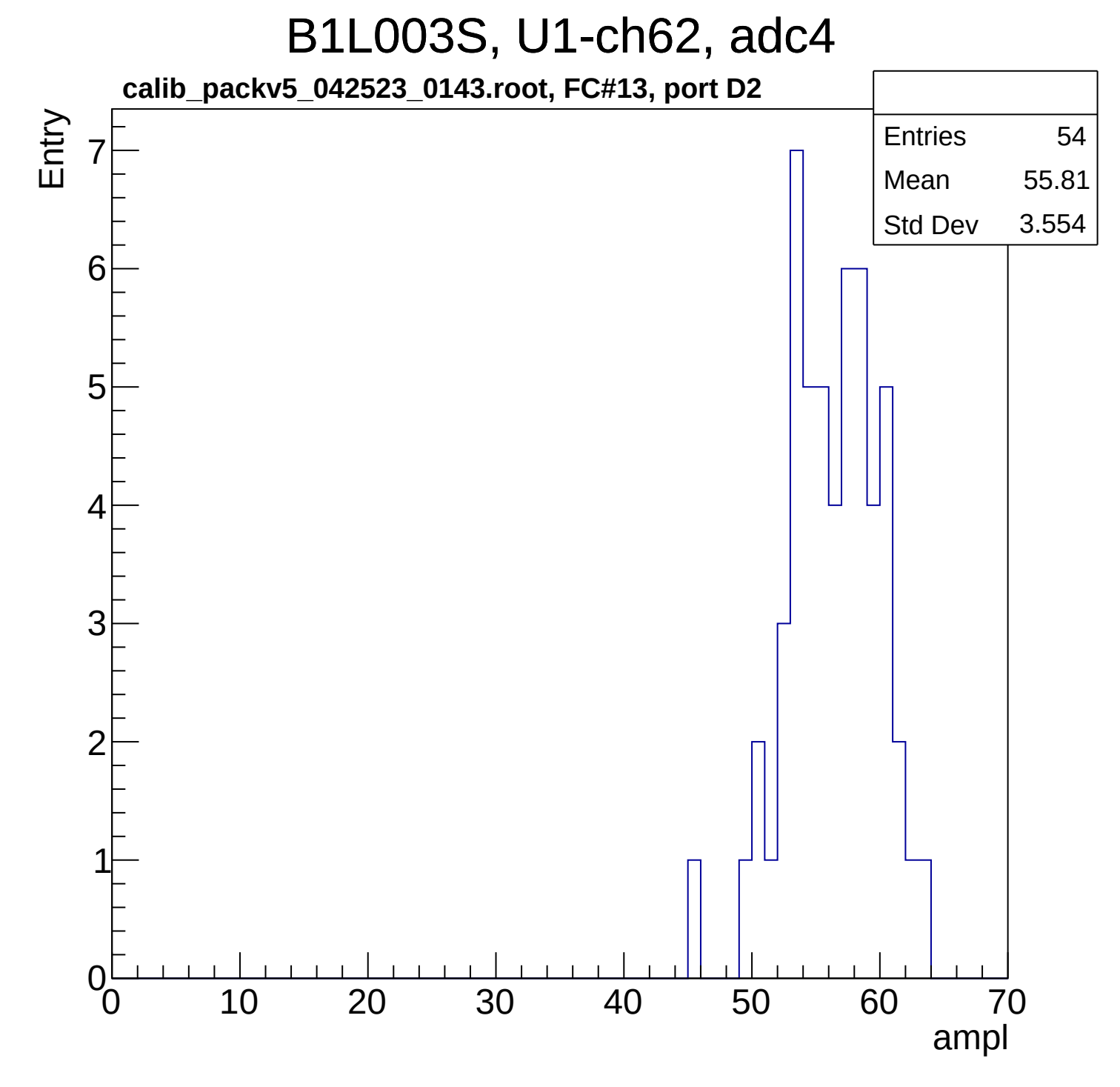
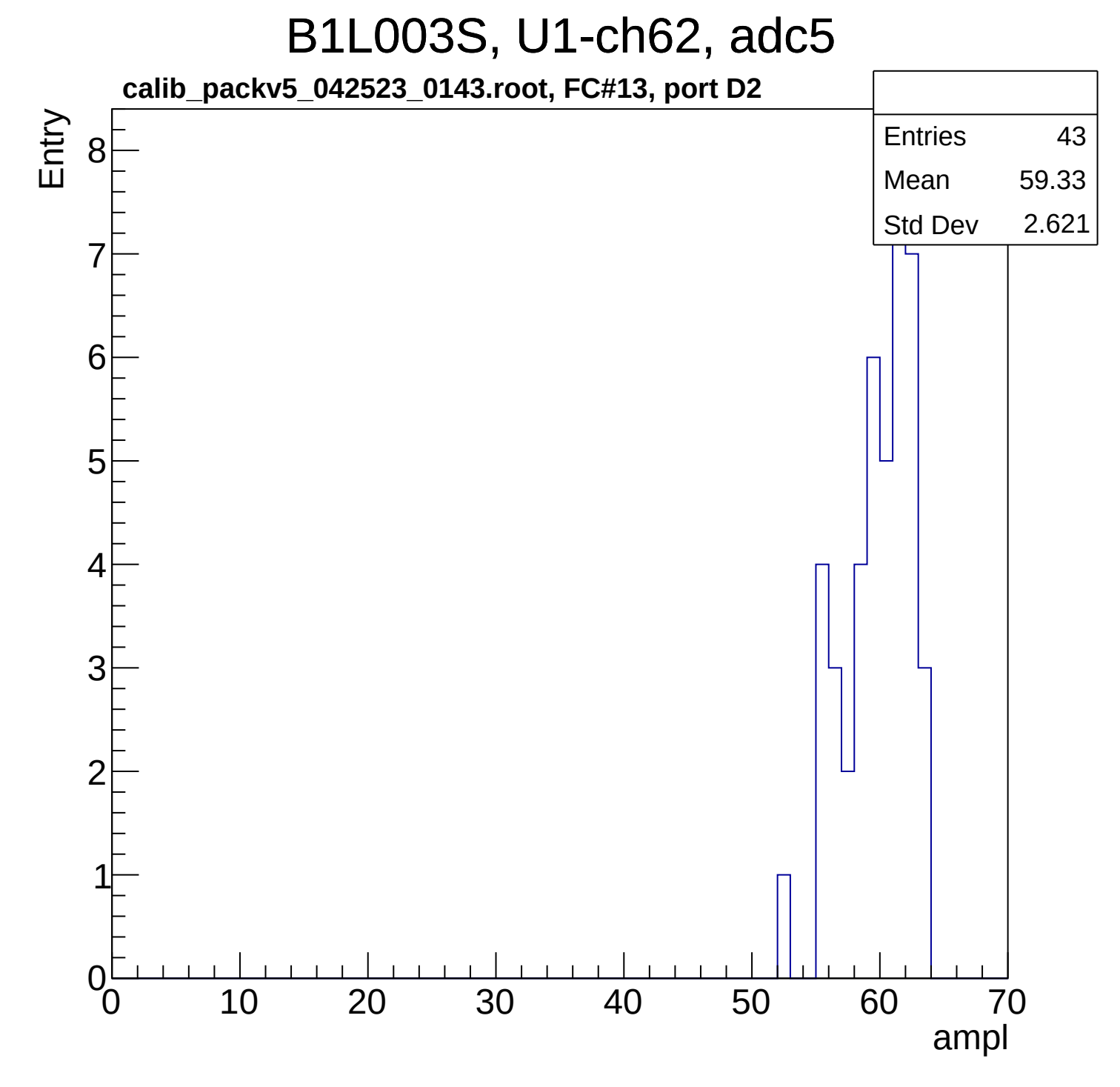
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 55.81
Std Dev 3.554
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.33
Std Dev 2.621
Entry
ampl
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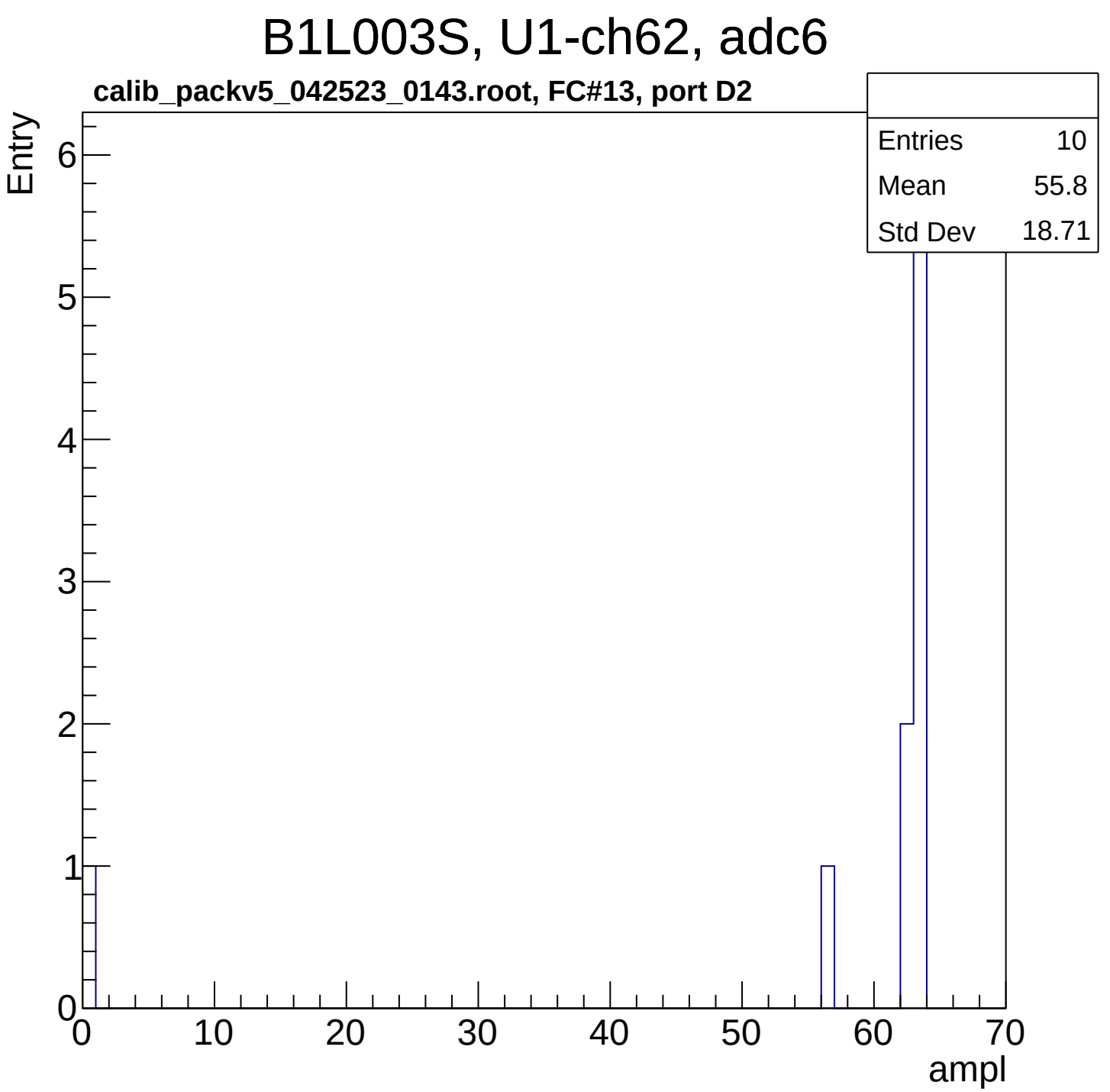
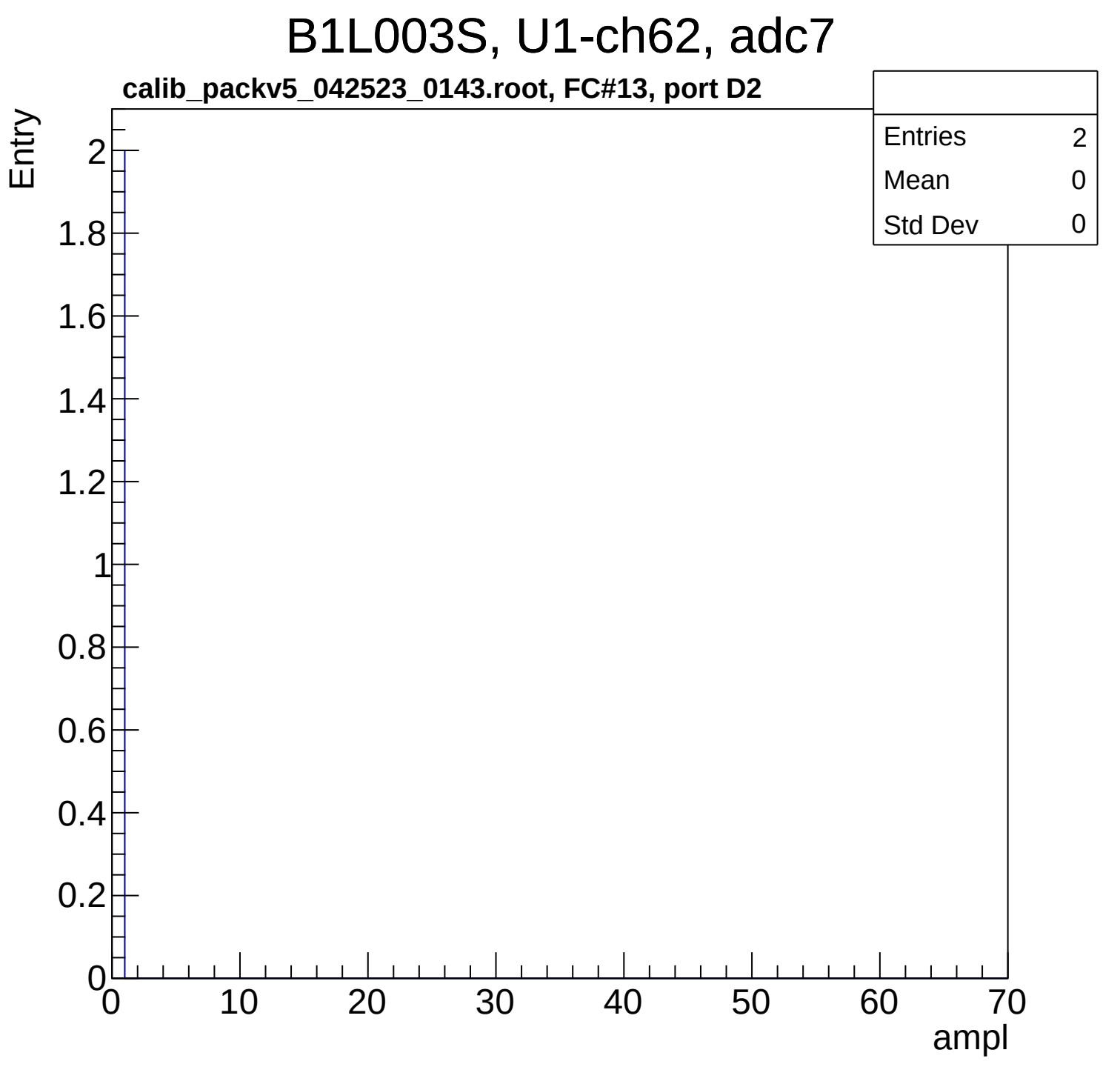
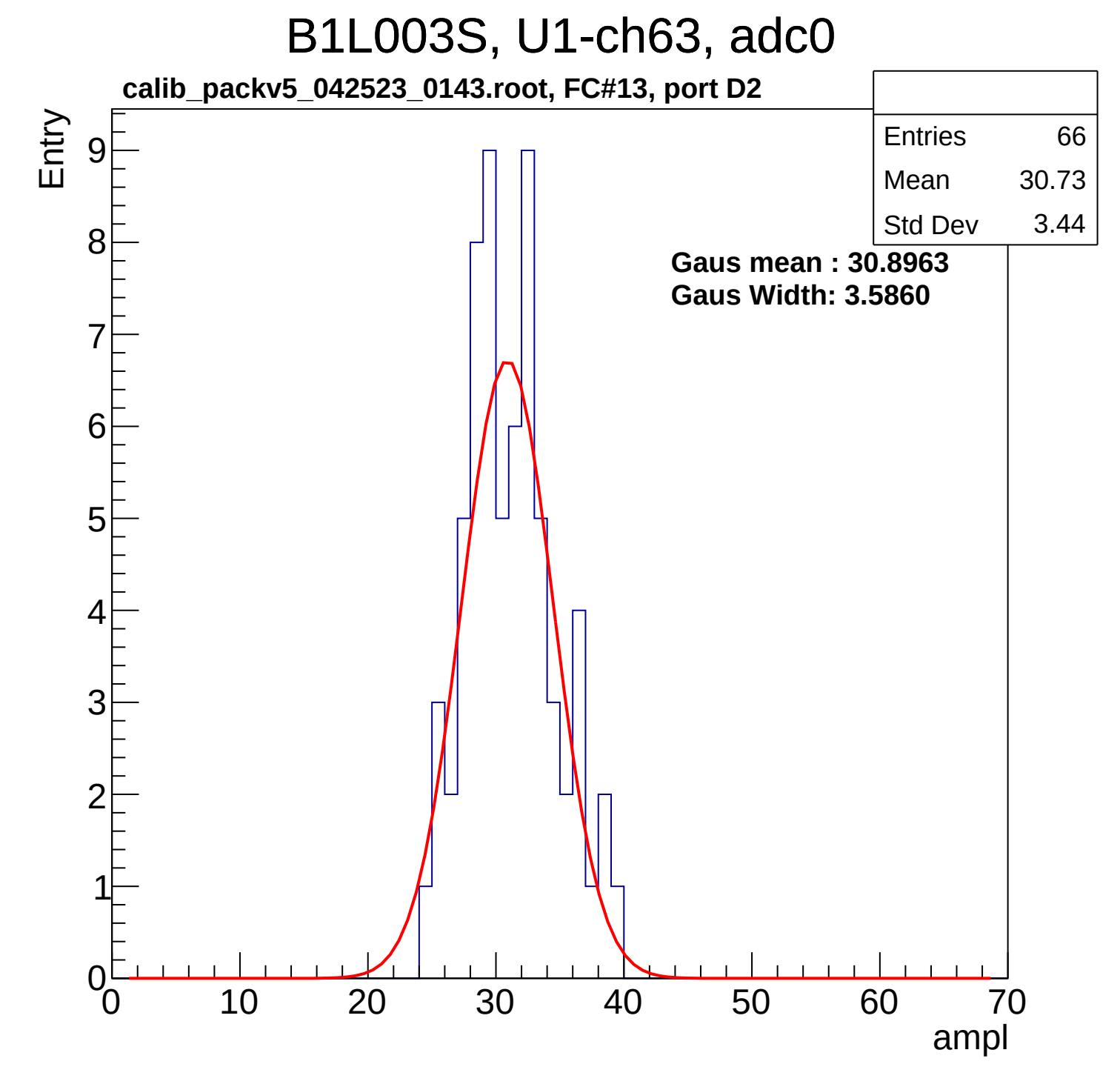
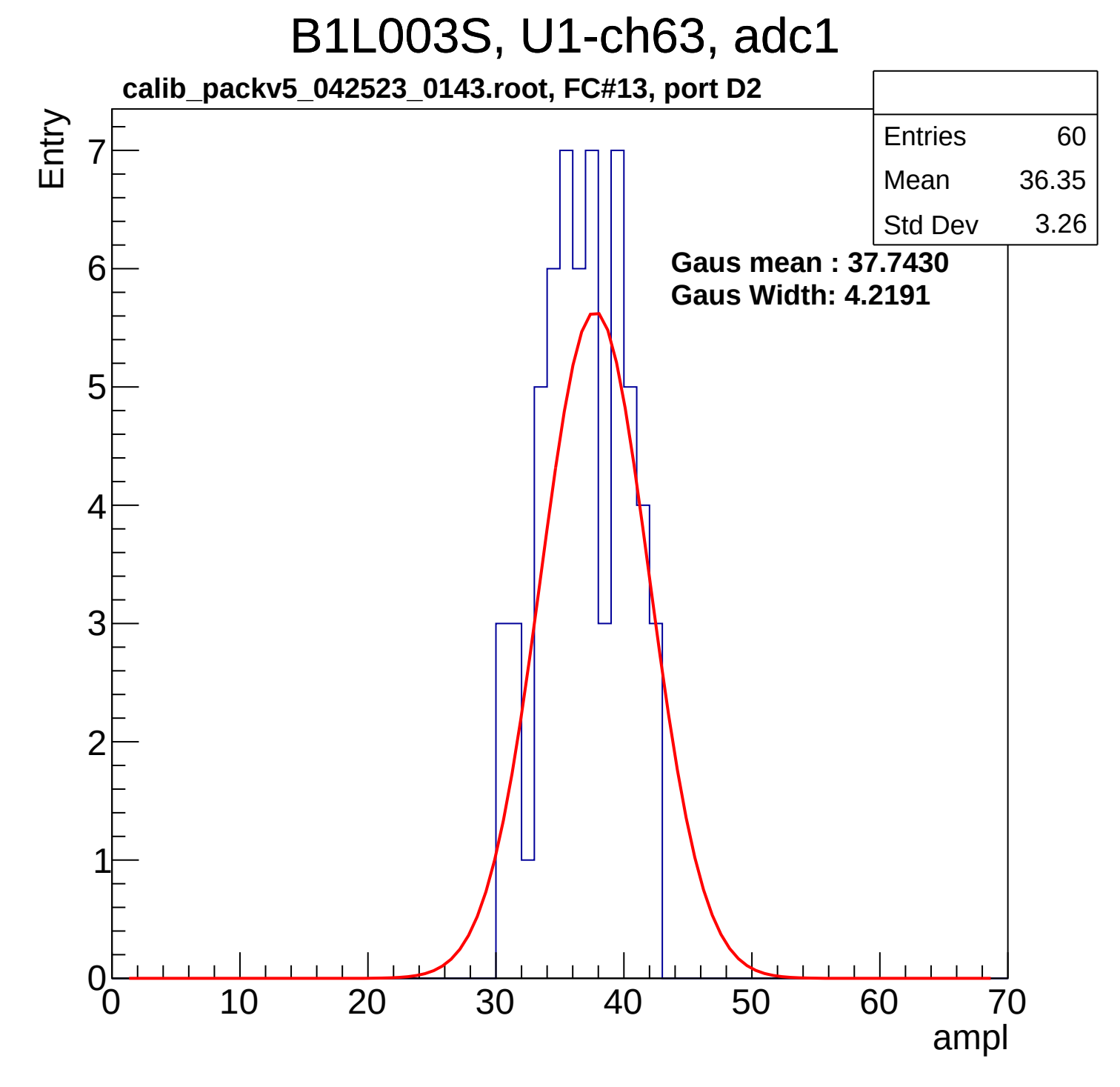
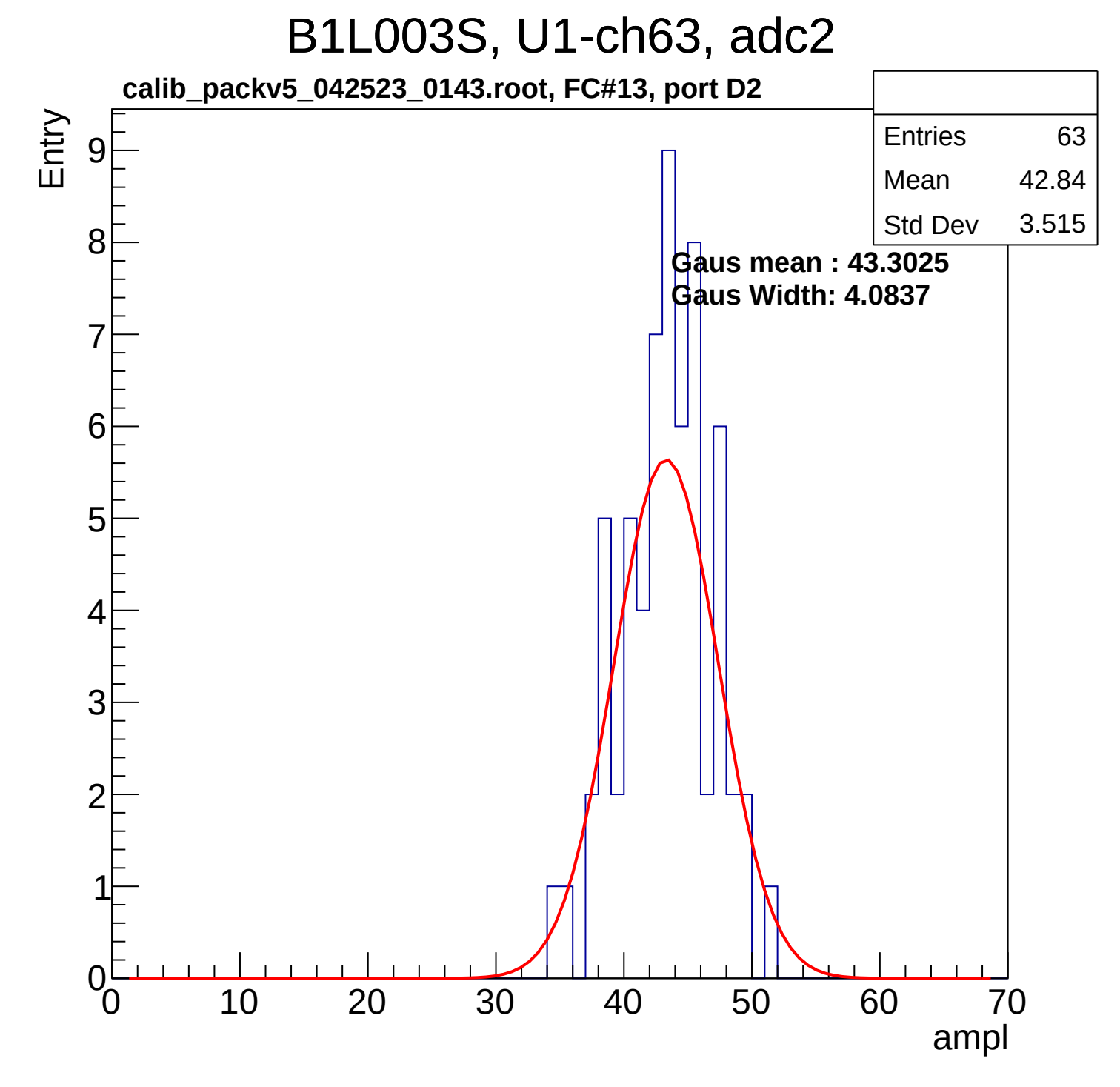
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch62, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.8
Std Dev 18.71
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch62, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch63, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 30.73
Std Dev 3.44
Gaus mean : 30.8963
Gaus Width: 3.5860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch63, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.35
Std Dev 3.26
Gaus mean : 37.7430
Gaus Width: 4.2191
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch63, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.84
Std Dev 3.515
Gaus mean : 43.3025
Gaus Width: 4.0837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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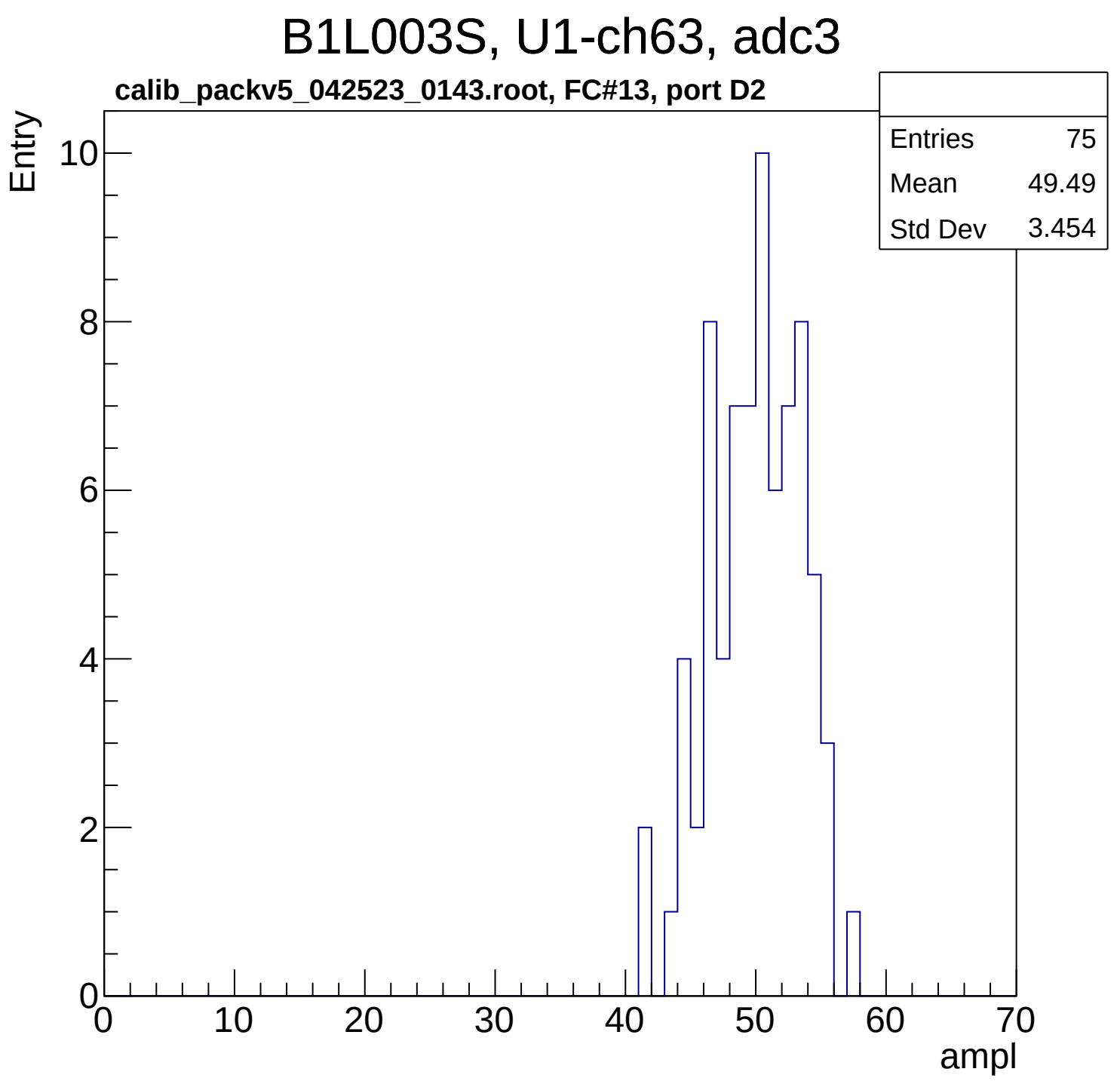
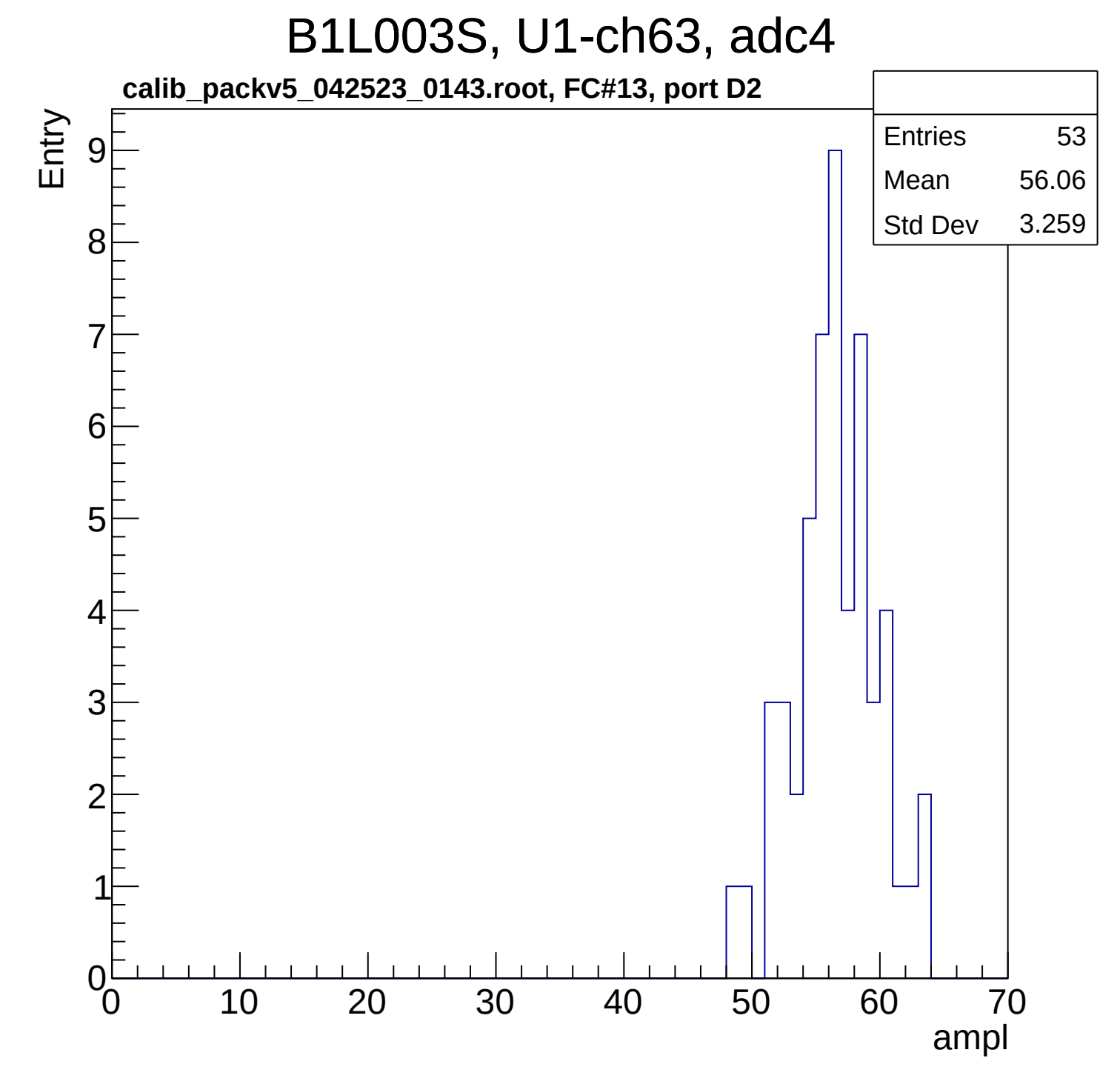
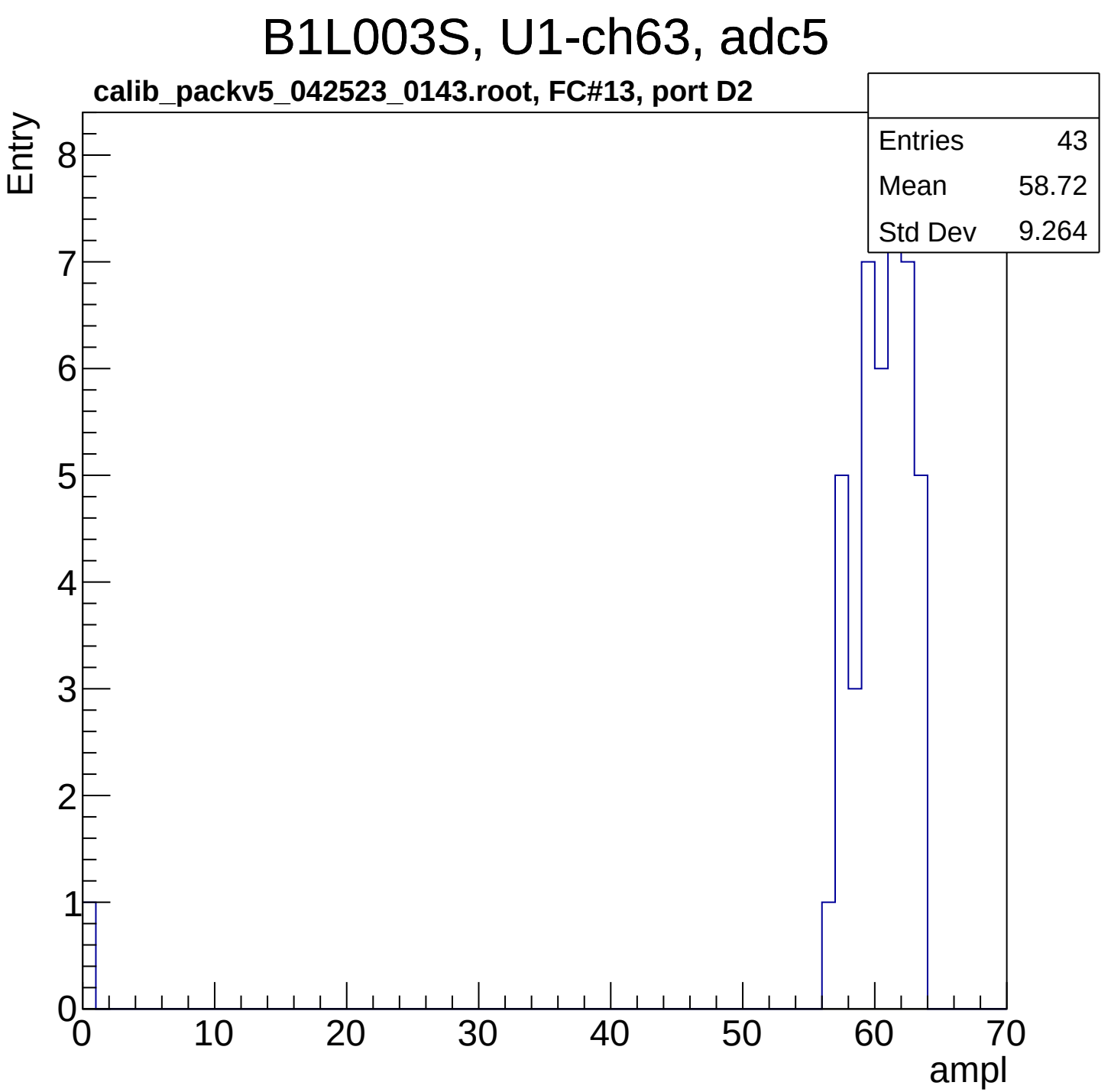

B1L003S, U1-ch63, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 49.49
Std Dev 3.454
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch63, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 56.06
Std Dev 3.259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch63, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.72
Std Dev 9.264
Entry
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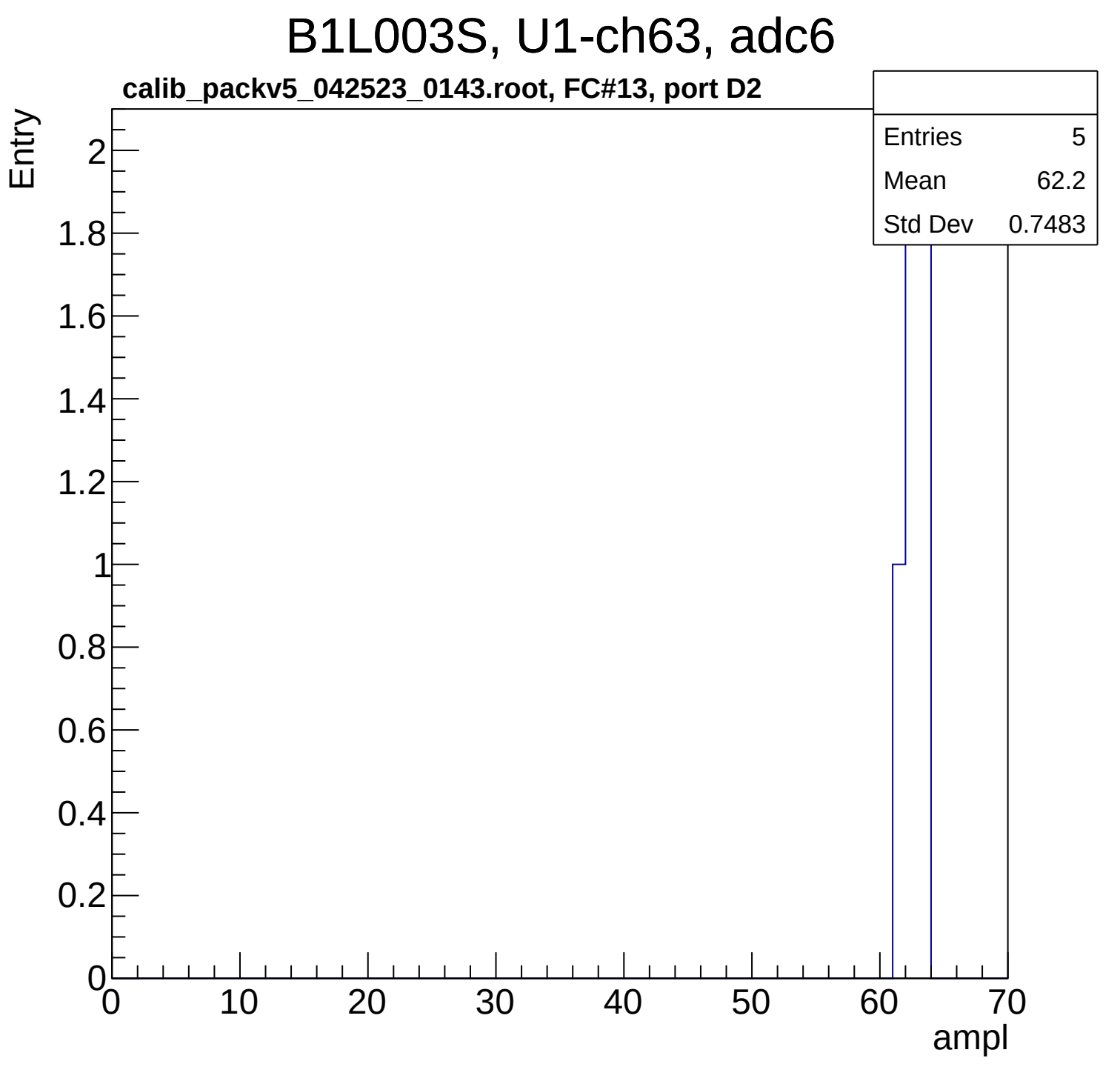
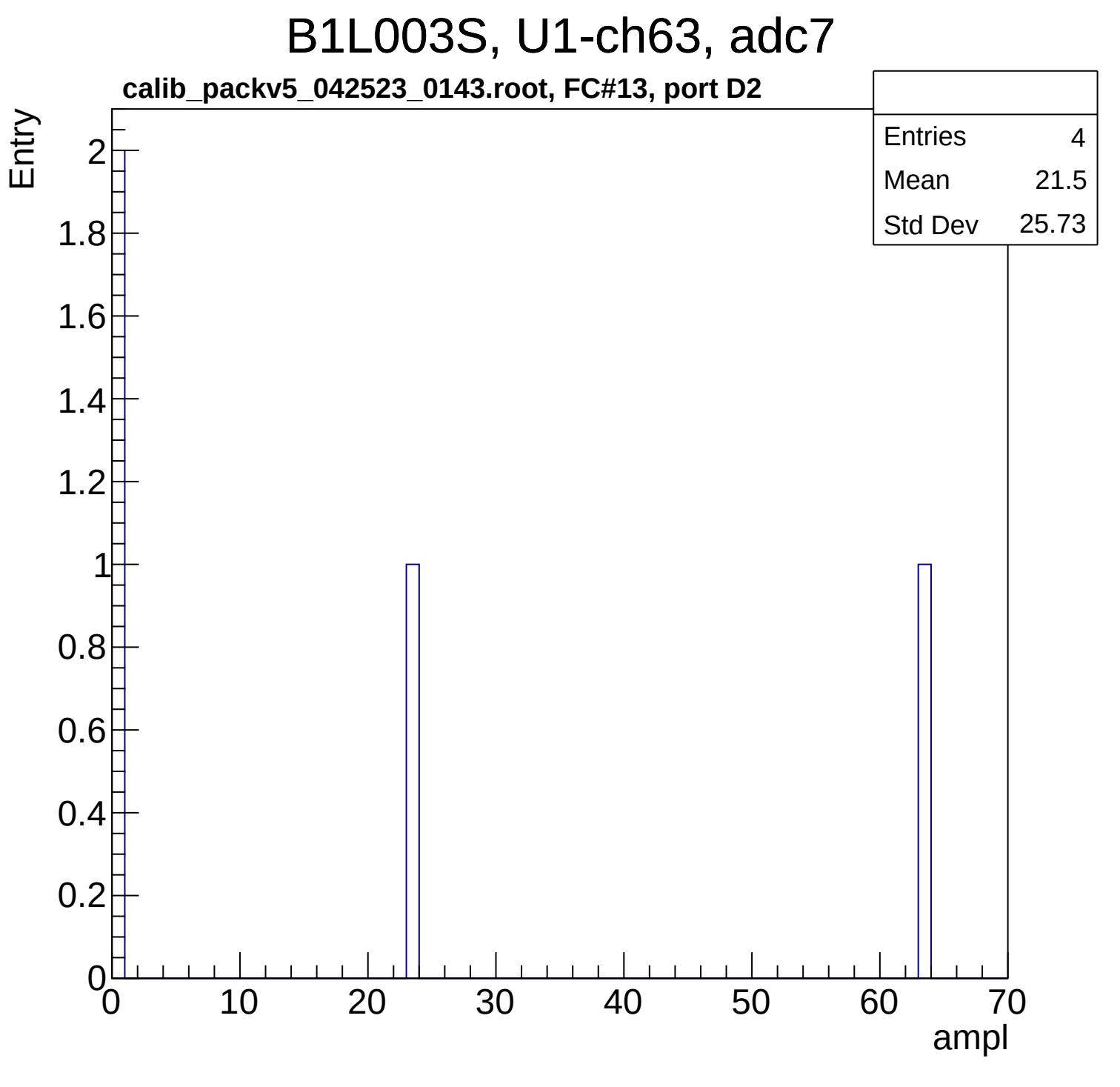
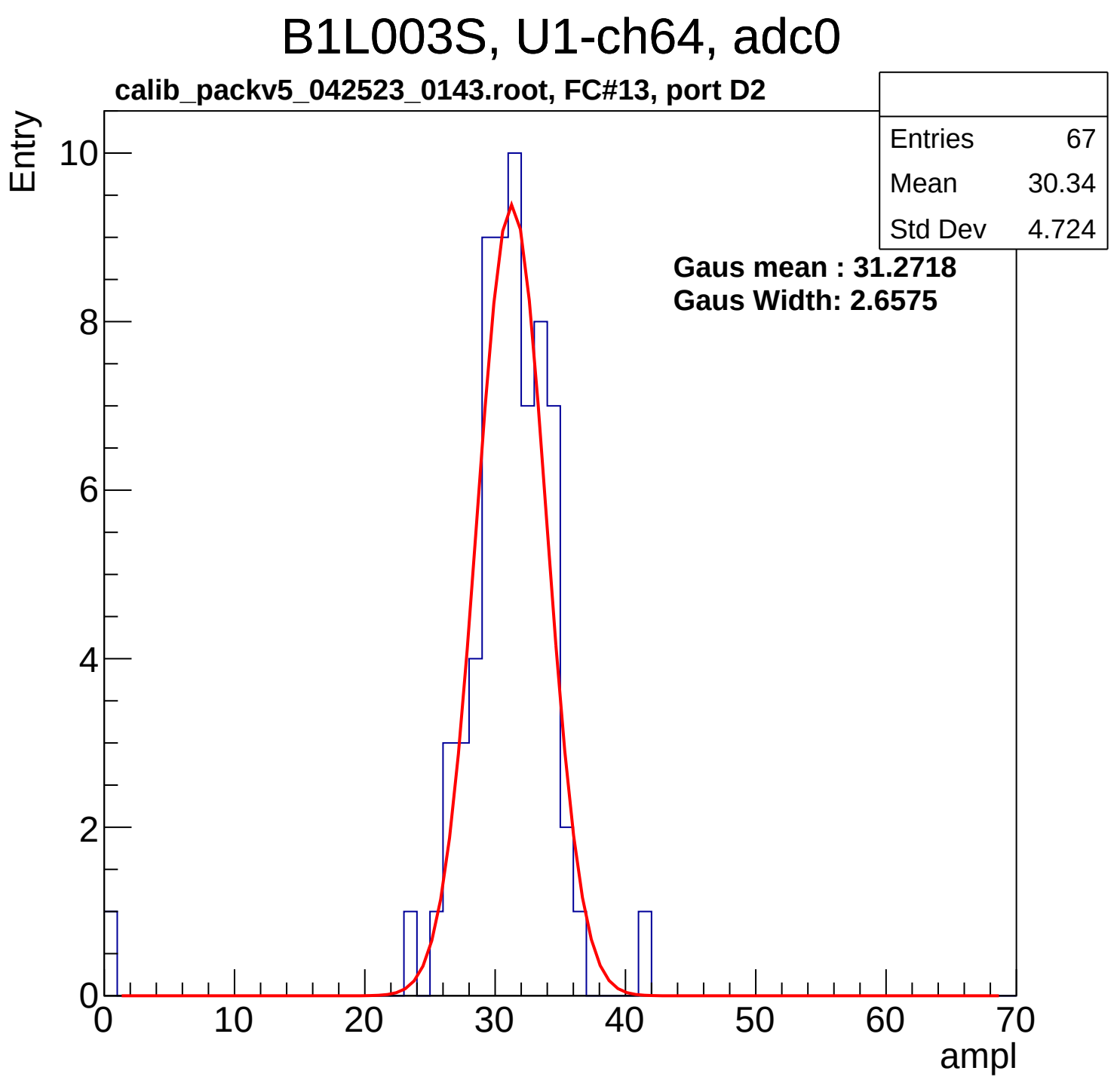
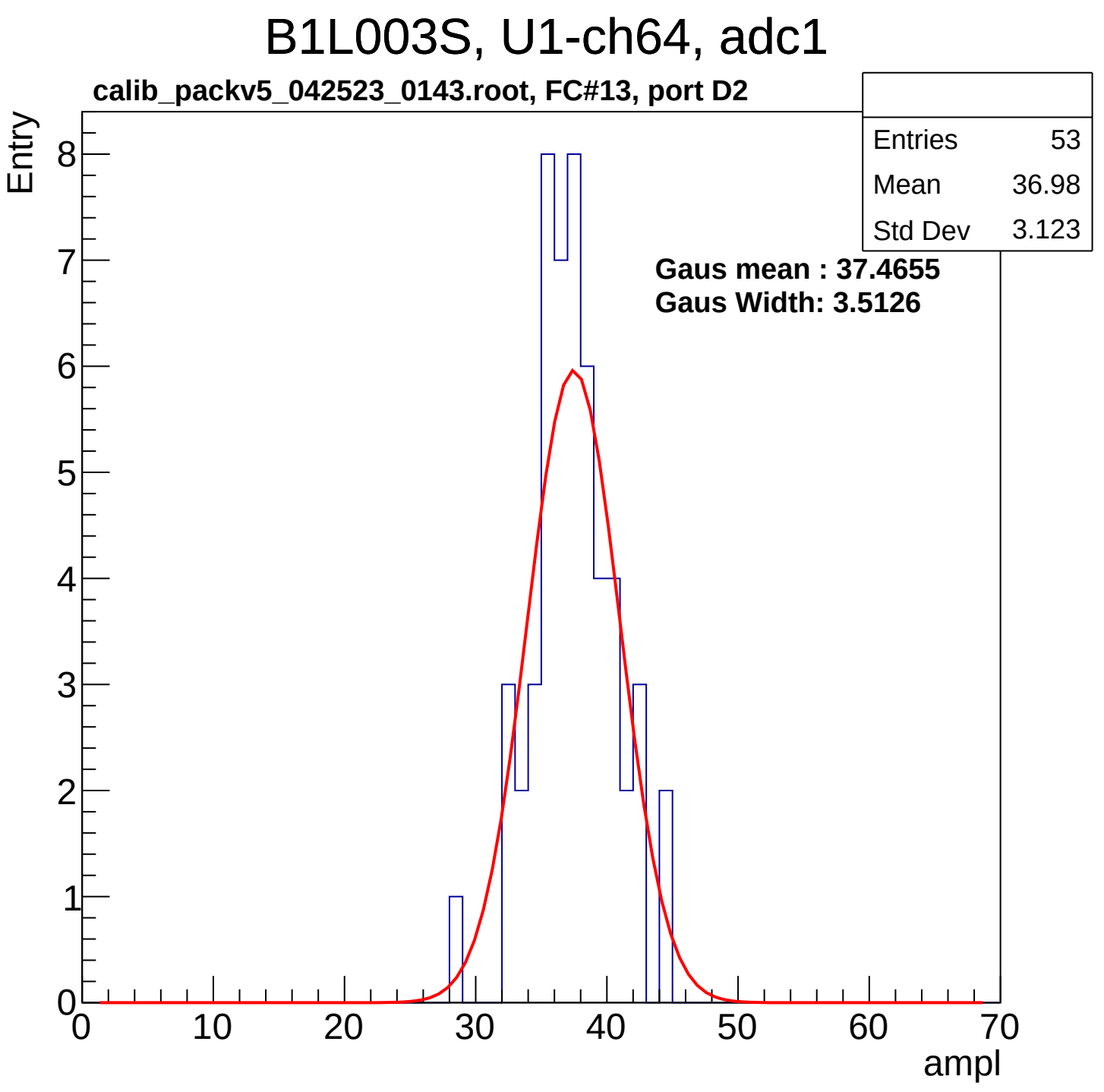
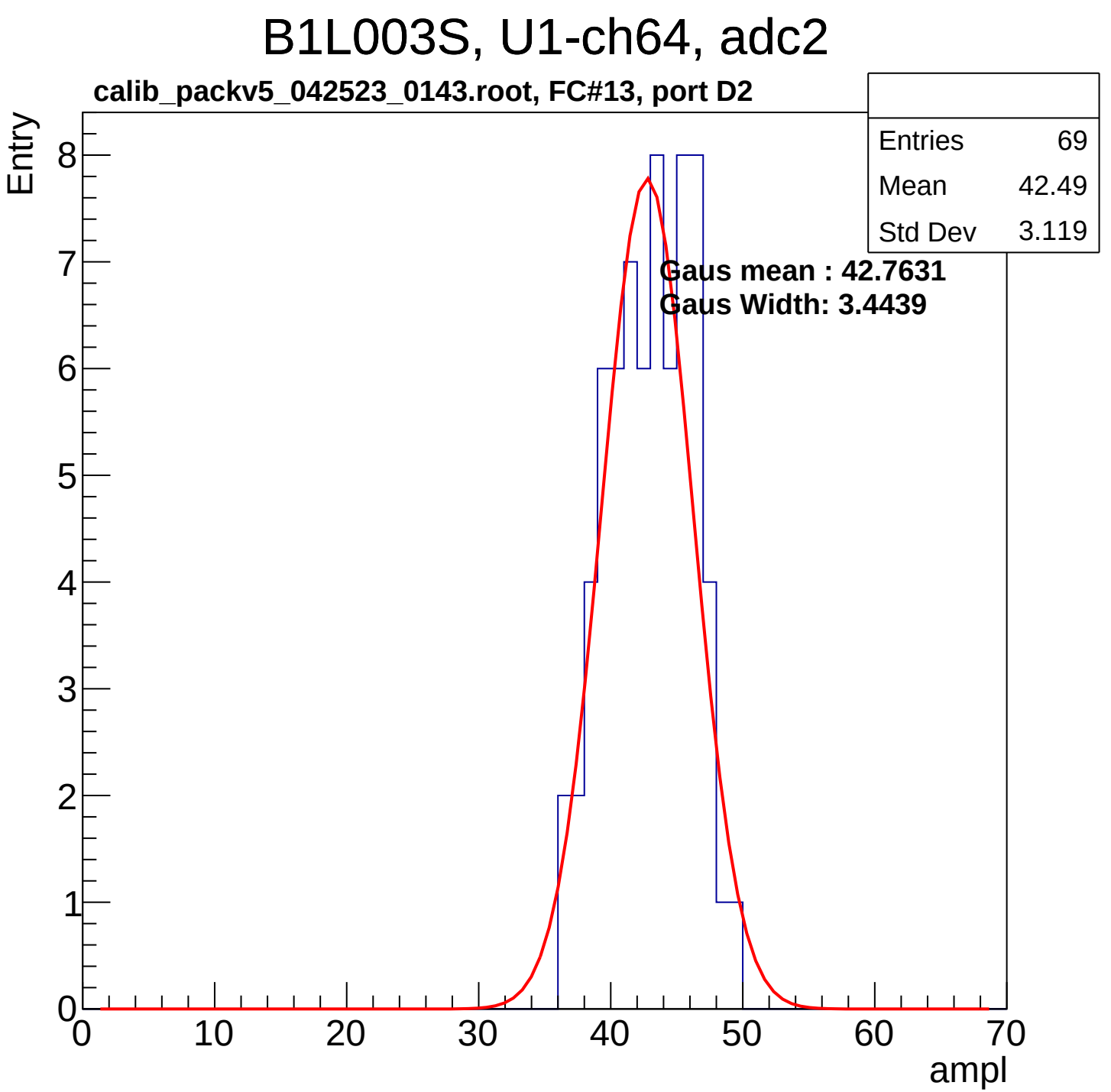
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

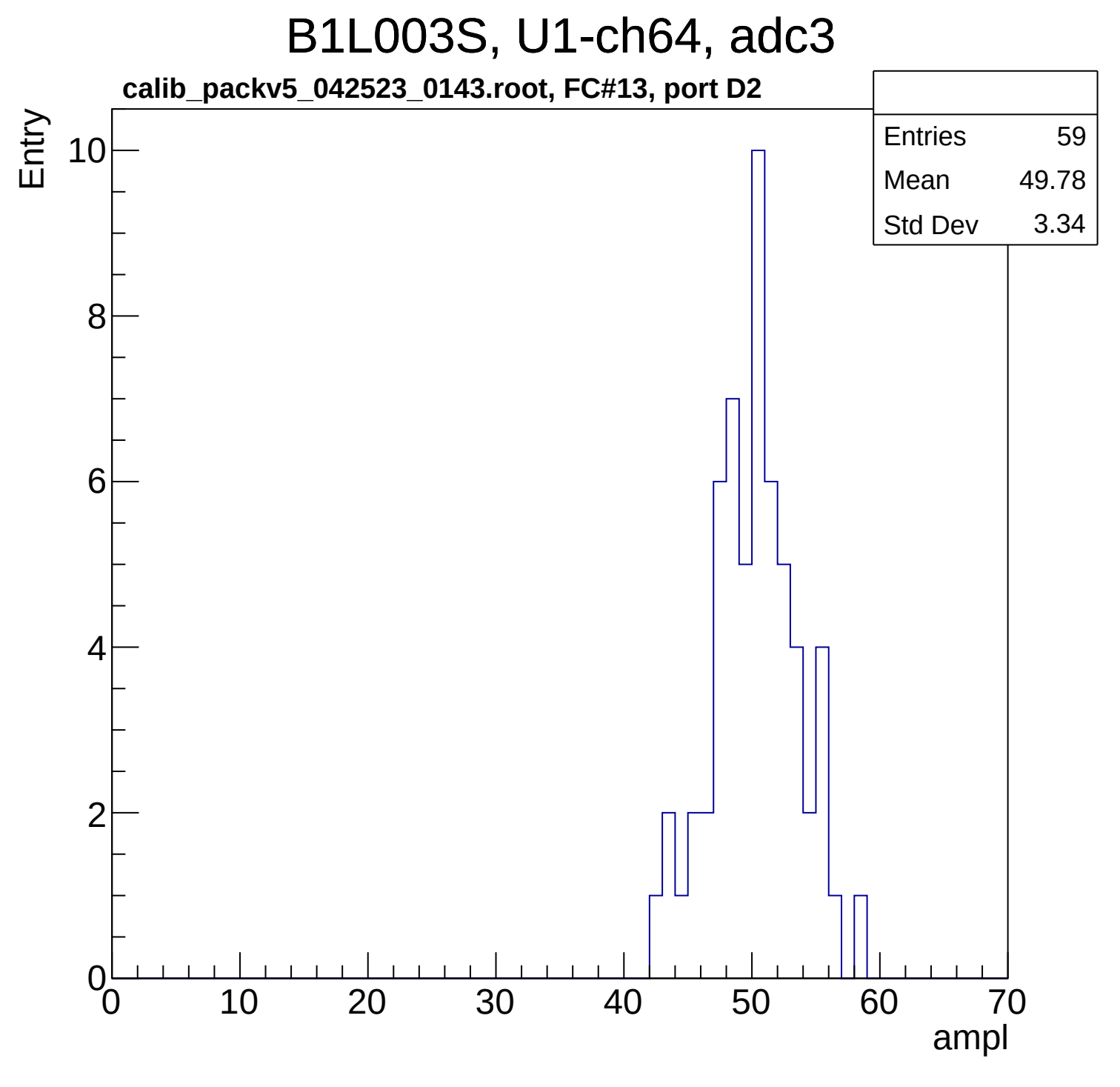

B1L003S, U1-ch63, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

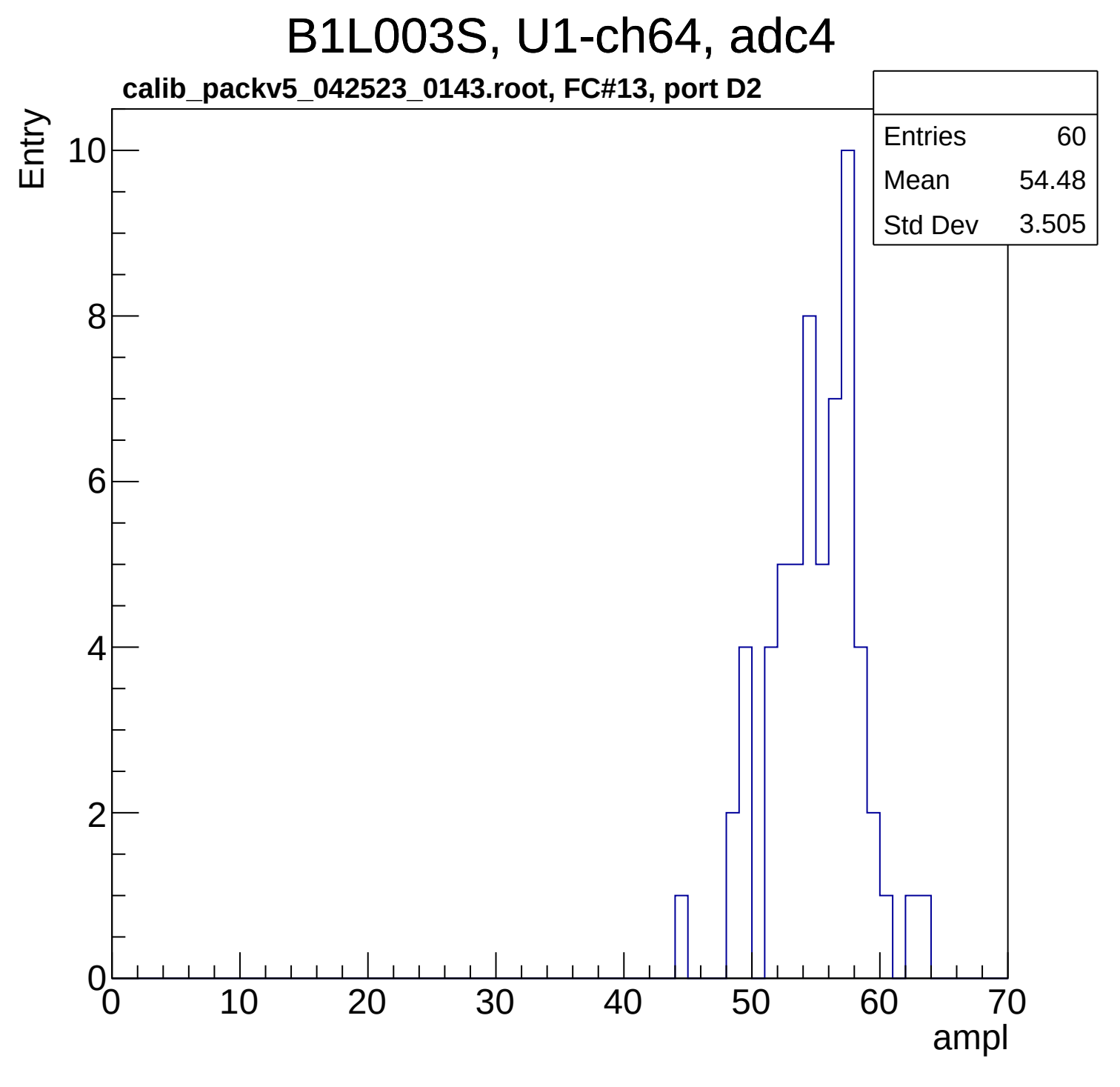

B1L003S, U1-ch63, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 21.5
Std Dev 25.73
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

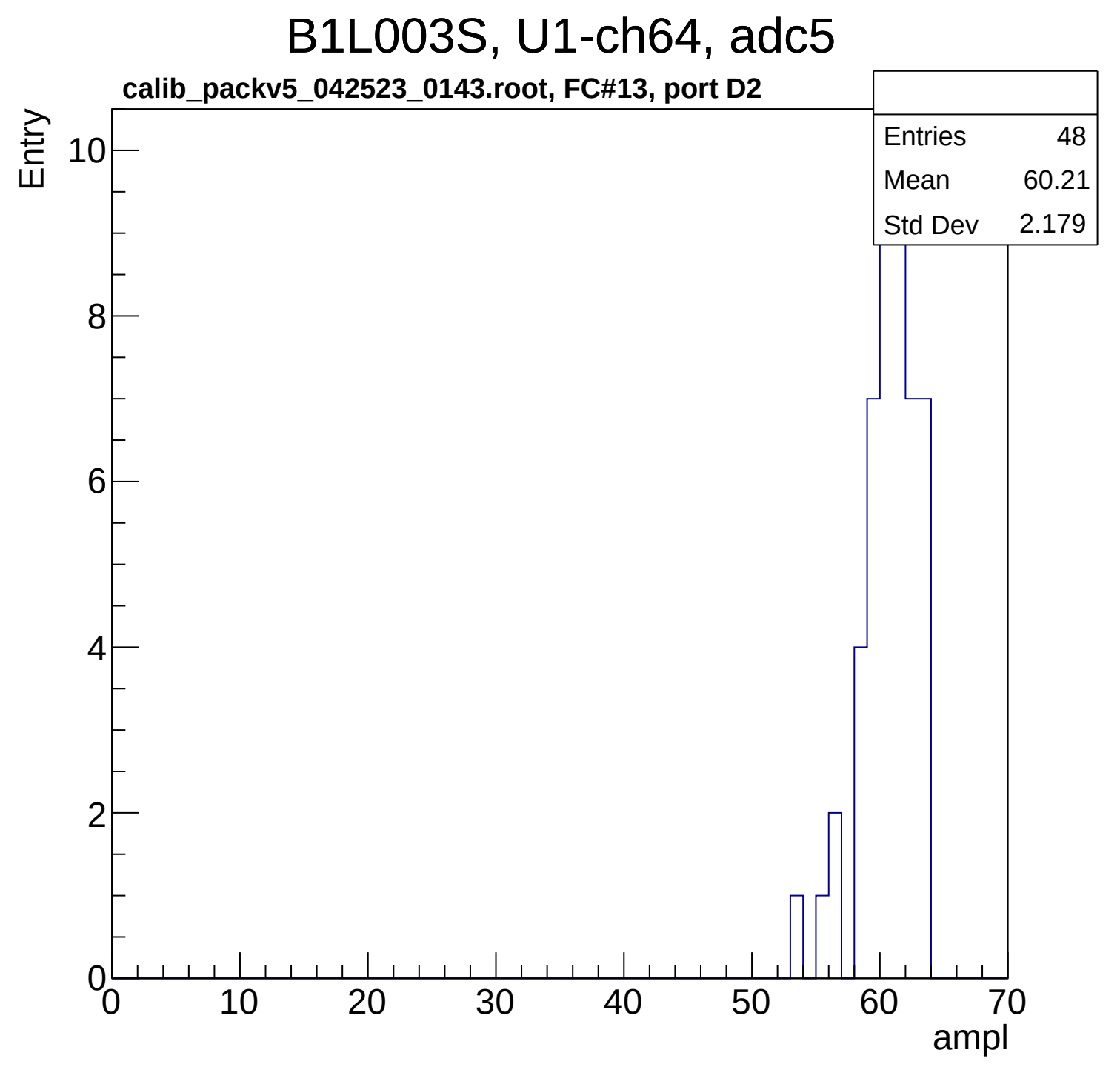

B1L003S, U1-ch64, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.34
Std Dev 4.724
Gaus mean : 31.2718
Gaus Width: 2.6575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

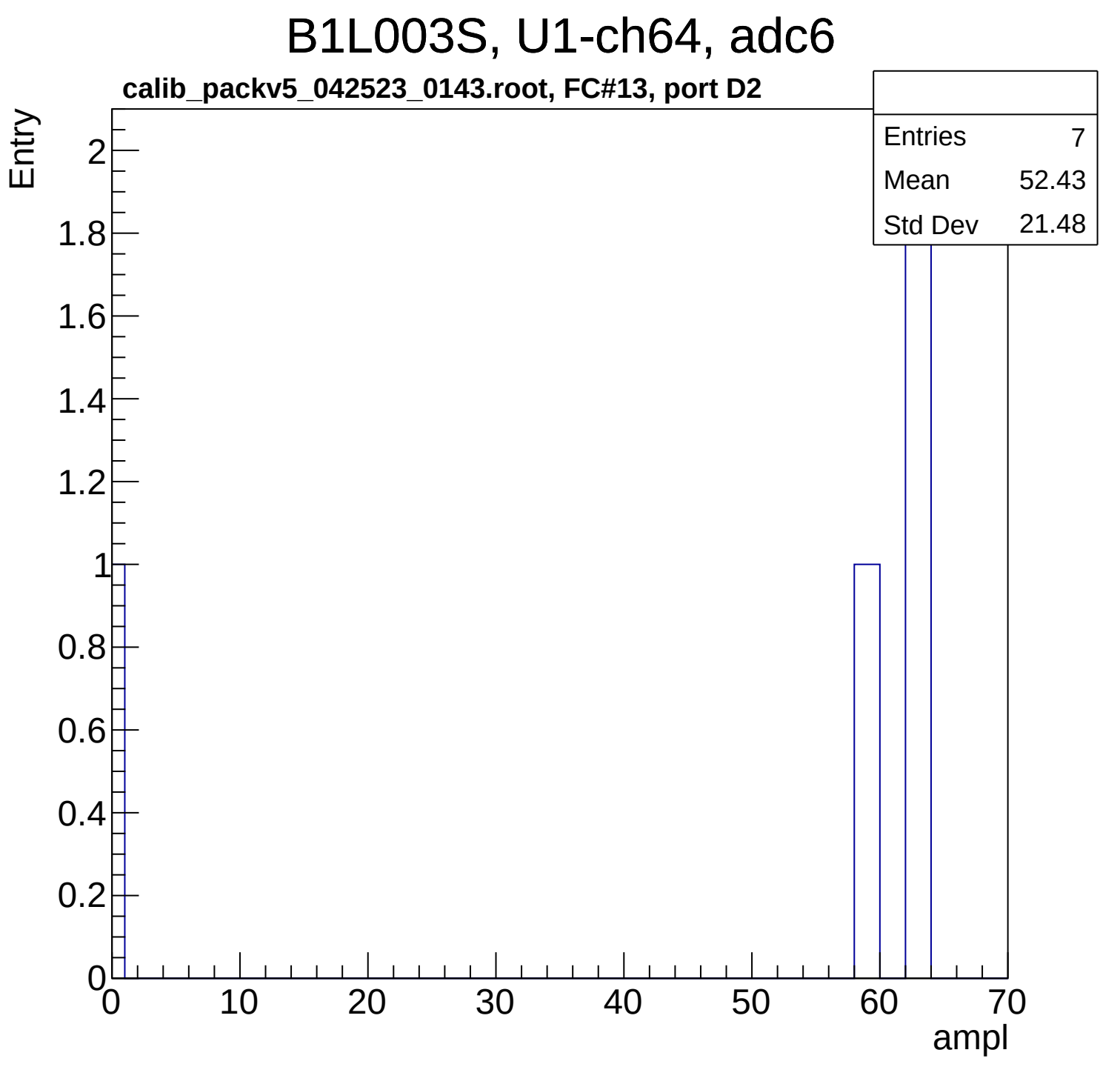

B1L003S, U1-ch64, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 36.98
Std Dev 3.123
Gaus mean : 37.4655
Gaus Width: 3.5126
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

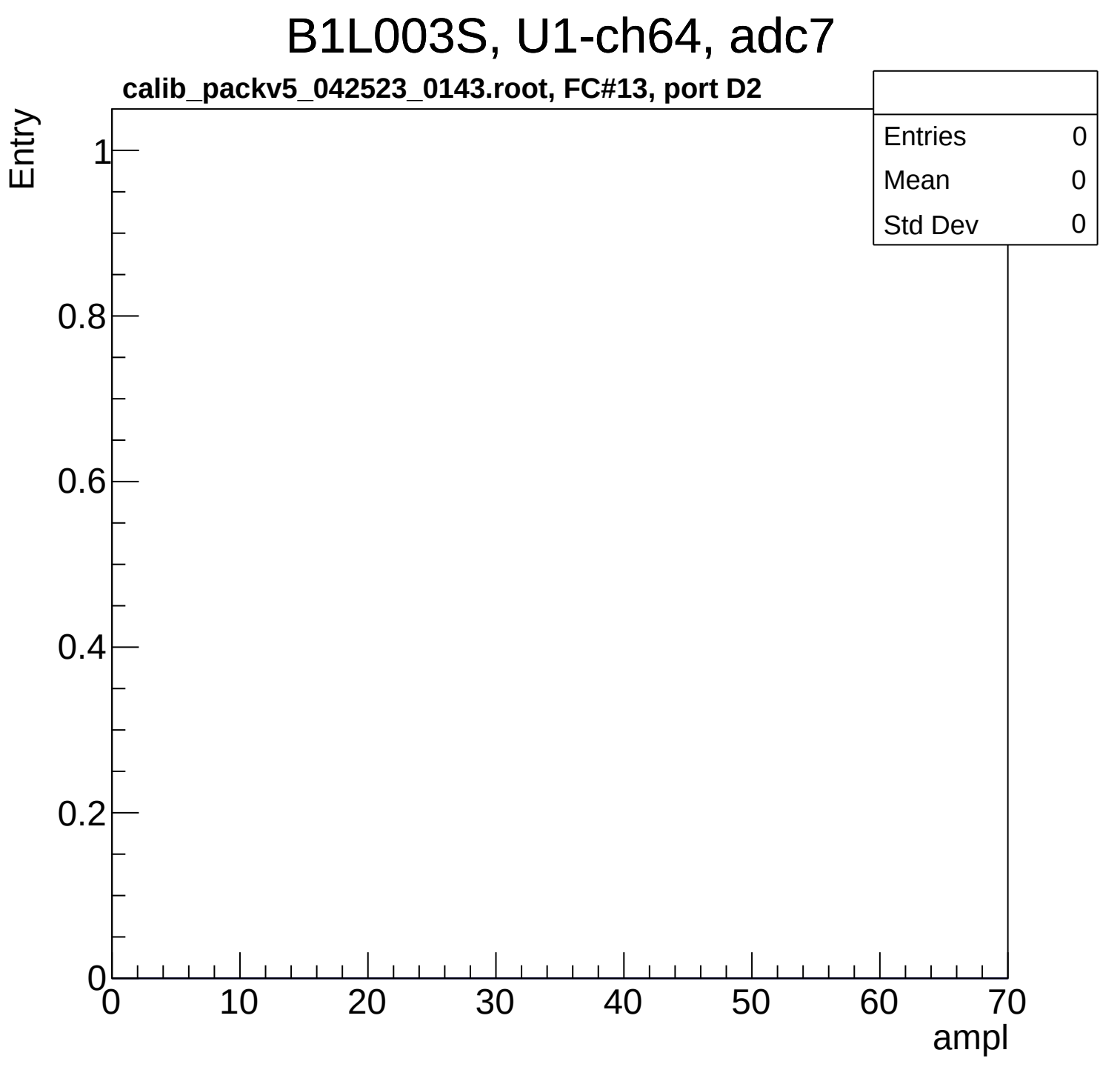

B1L003S, U1-ch64, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.49
Std Dev 3.119
Gaus mean : 42.7631
Gaus Width: 3.4439
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

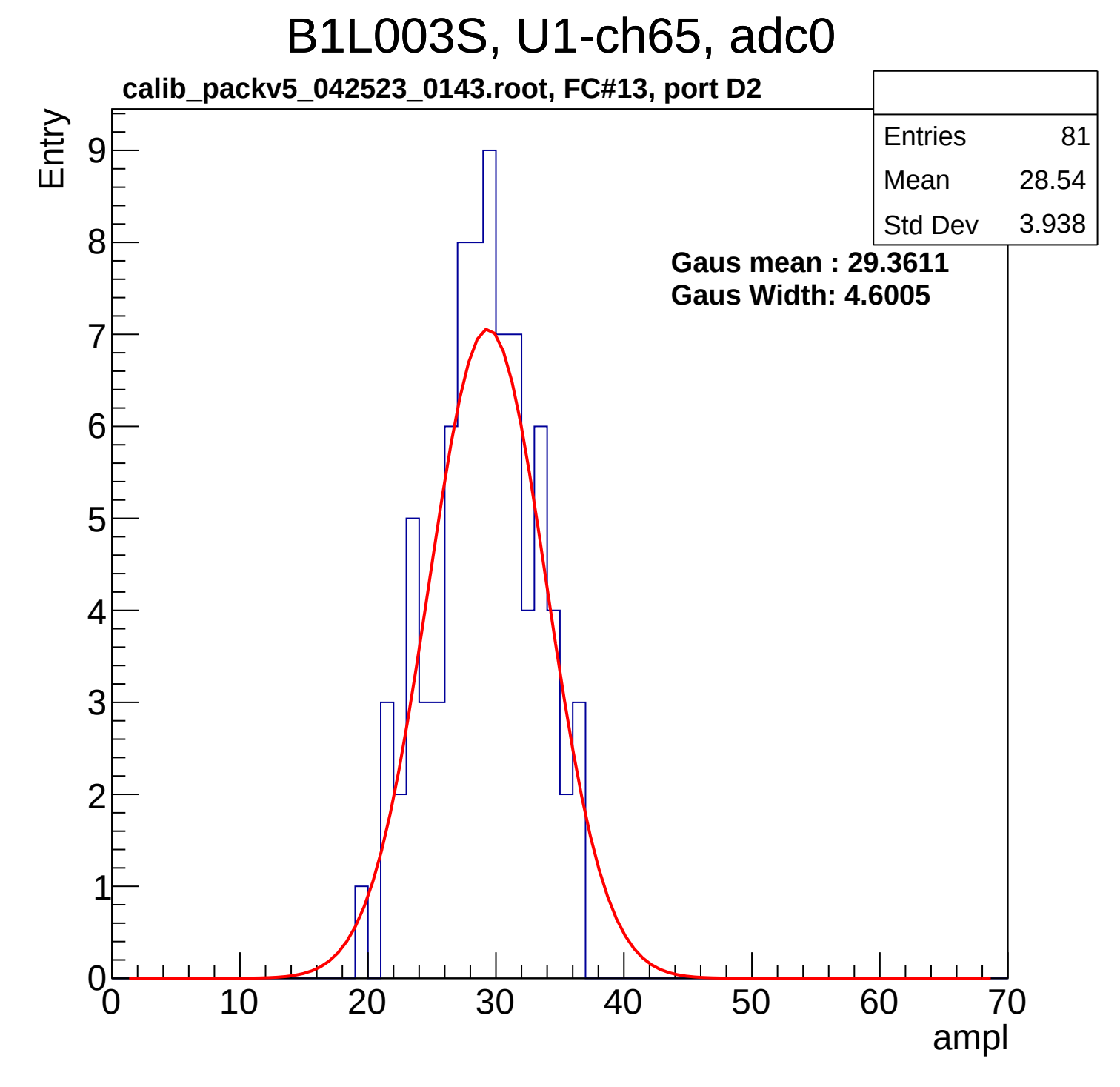

B1L003S, U1-ch64, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 49.78
Std Dev 3.34
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

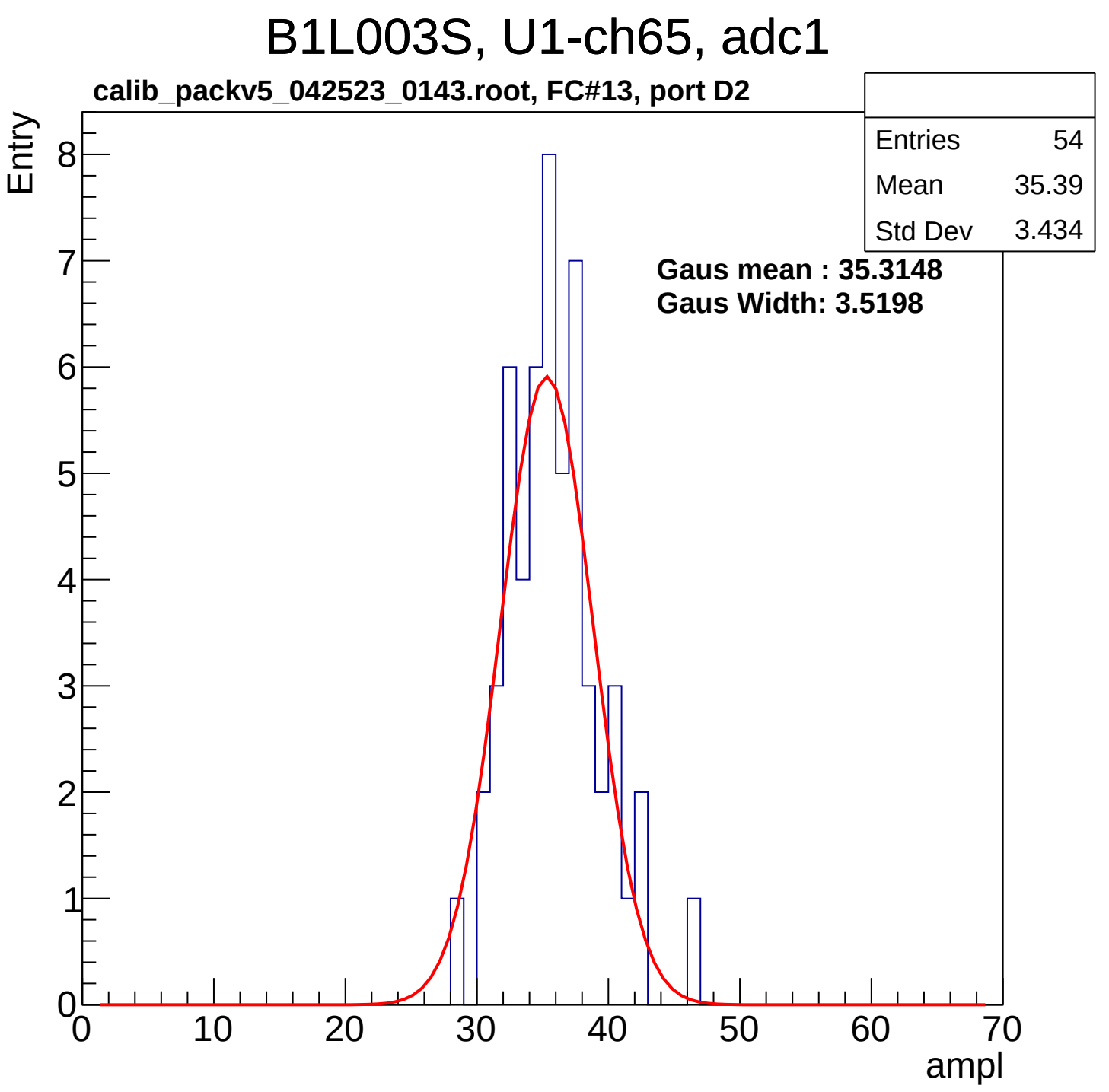

B1L003S, U1-ch64, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 54.48
Std Dev 3.505
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

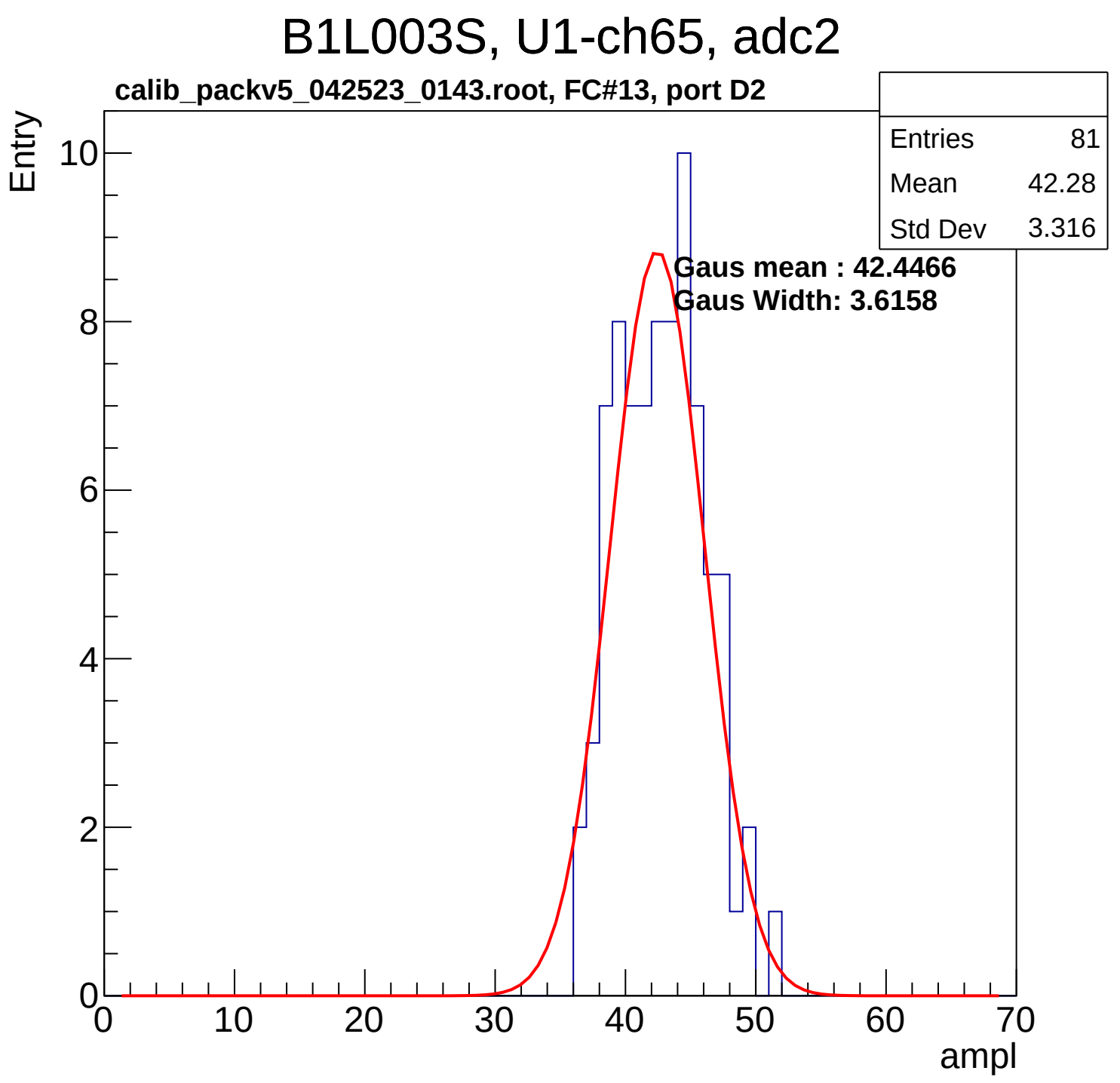

B1L003S, U1-ch64, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 60.21
Std Dev 2.179
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

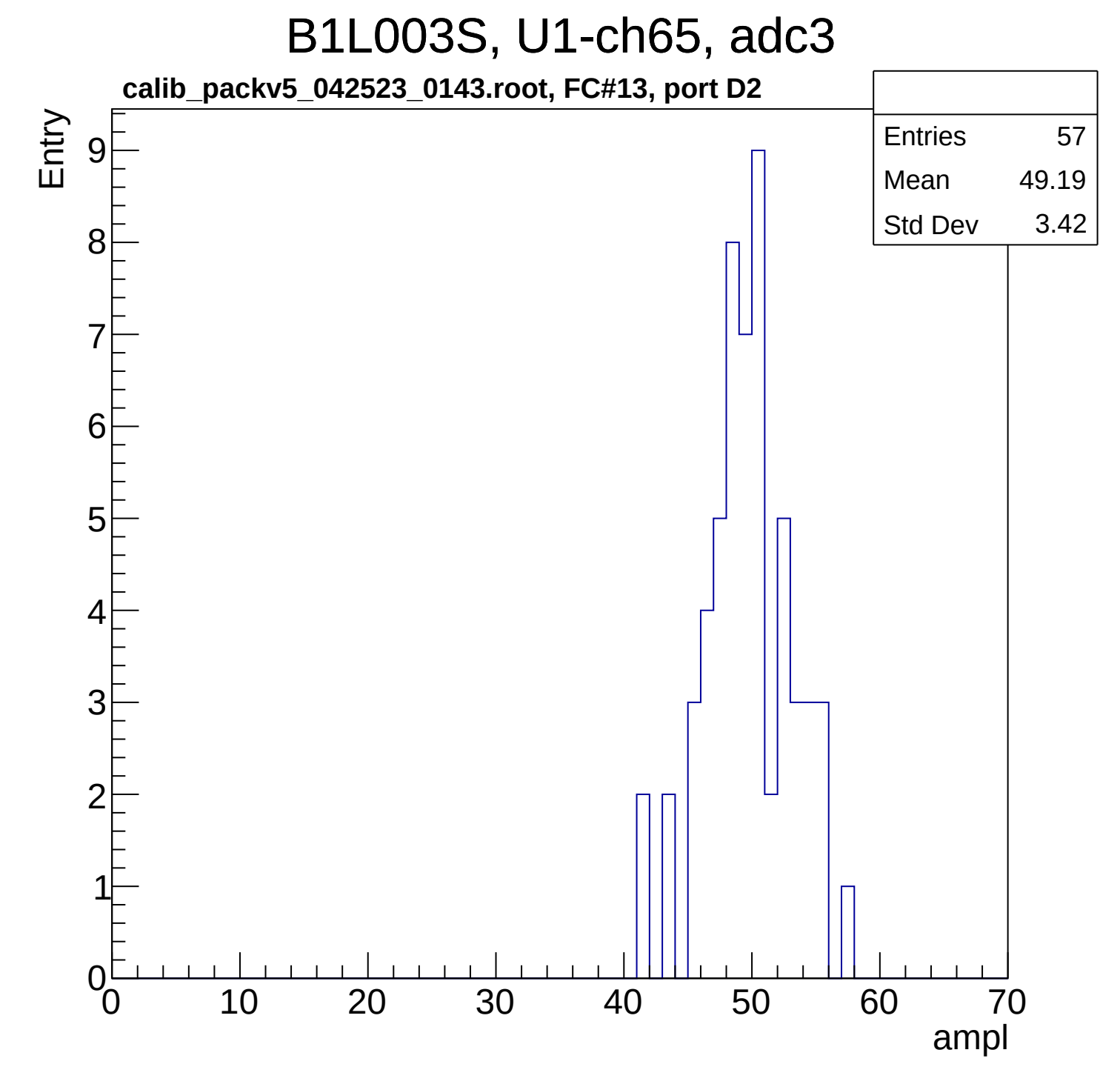

B1L003S, U1-ch64, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 52.43
Std Dev 21.48
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

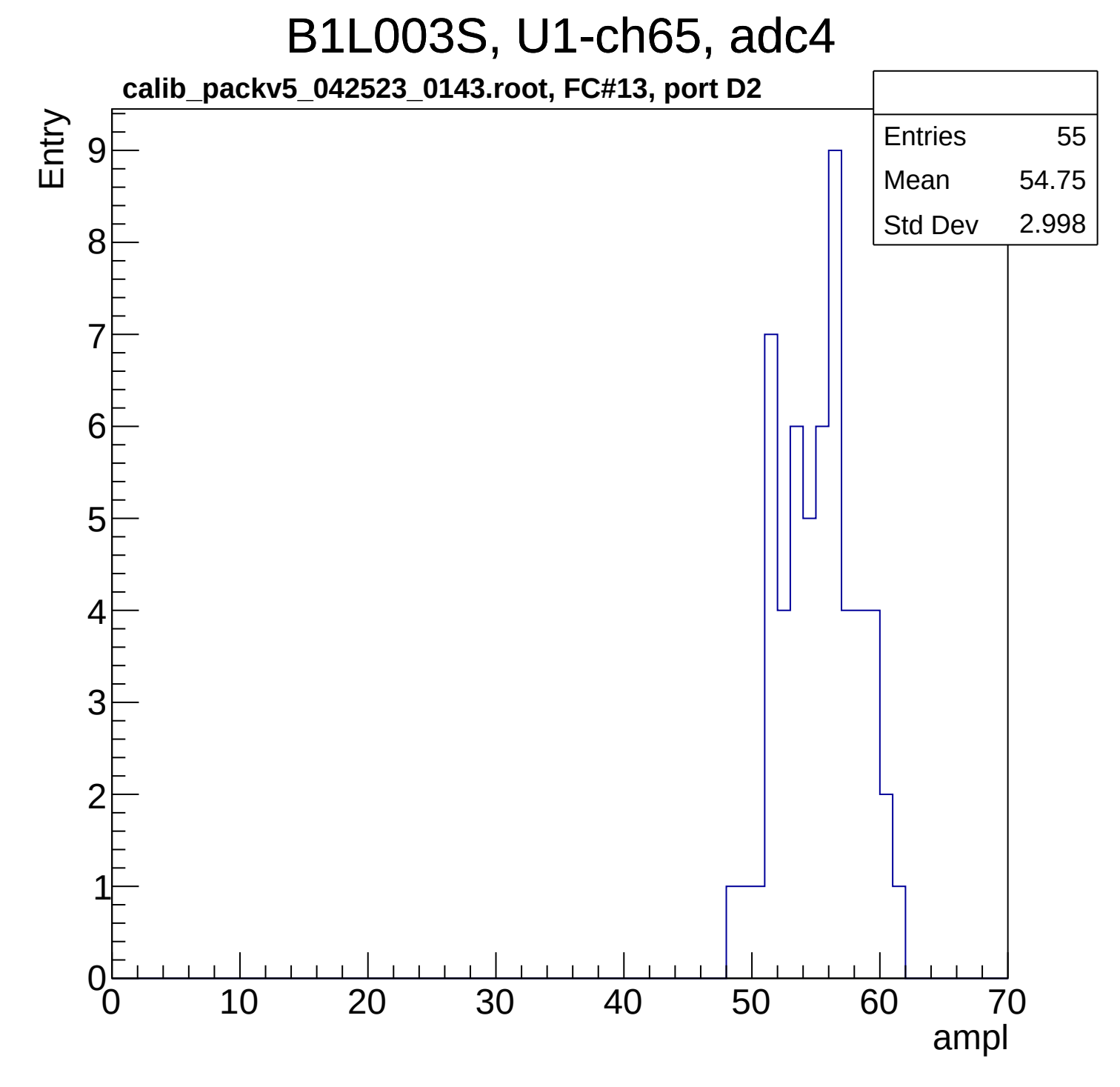

B1L003S, U1-ch64, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

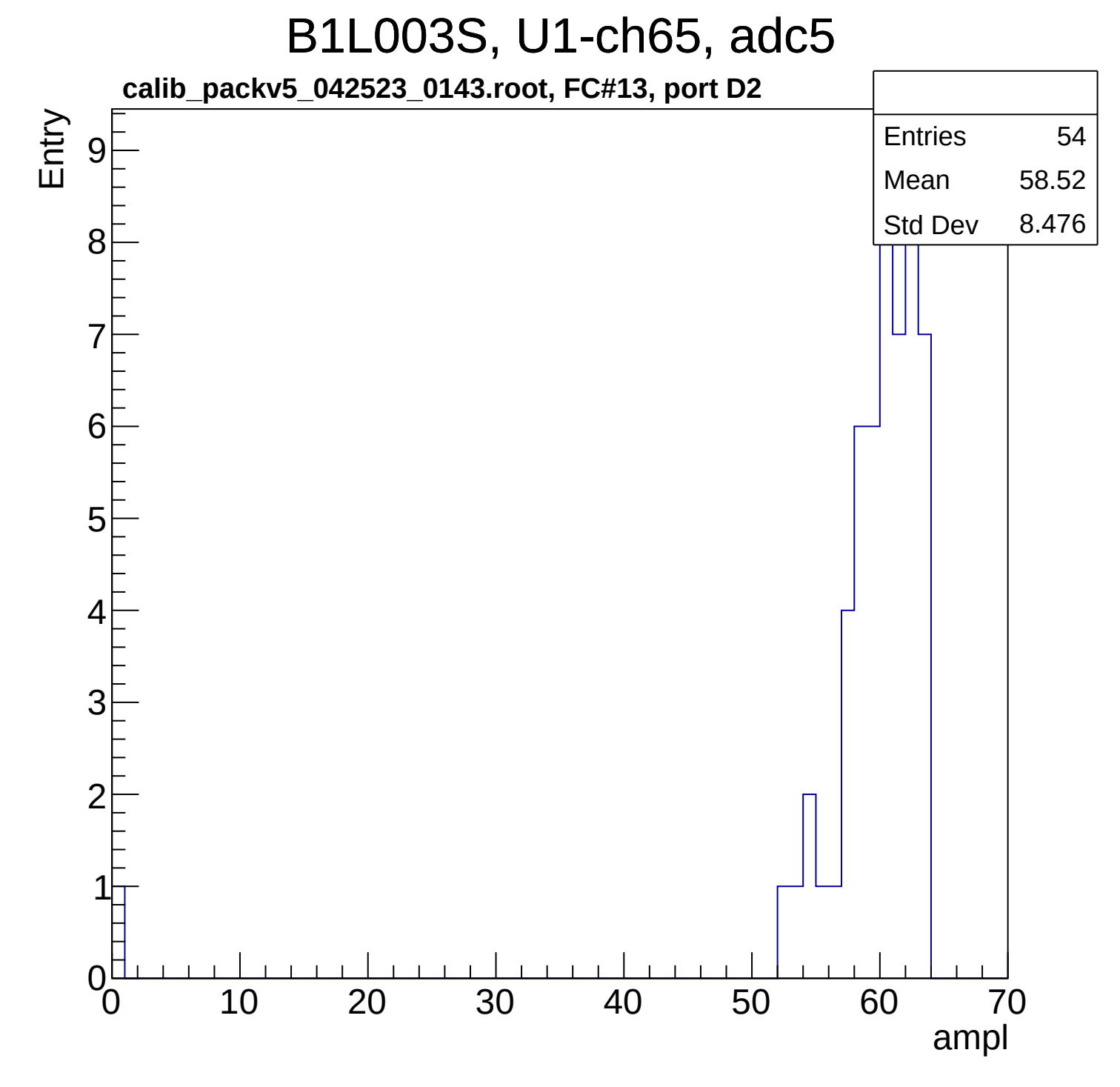

B1L003S, U1-ch65, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 28.54
Std Dev 3.938
Gaus mean : 29.3611
Gaus Width: 4.6005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch65, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.39
Std Dev 3.434
Gaus mean : 35.3148
Gaus Width: 3.5198
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch65, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 42.28
Std Dev 3.316
Gaus mean : 42.4466
Gaus Width: 3.6158
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch65, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.19
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch65, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.75
Std Dev 2.998
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch65, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.52
Std Dev 8.476
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
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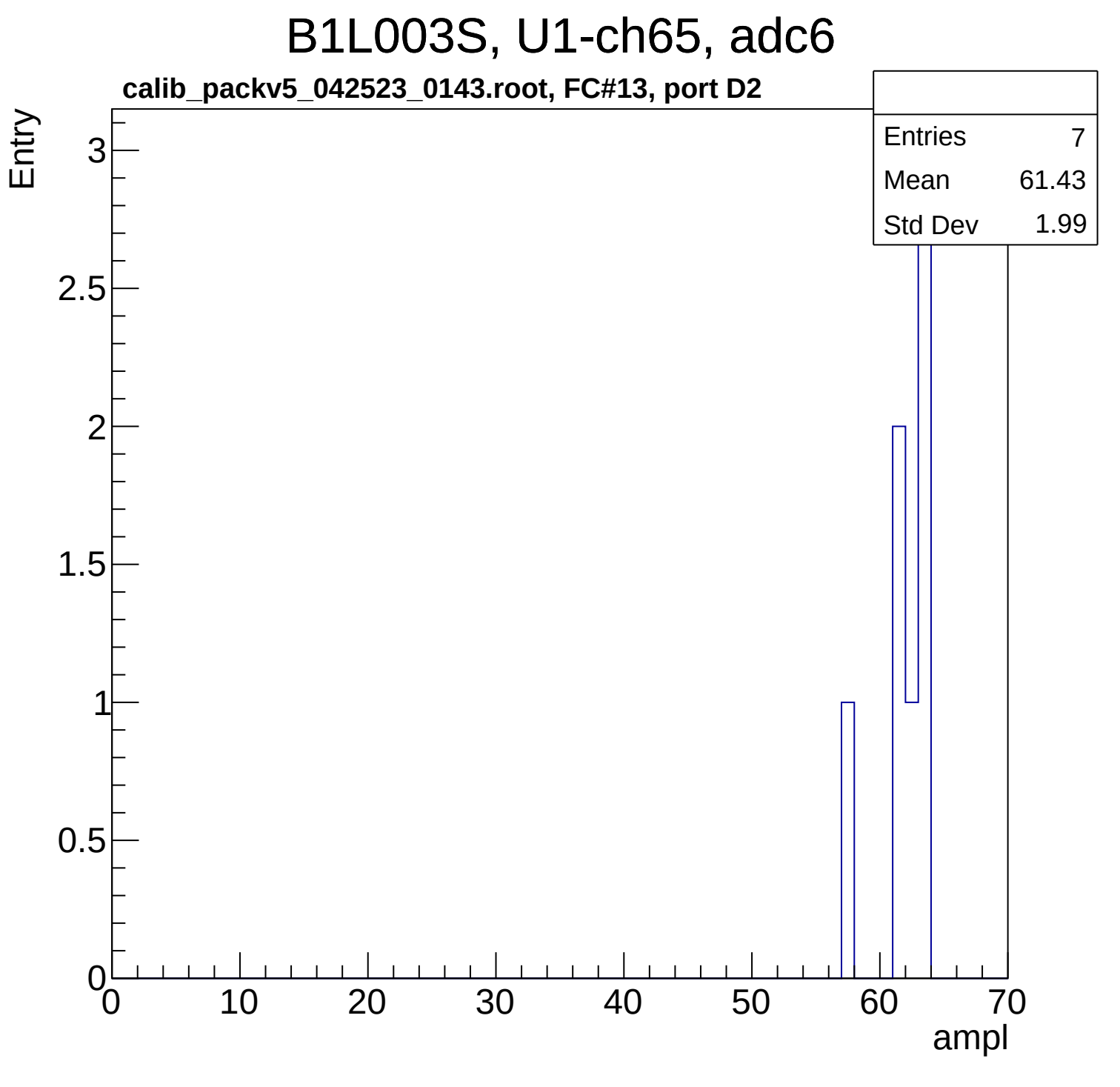
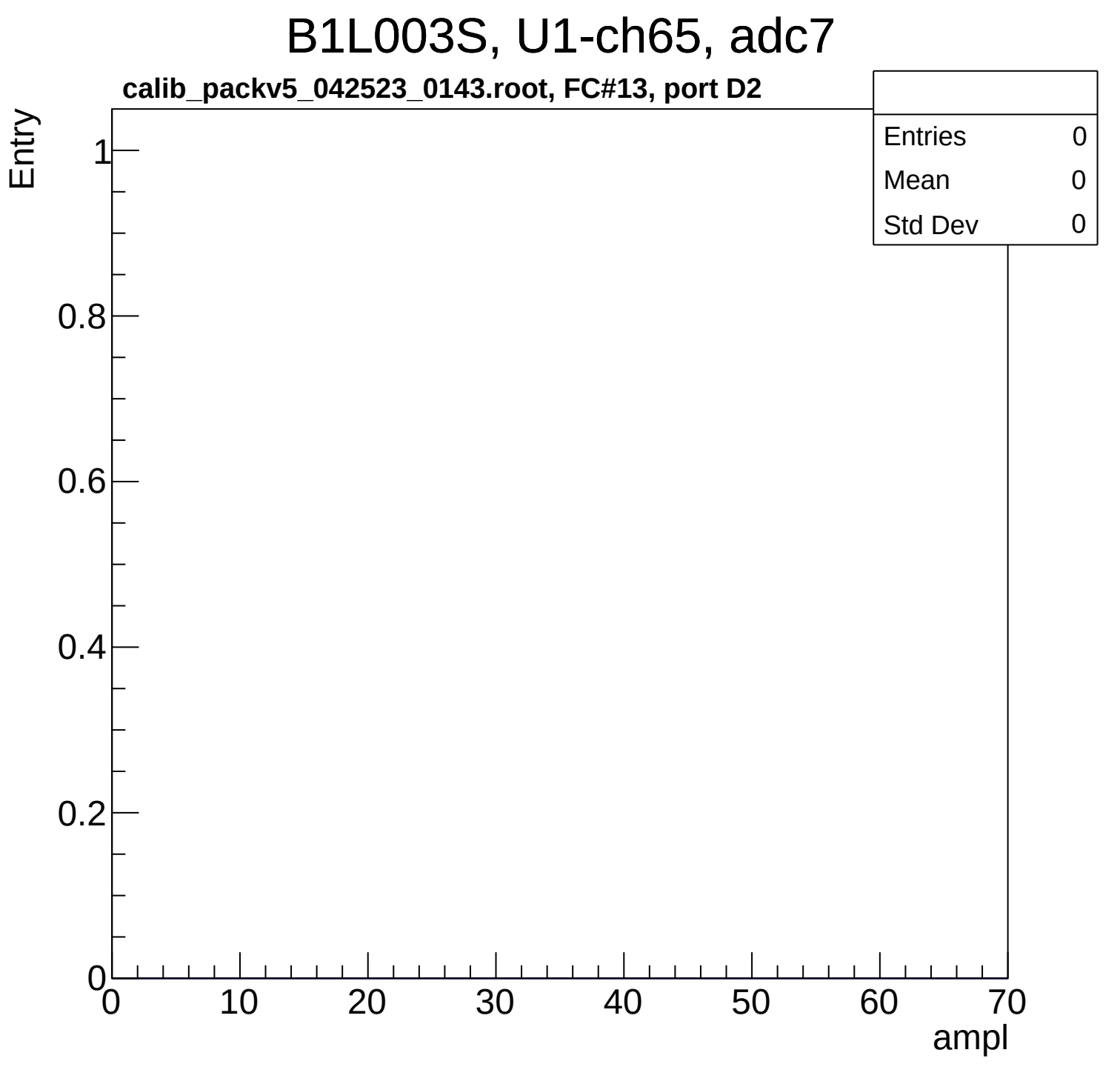
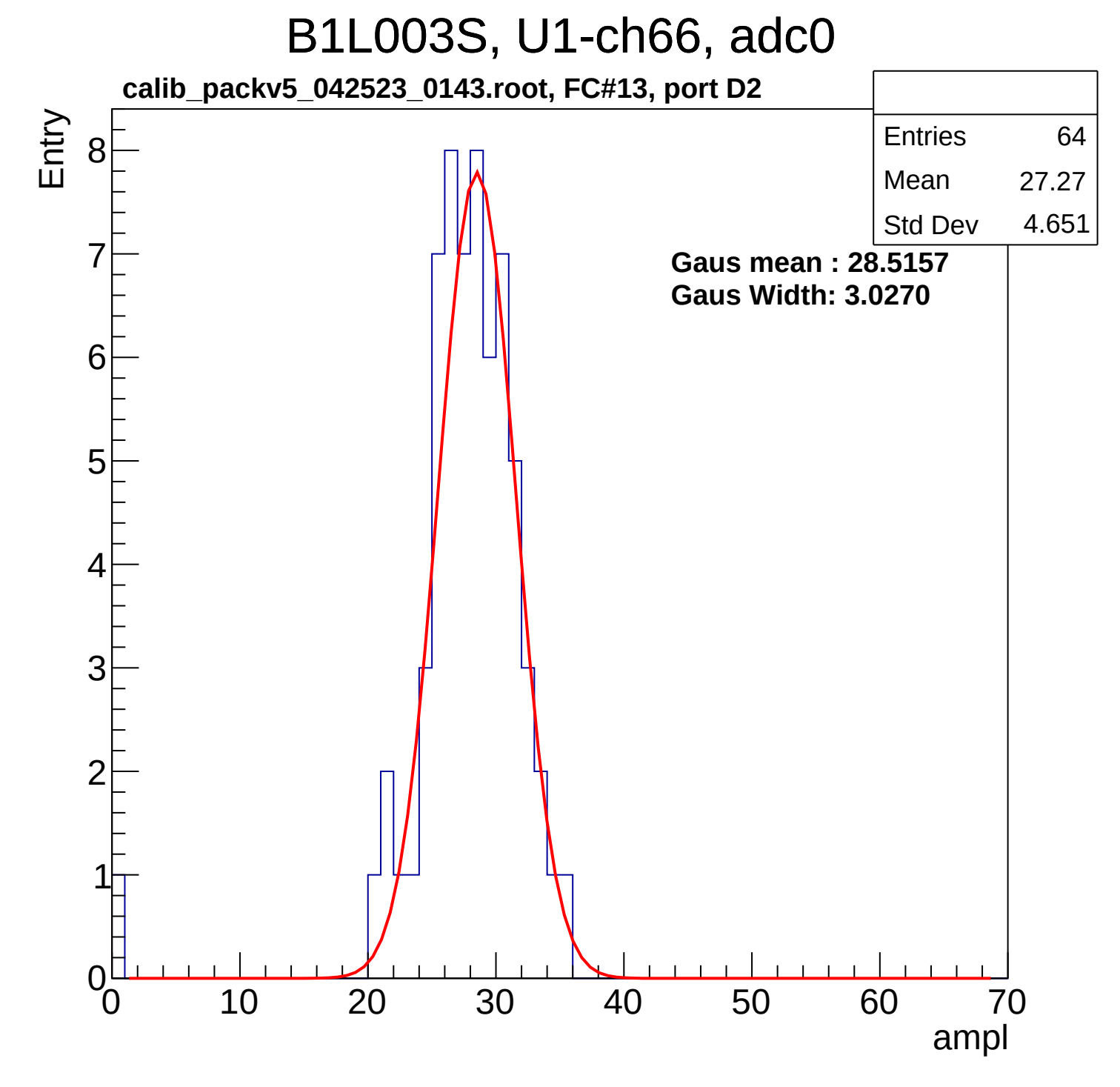
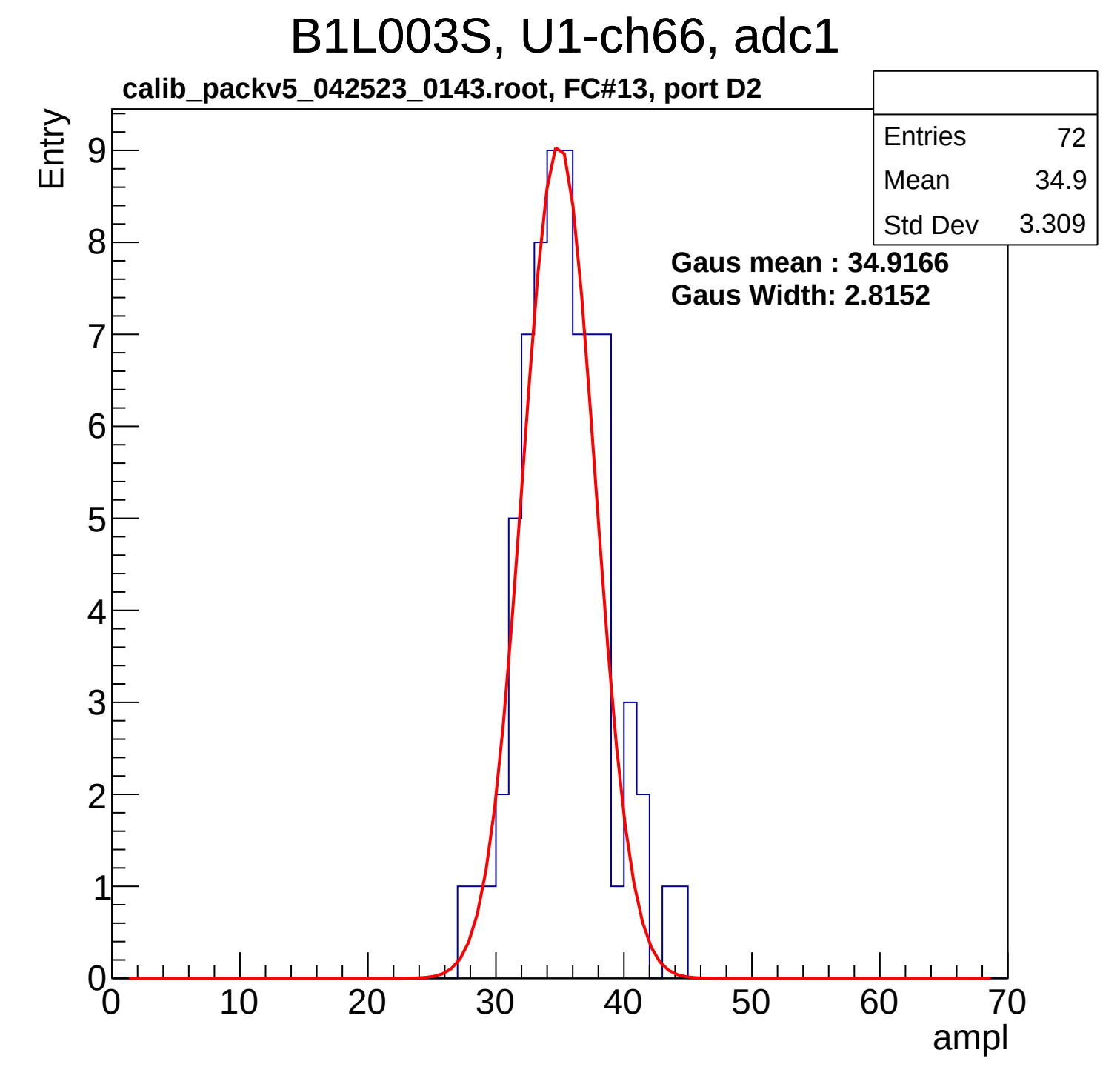
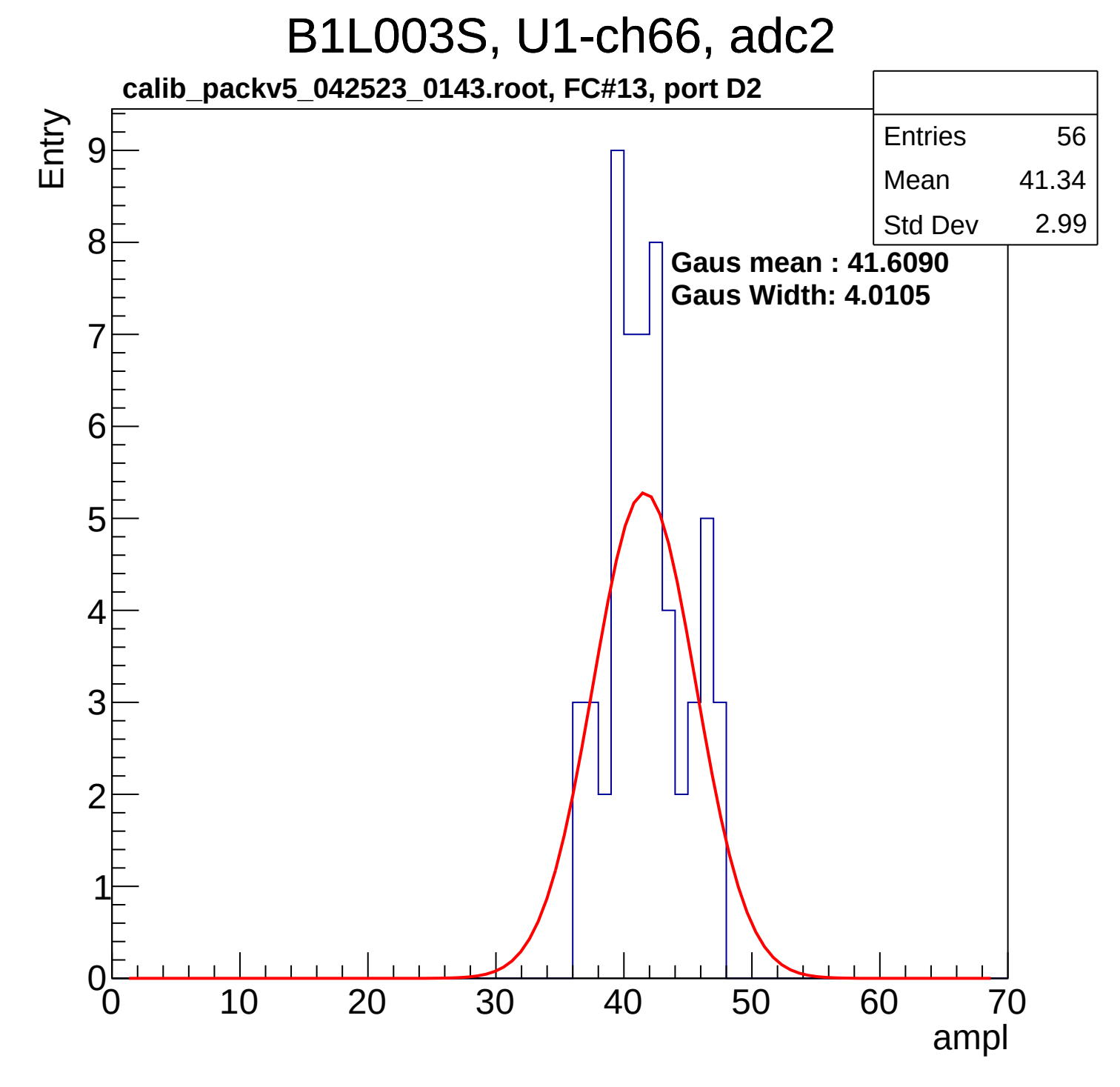
60
70

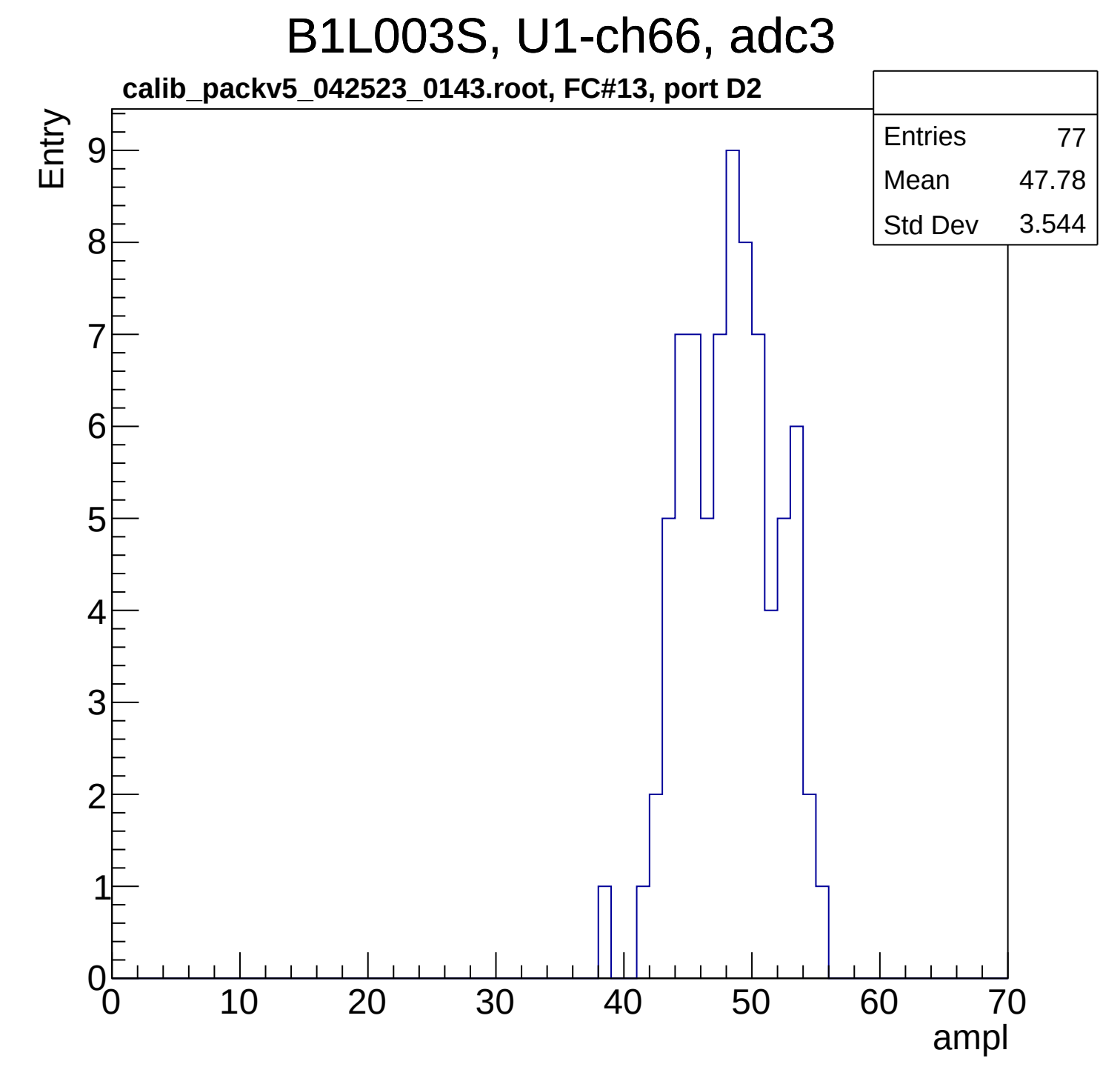

B1L003S, U1-ch65, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.43
Std Dev 1.99
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch65, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch66, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 27.27
Std Dev 4.651
Gaus mean : 28.5157
Gaus Width: 3.0270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch66, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.9
Std Dev 3.309
Gaus mean : 34.9166
Gaus Width: 2.8152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch66, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 41.34
Std Dev 2.99
Gaus mean : 41.6090
Gaus Width: 4.0105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch66, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 47.78
Std Dev 3.544
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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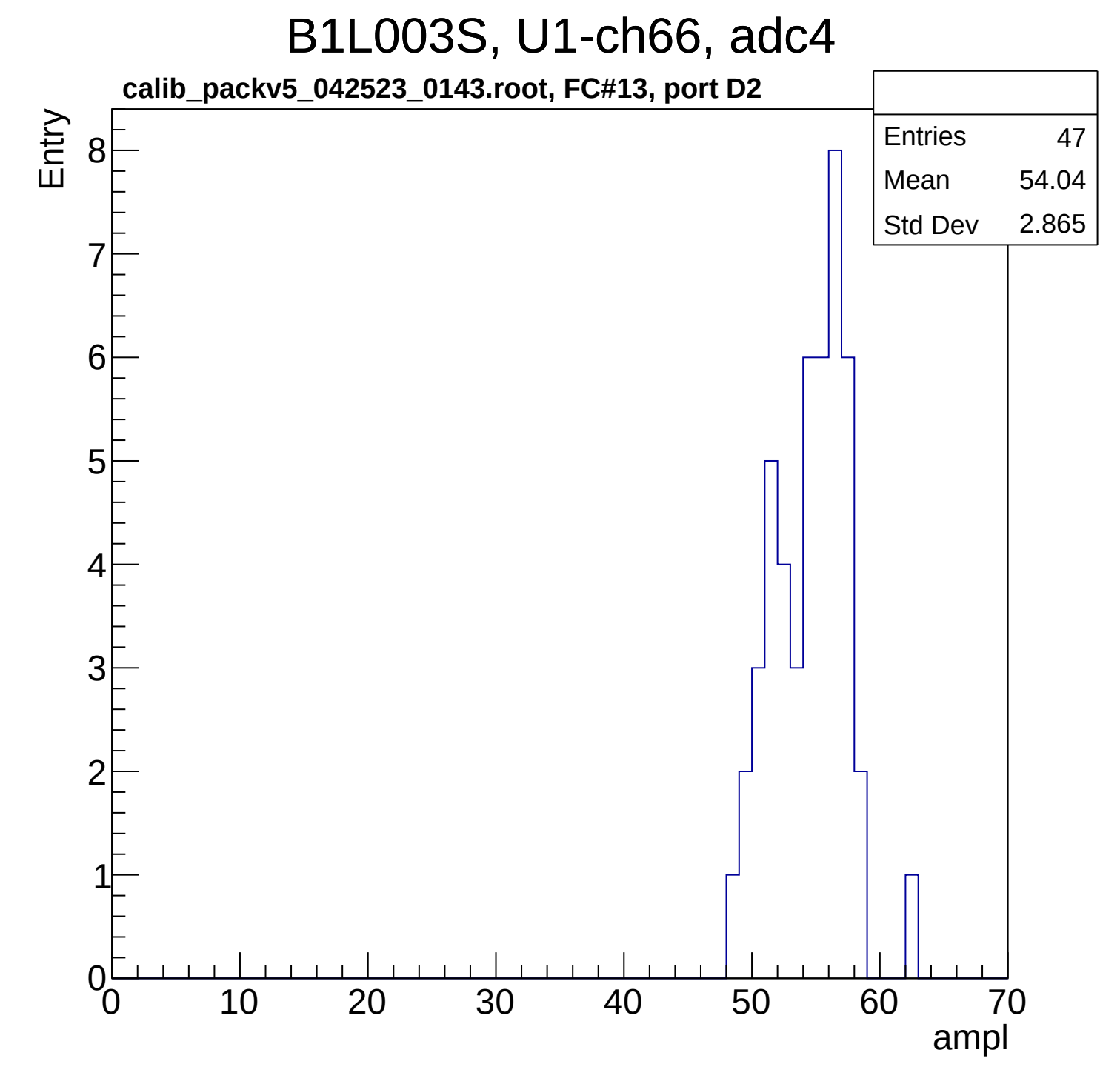
60
70


B1L003S, U1-ch66, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.04
Std Dev 2.865
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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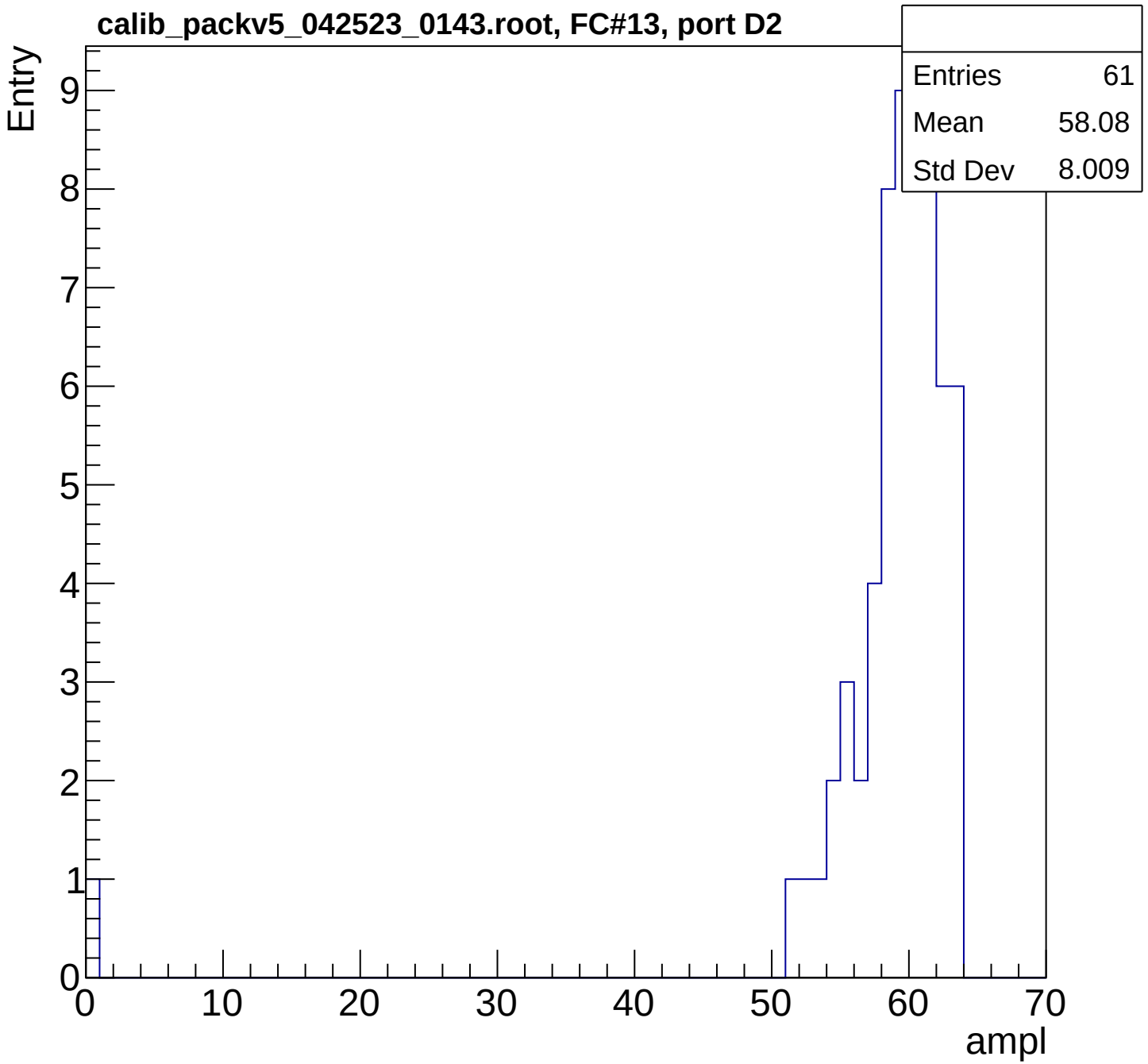
40
50
60
70


B1L003S, U1-ch66, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 58.08
Std Dev 8.009
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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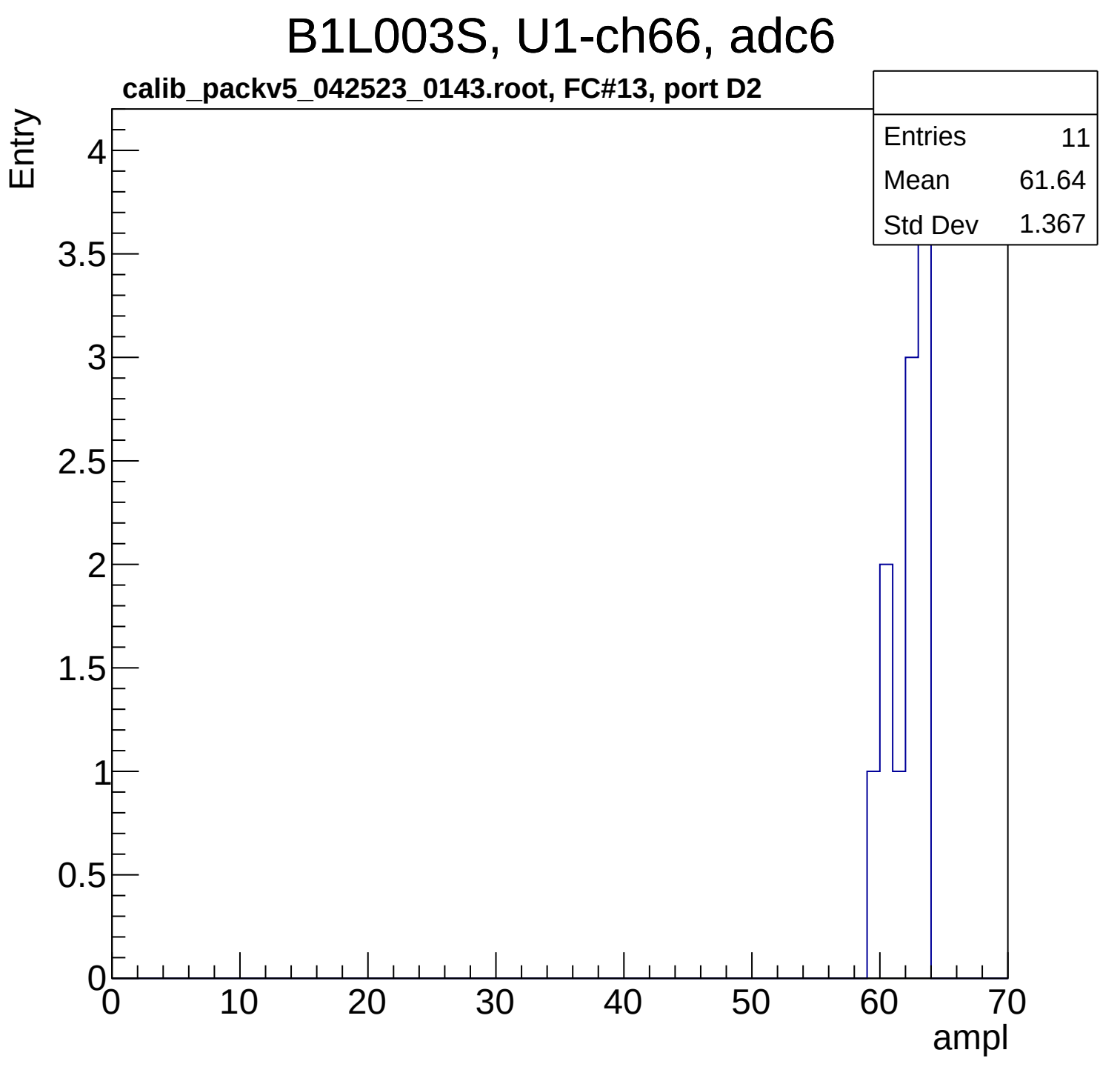
60
70


B1L003S, U1-ch66, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.64
Std Dev 1.367
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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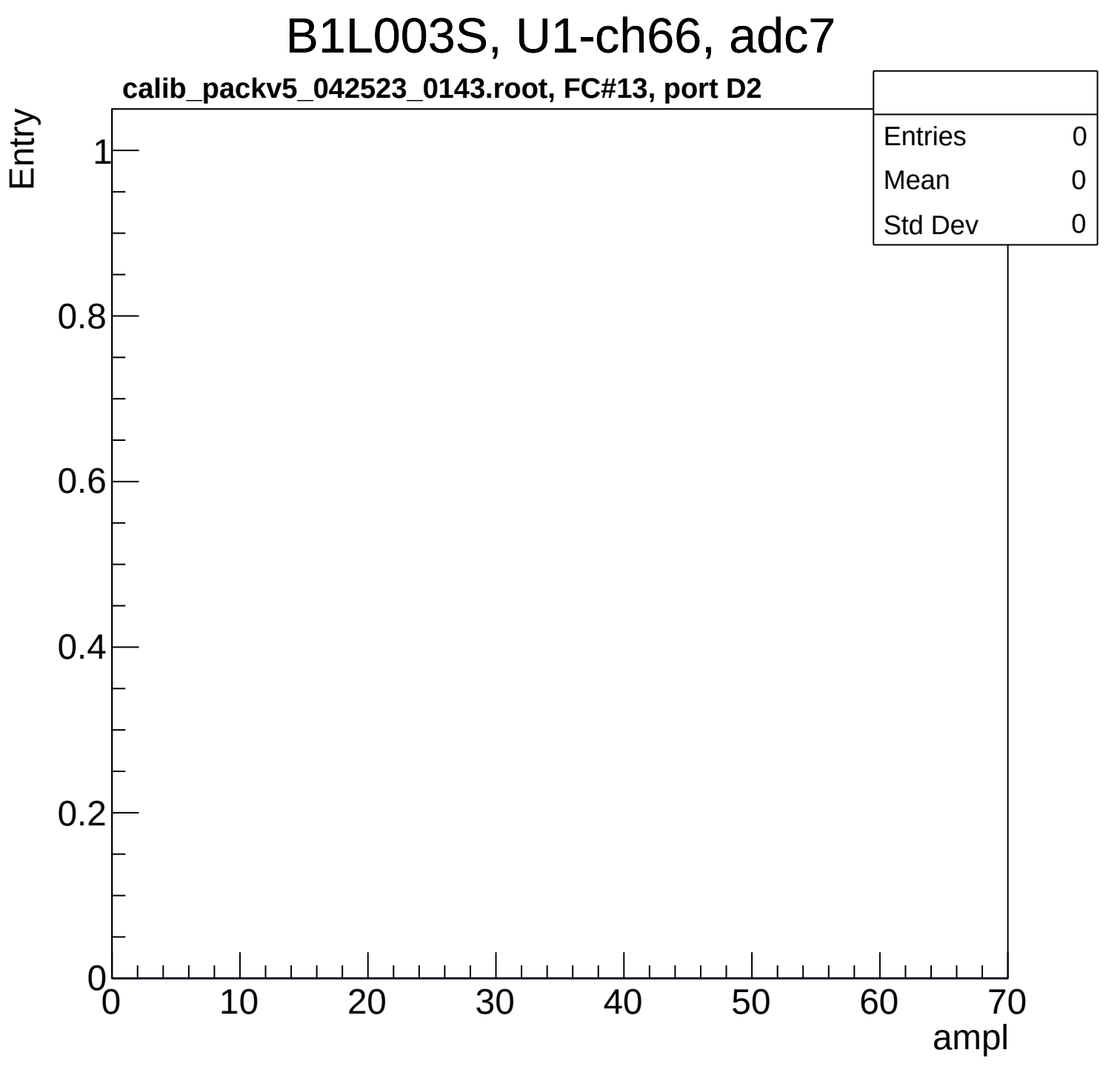
40
50
60
70


B1L003S, U1-ch66, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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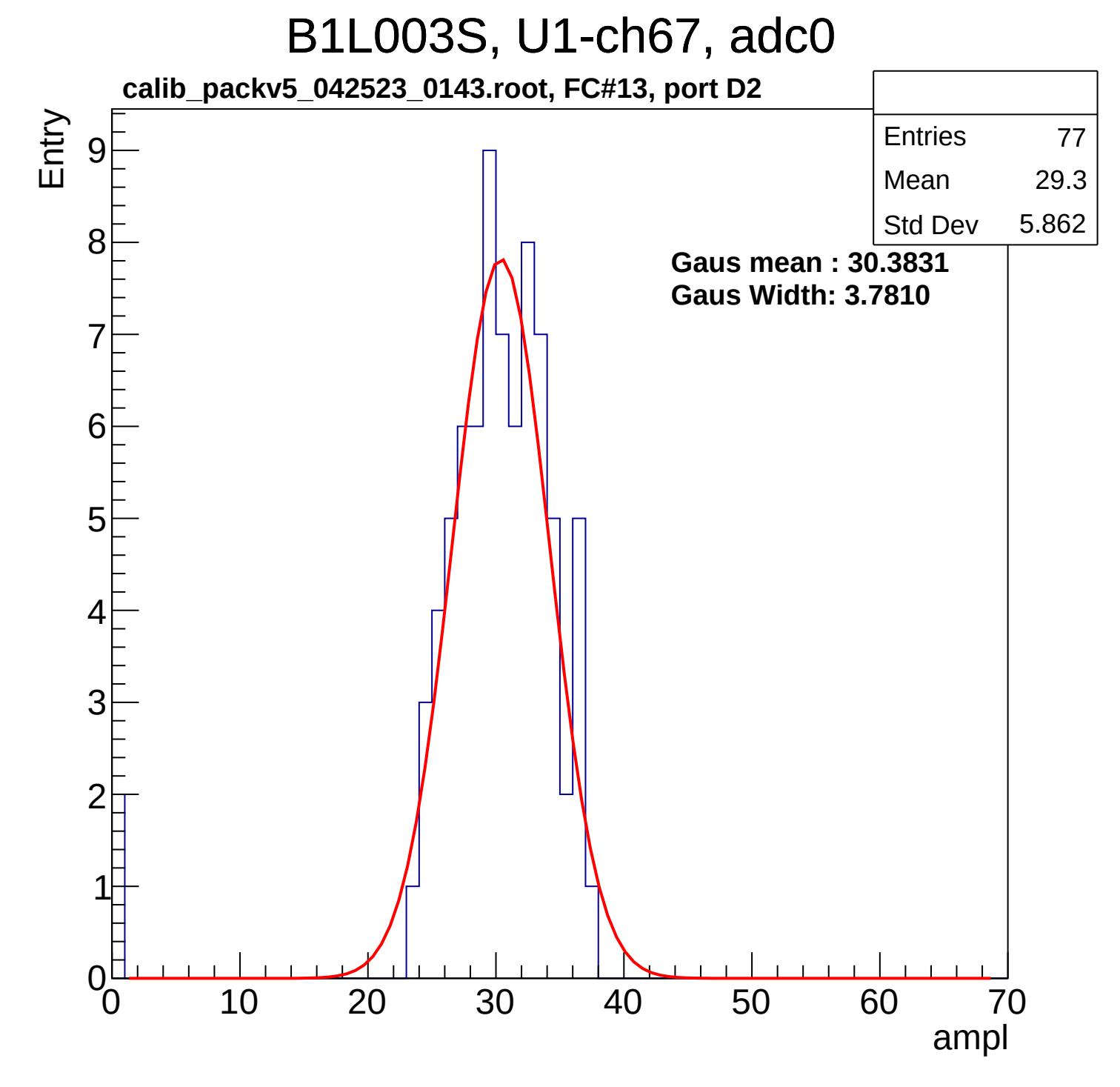
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch67, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 29.3
Std Dev 5.862
Gaus mean : 30.3831
Gaus Width: 3.7810
Entry
ampl
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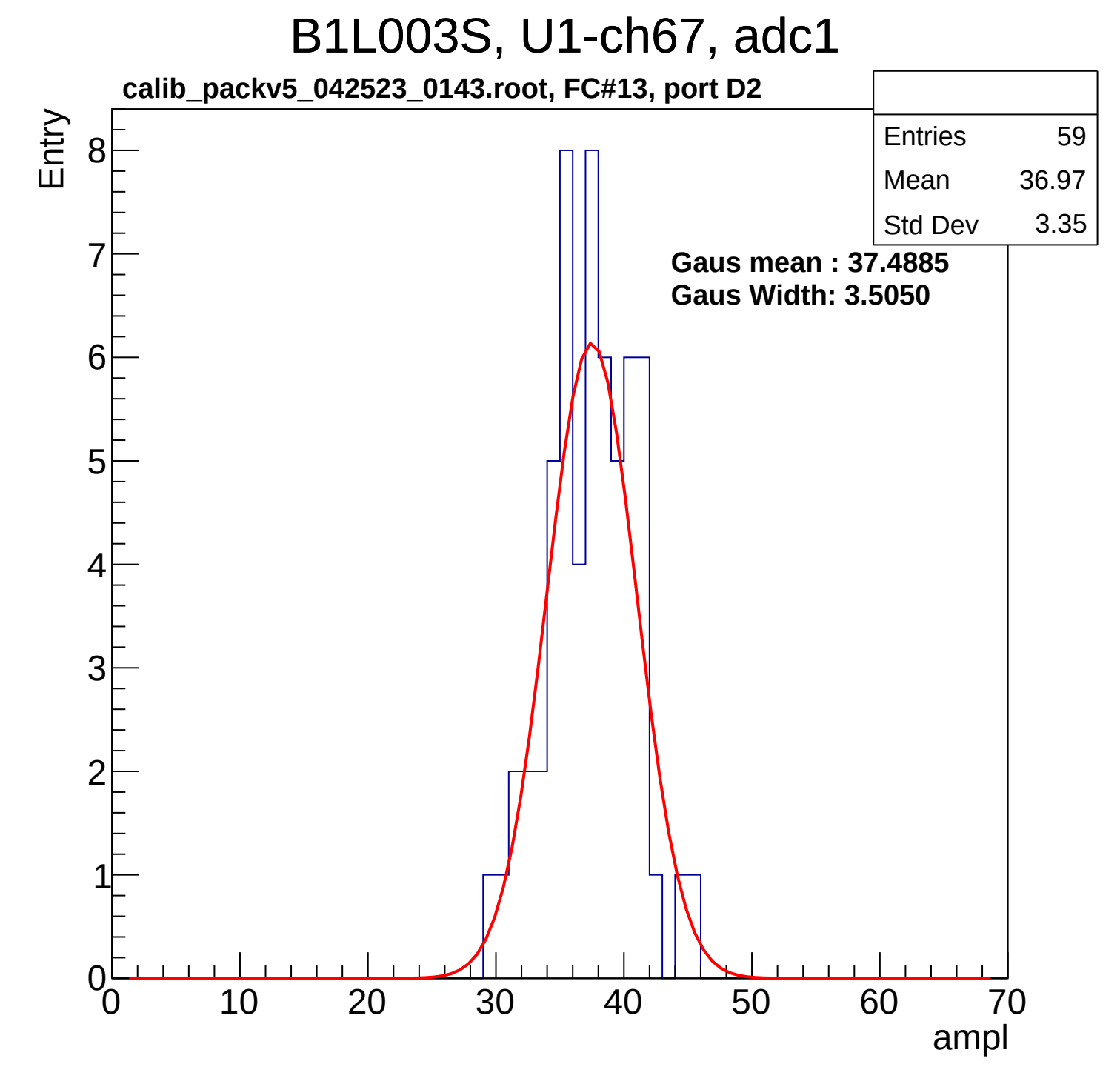
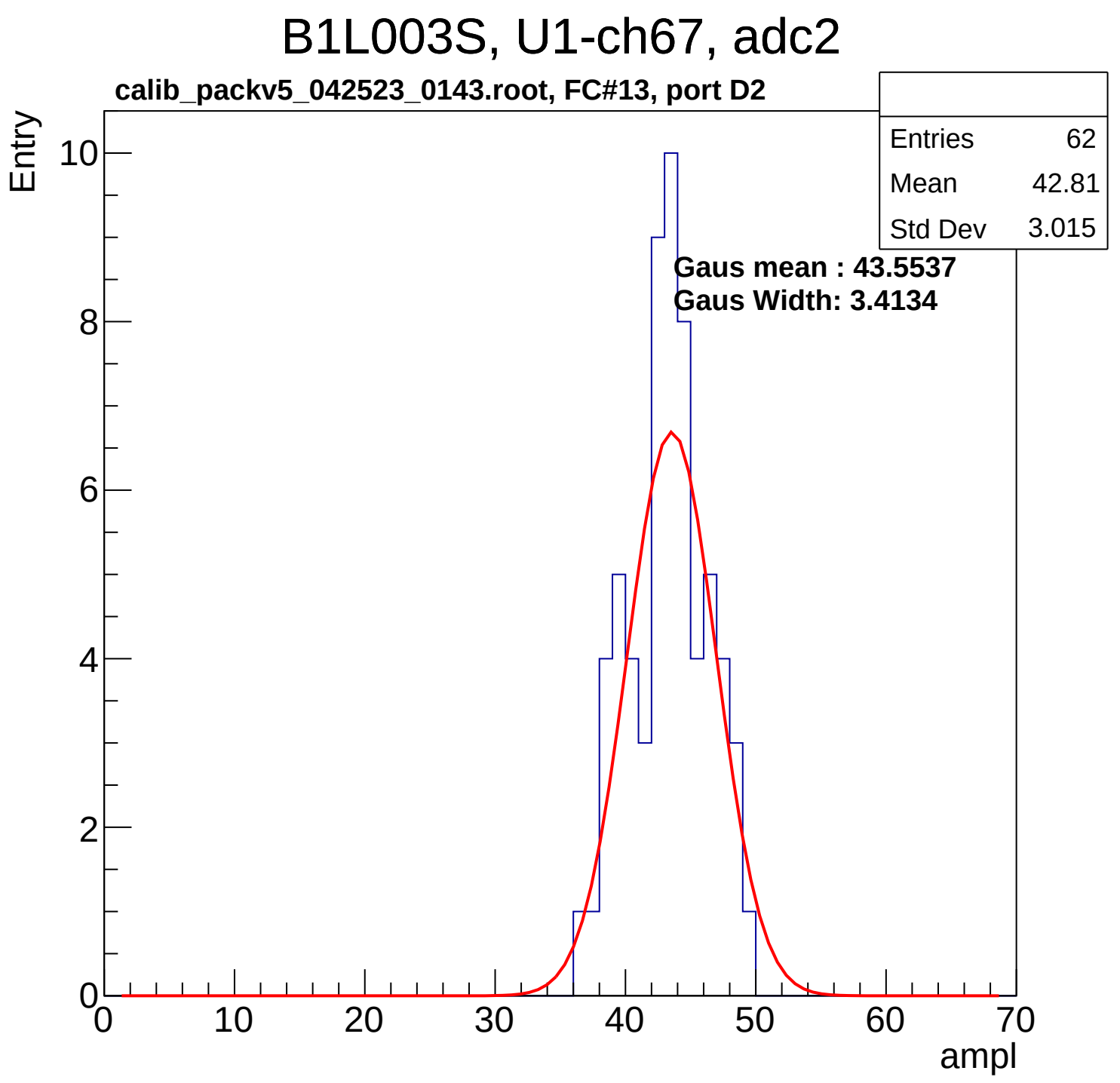
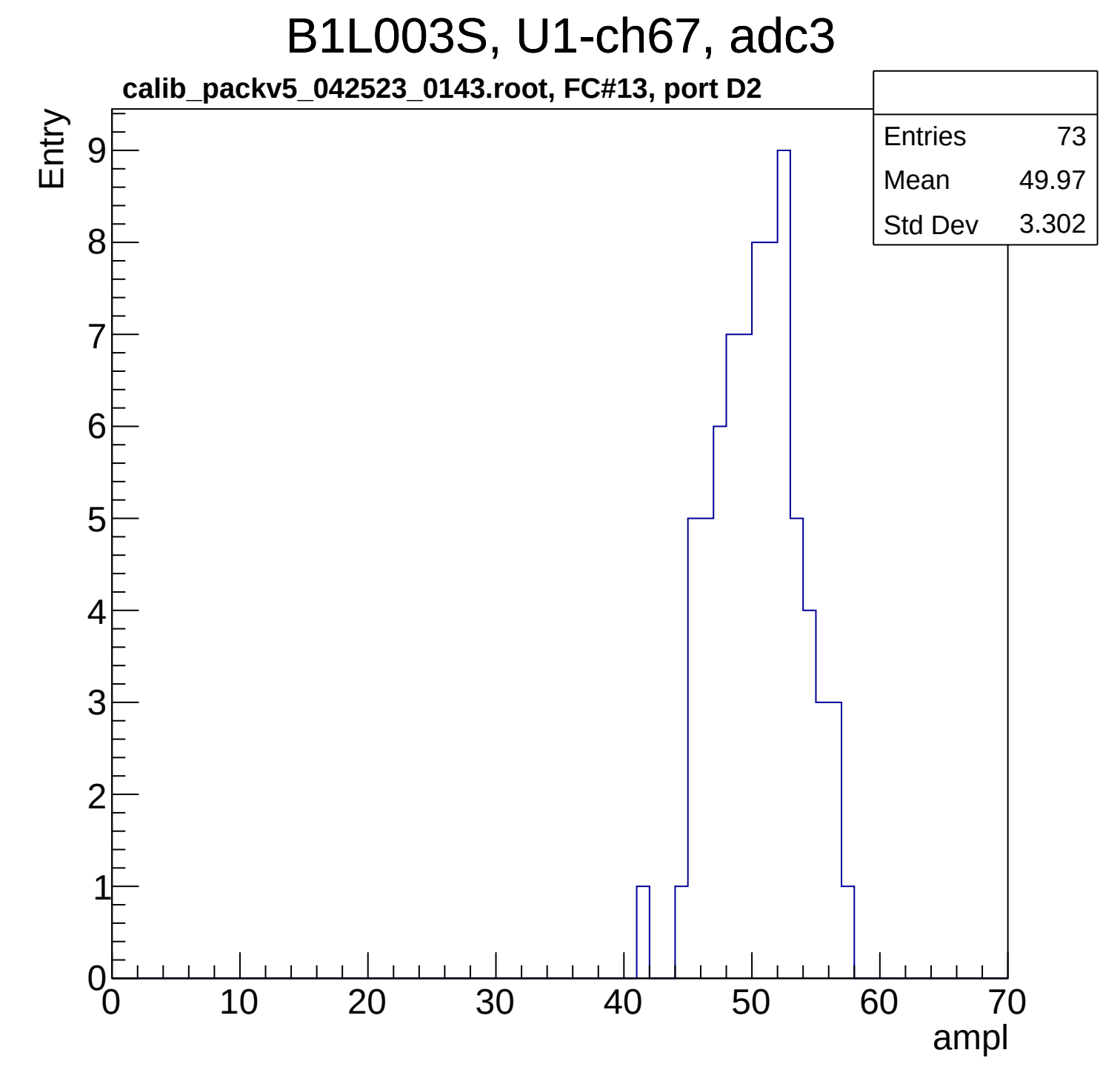
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch67, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 36.97
Std Dev 3.35
Gaus mean : 37.4885
Gaus Width: 3.5050
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch67, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.81
Std Dev 3.015
Gaus mean : 43.5537
Gaus Width: 3.4134
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch67, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.97
Std Dev 3.302
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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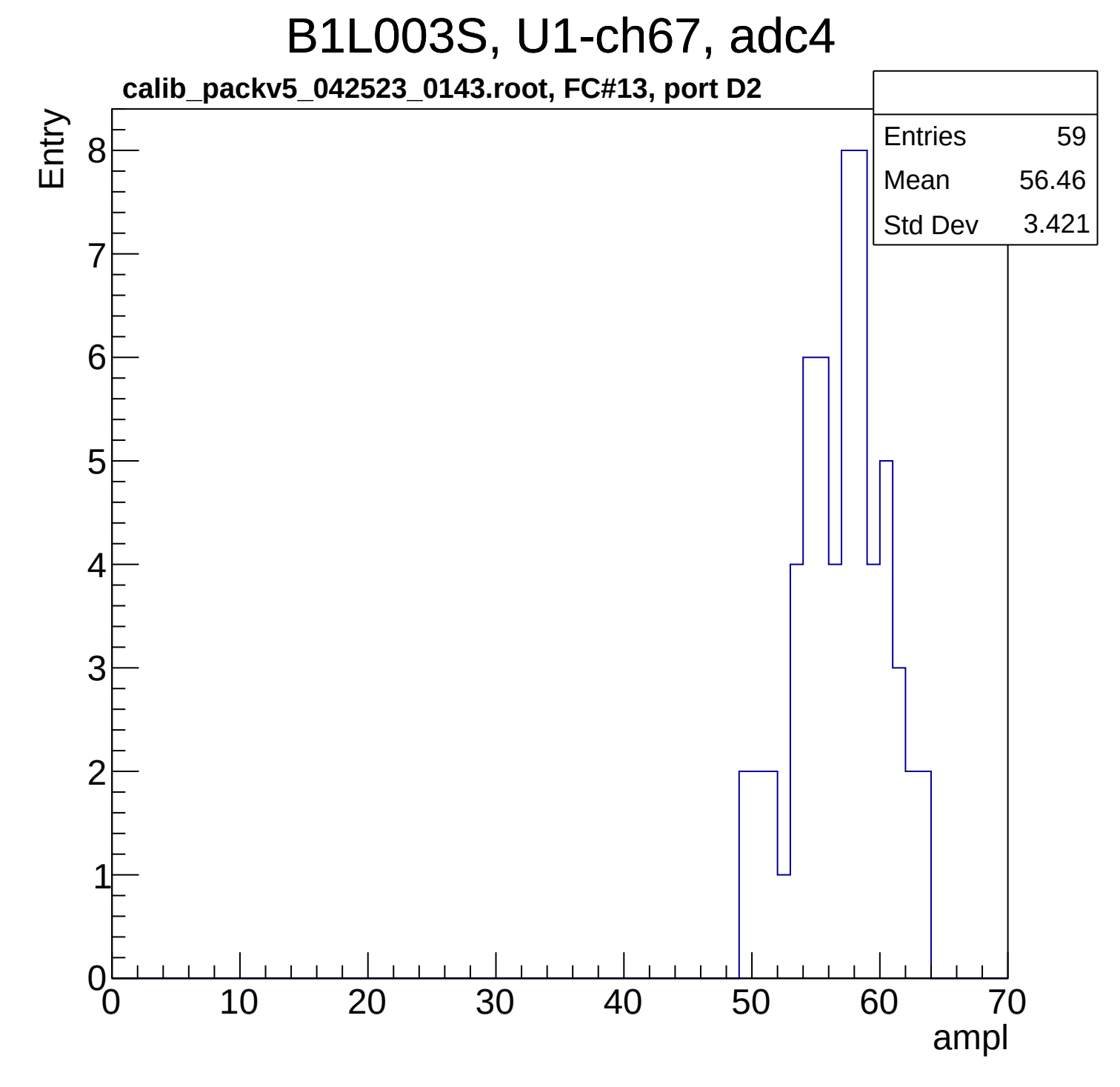
B1L003S, U1-ch67, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 56.46
Std Dev 3.421
Entry
ampl
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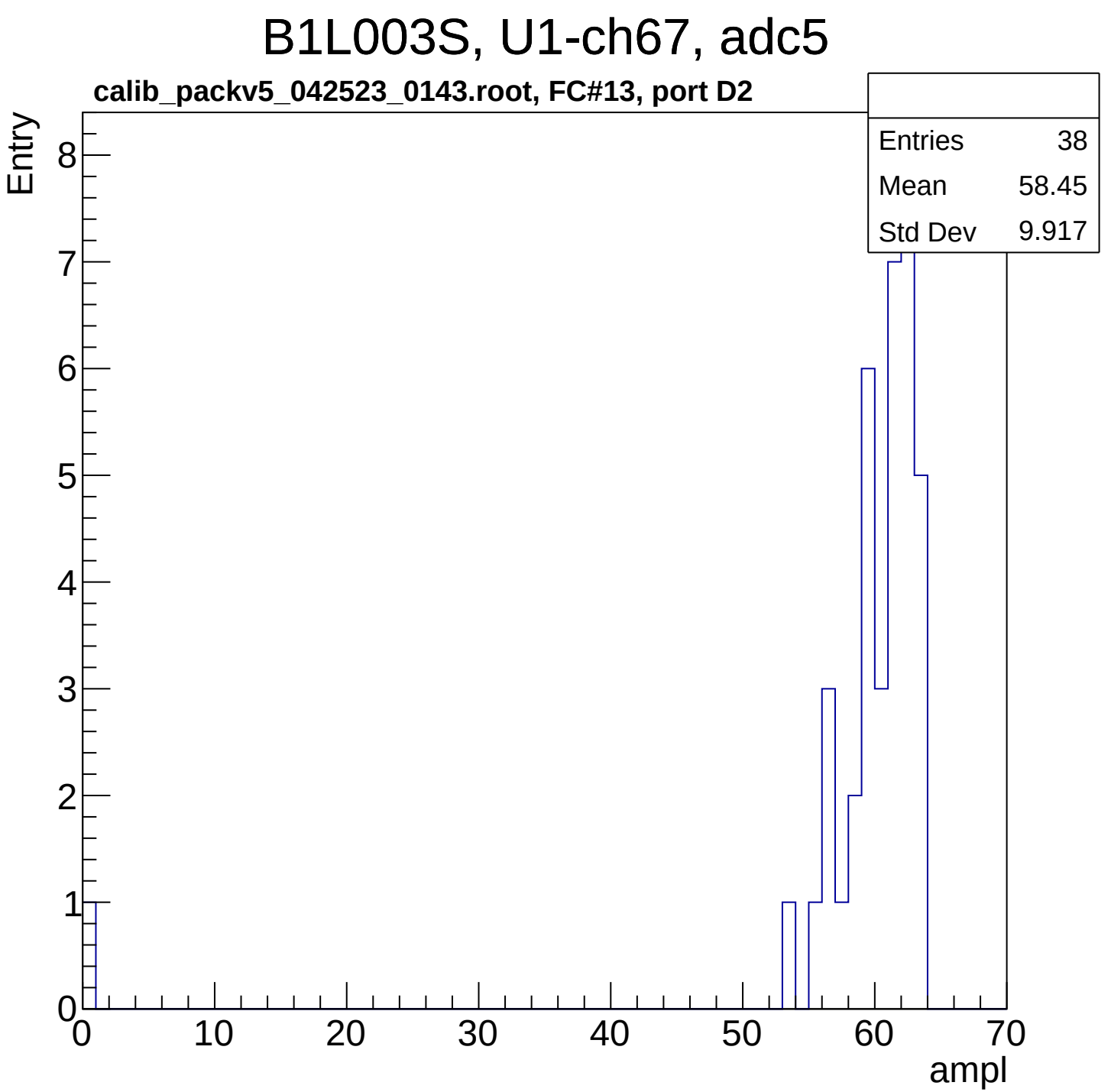
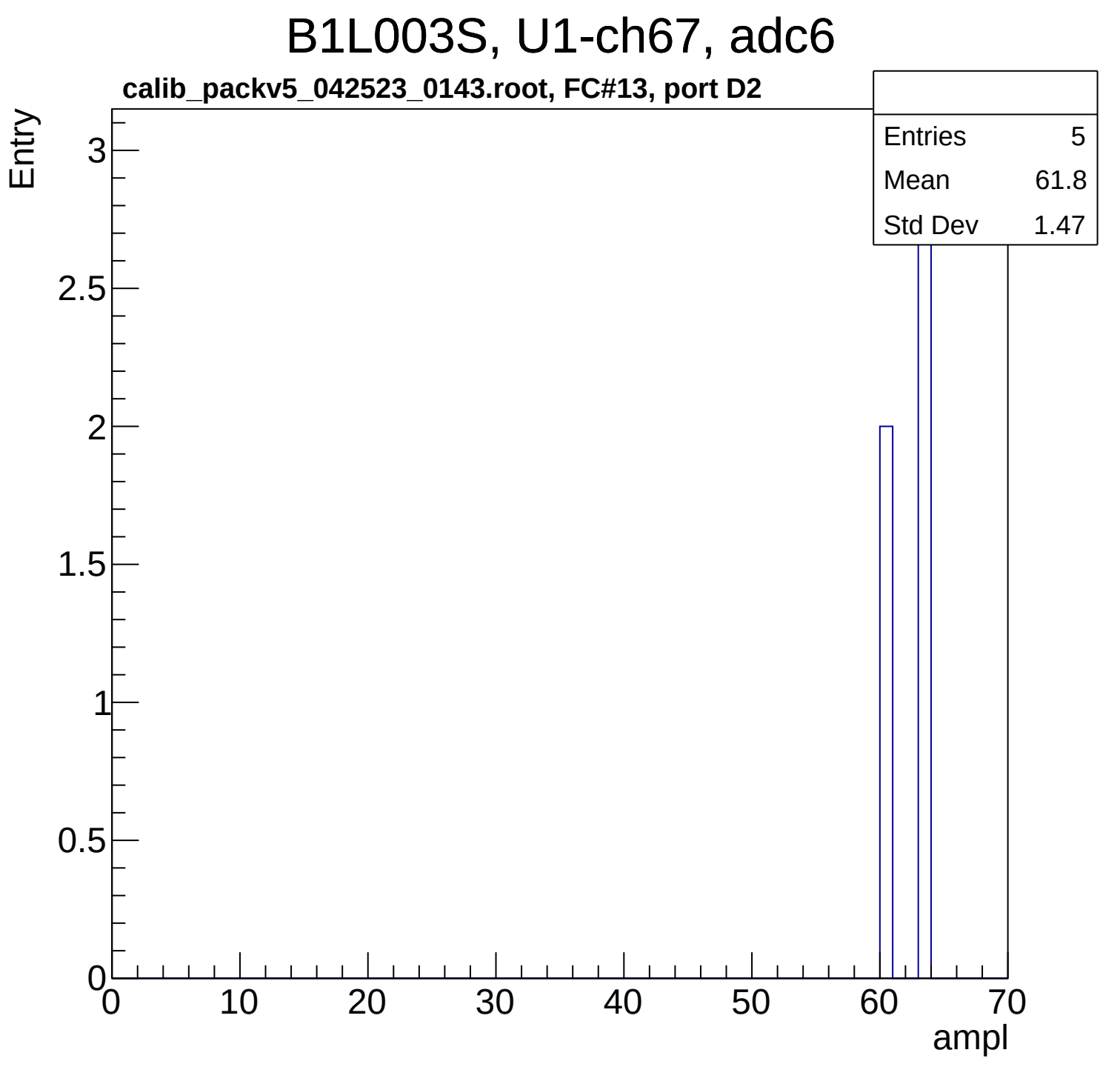
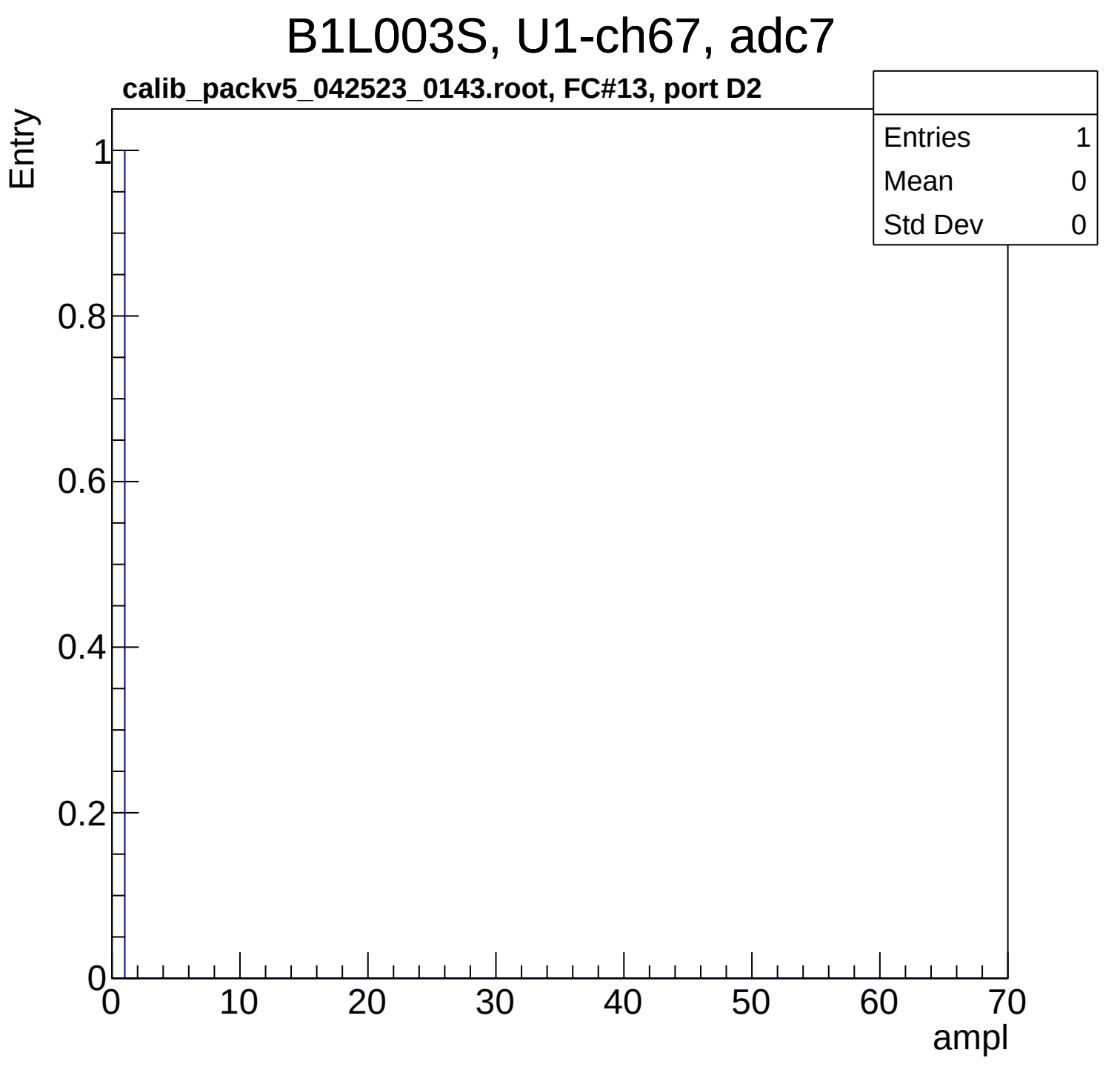
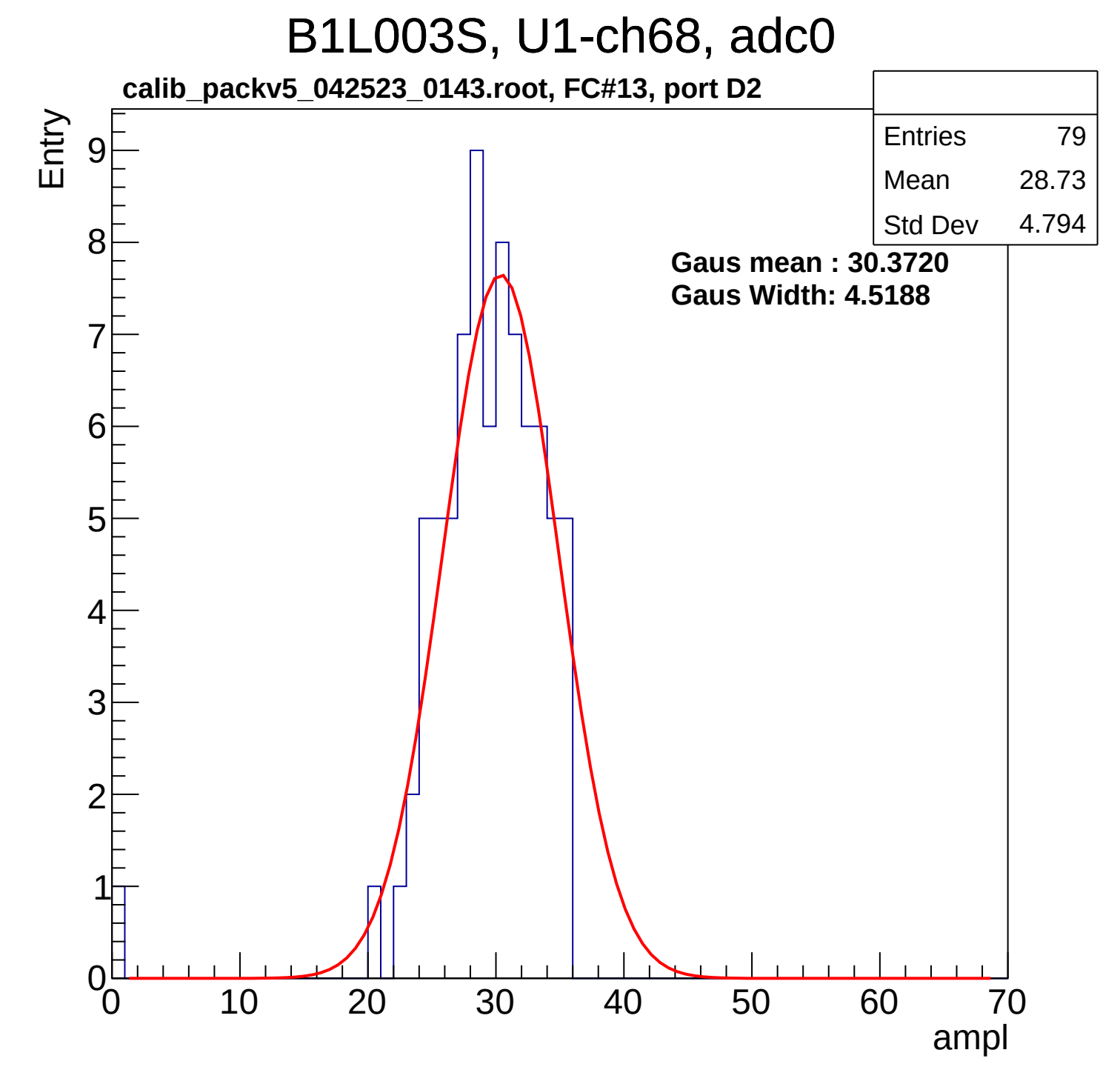
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch67, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 58.45
Std Dev 9.917
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch67, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.8
Std Dev 1.47
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch67, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch68, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 28.73
Std Dev 4.794
Gaus mean : 30.3720
Gaus Width: 4.5188
Entry
ampl
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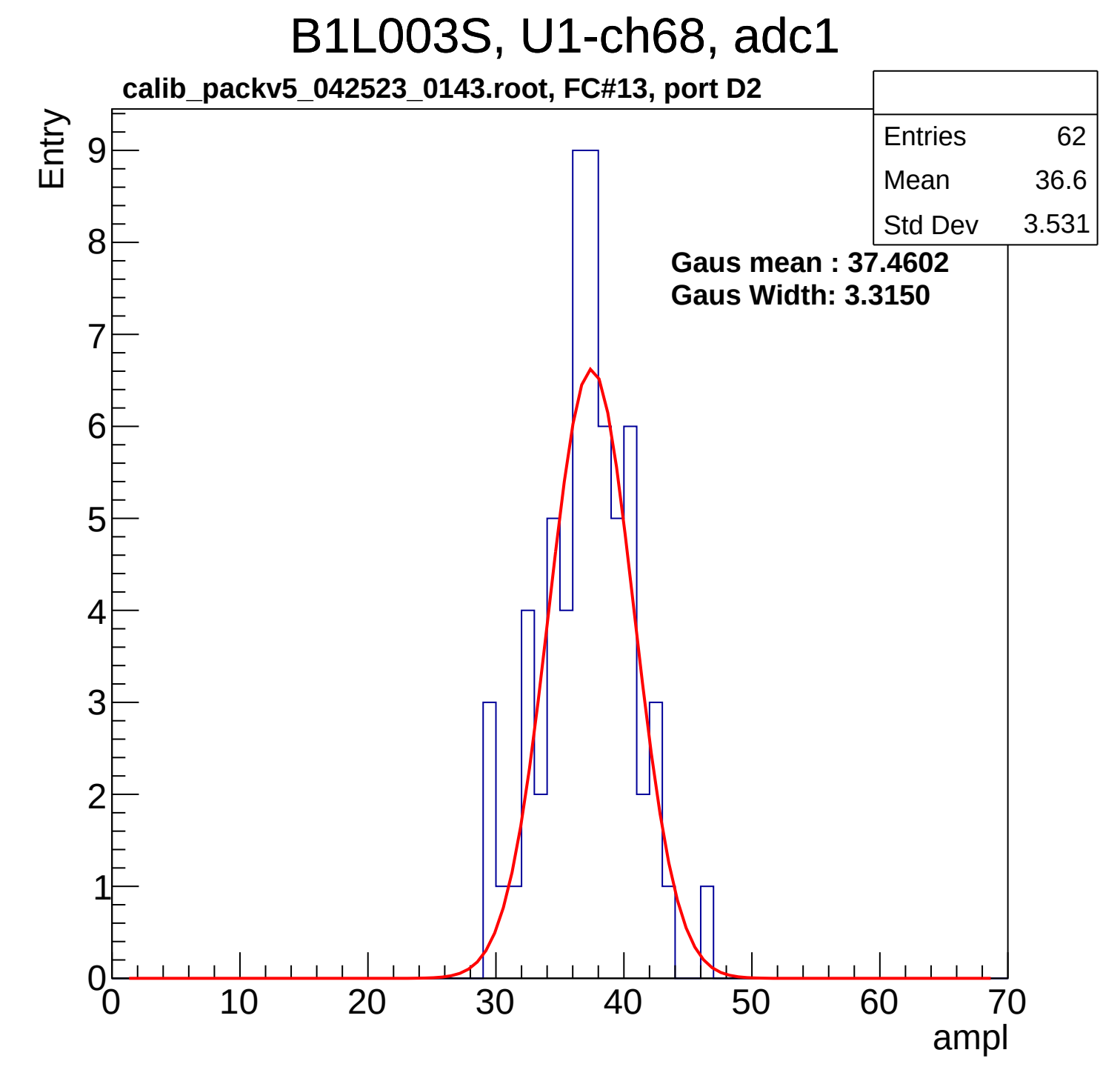
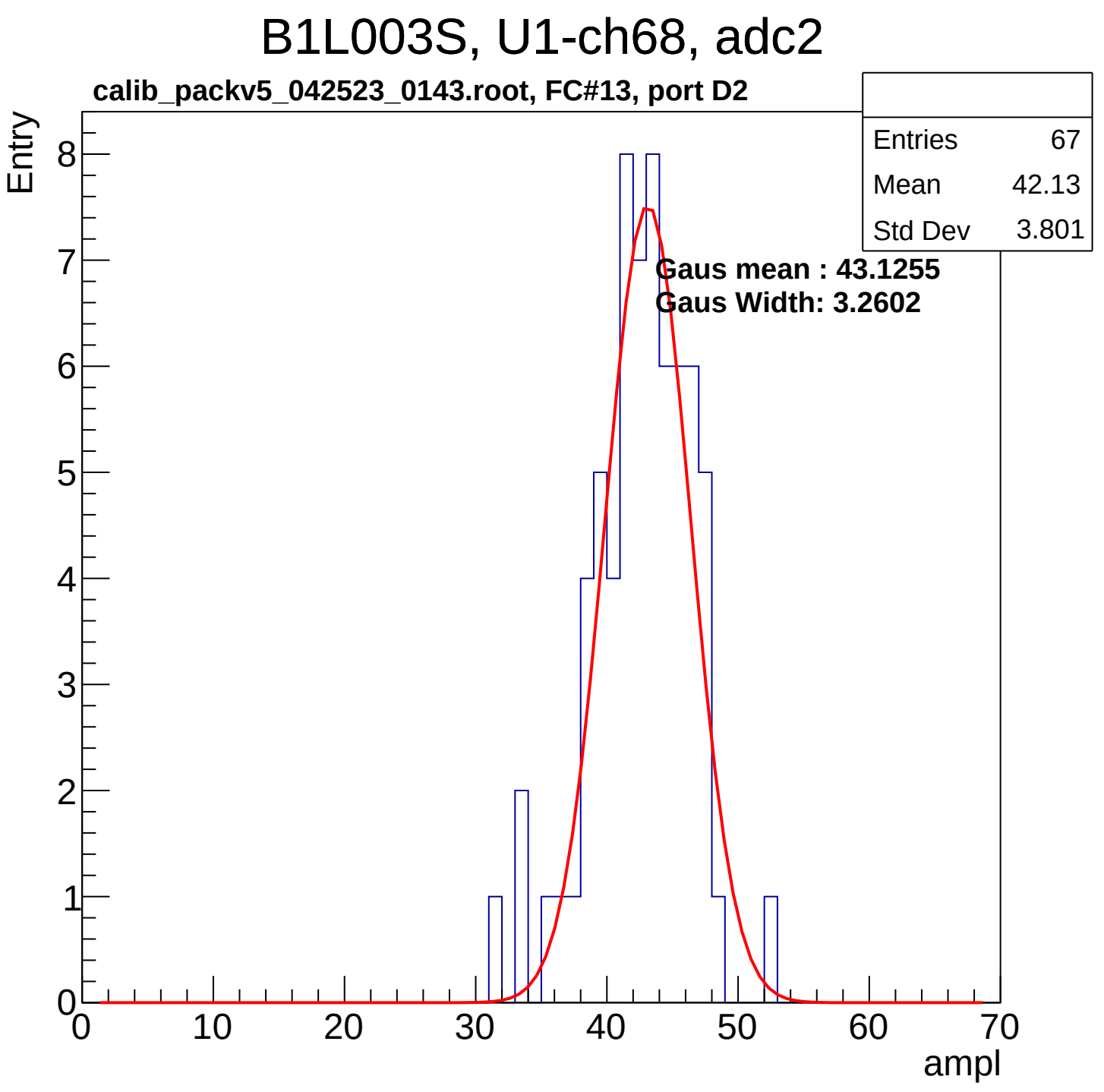
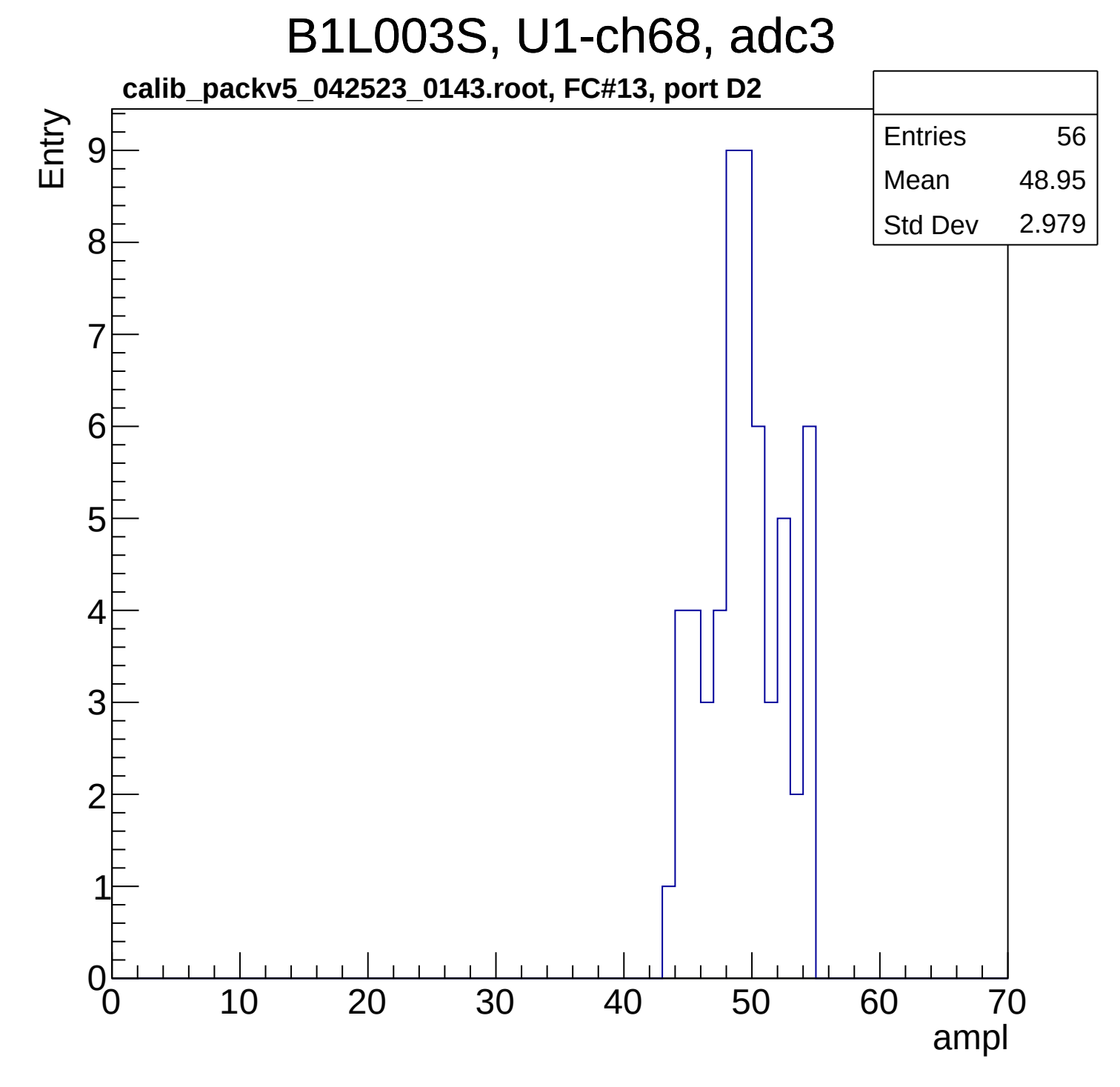
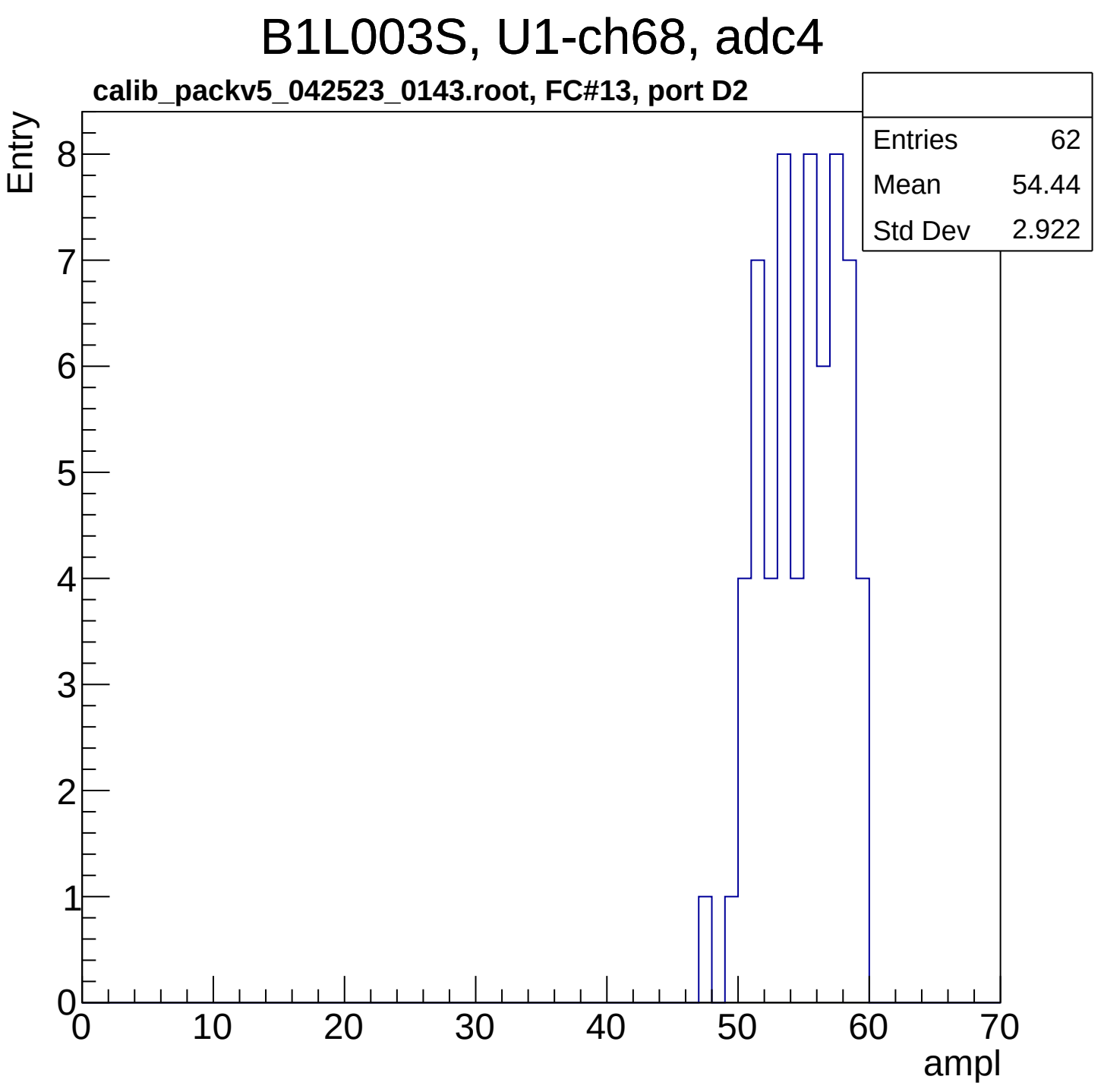
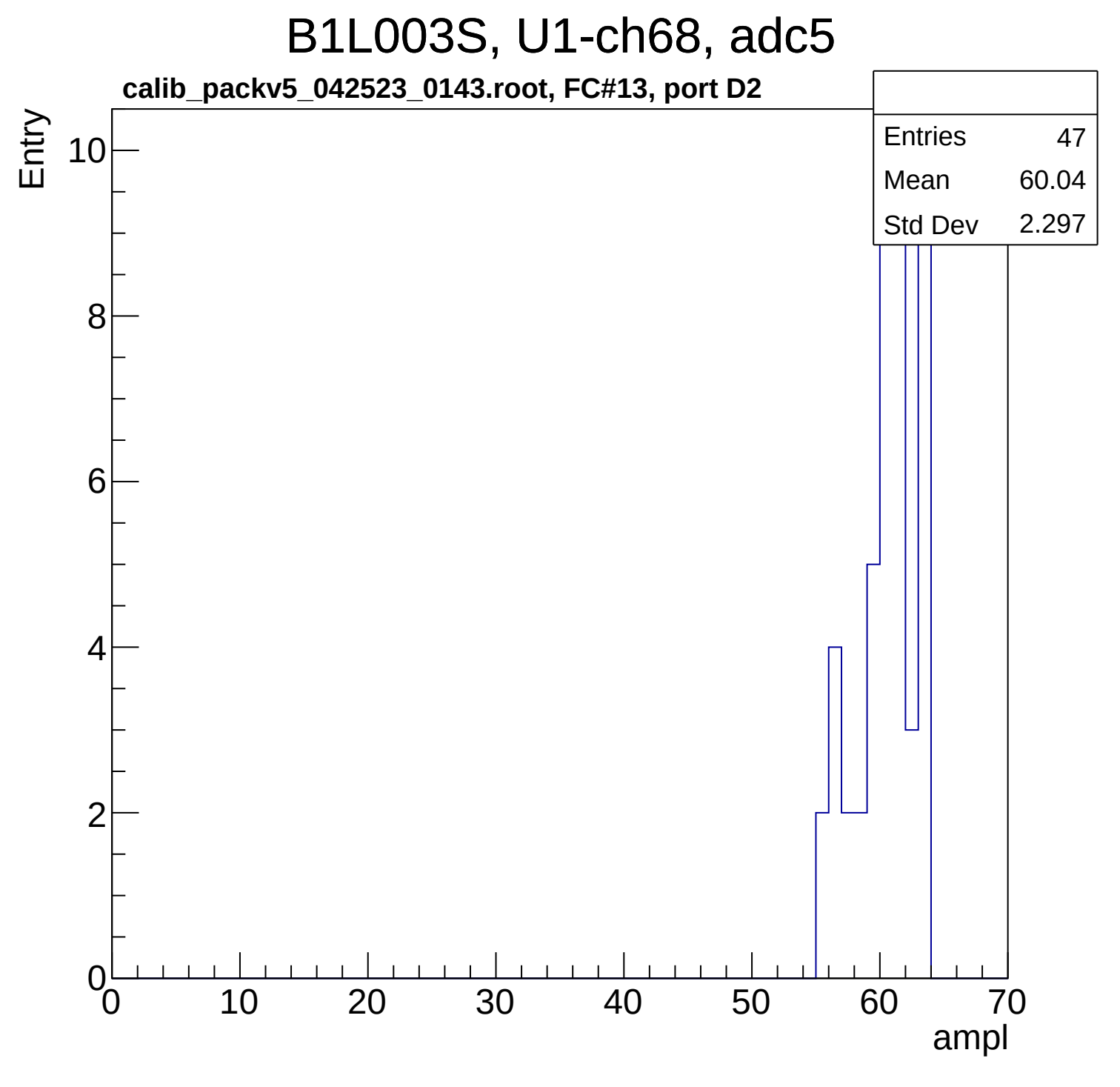
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

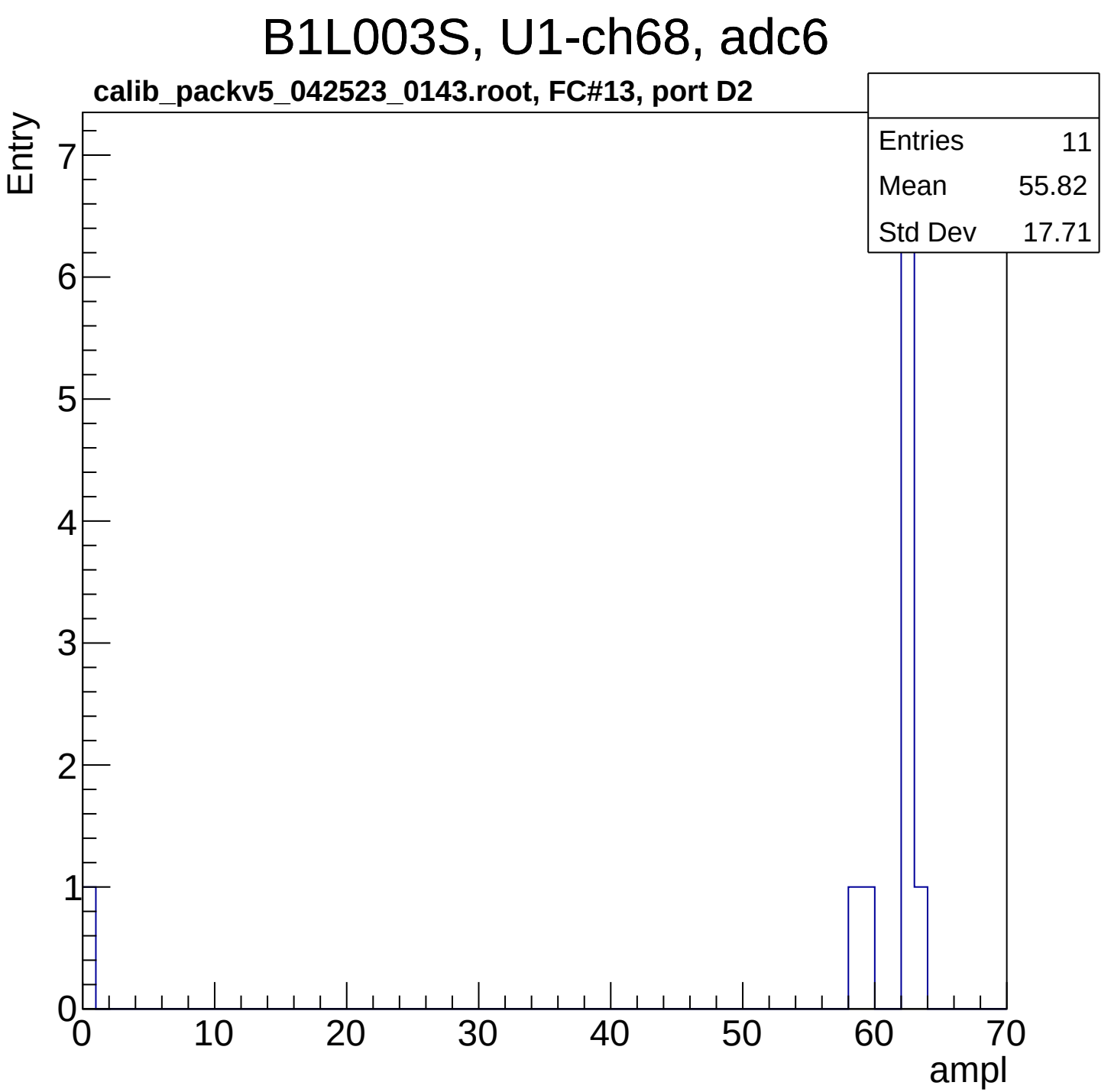

B1L003S, U1-ch68, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.6
Std Dev 3.531
Gaus mean : 37.4602
Gaus Width: 3.3150
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

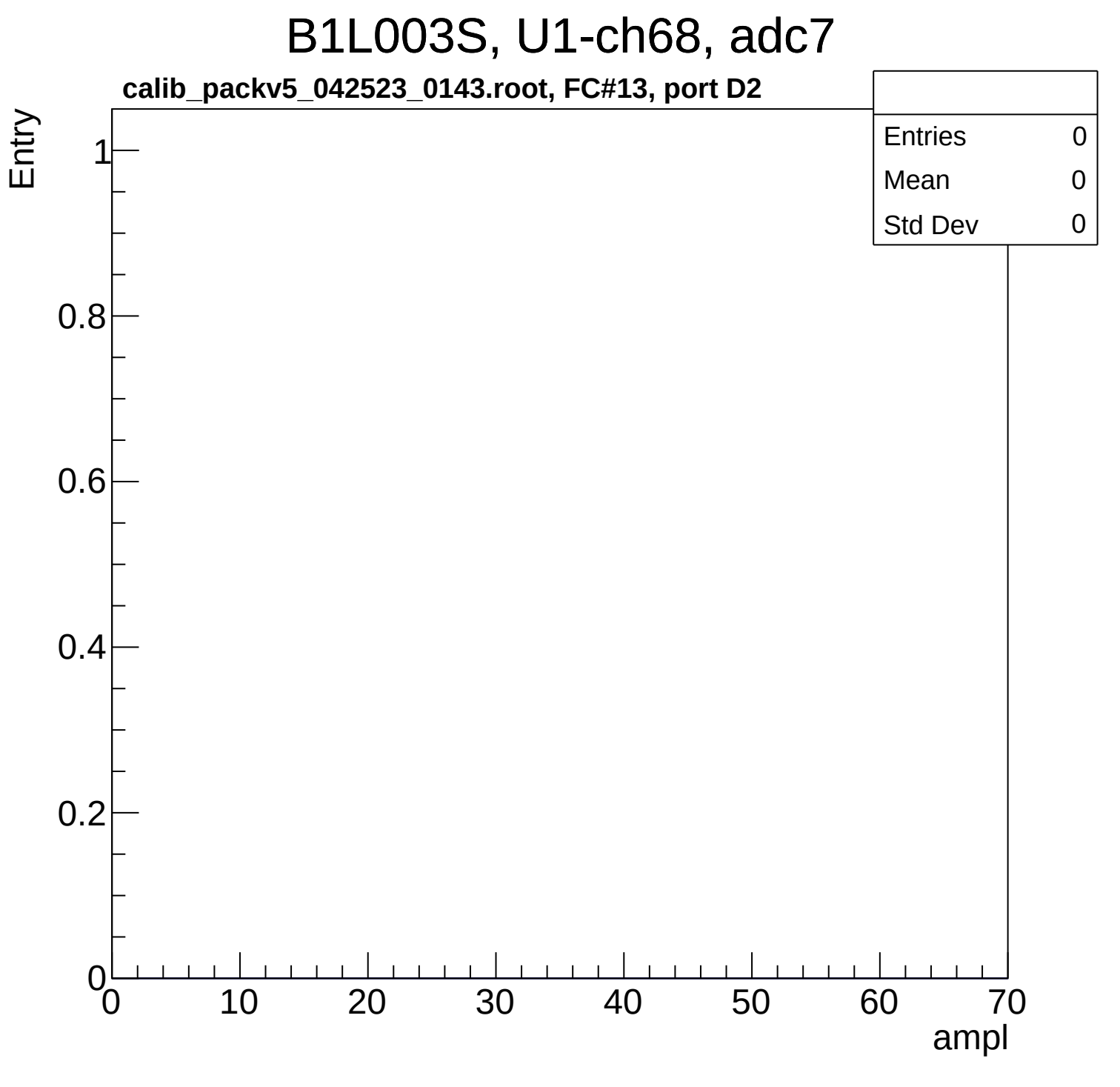

B1L003S, U1-ch68, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 42.13
Std Dev 3.801
Gaus mean : 43.1255
Gaus Width: 3.2602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

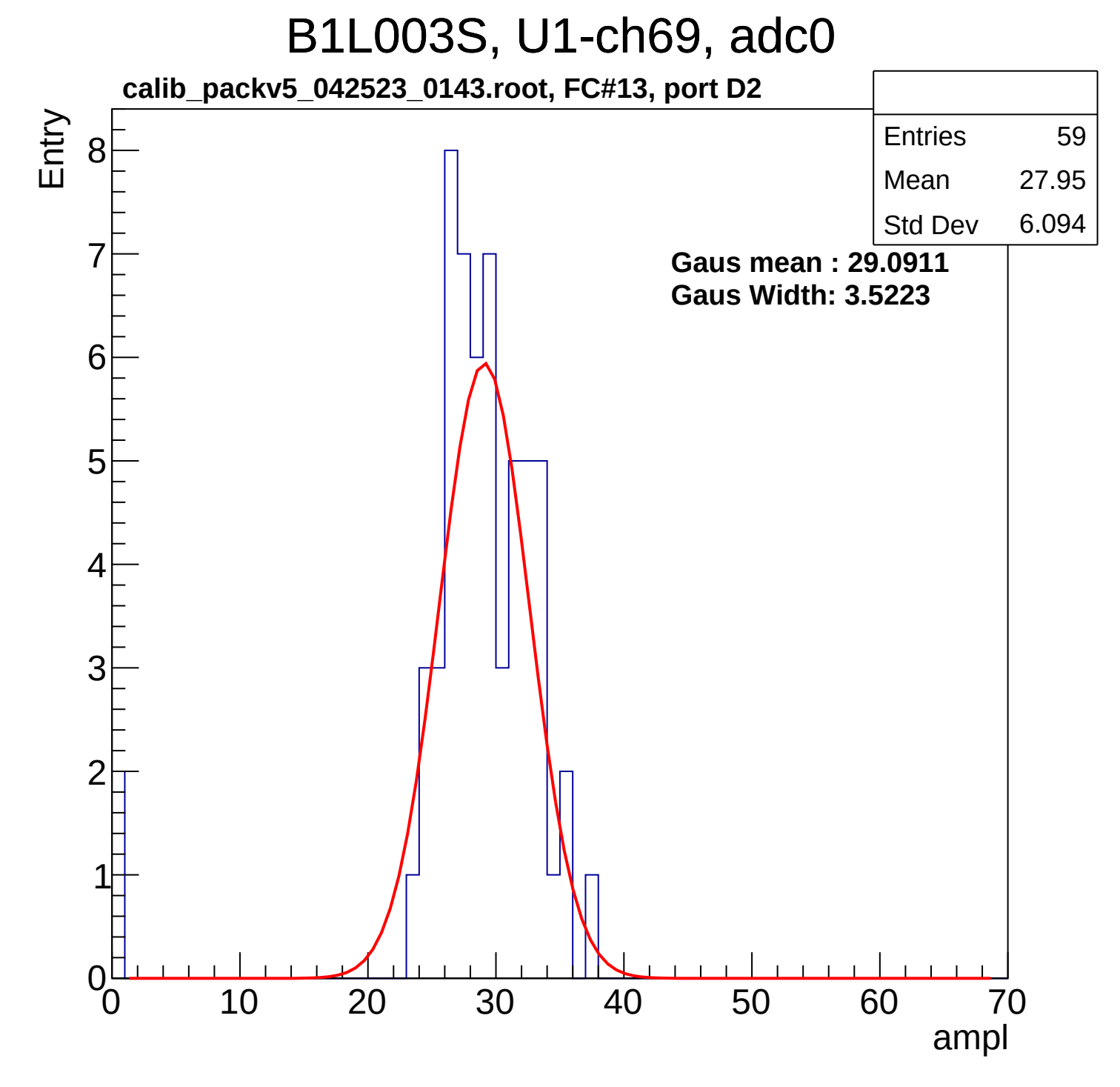

B1L003S, U1-ch68, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.95
Std Dev 2.979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

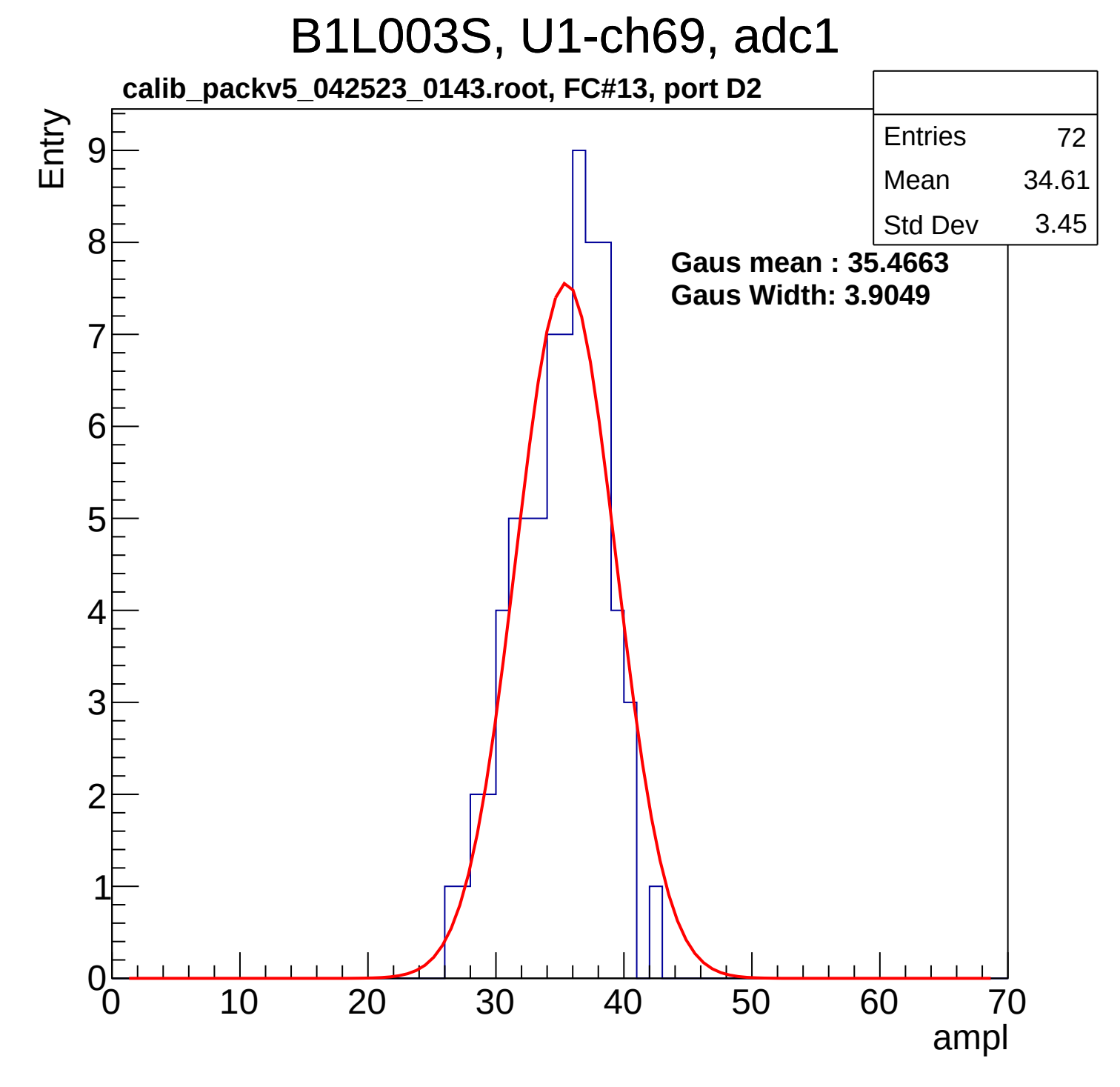

B1L003S, U1-ch68, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.44
Std Dev 2.922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch68, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 60.04
Std Dev 2.297
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch68, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.82
Std Dev 17.71
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch68, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch69, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 27.95
Std Dev 6.094
Gaus mean : 29.0911
Gaus Width: 3.5223
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch69, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 34.61
Std Dev 3.45
Gaus mean : 35.4663
Gaus Width: 3.9049
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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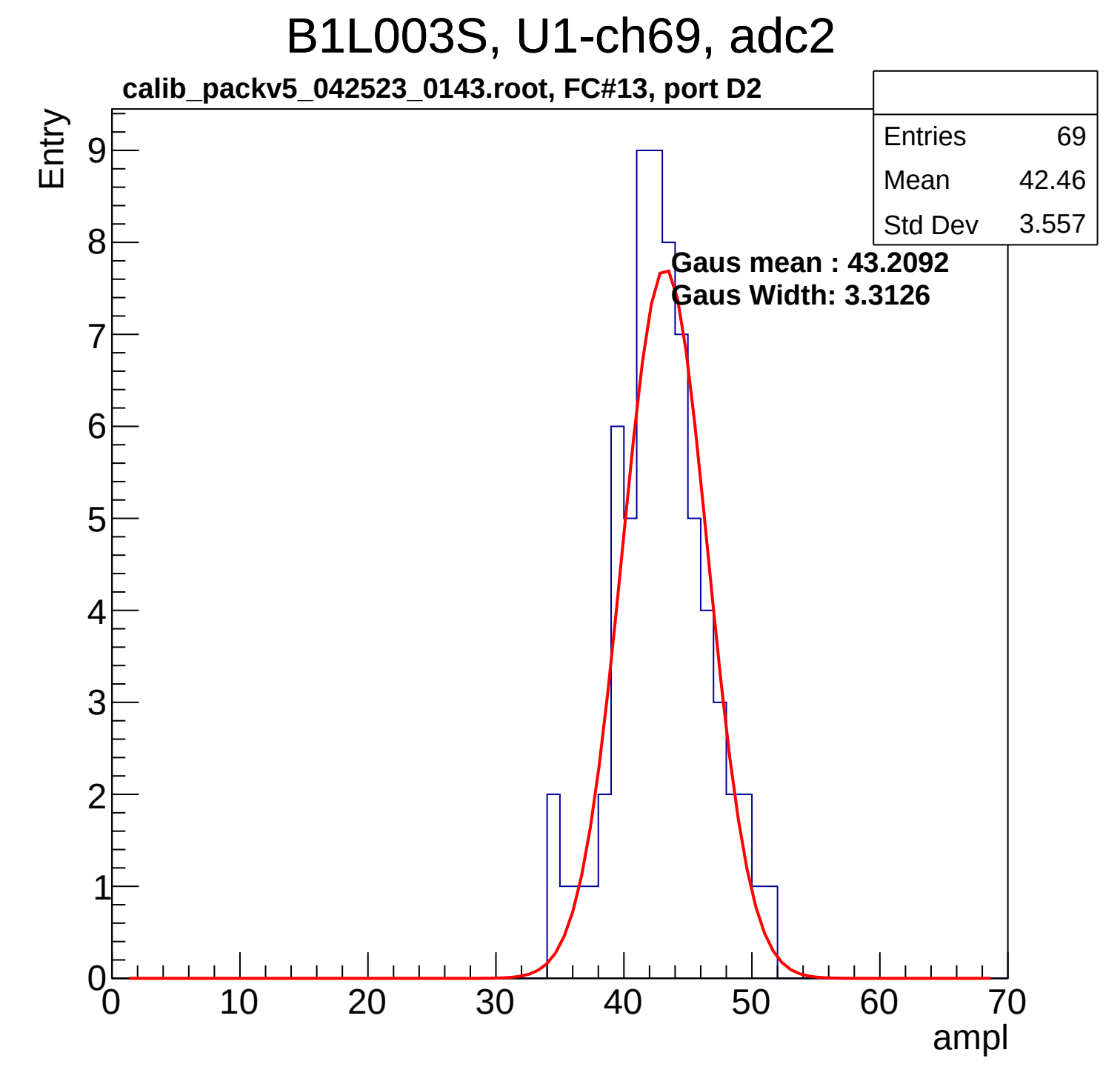
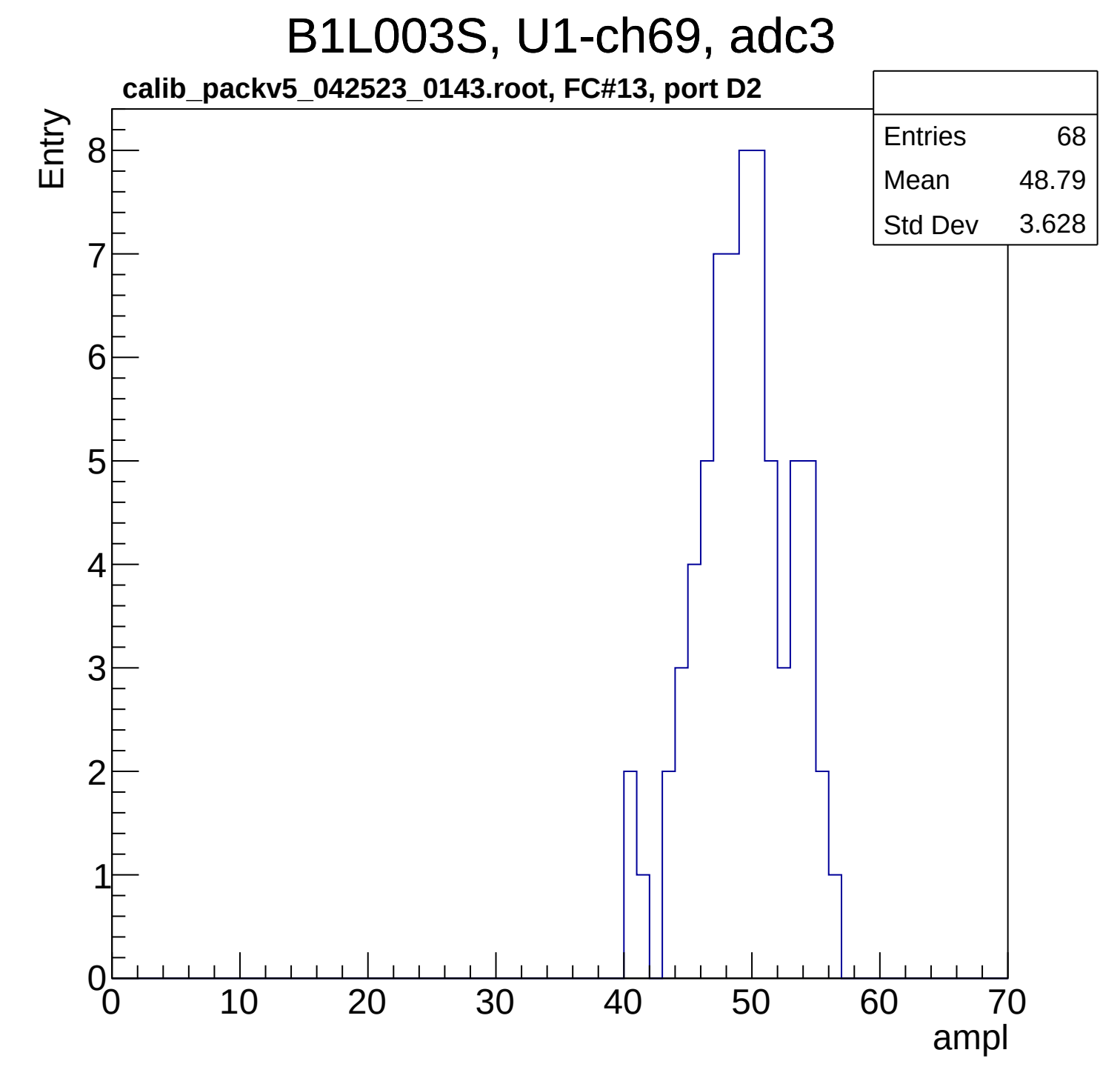
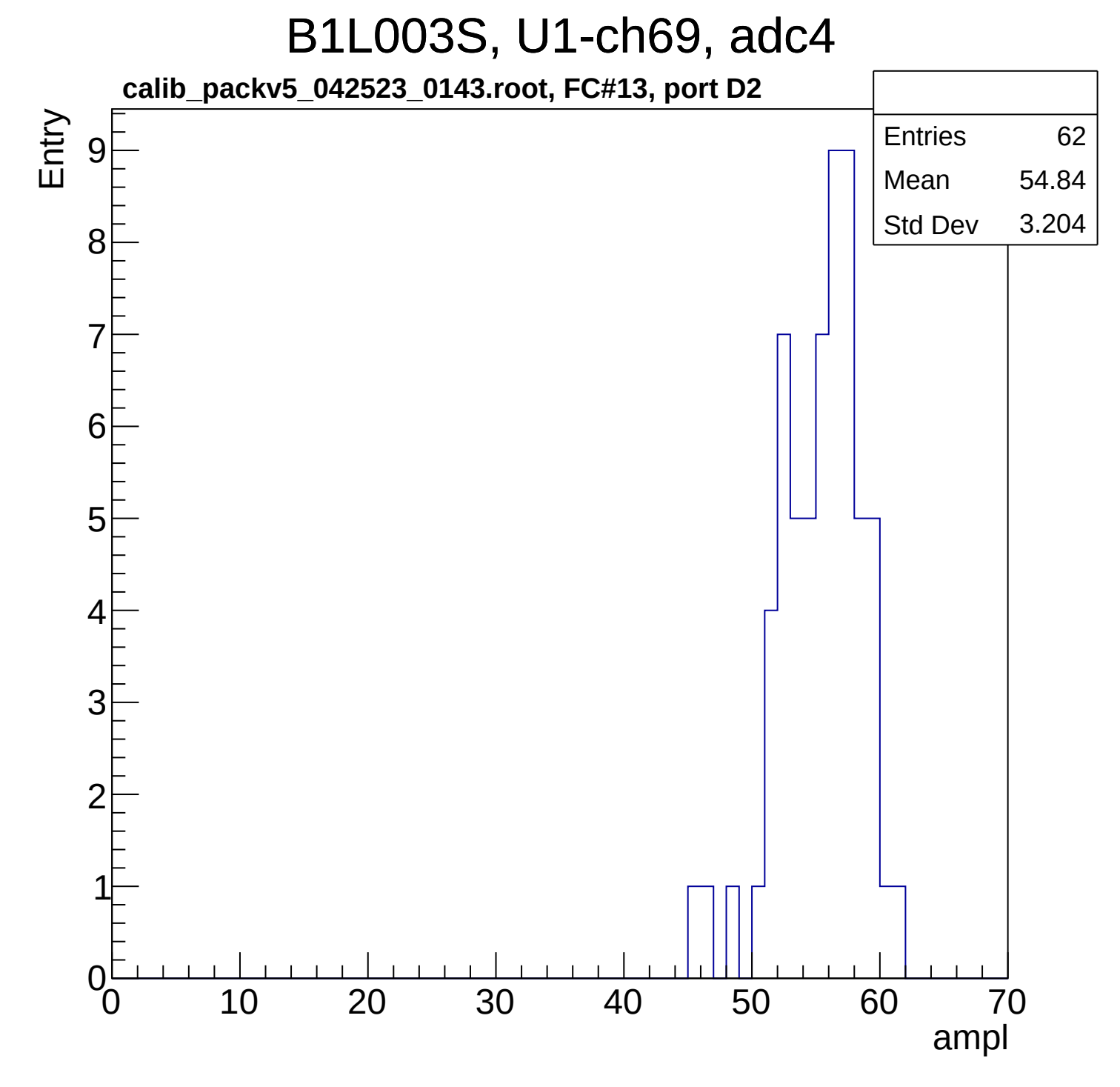
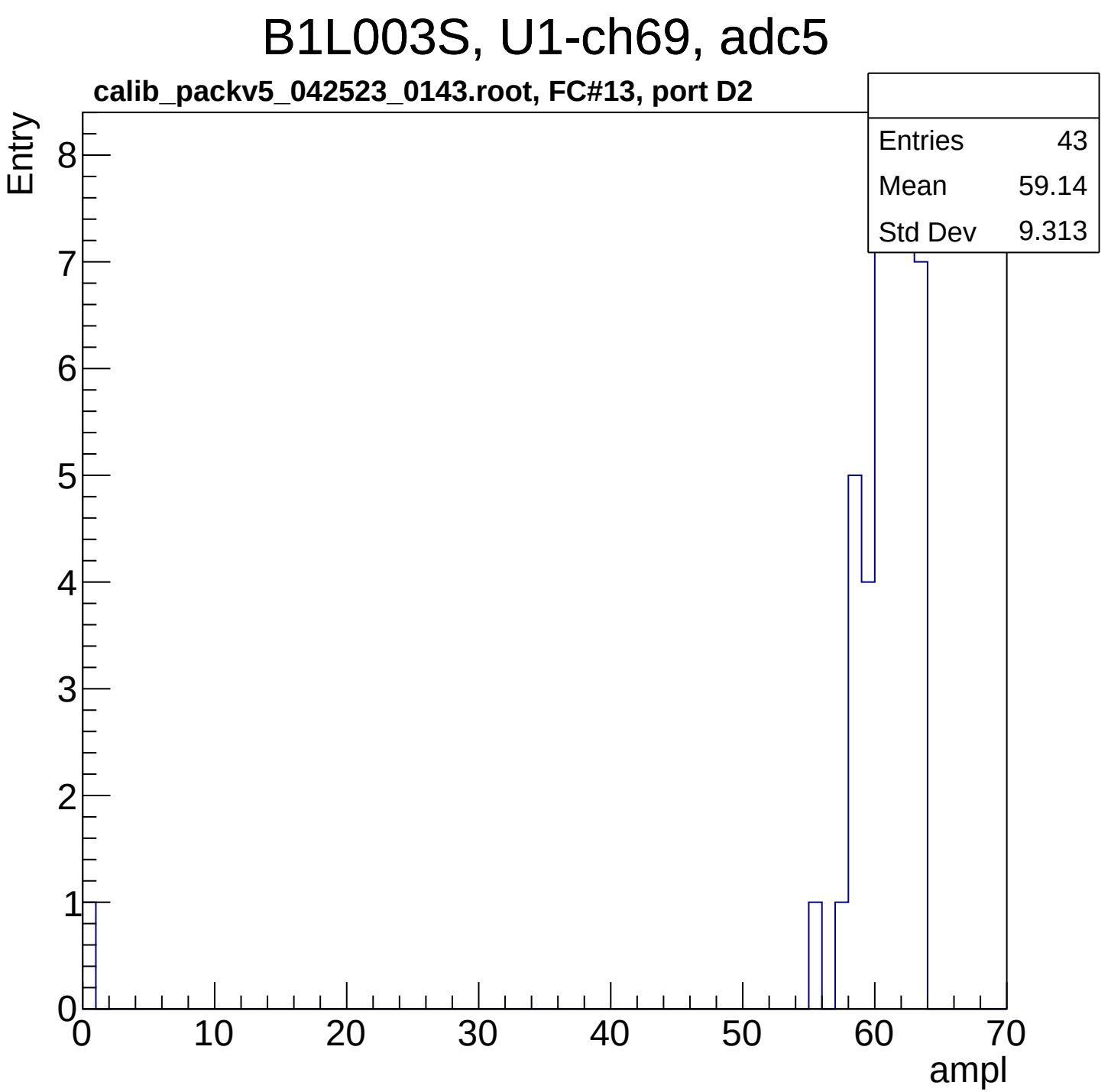
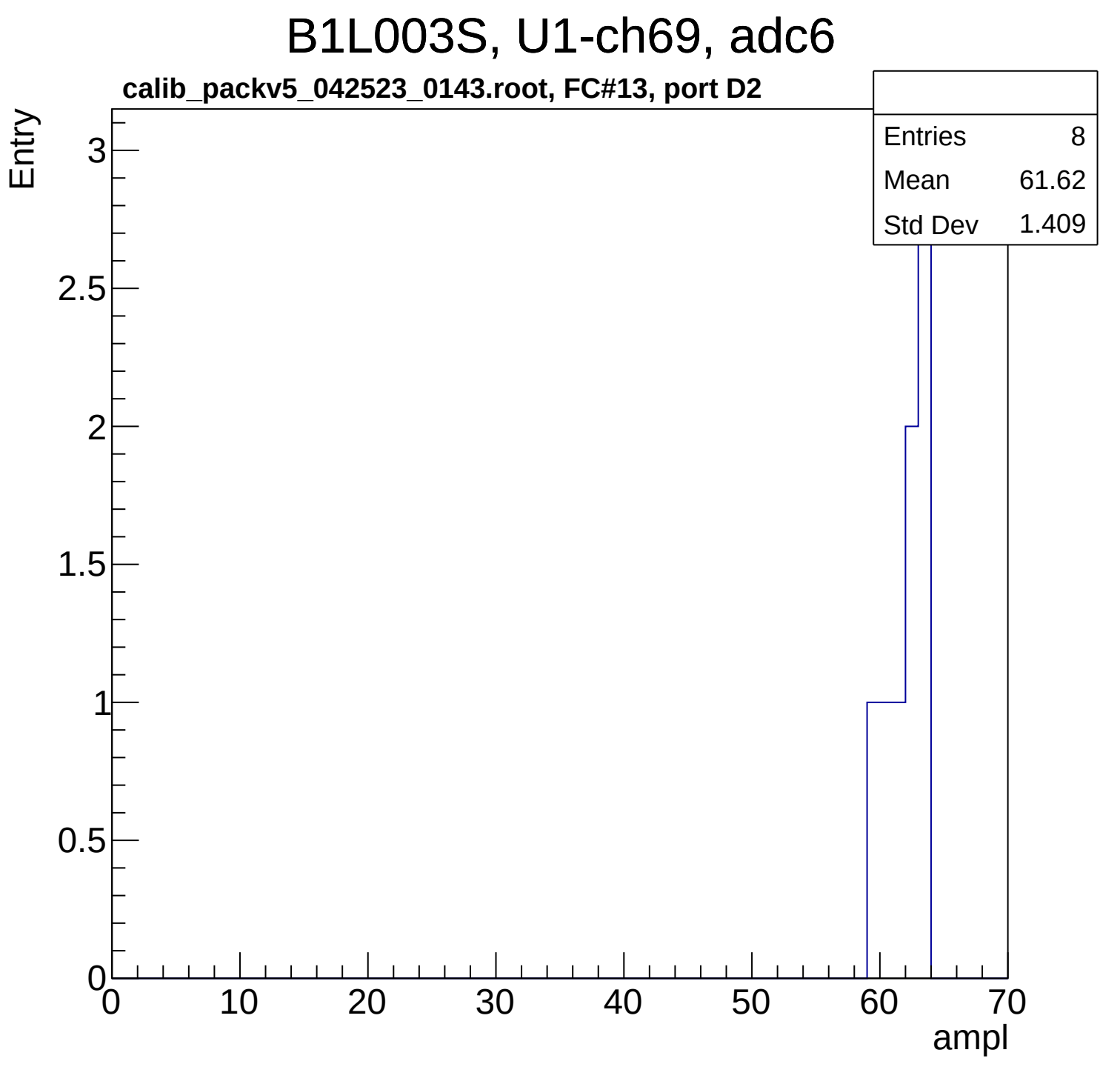
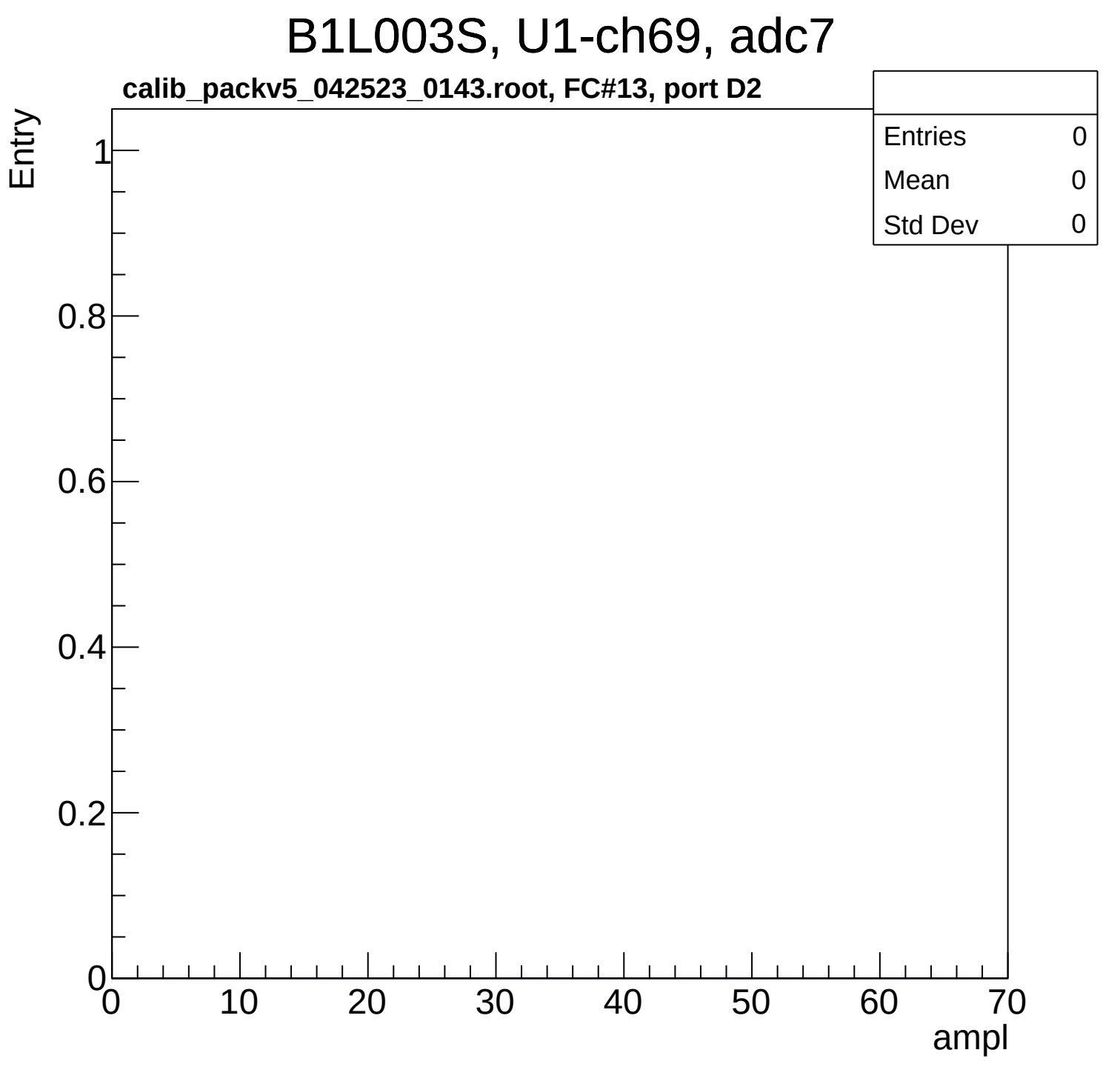

B1L003S, U1-ch69, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 42.46
Std Dev 3.557
Gaus mean : 43.2092
Gaus Width: 3.3126
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

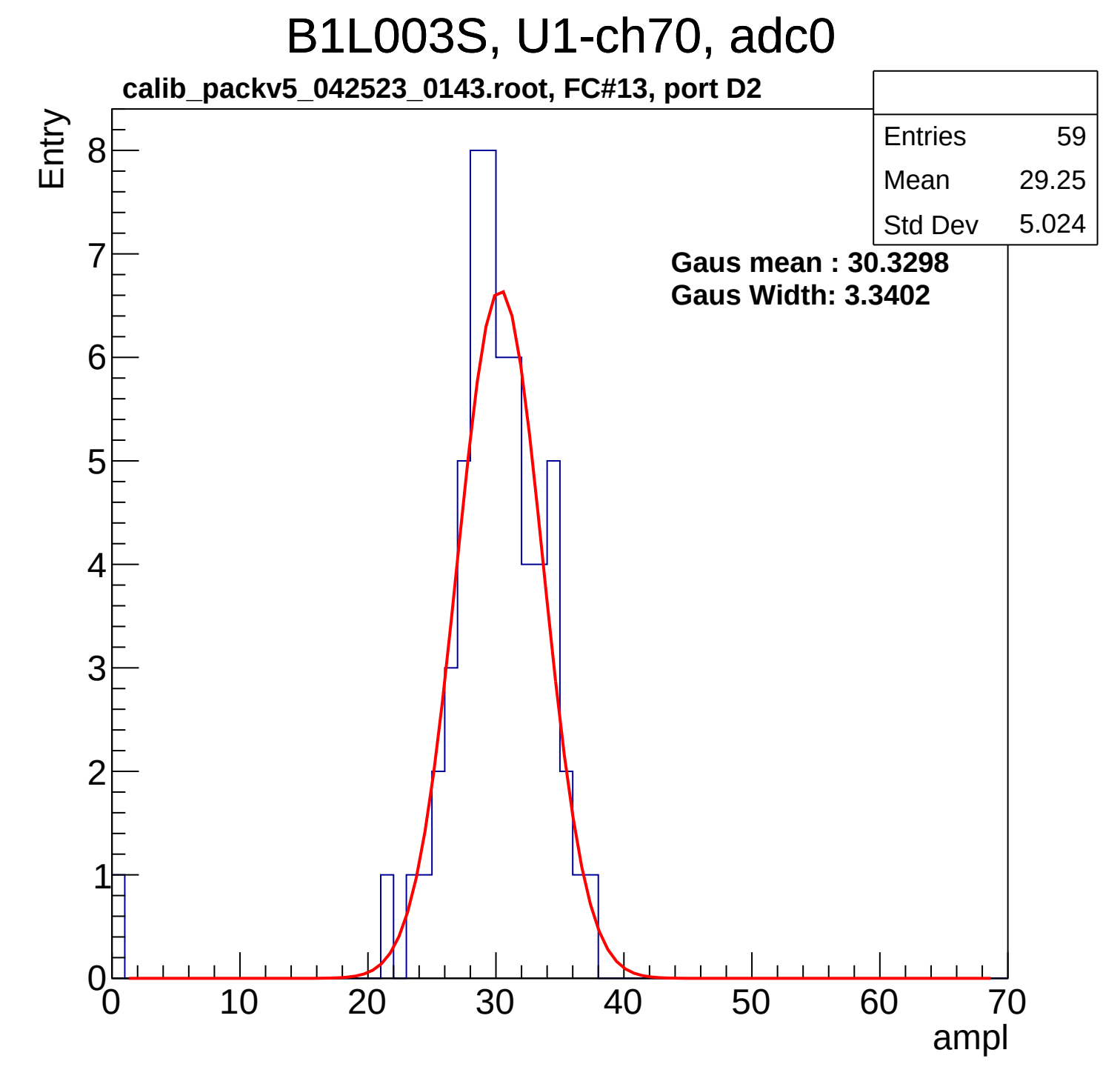

B1L003S, U1-ch69, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.79
Std Dev 3.628
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch69, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.84
Std Dev 3.204
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch69, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.14
Std Dev 9.313
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch69, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.62
Std Dev 1.409
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch69, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch70, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 29.25
Std Dev 5.024
Gaus mean : 30.3298
Gaus Width: 3.3402
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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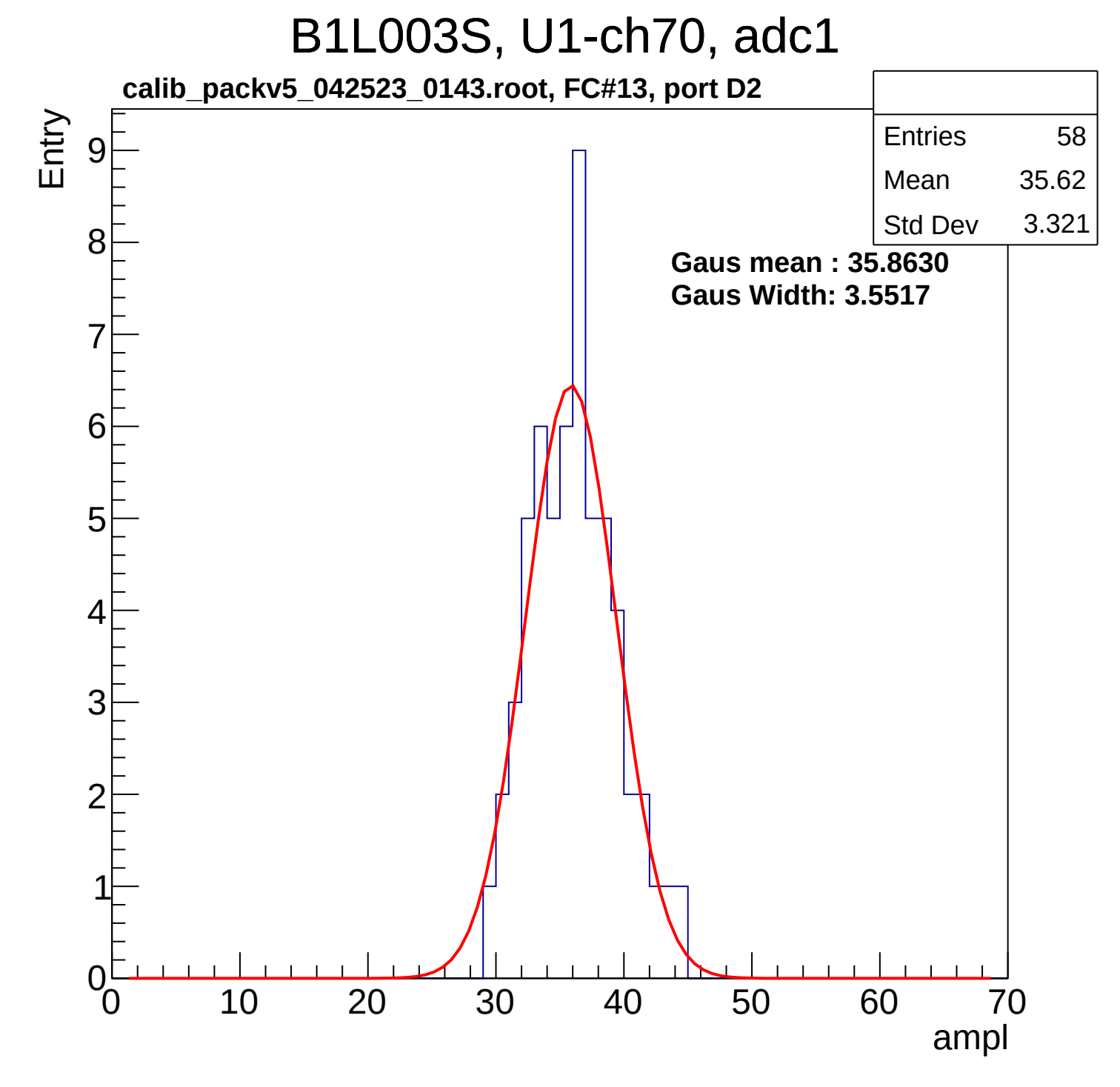
40
50
60
70


B1L003S, U1-ch70, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 35.62
Std Dev 3.321
Gaus mean : 35.8630
Gaus Width: 3.5517
Entry
ampl
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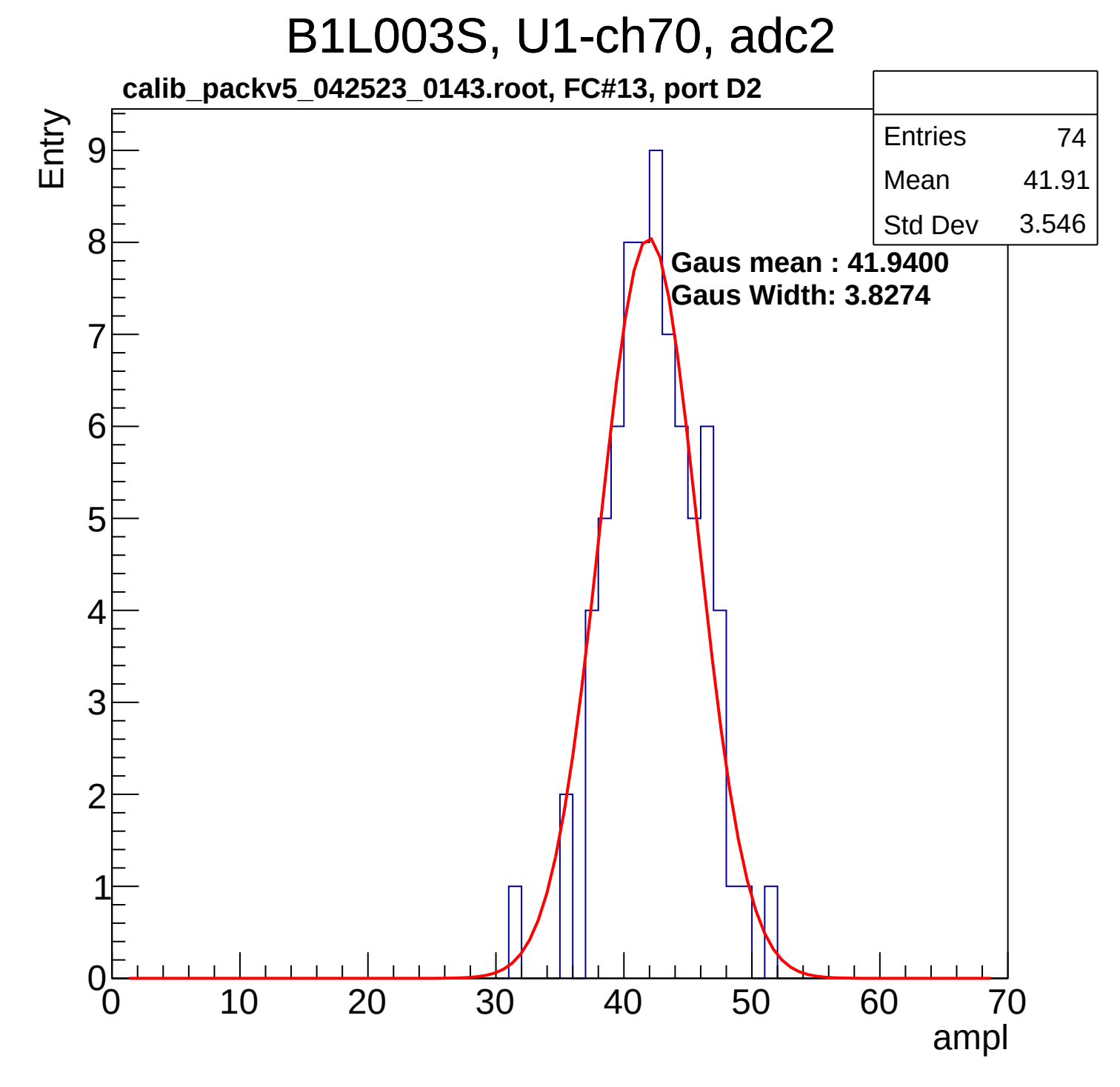
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch70, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 41.91
Std Dev 3.546
Gaus mean : 41.9400
Gaus Width: 3.8274
Entry
ampl
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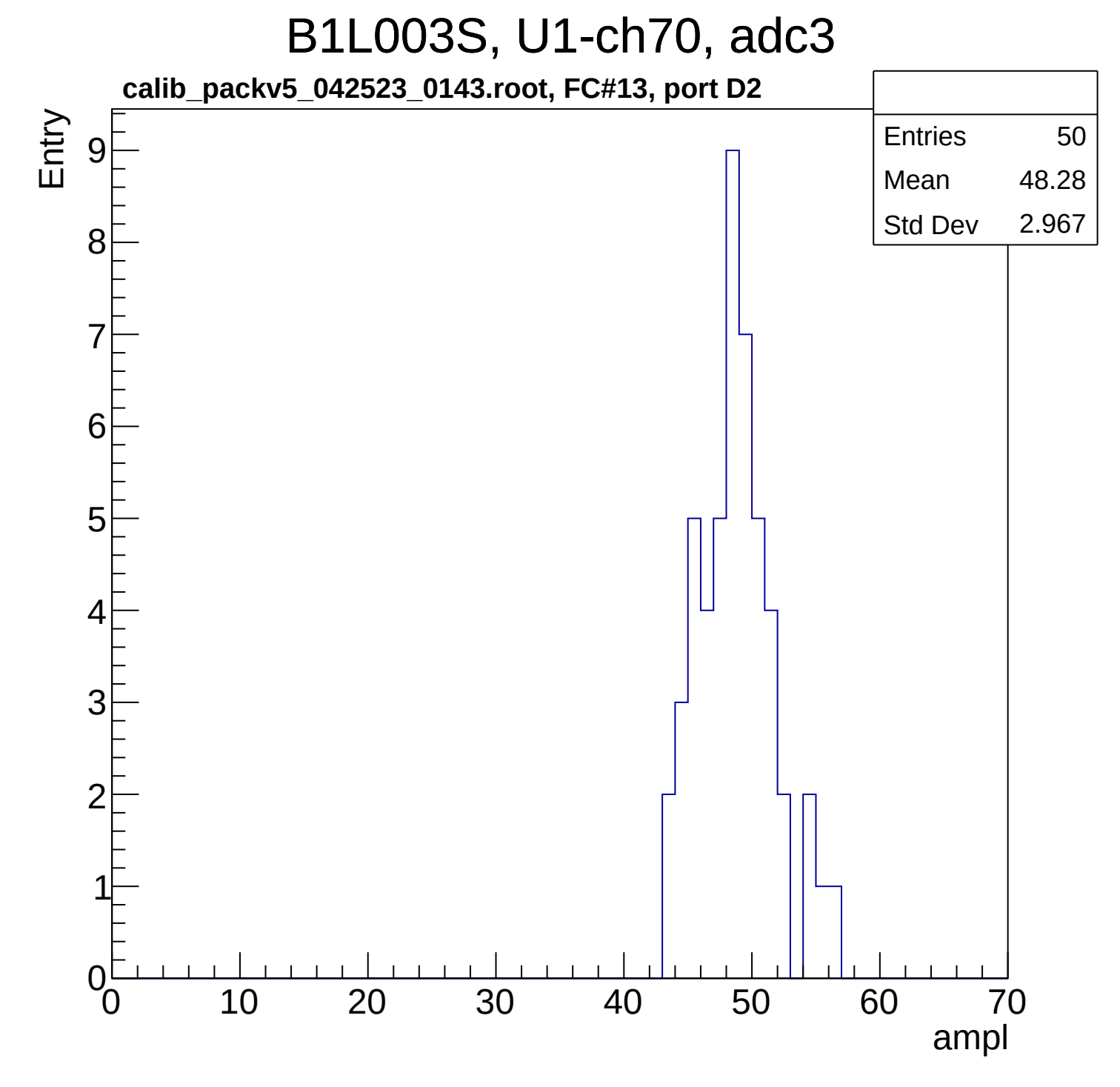
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch70, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 48.28
Std Dev 2.967
Entry
ampl
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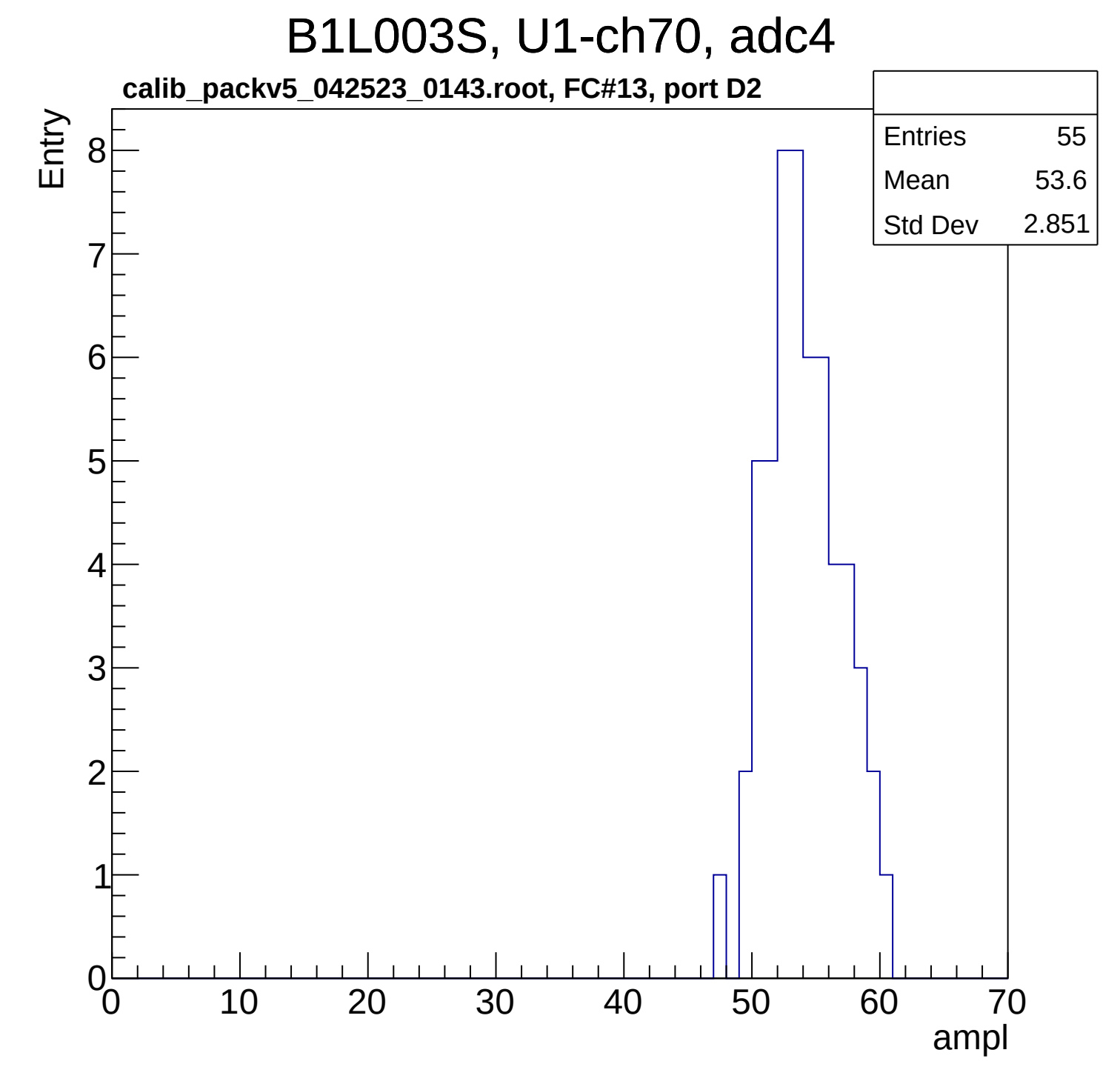
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch70, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.6
Std Dev 2.851
Entry
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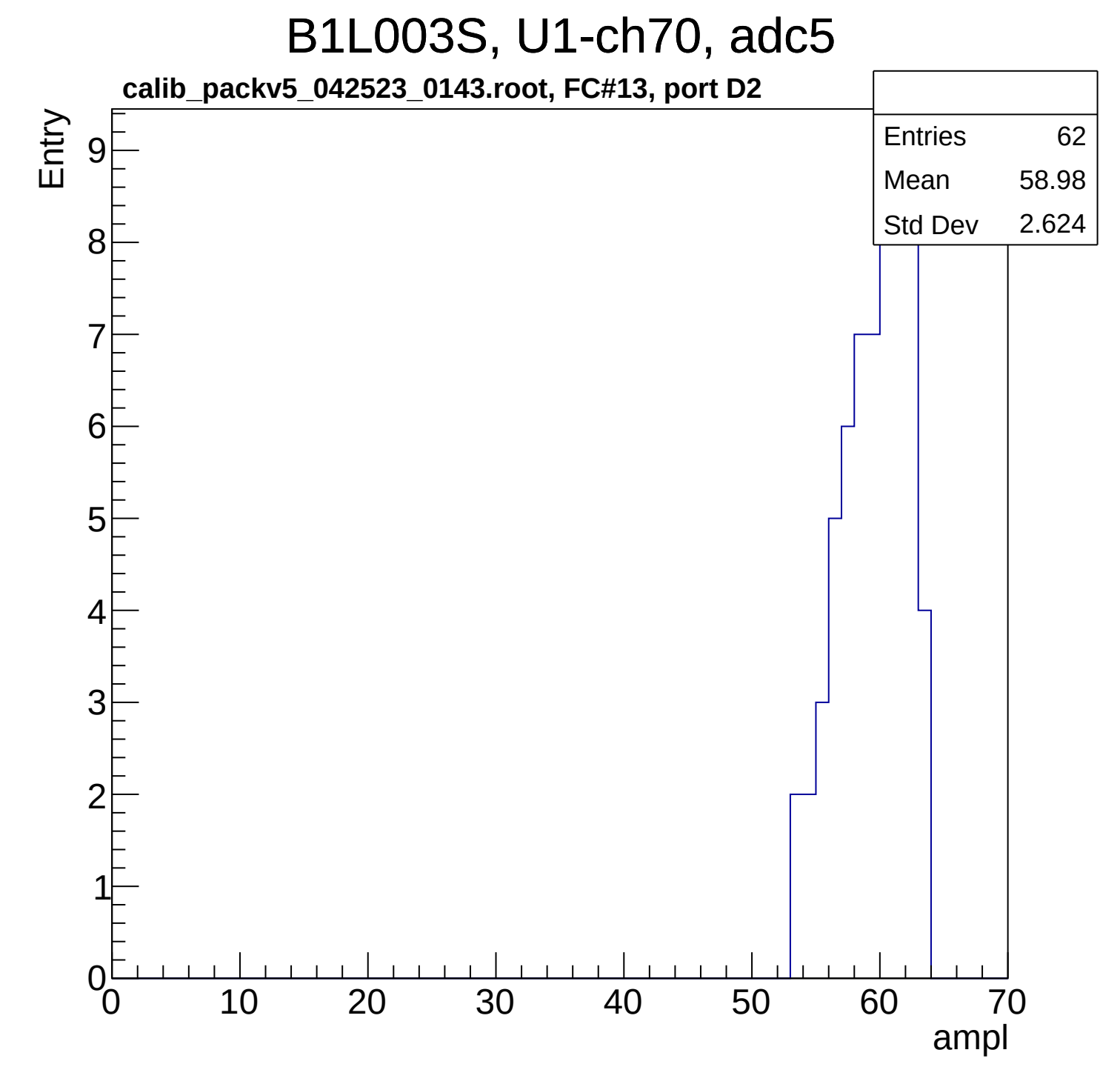
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch70, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.98
Std Dev 2.624
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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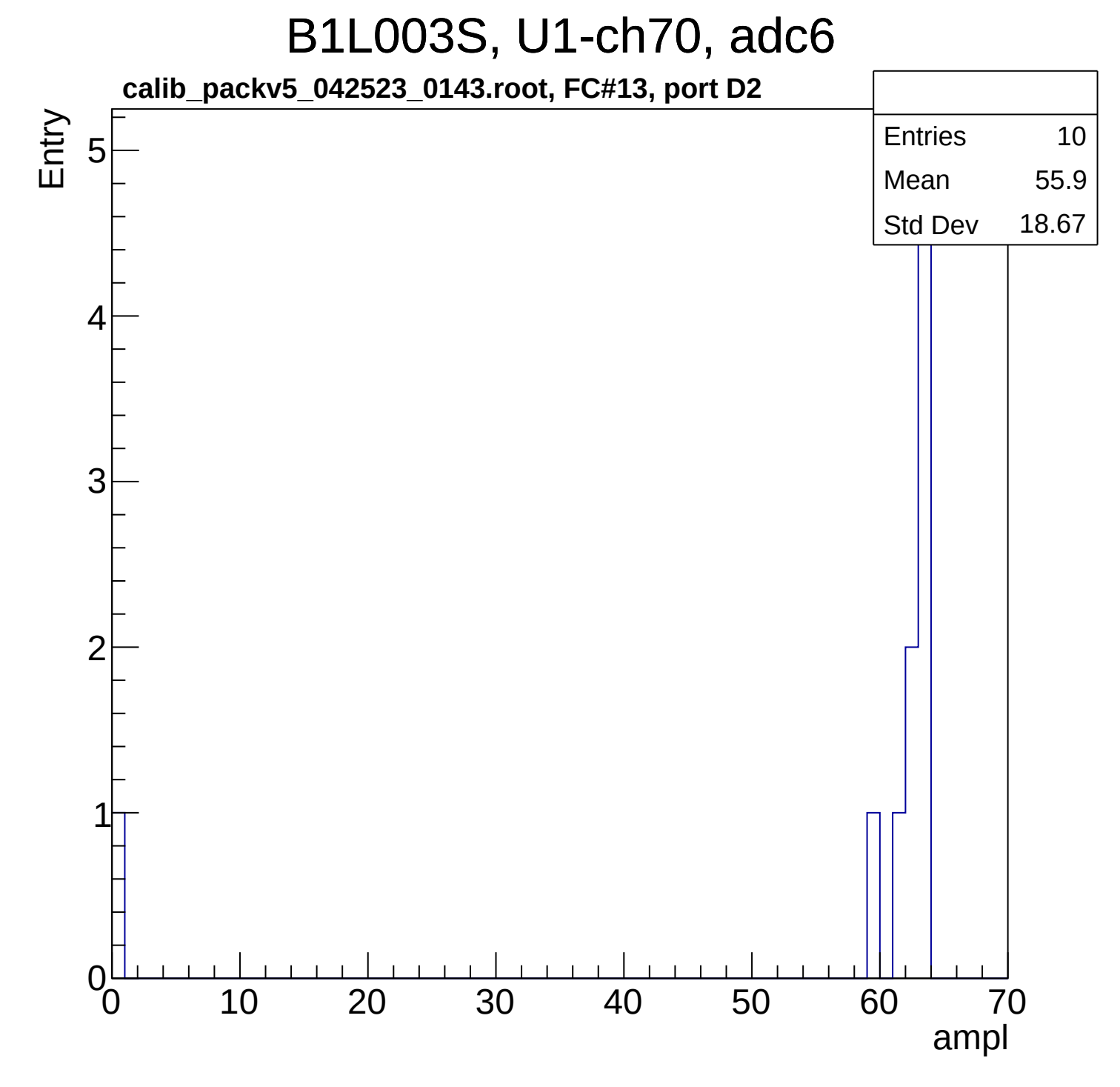
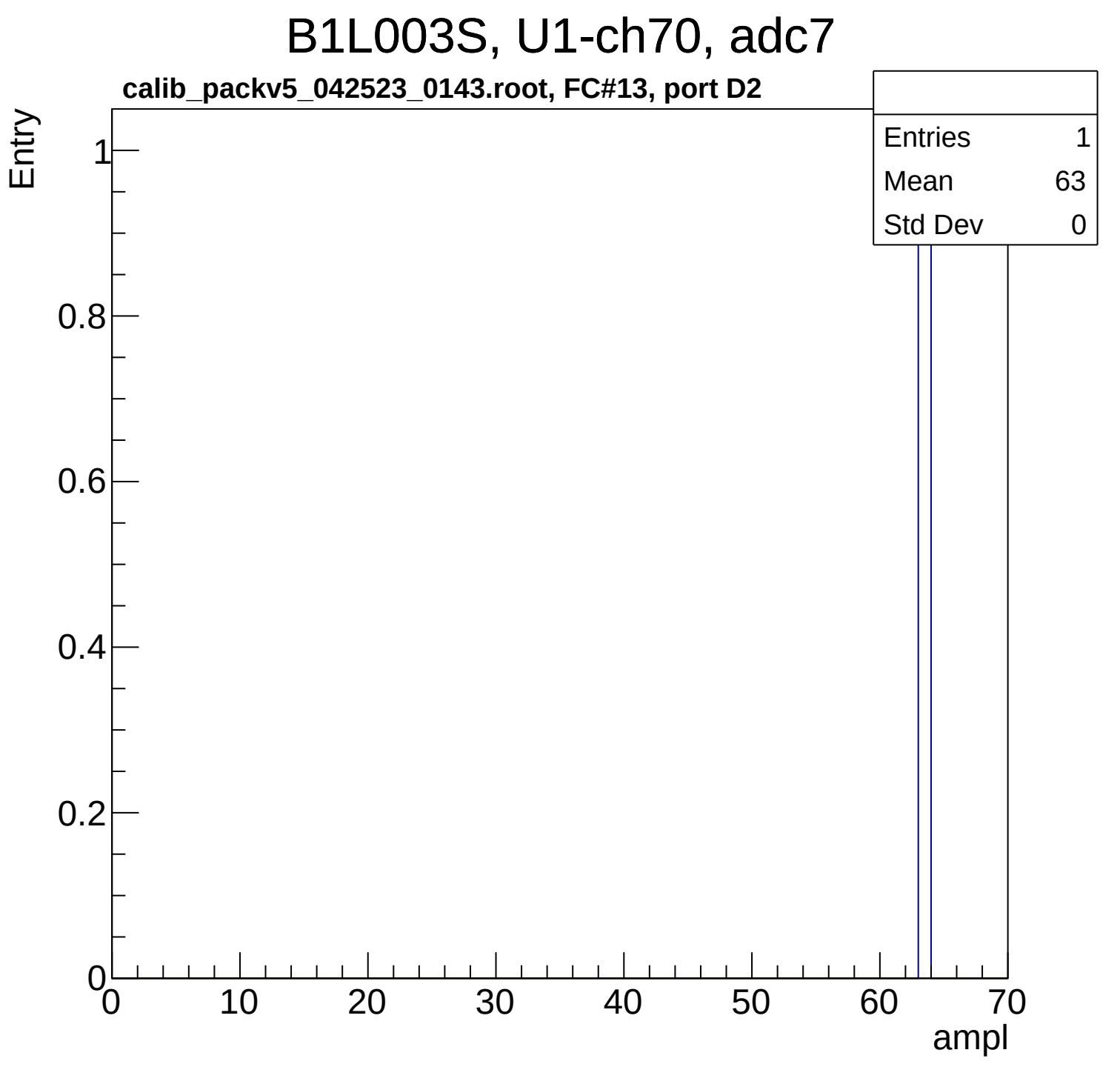

B1L003S, U1-ch70, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.9
Std Dev 18.67
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch70, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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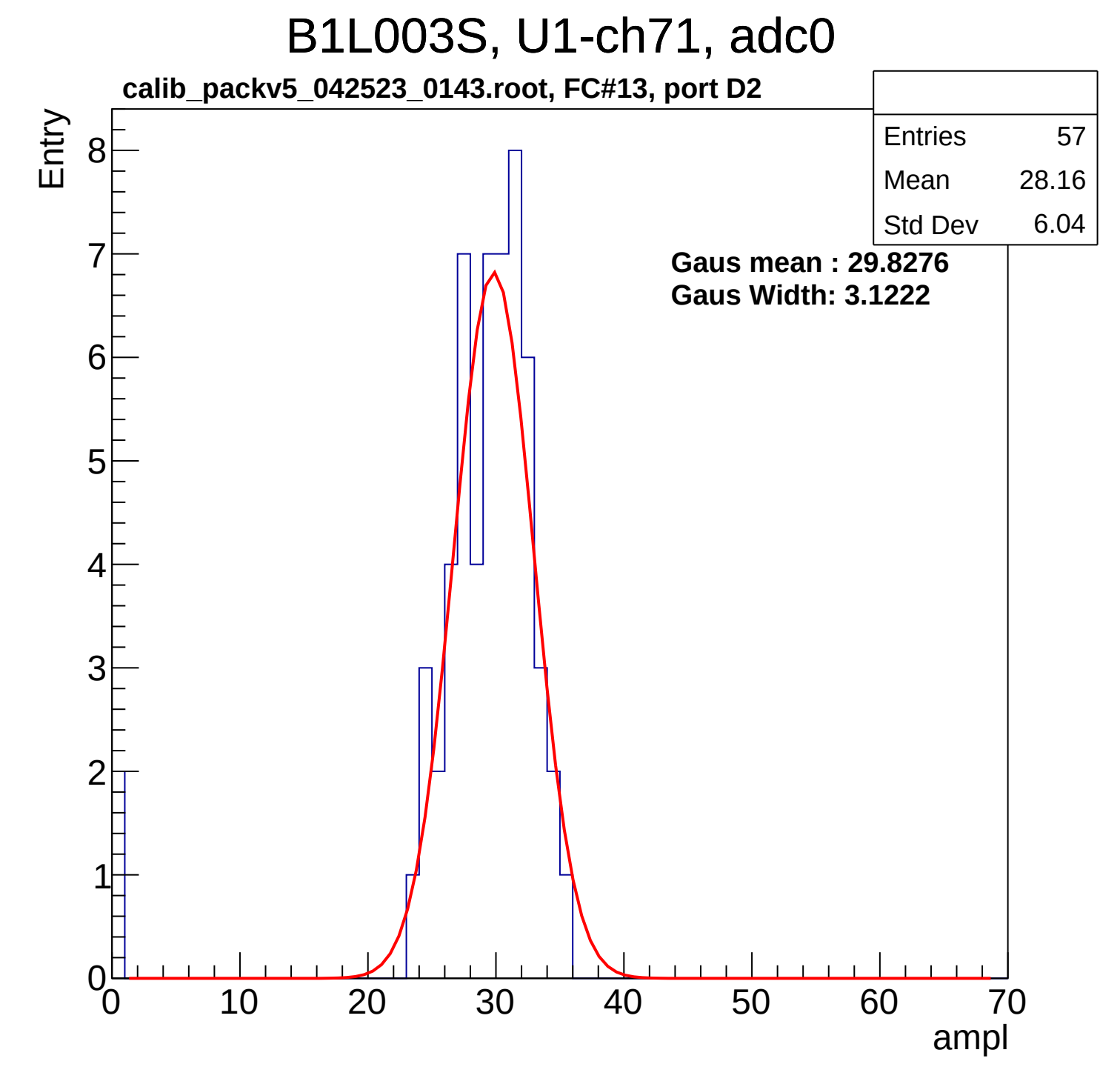
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch71, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.16
Std Dev 6.04
Gaus mean : 29.8276
Gaus Width: 3.1222
Entry
ampl
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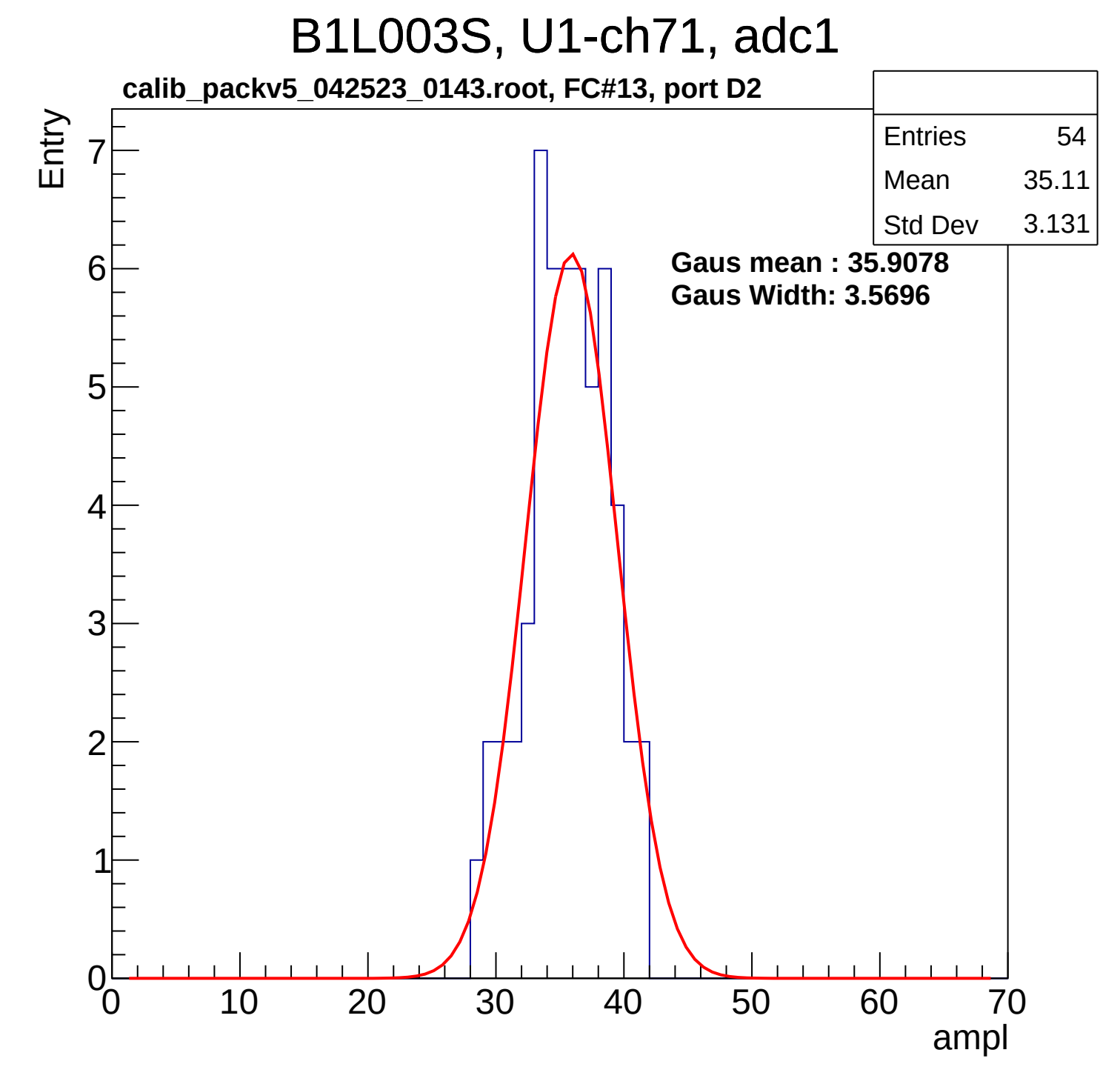
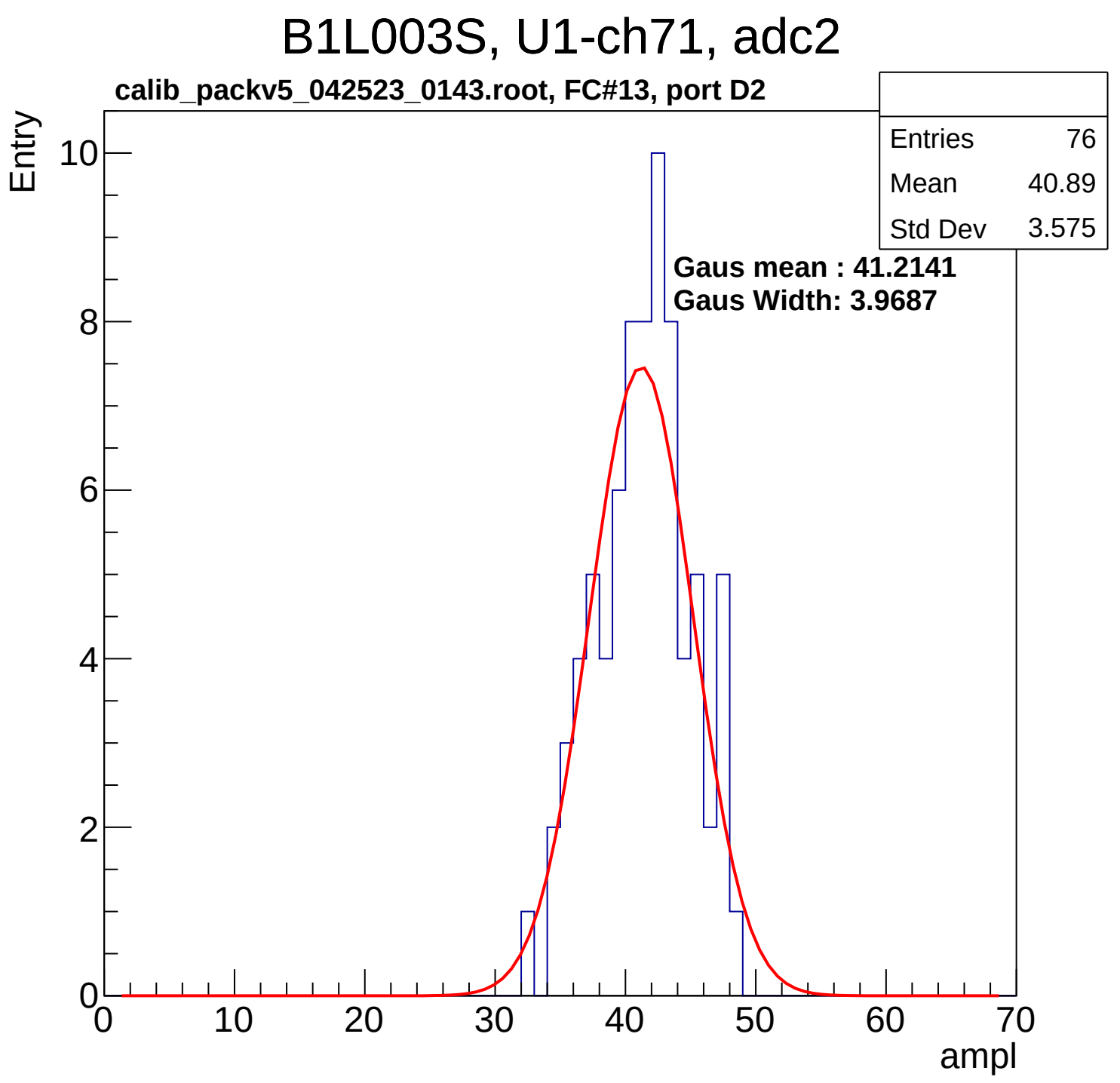
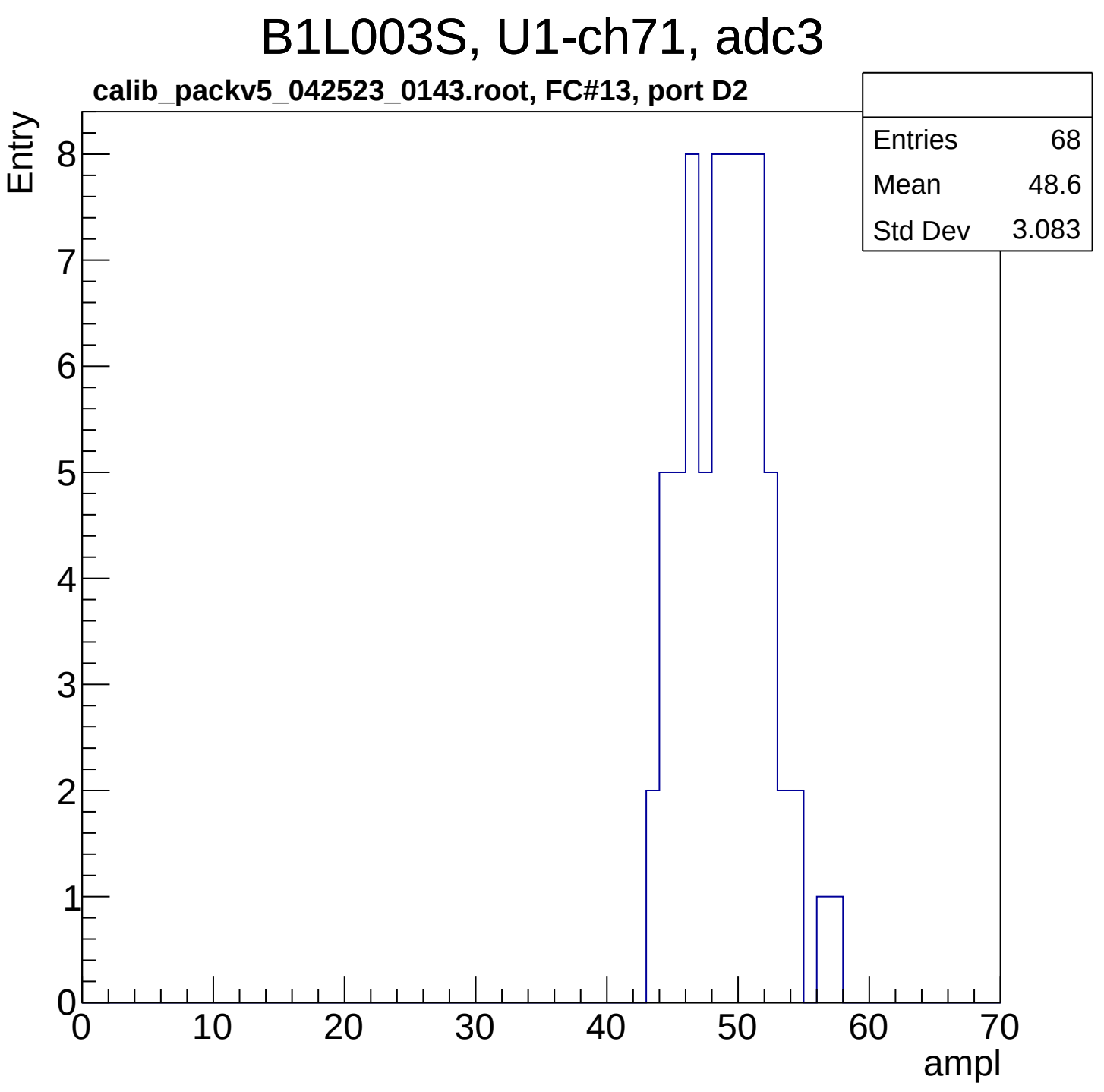
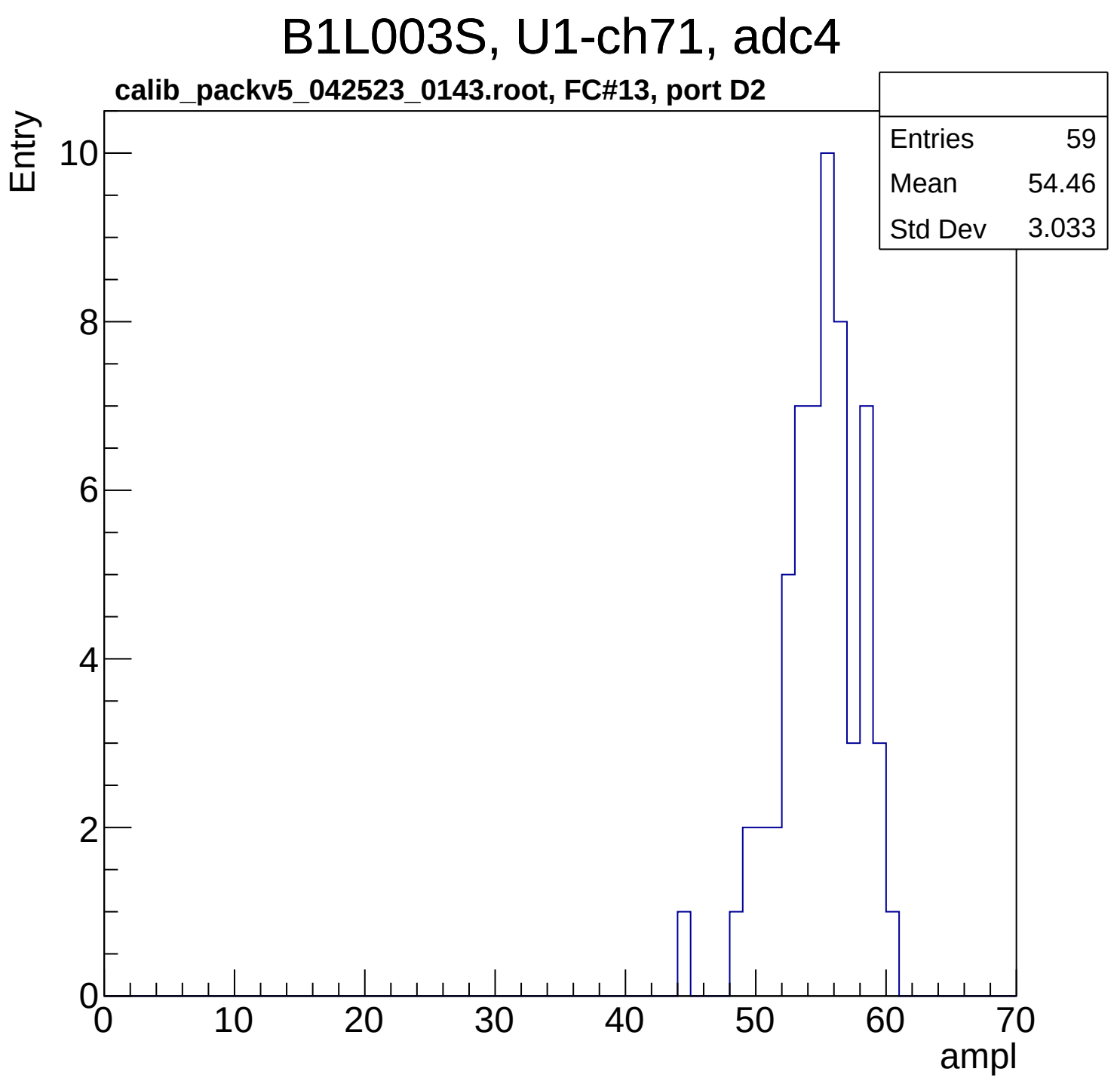
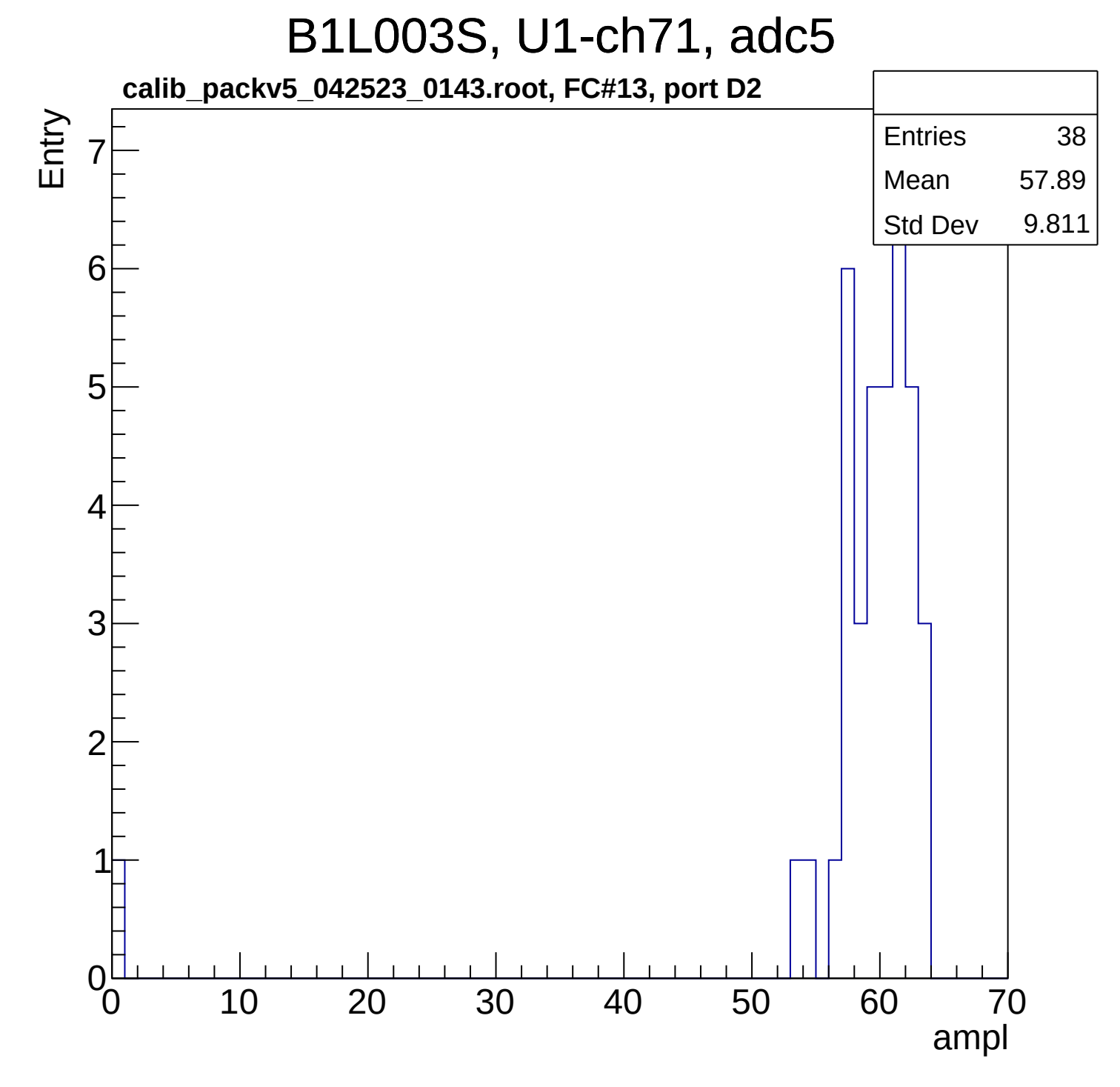
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch71, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 35.11
Std Dev 3.131
Gaus mean : 35.9078
Gaus Width: 3.5696
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch71, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 40.89
Std Dev 3.575
Gaus mean : 41.2141
Gaus Width: 3.9687
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch71, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 48.6
Std Dev 3.083
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch71, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.46
Std Dev 3.033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch71, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 38
Mean 57.89
Std Dev 9.811
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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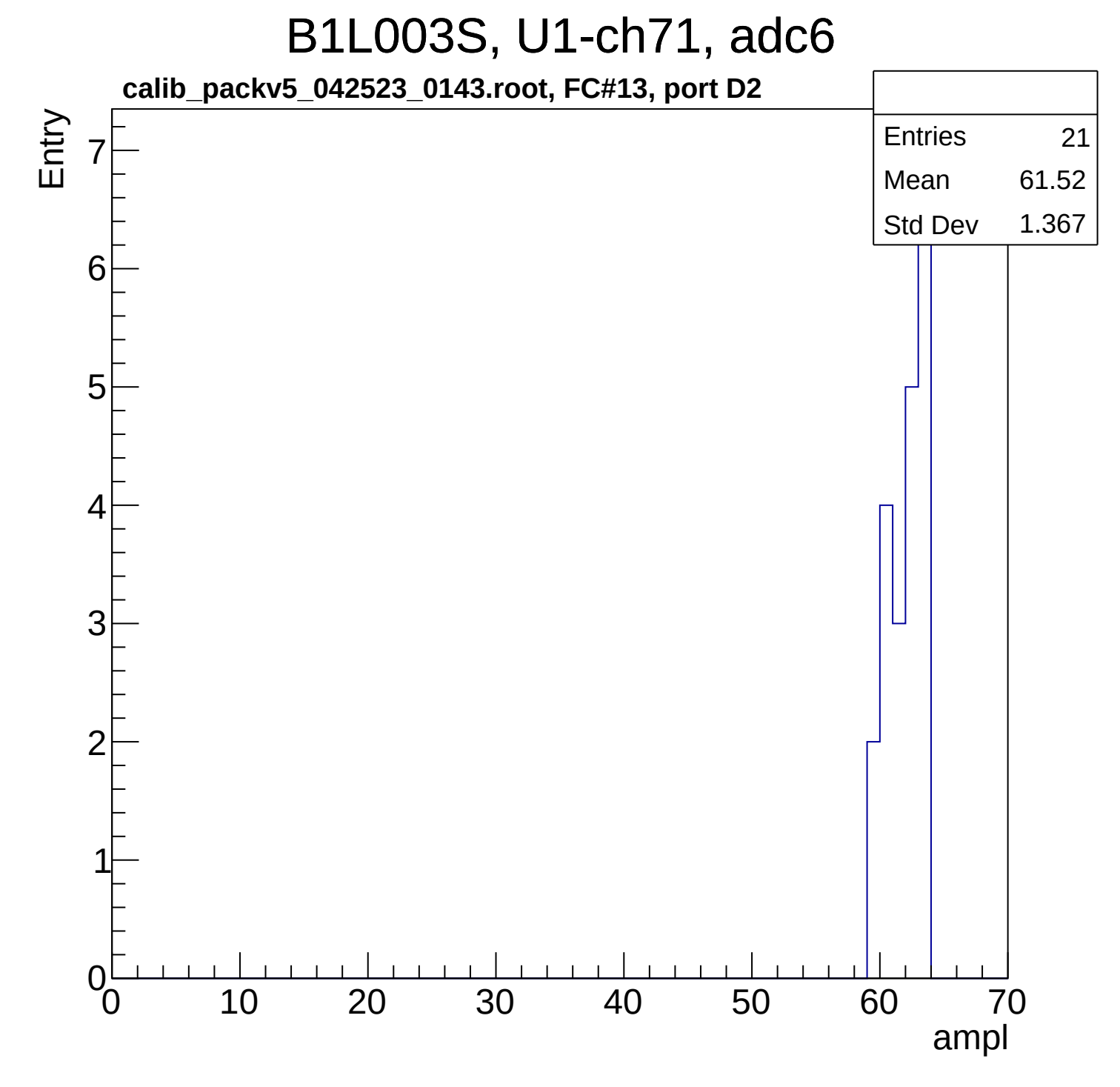
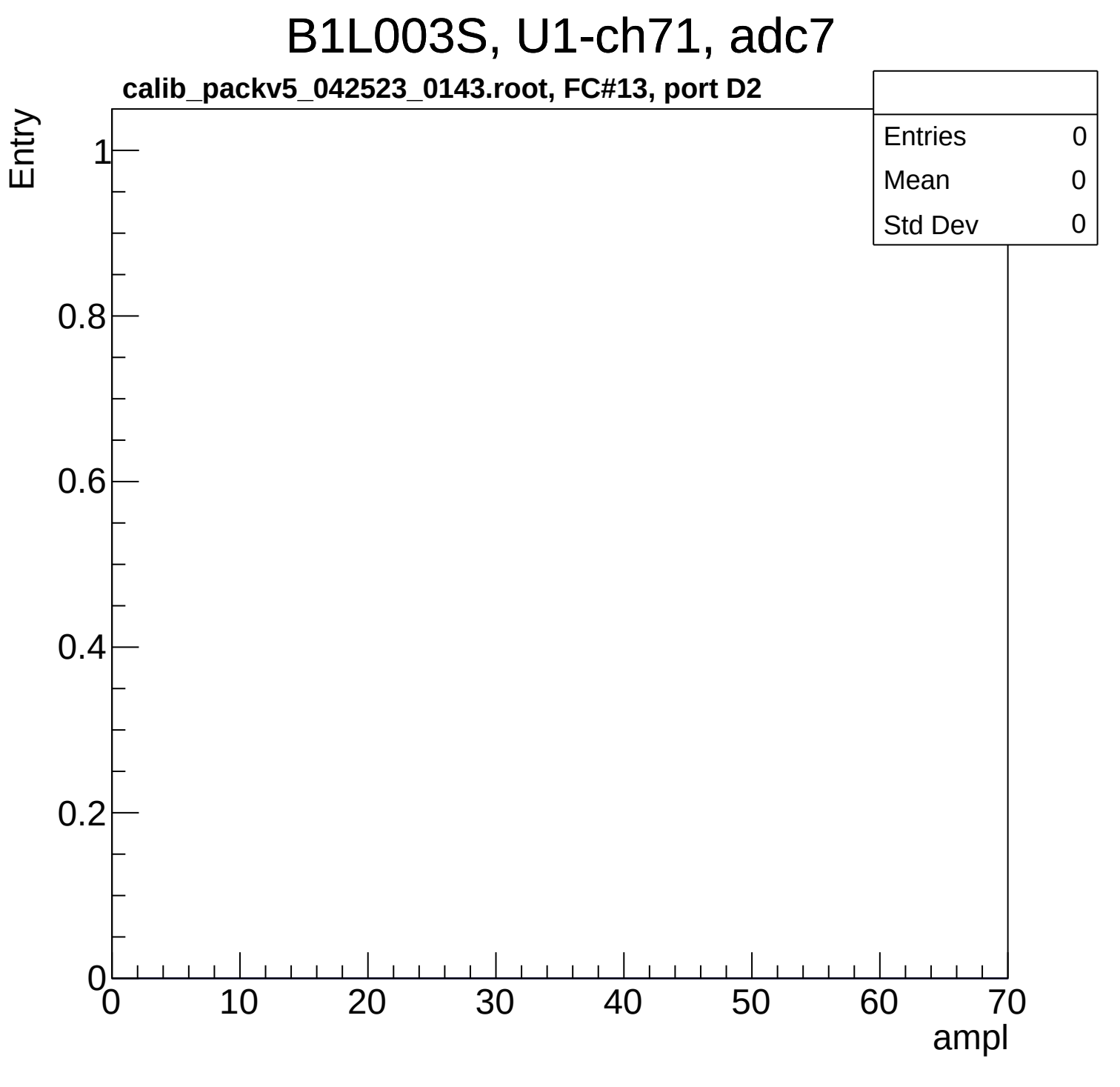
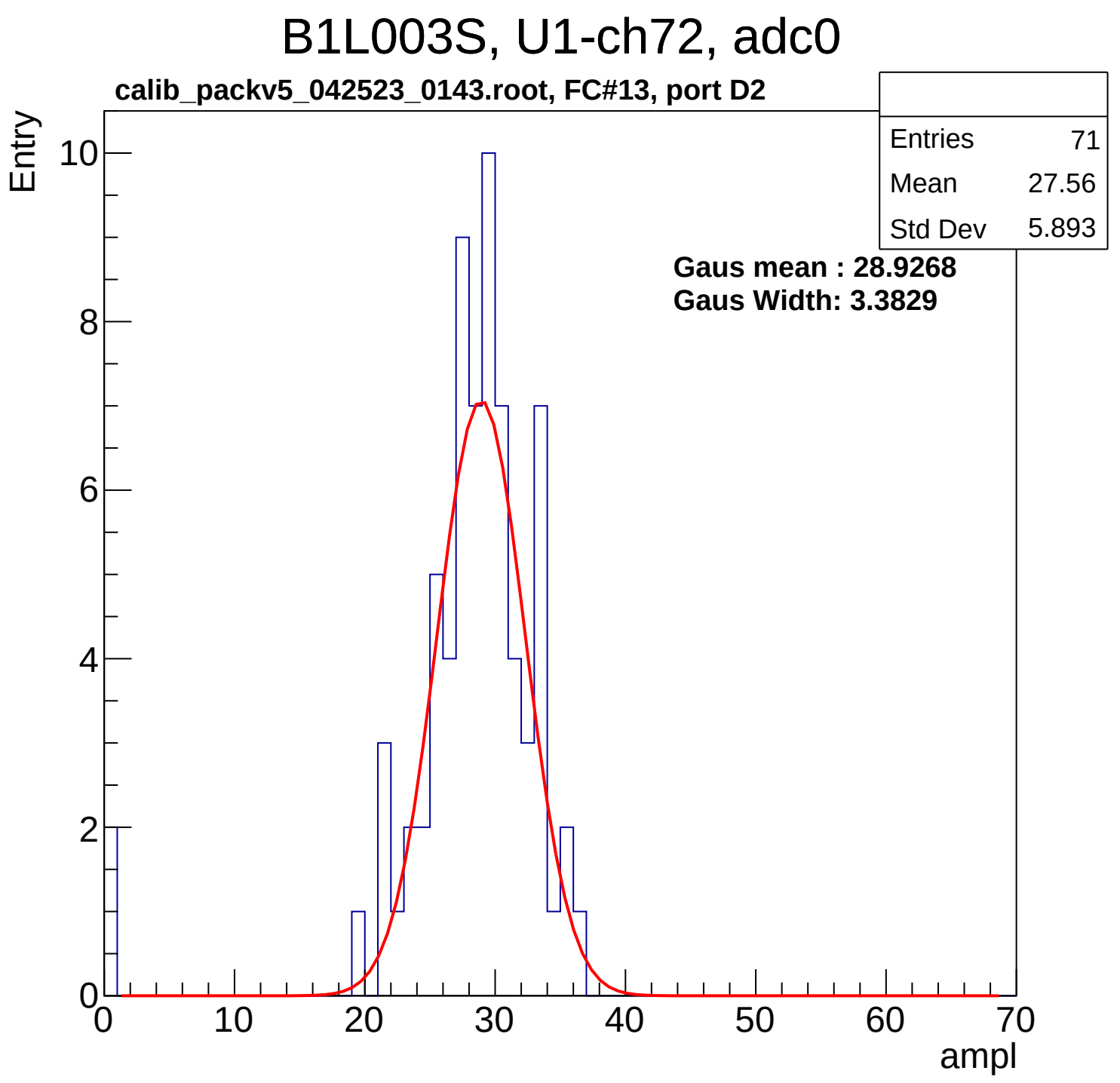
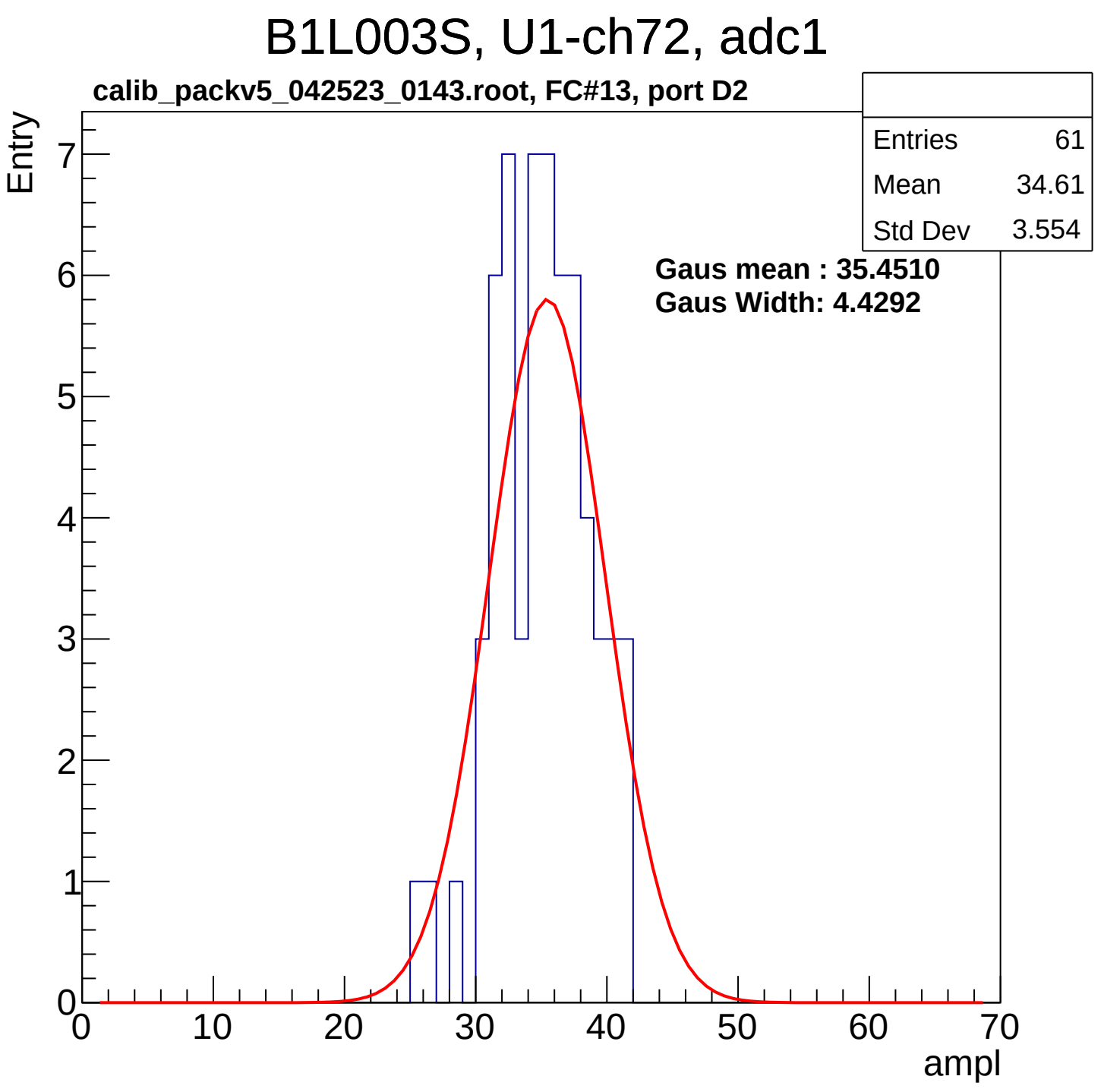
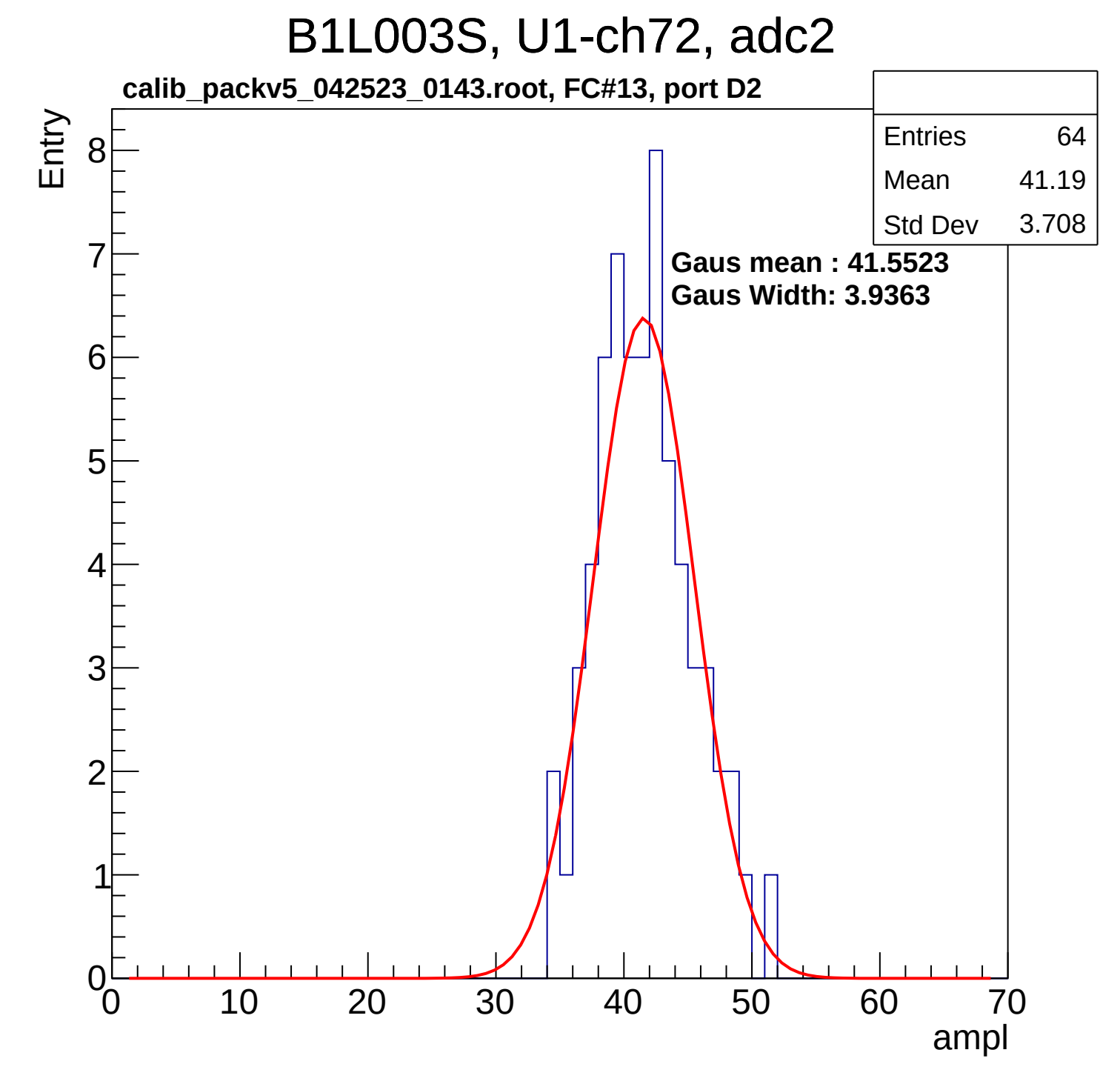
40
50
60
70

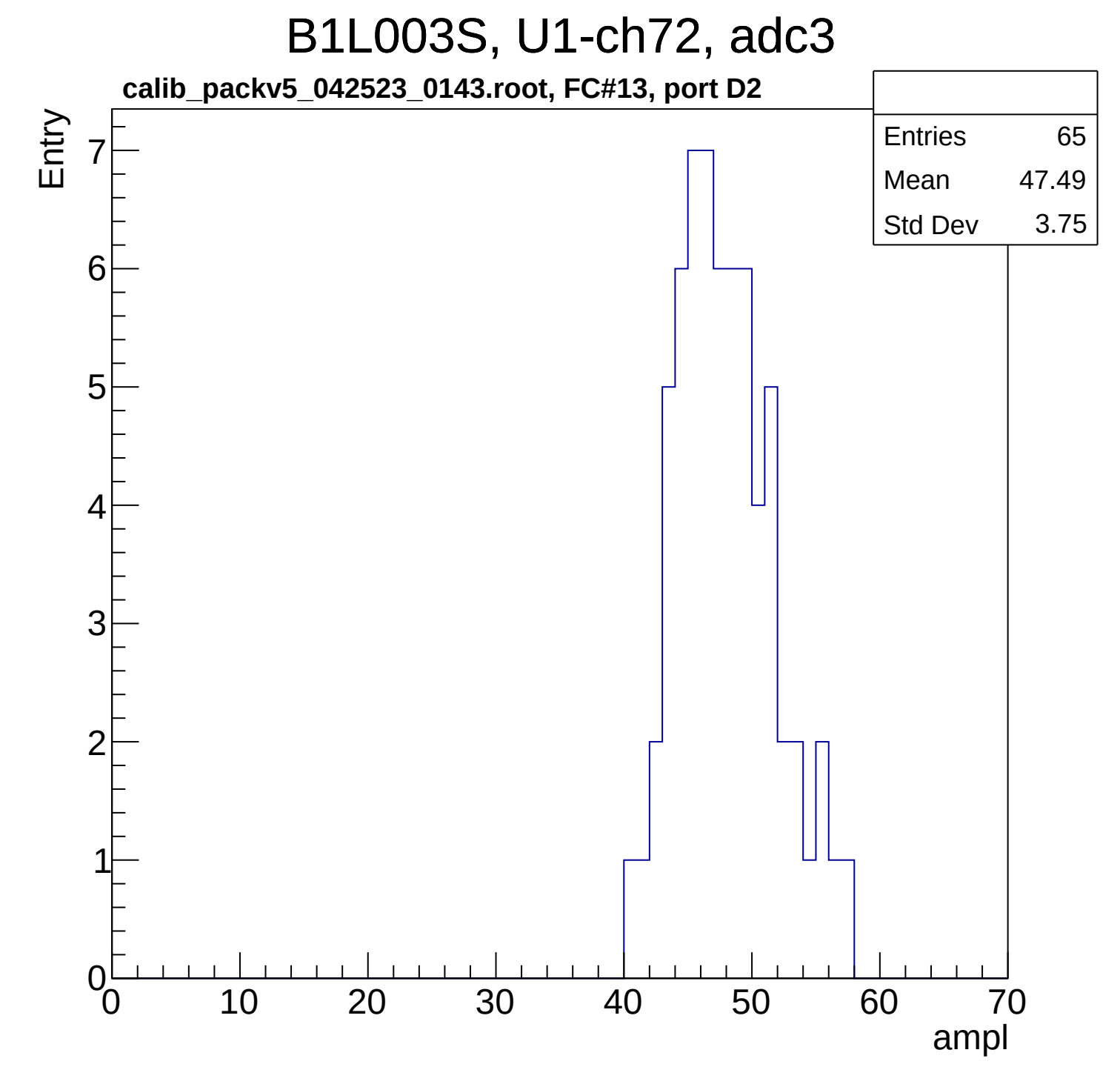

B1L003S, U1-ch71, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 21
Mean 61.52
Std Dev 1.367
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

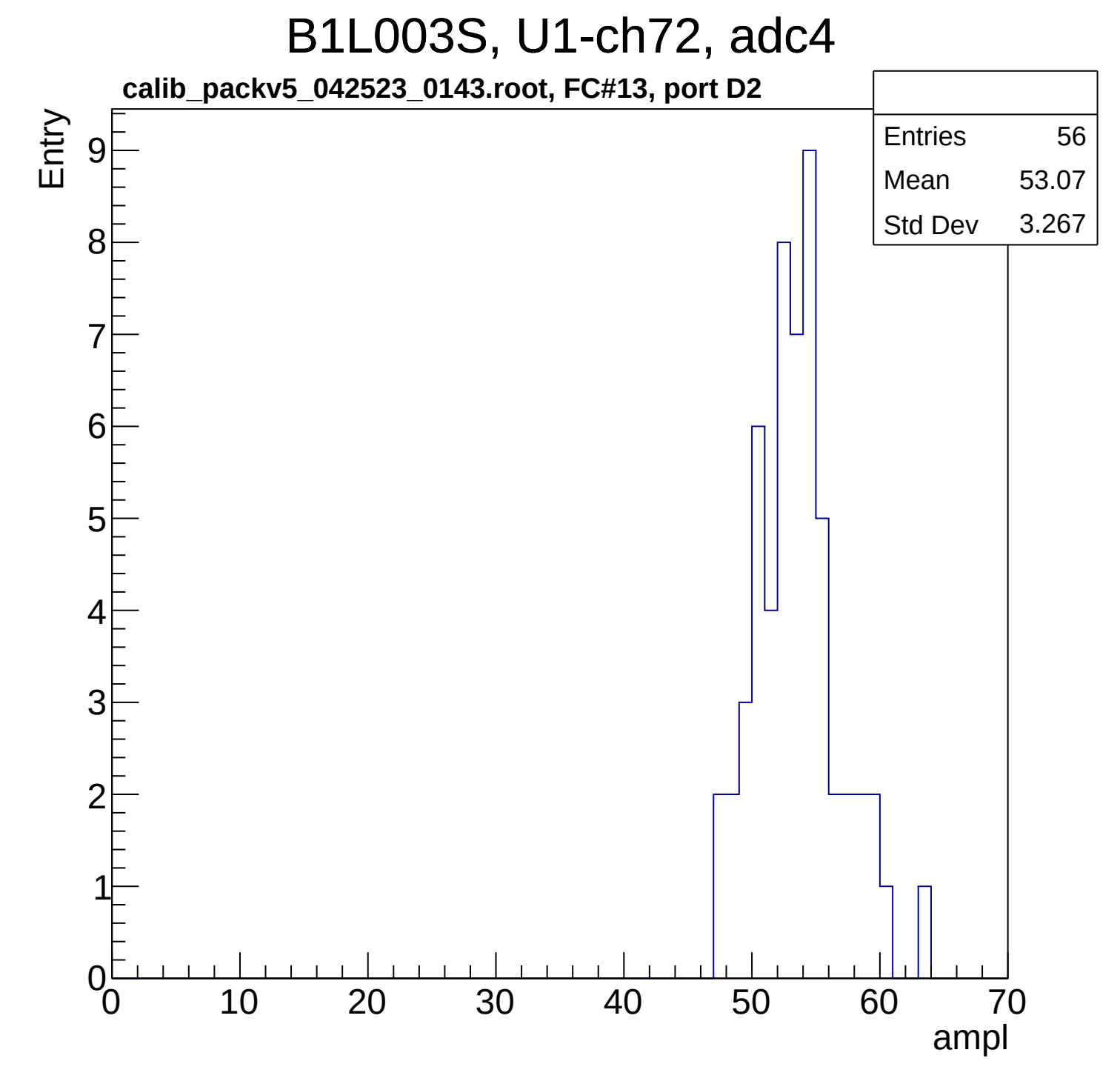

B1L003S, U1-ch71, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

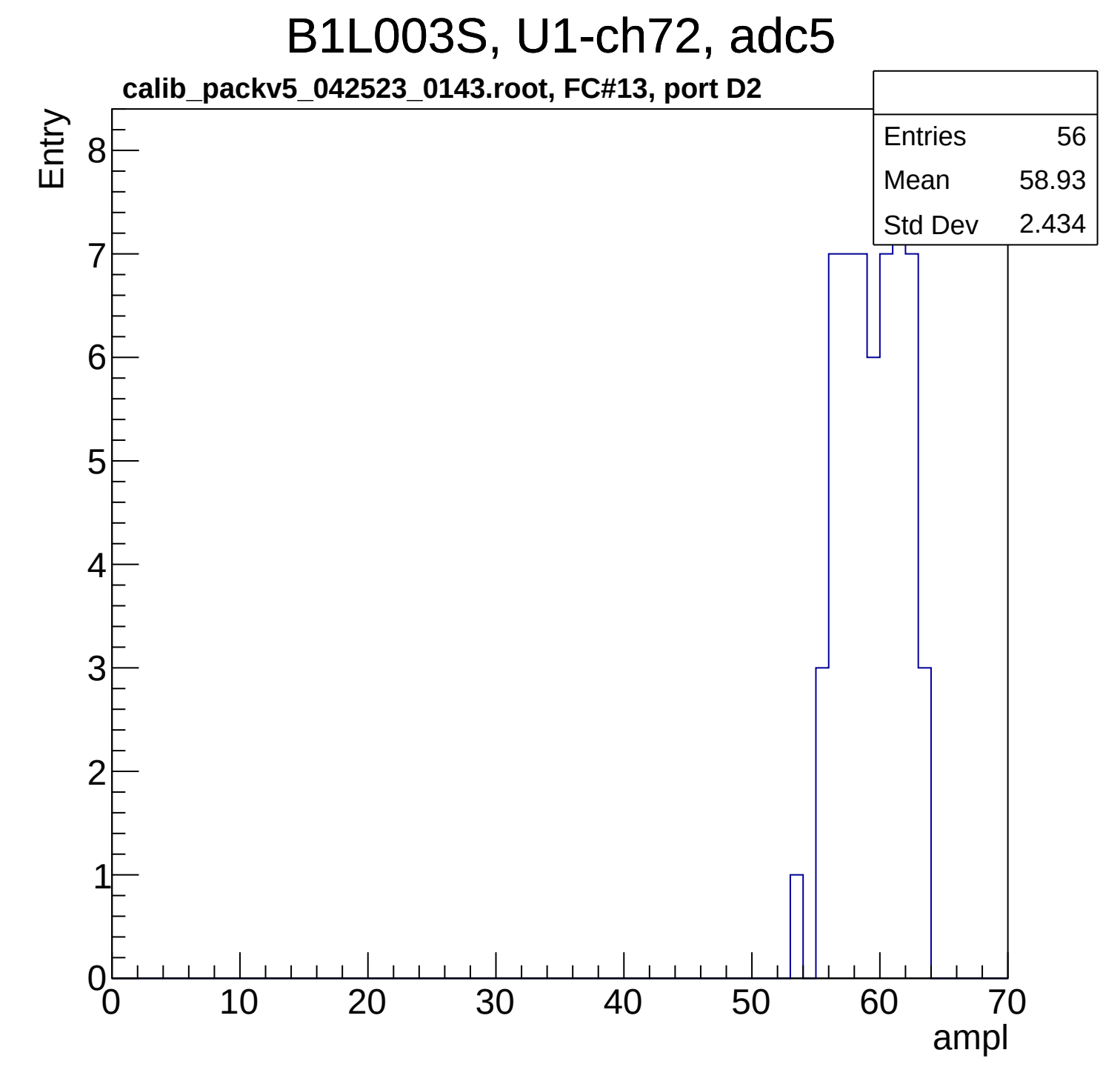

B1L003S, U1-ch72, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 27.56
Std Dev 5.893
Gaus mean : 28.9268
Gaus Width: 3.3829
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

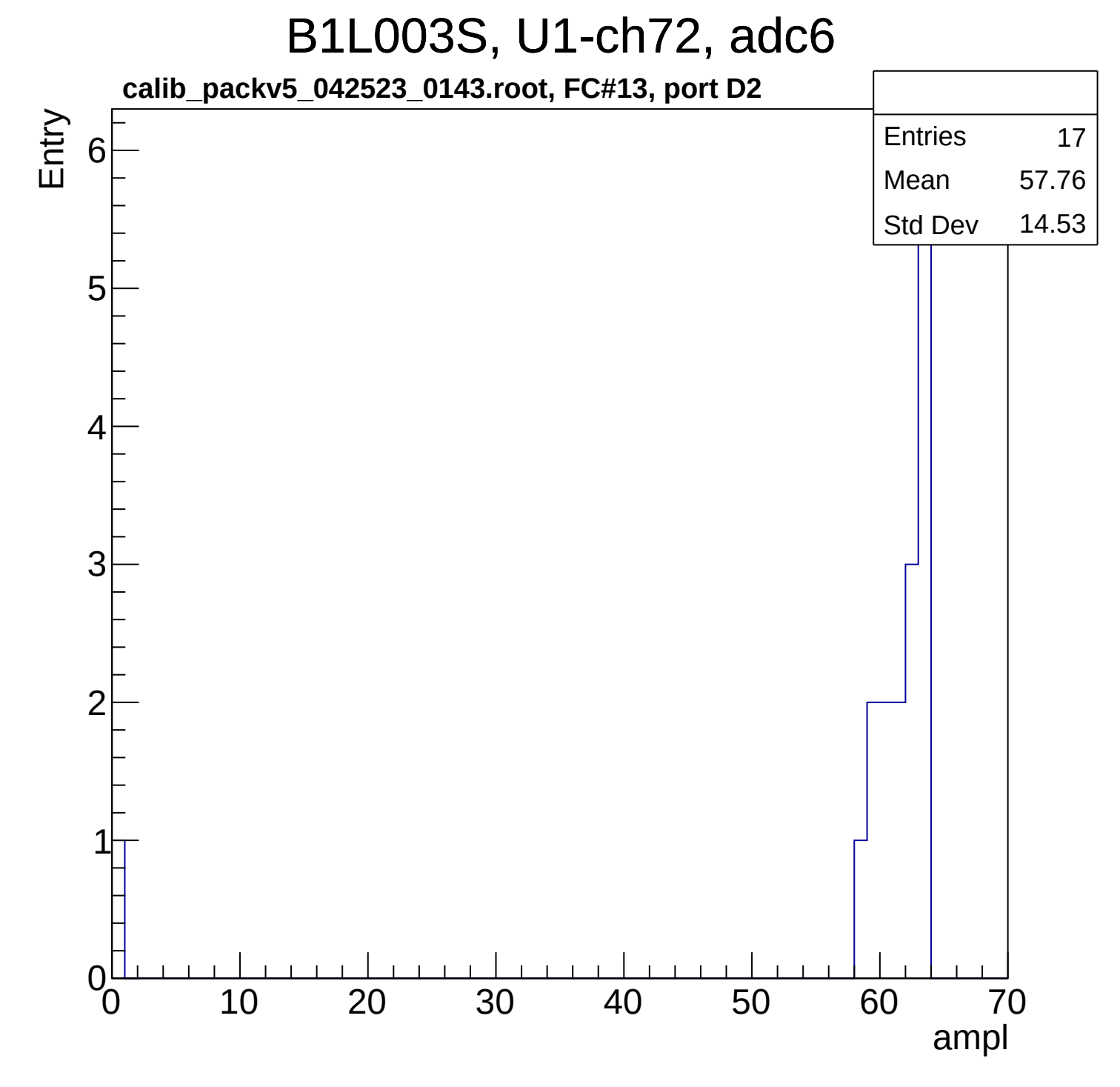

B1L003S, U1-ch72, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 34.61
Std Dev 3.554
Gaus mean : 35.4510
Gaus Width: 4.4292
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

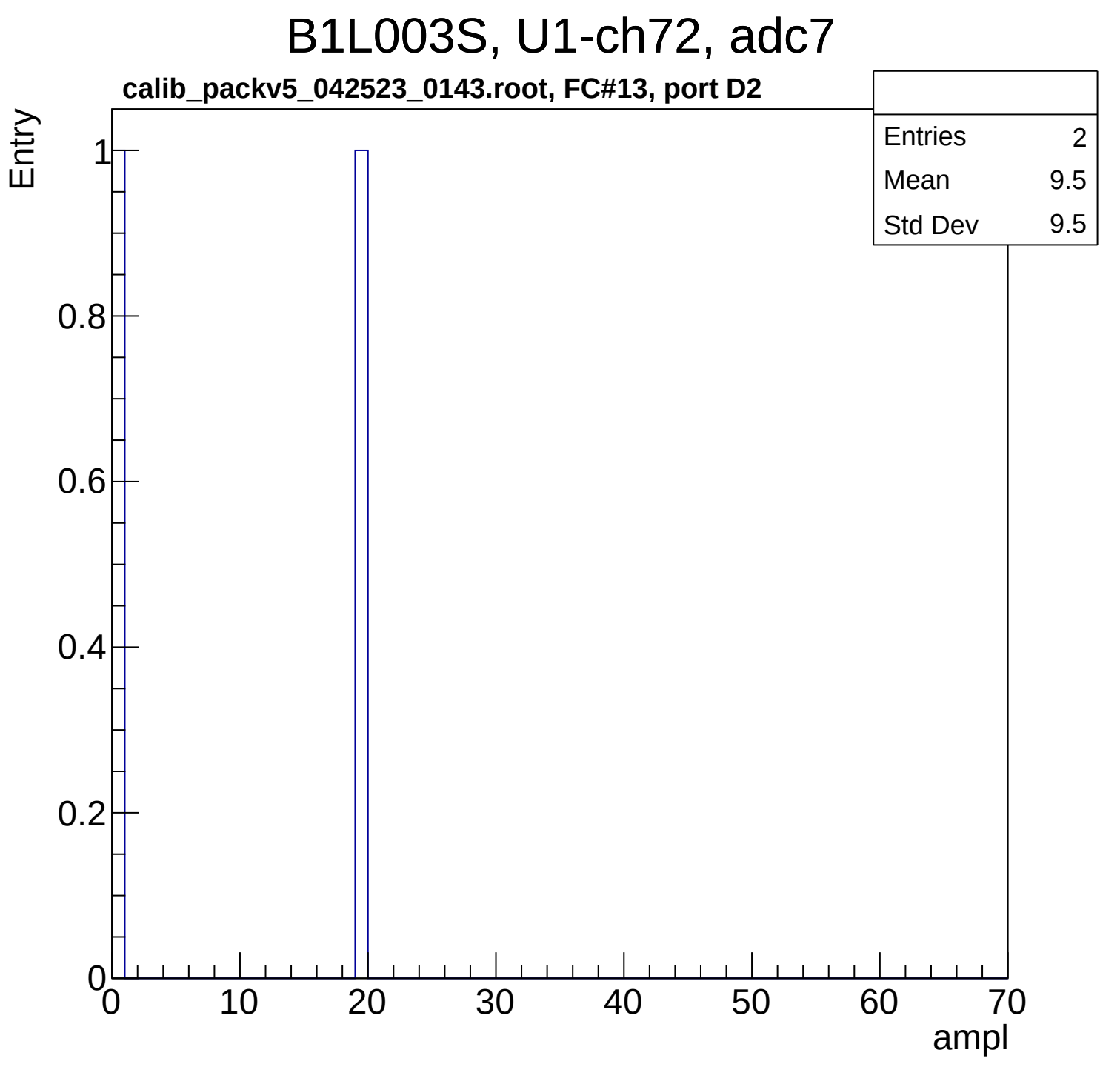

B1L003S, U1-ch72, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.19
Std Dev 3.708
Gaus mean : 41.5523
Gaus Width: 3.9363
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

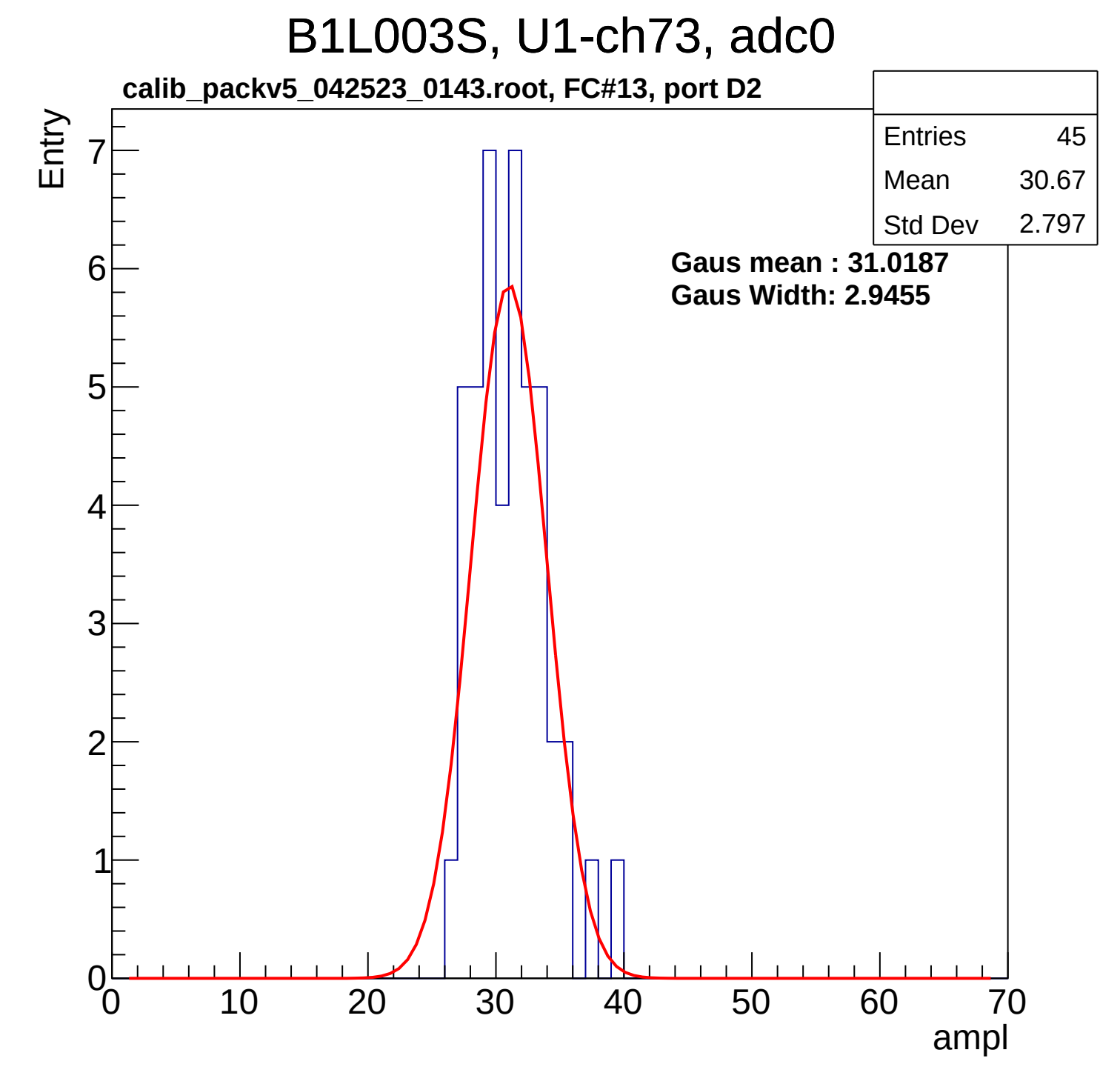

B1L003S, U1-ch72, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.49
Std Dev 3.75
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

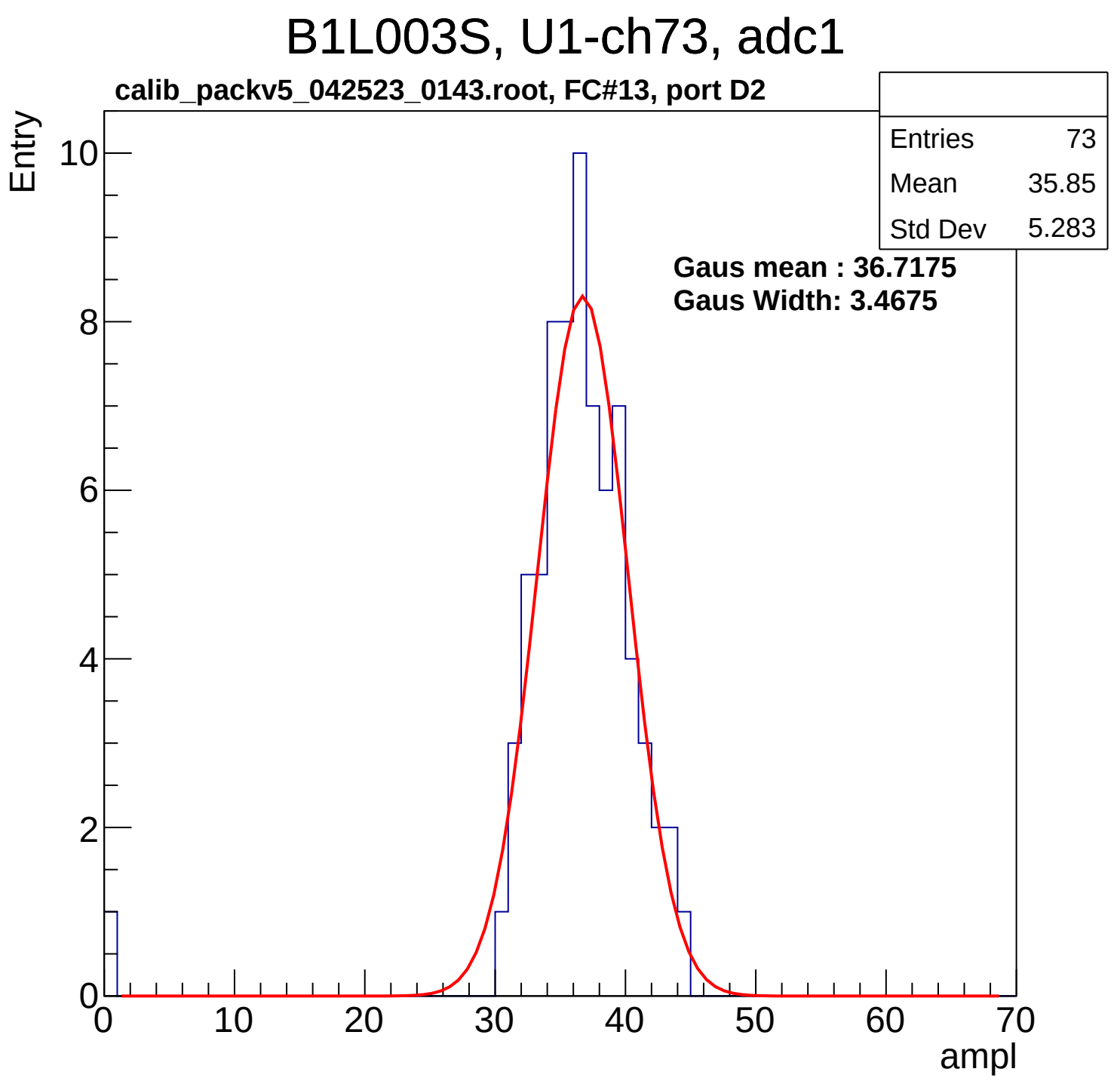

B1L003S, U1-ch72, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.07
Std Dev 3.267
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

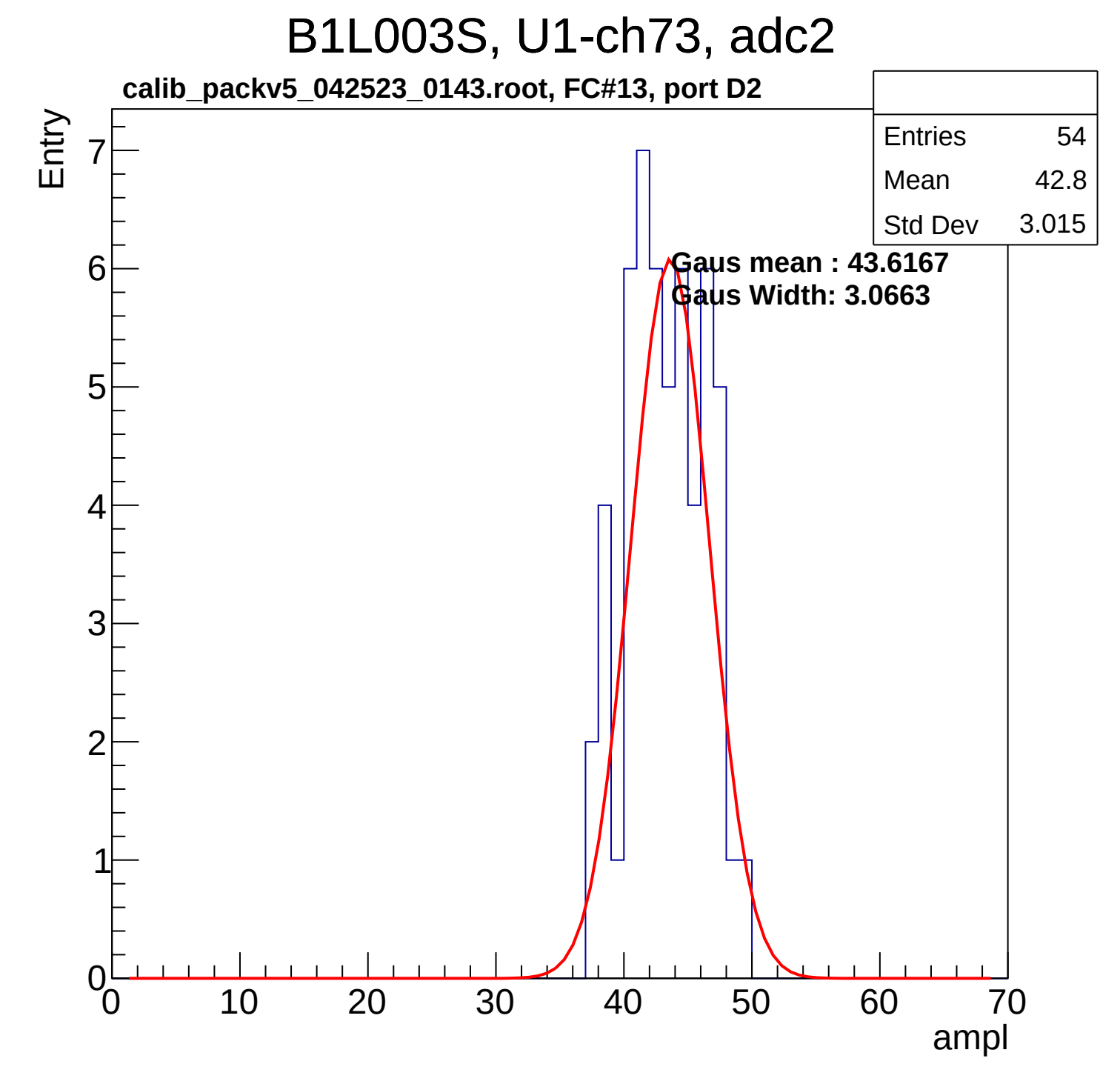

B1L003S, U1-ch72, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.93
Std Dev 2.434
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

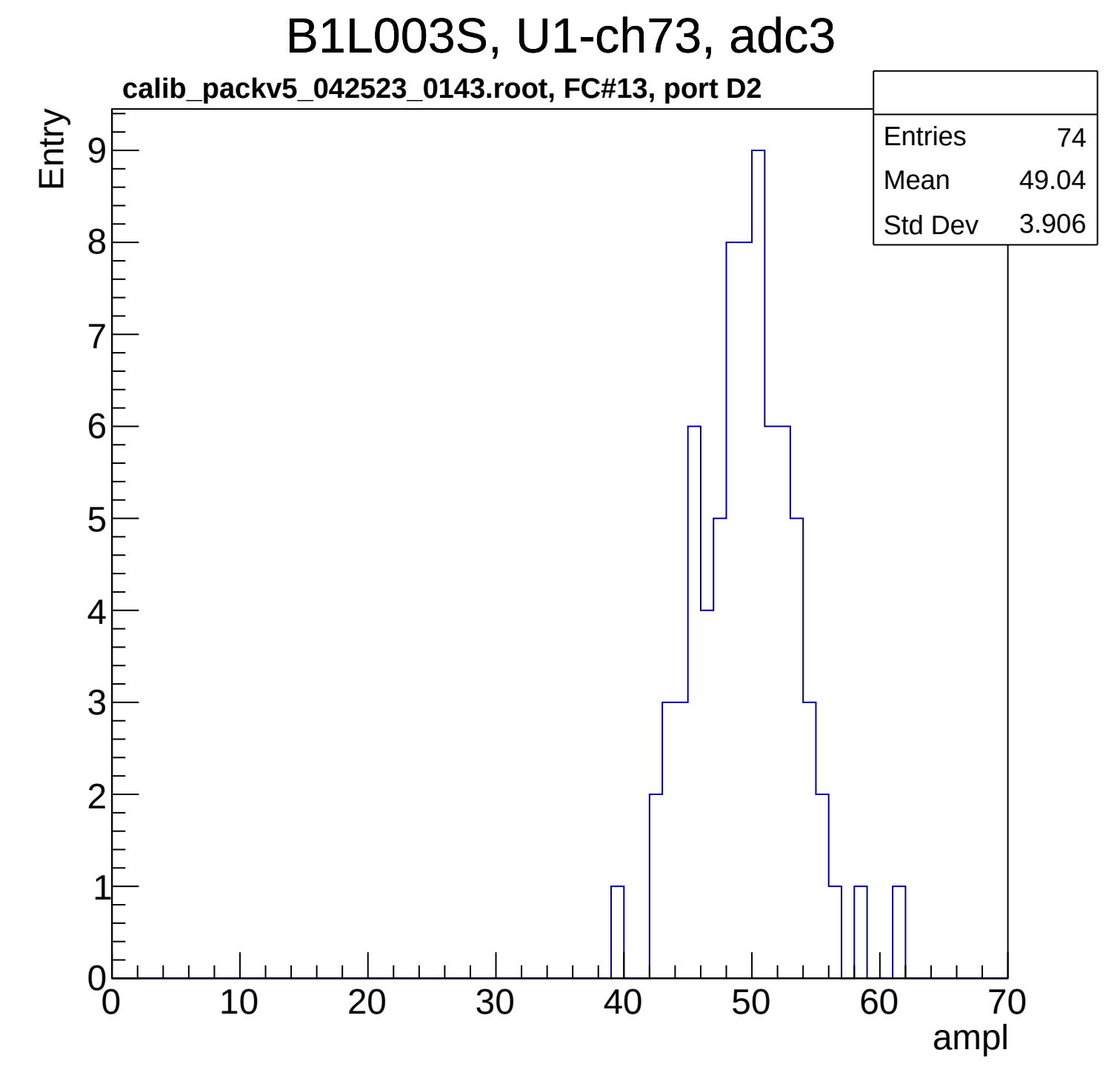

B1L003S, U1-ch72, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 57.76
Std Dev 14.53
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch72, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch73, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 30.67
Std Dev 2.797
Gaus mean : 31.0187
Gaus Width: 2.9455
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch73, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.85
Std Dev 5.283
Gaus mean : 36.7175
Gaus Width: 3.4675
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch73, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 42.8
Std Dev 3.015
Gaus mean : 43.6167
Gaus Width: 3.0663
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch73, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 49.04
Std Dev 3.906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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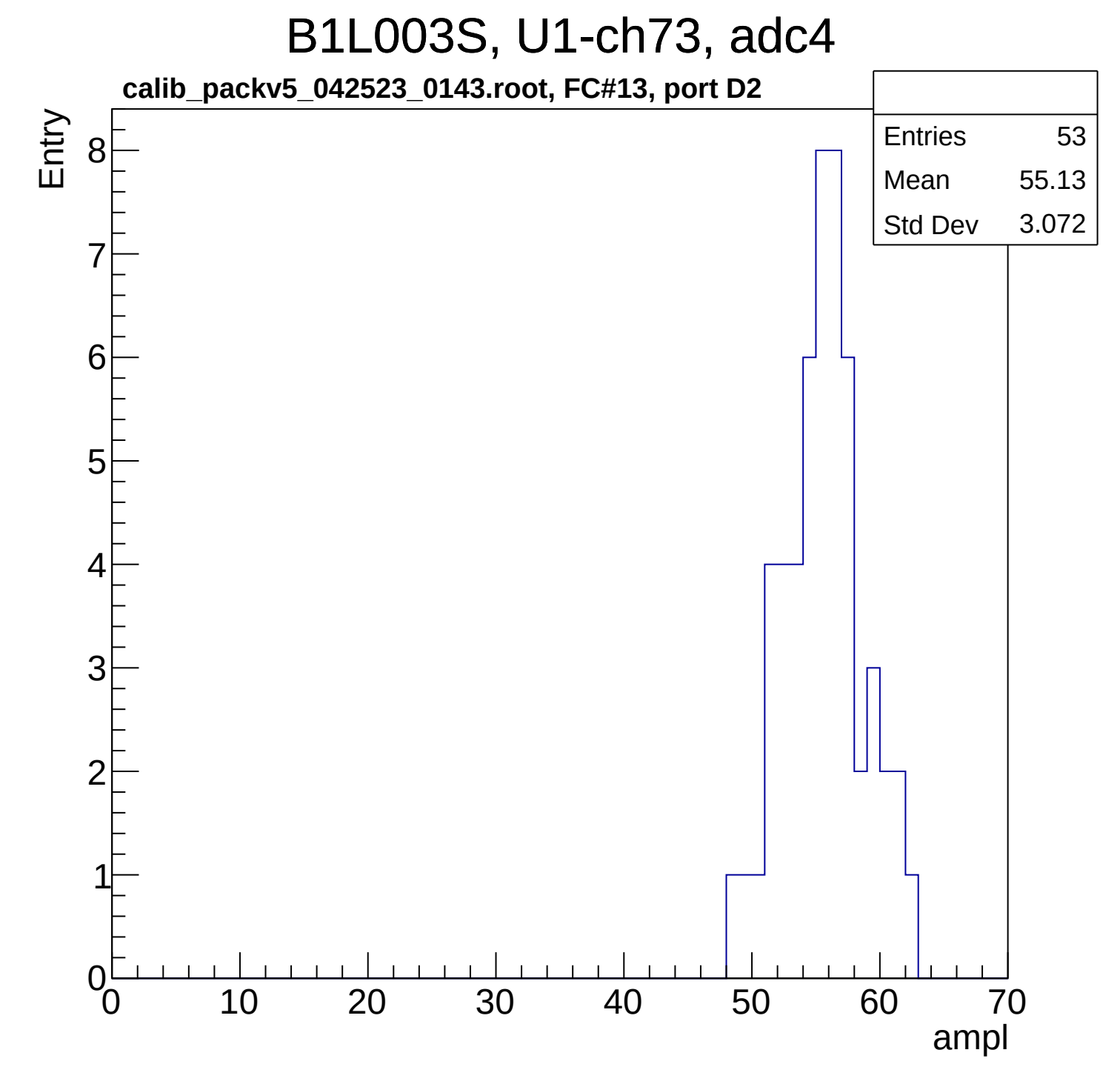
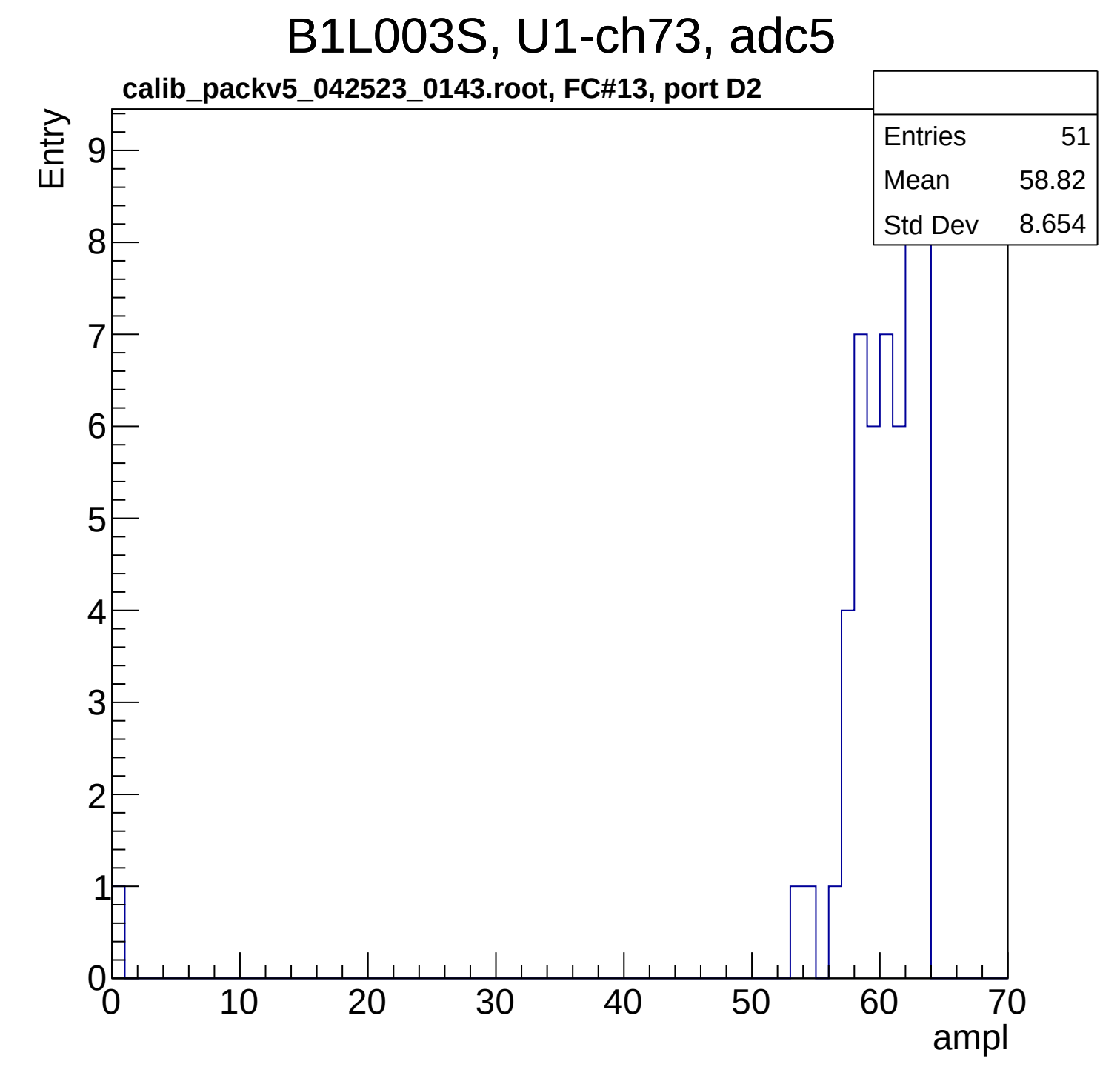
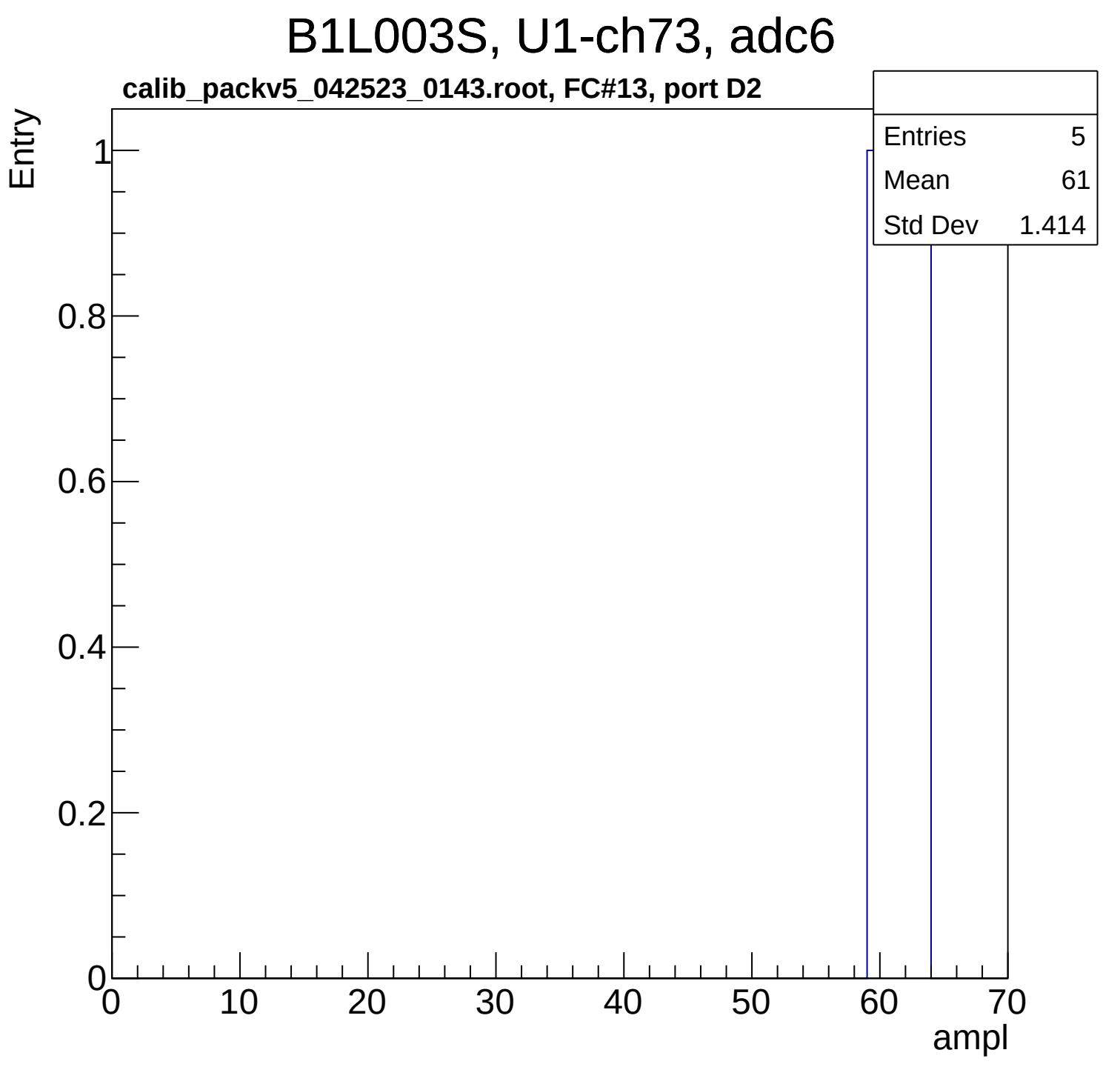
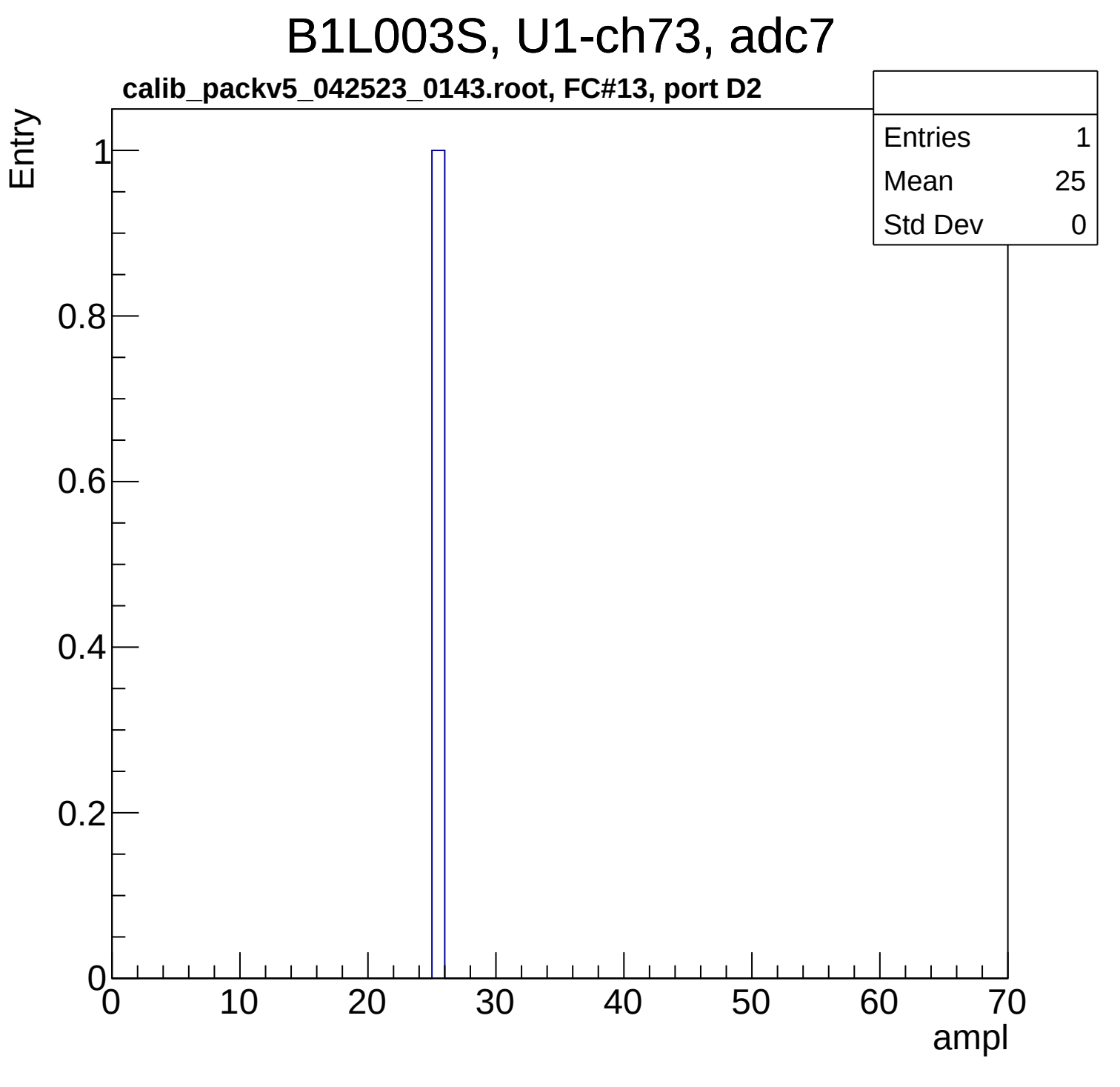
40
50
60
70


B1L003S, U1-ch73, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 55.13
Std Dev 3.072
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch73, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.82
Std Dev 8.654
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch73, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61
Std Dev 1.414
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch73, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 25
Std Dev 0
Entry
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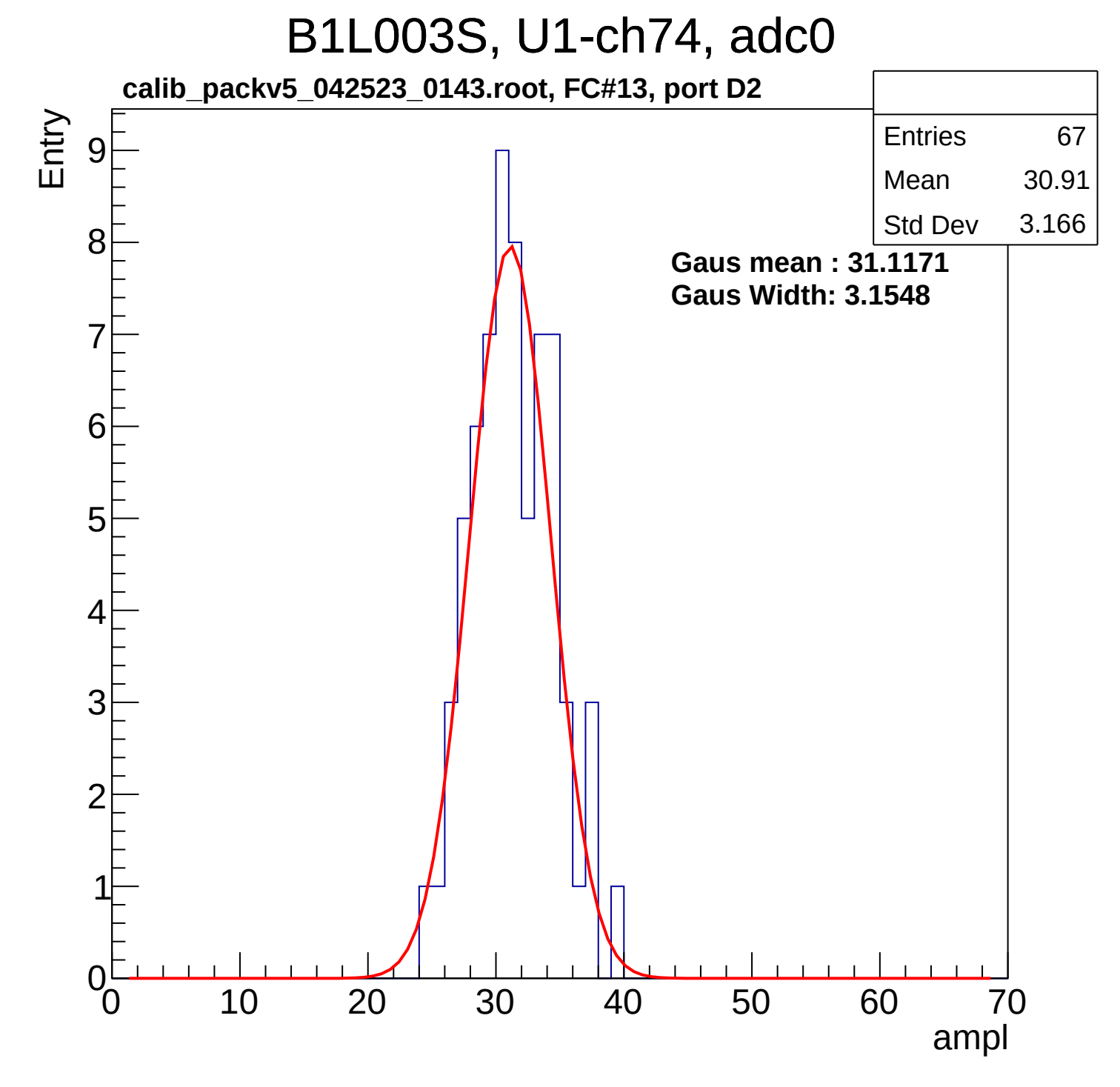
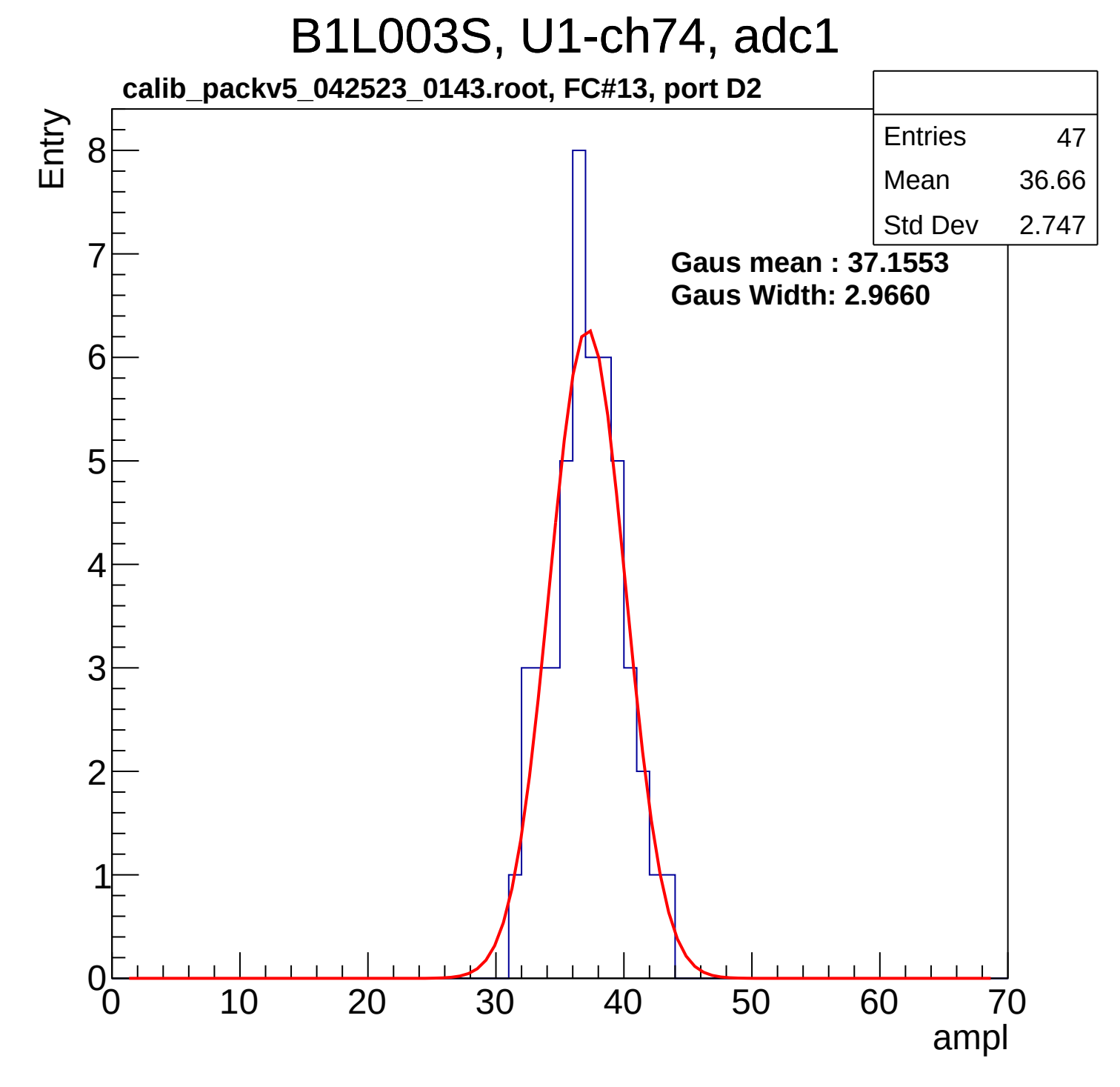
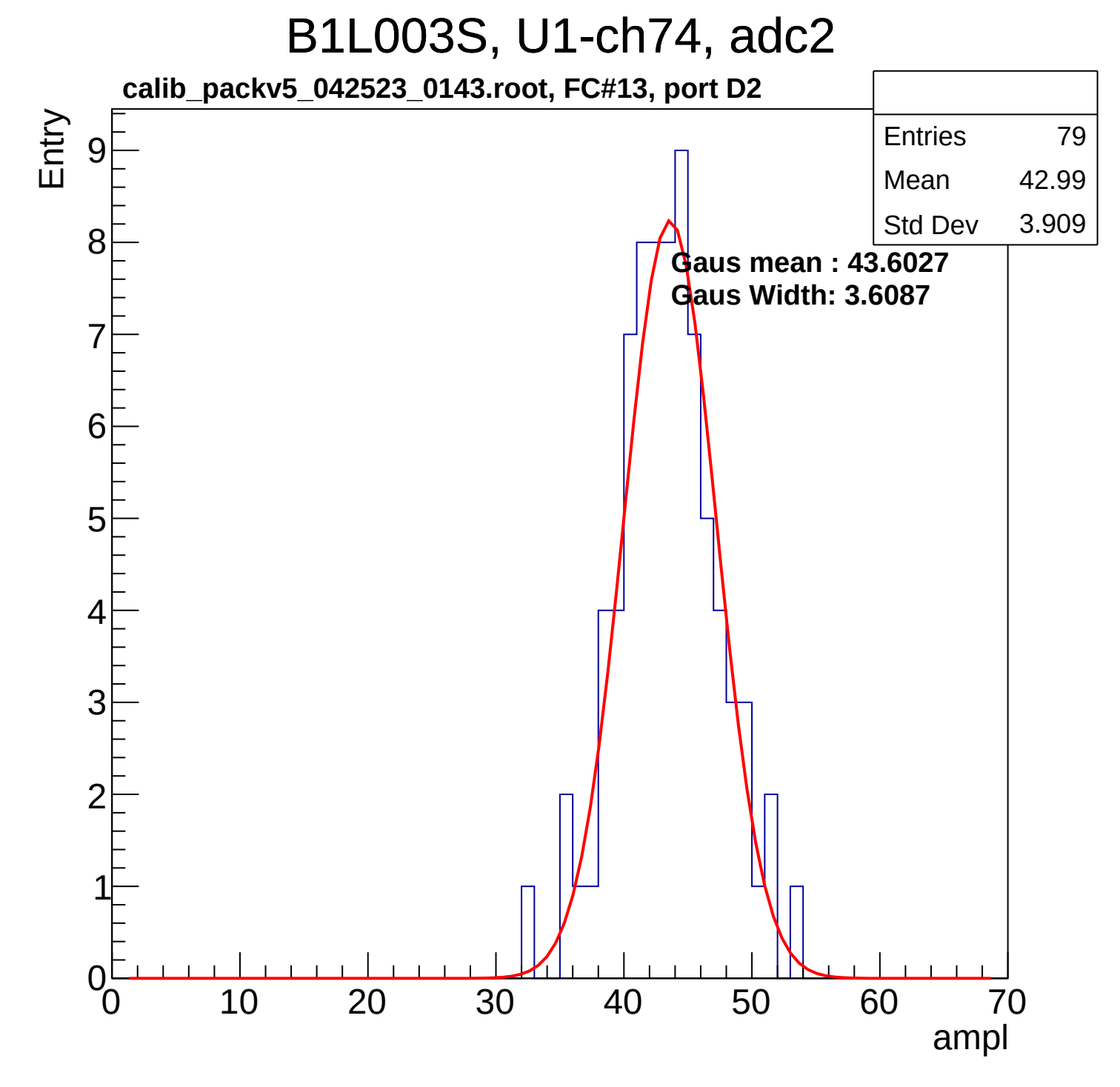
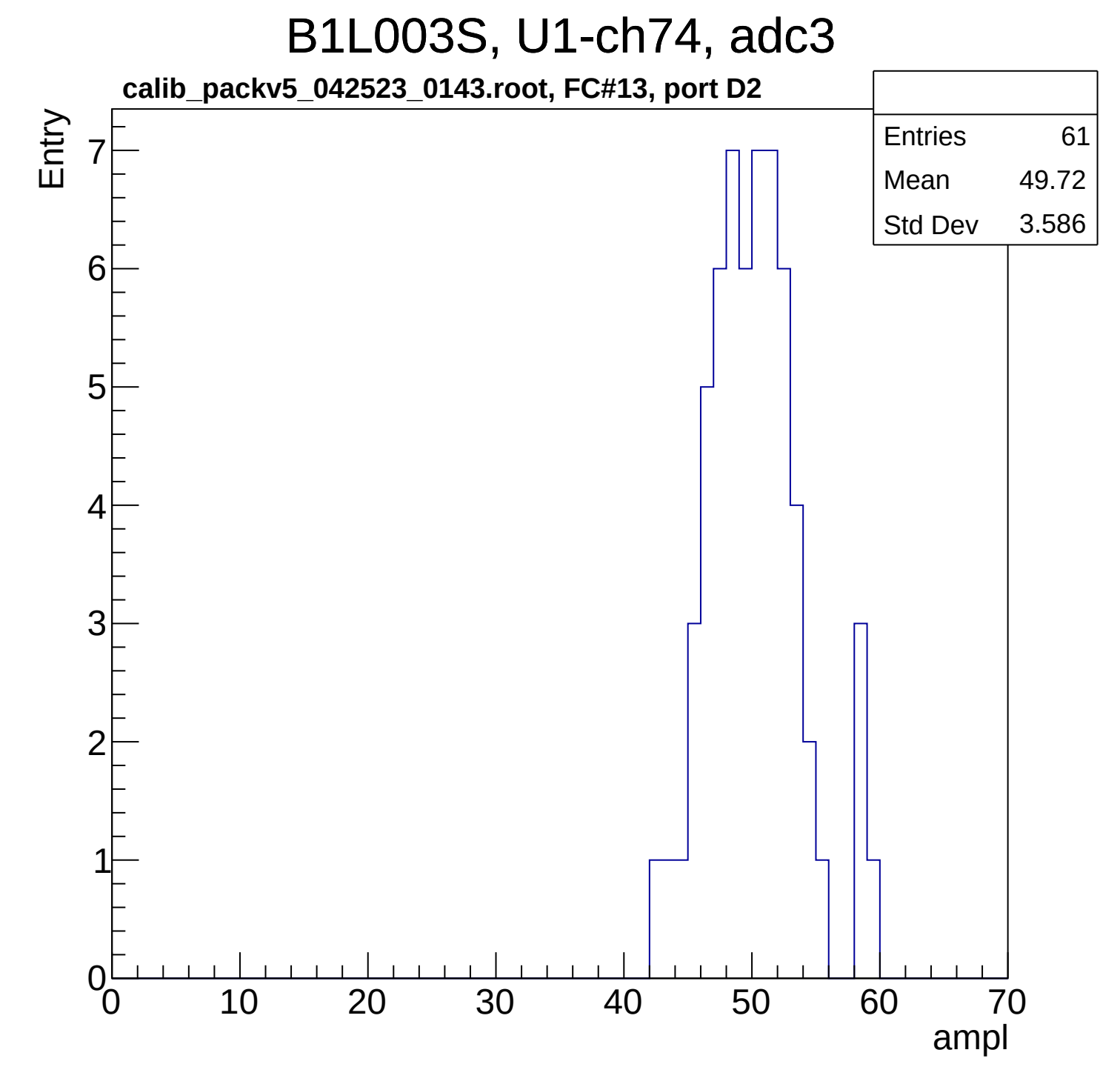
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch74, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 30.91
Std Dev 3.166
Gaus mean : 31.1171
Gaus Width: 3.1548
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch74, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 36.66
Std Dev 2.747
Gaus mean : 37.1553
Gaus Width: 2.9660
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch74, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 42.99
Std Dev 3.909
Gaus mean : 43.6027
Gaus Width: 3.6087
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch74, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.72
Std Dev 3.586
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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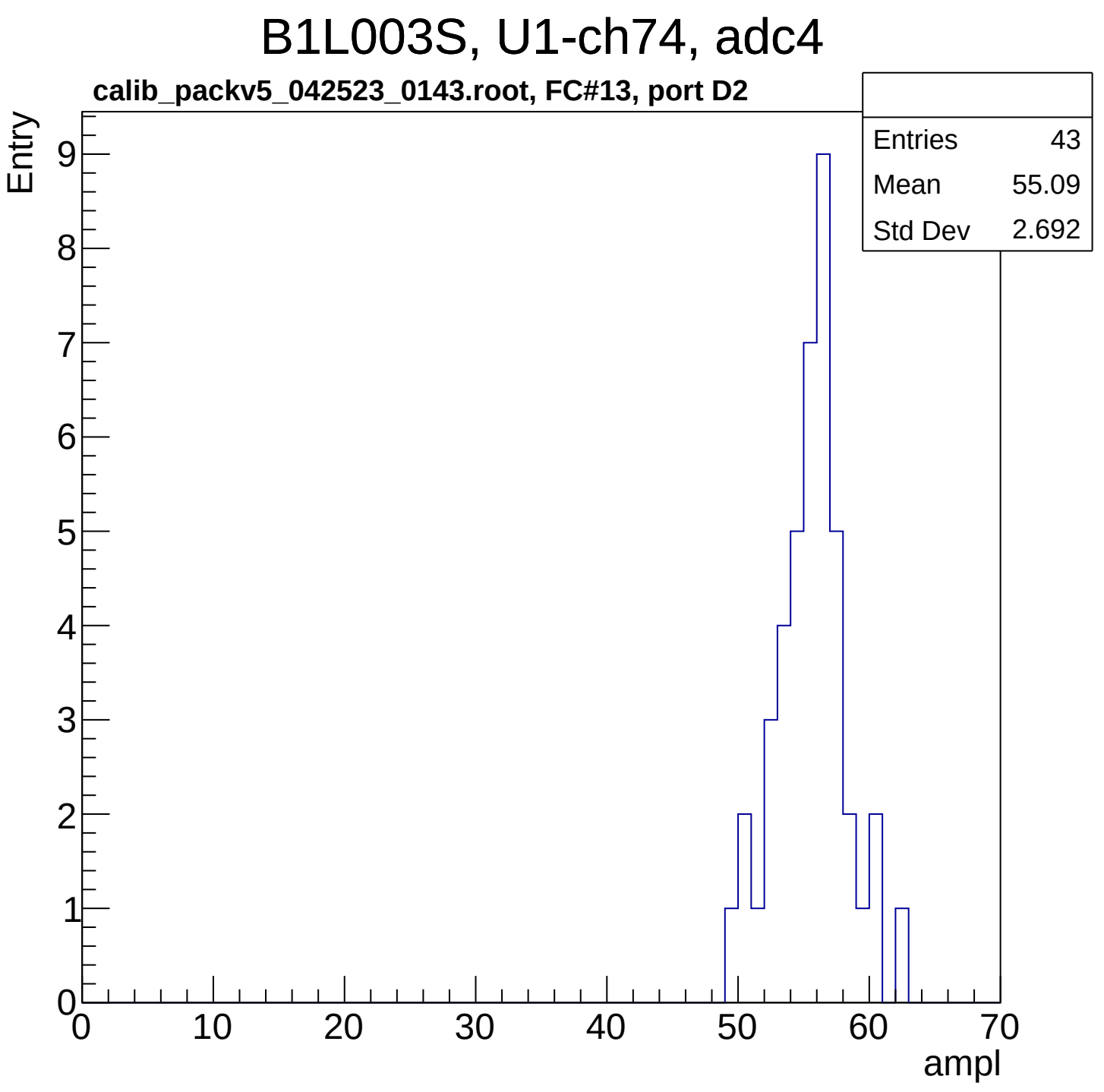
40
50
60
70


B1L003S, U1-ch74, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 55.09
Std Dev 2.692
Entry
ampl
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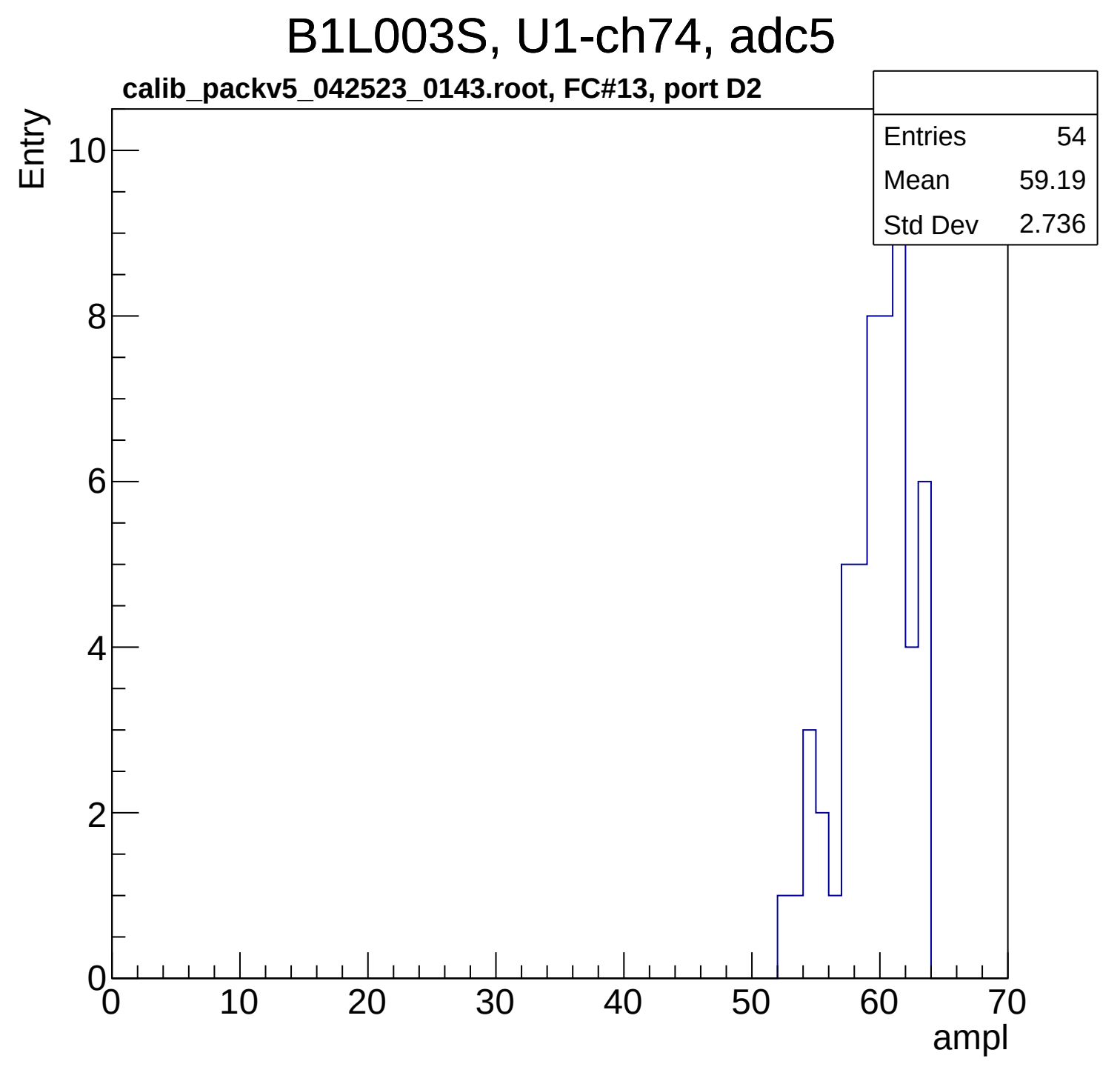
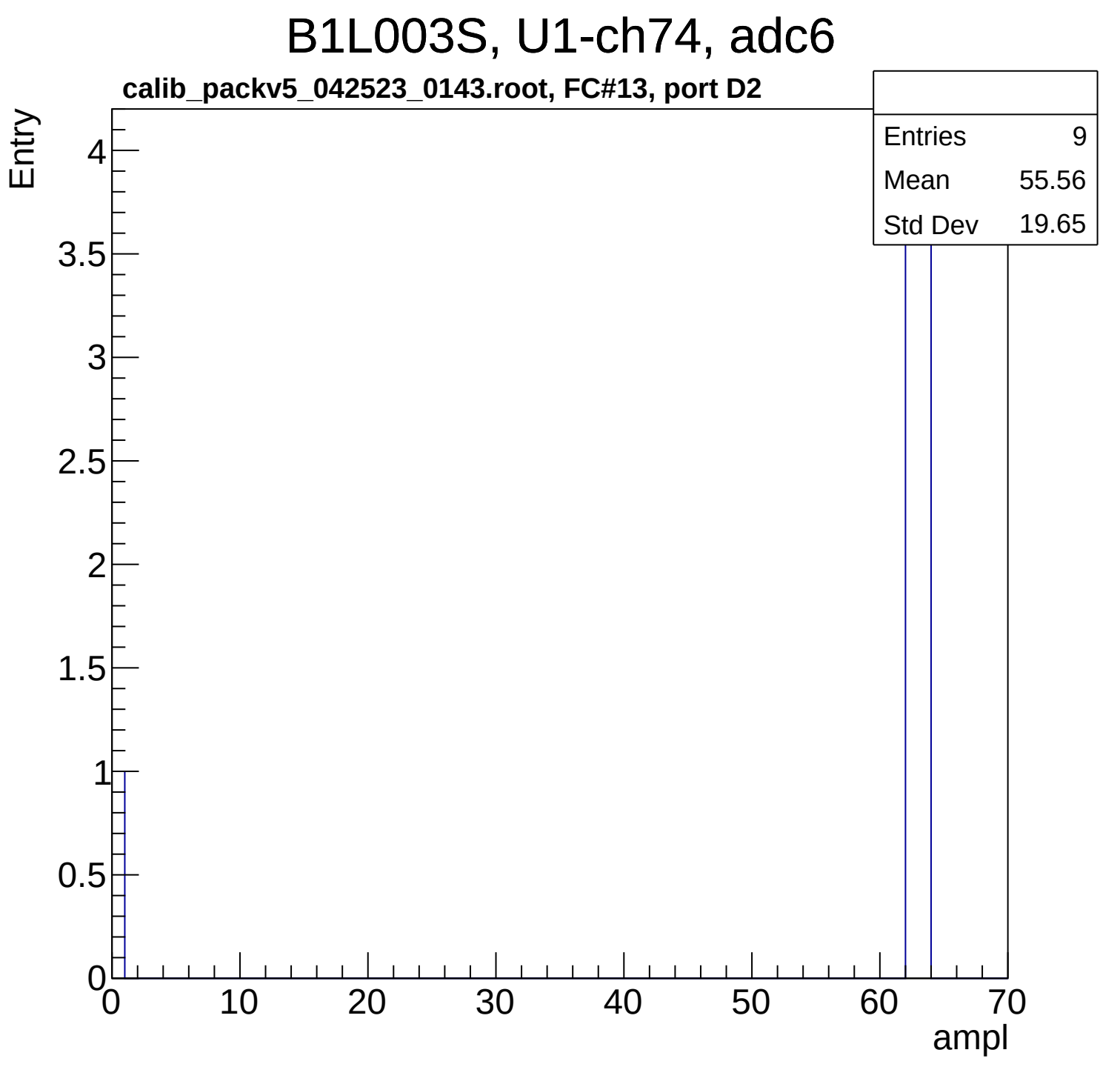
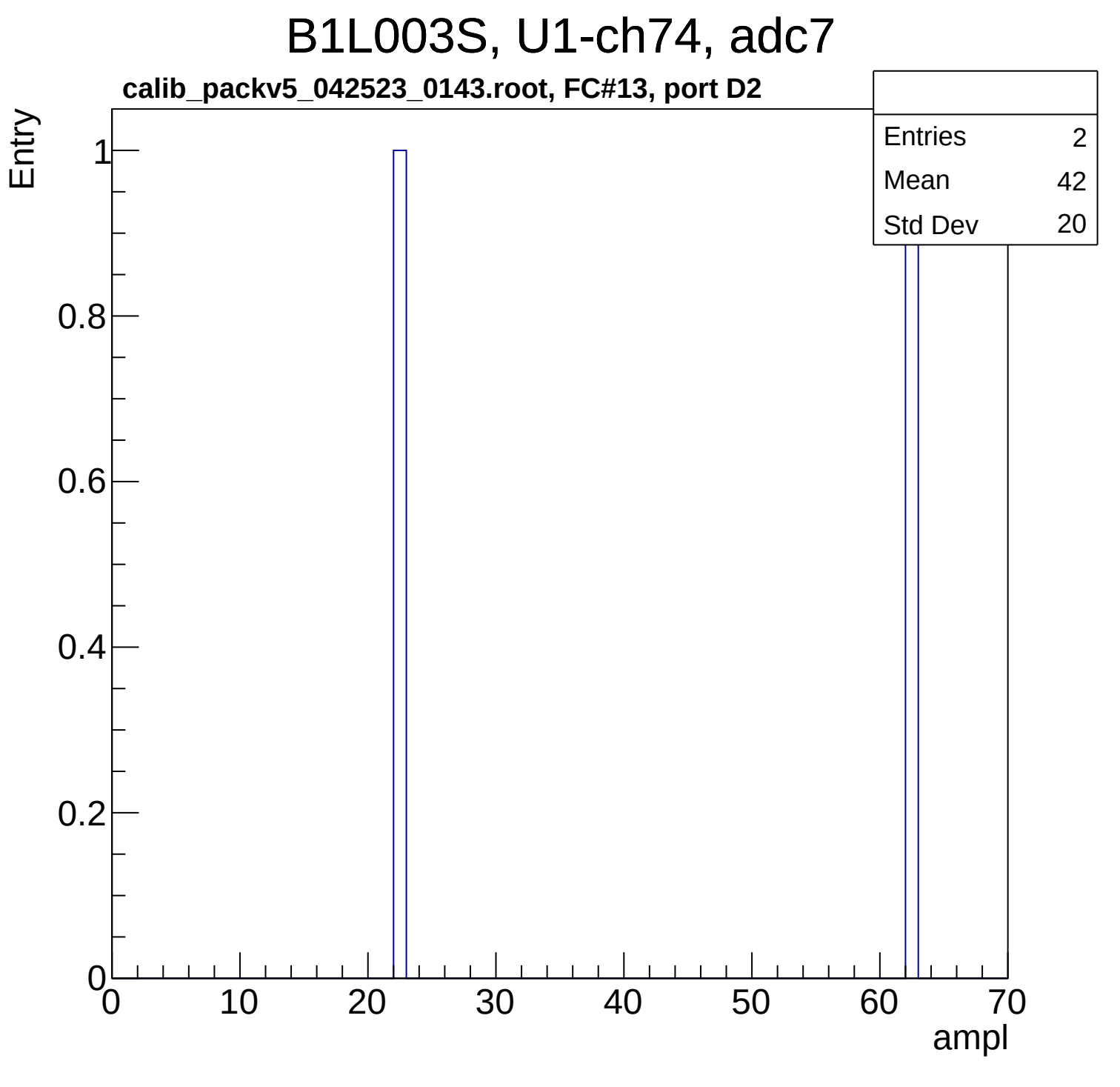
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch74, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 59.19
Std Dev 2.736
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch74, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.56
Std Dev 19.65
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch74, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 20
Entry
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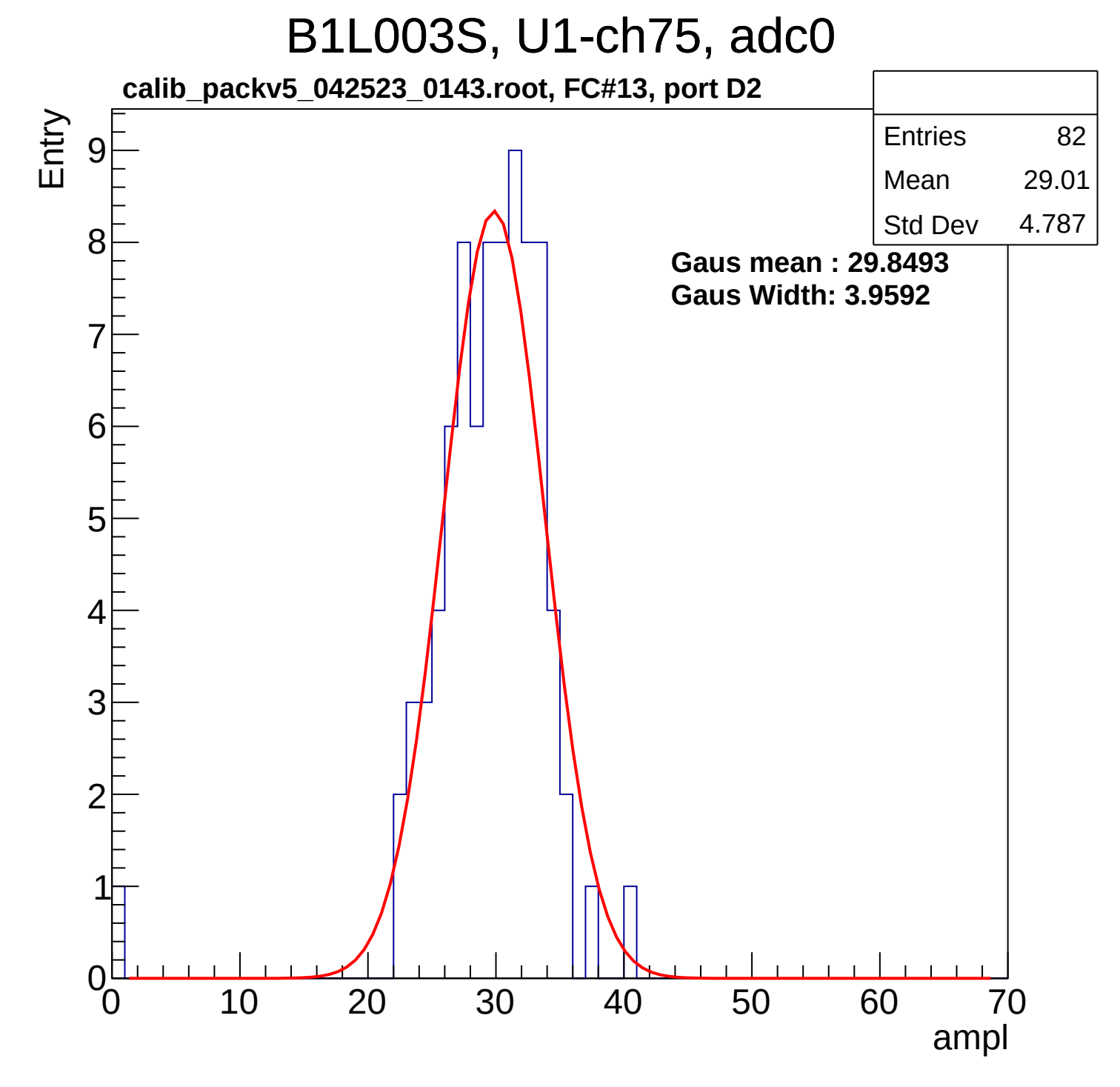
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch75, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 29.01
Std Dev 4.787
Gaus mean : 29.8493
Gaus Width: 3.9592
Entry
ampl
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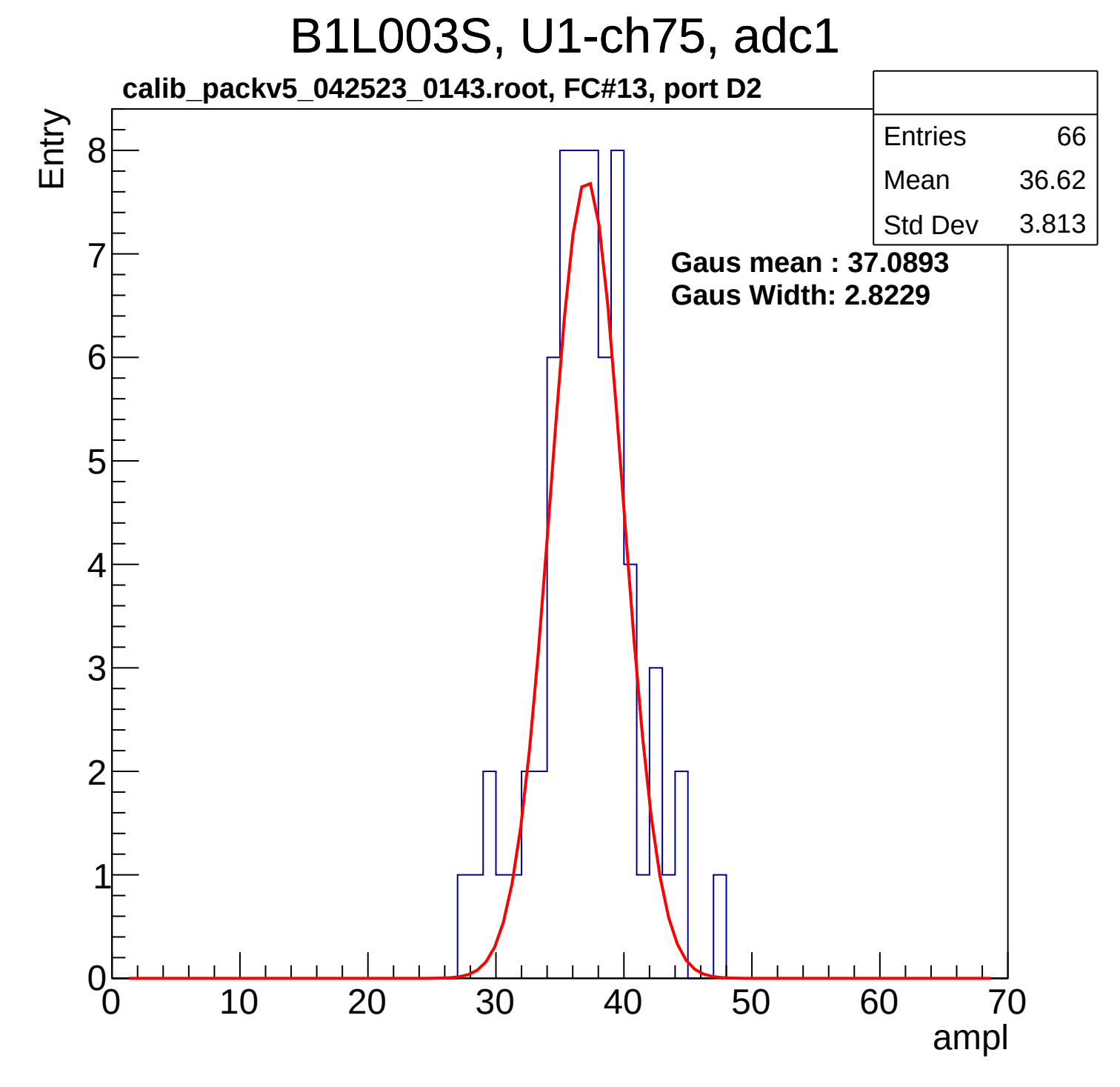
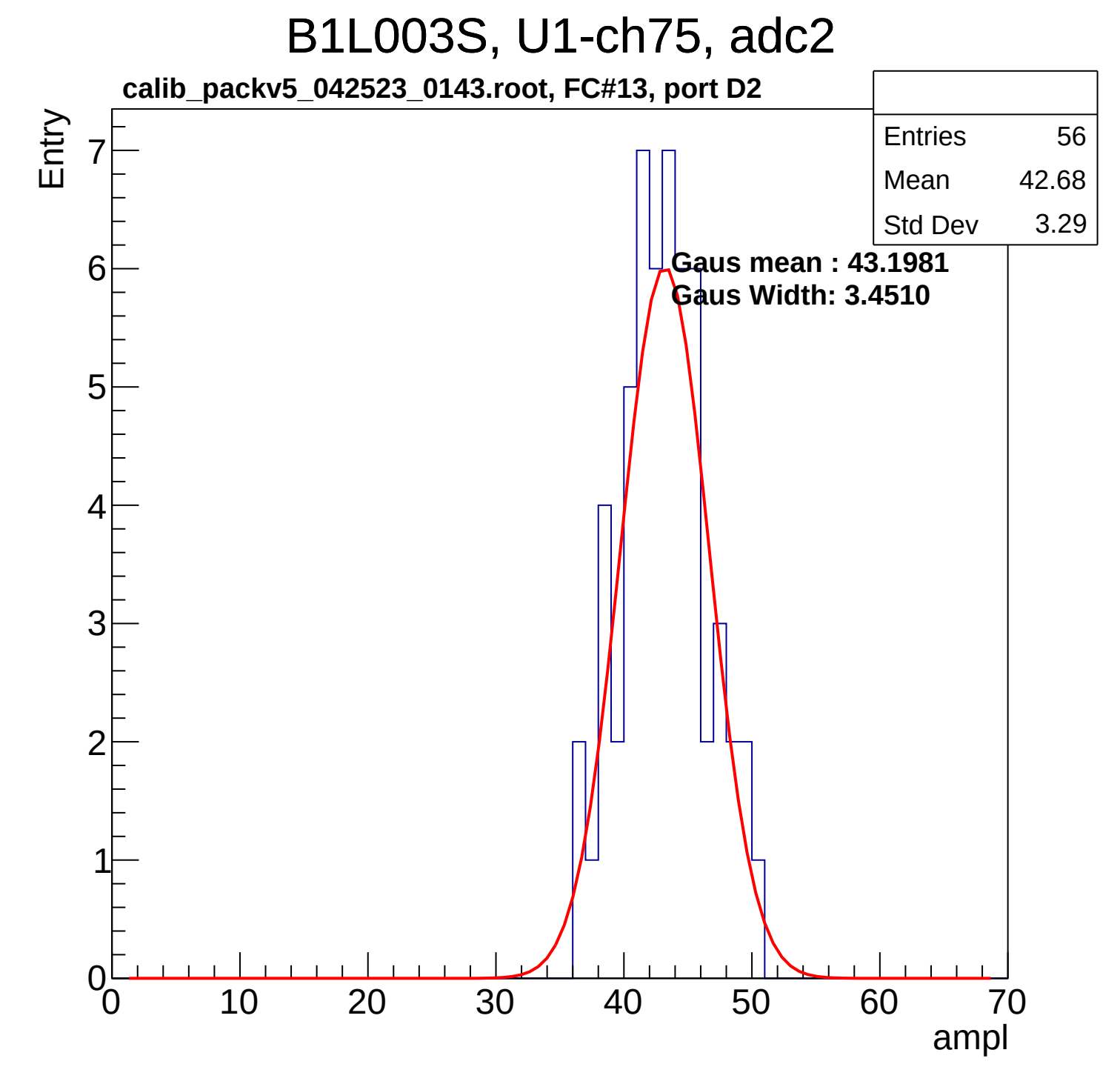
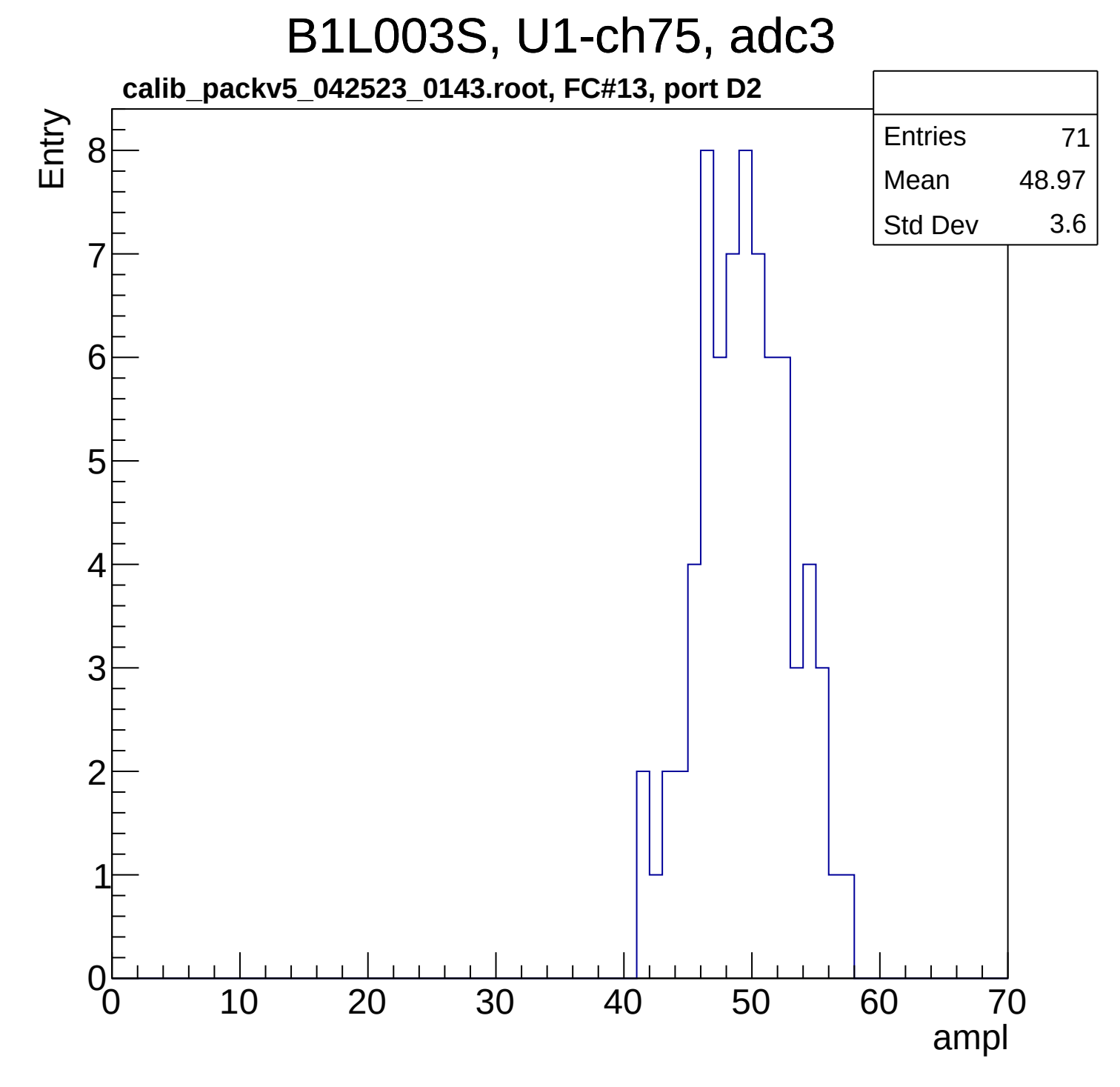
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch75, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 36.62
Std Dev 3.813
Gaus mean : 37.0893
Gaus Width: 2.8229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch75, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.68
Std Dev 3.29
Gaus mean : 43.1981
Gaus Width: 3.4510
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch75, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 48.97
Std Dev 3.6
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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60
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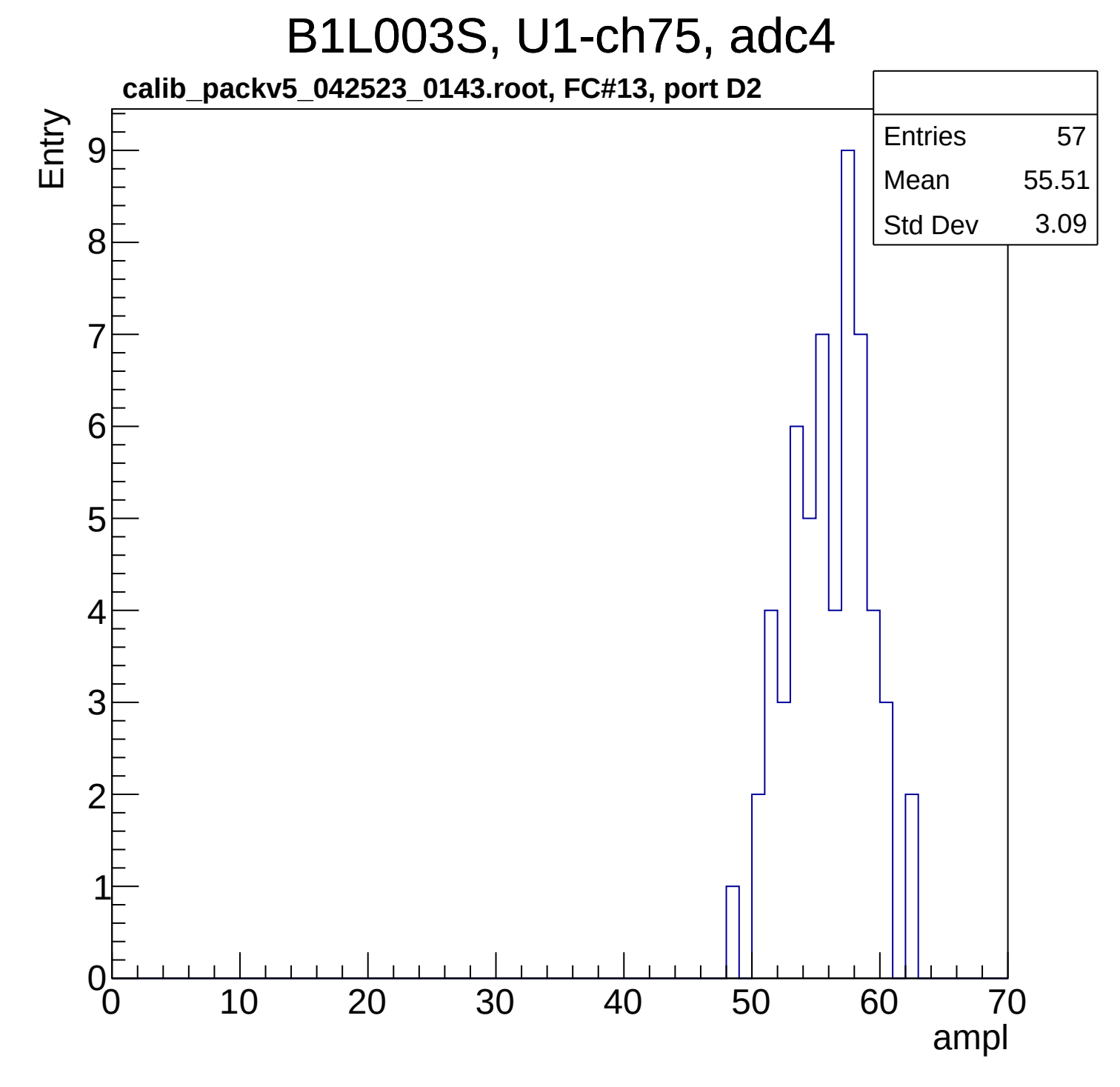
B1L003S, U1-ch75, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.51
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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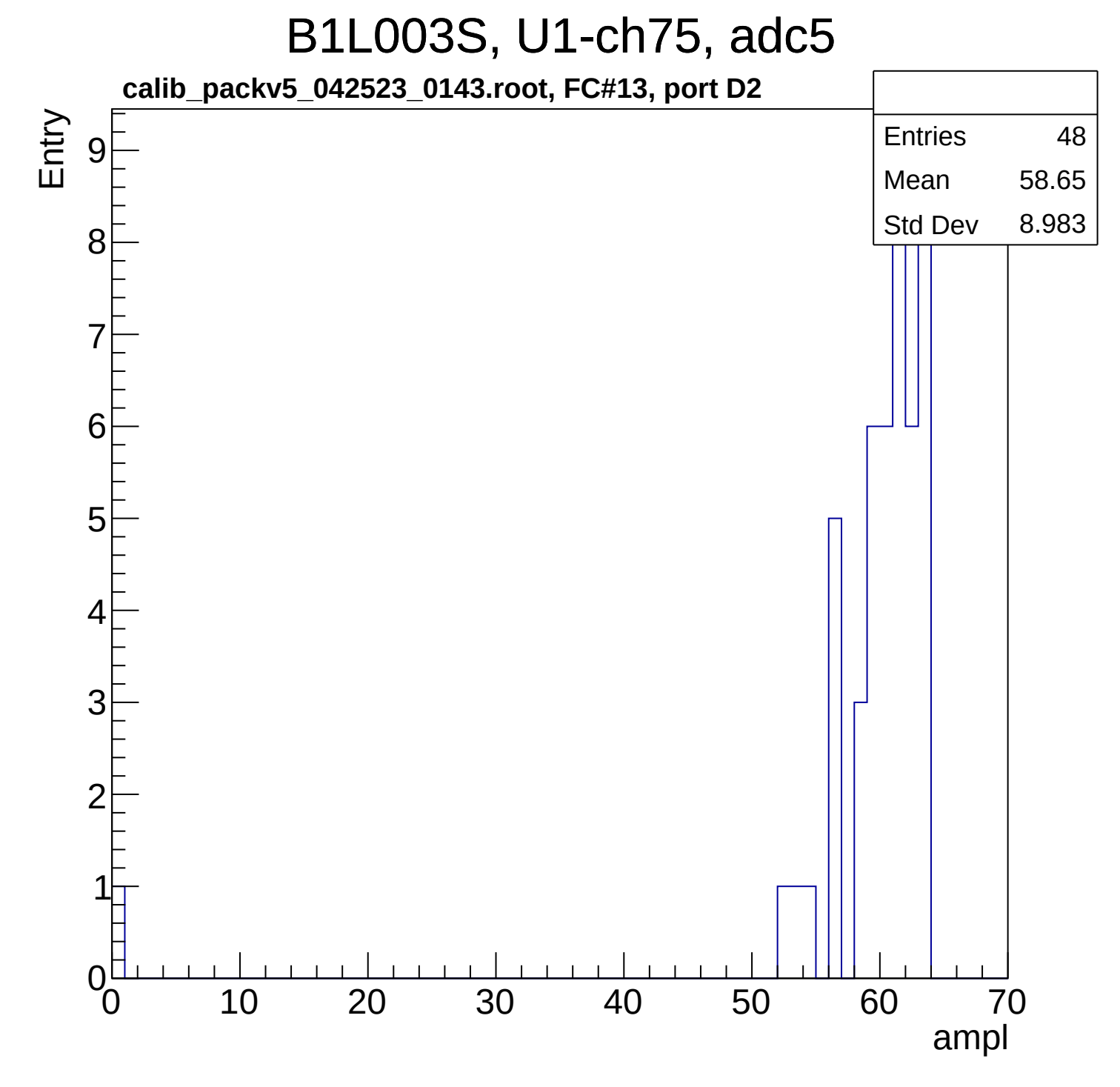
B1L003S, U1-ch75, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.65
Std Dev 8.983
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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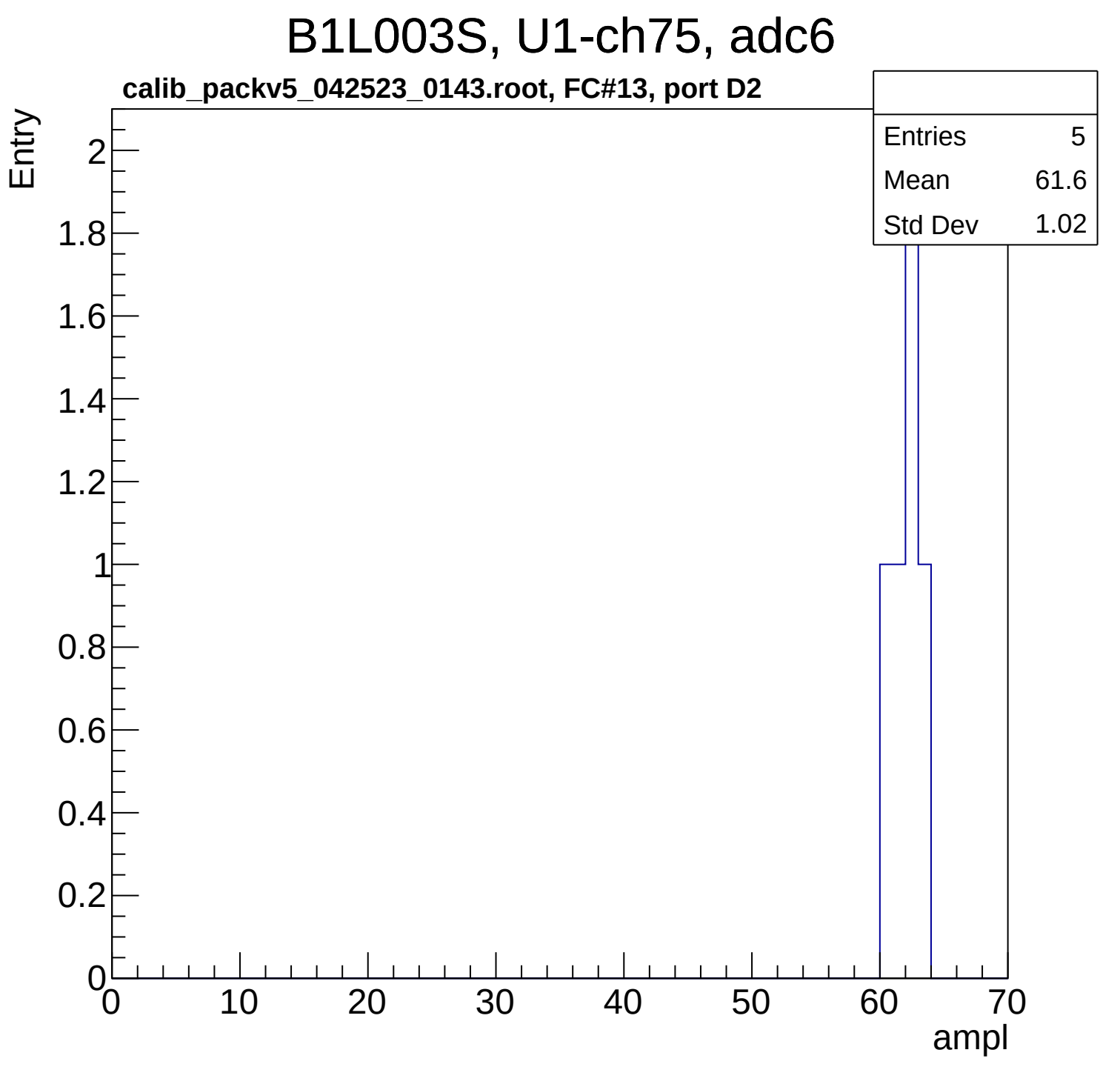
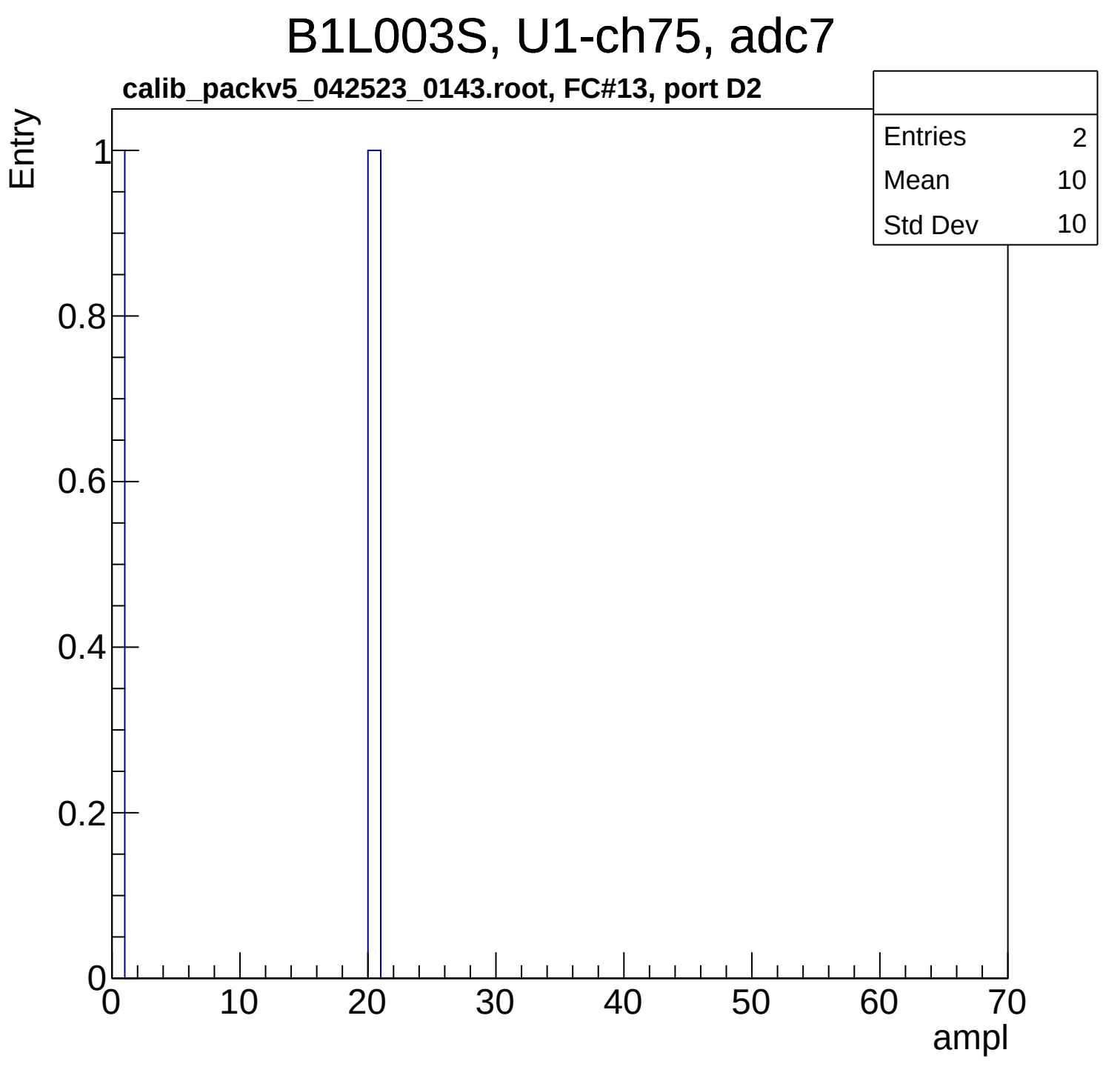
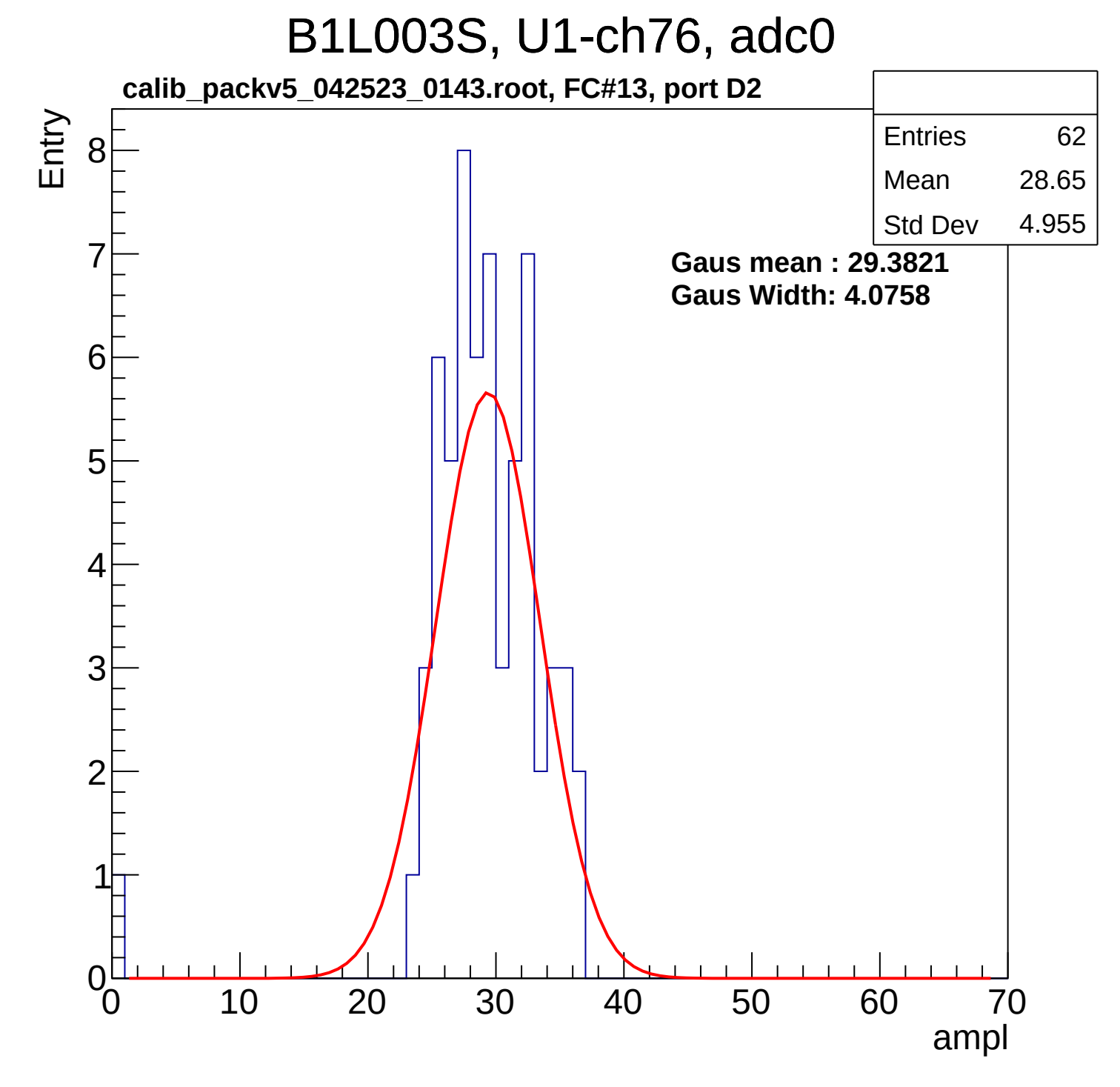
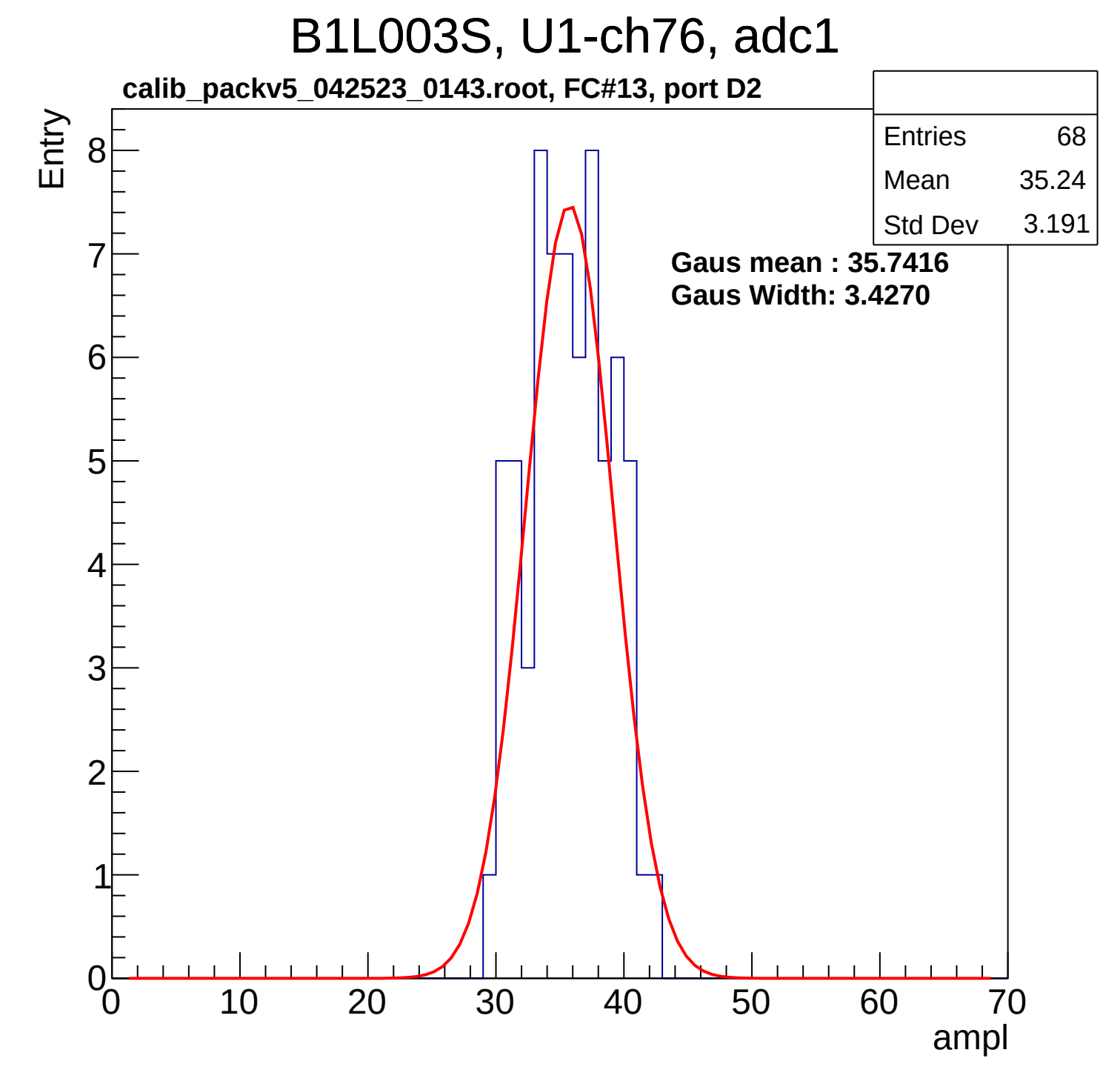
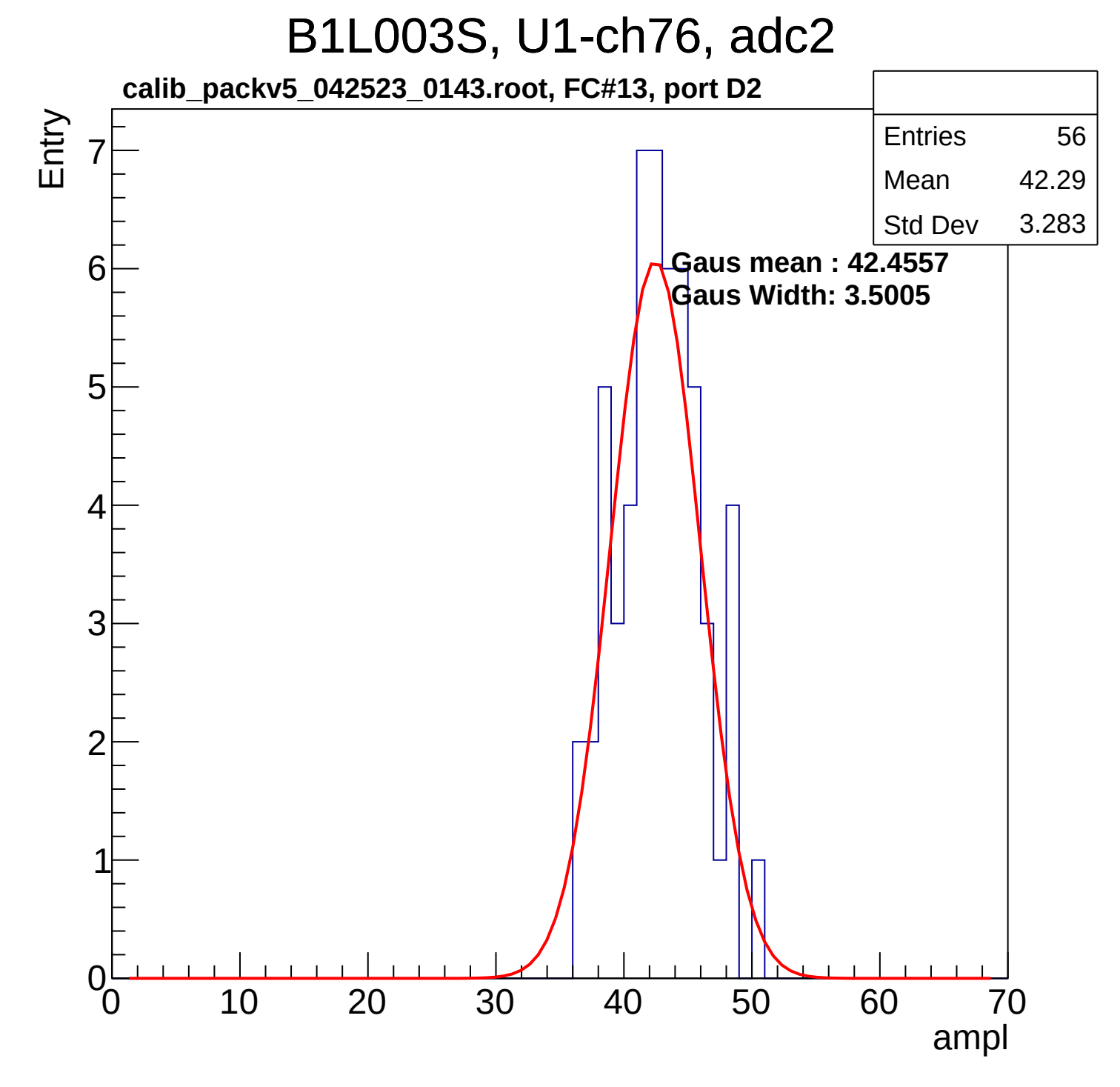
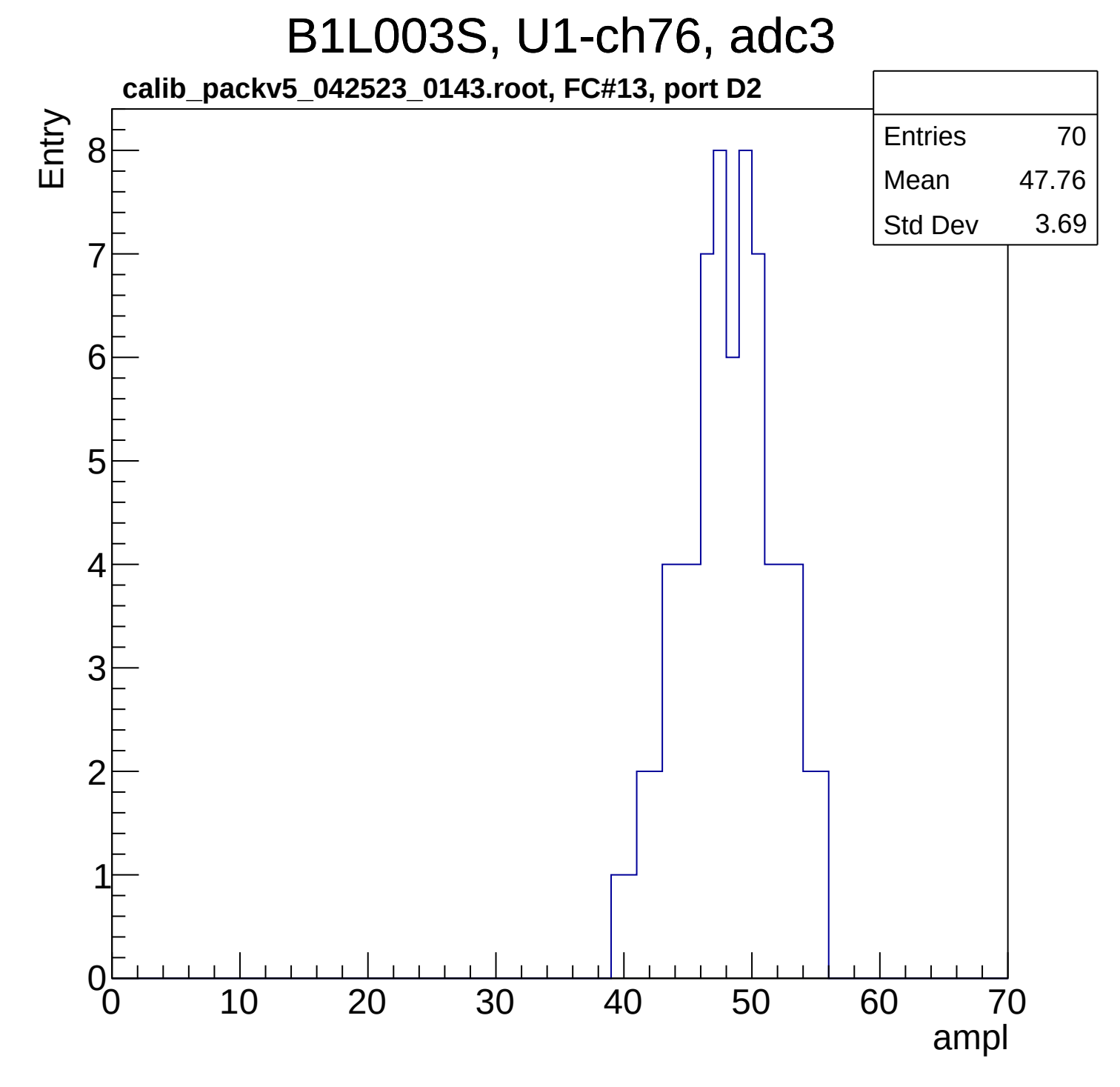

B1L003S, U1-ch75, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.6
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

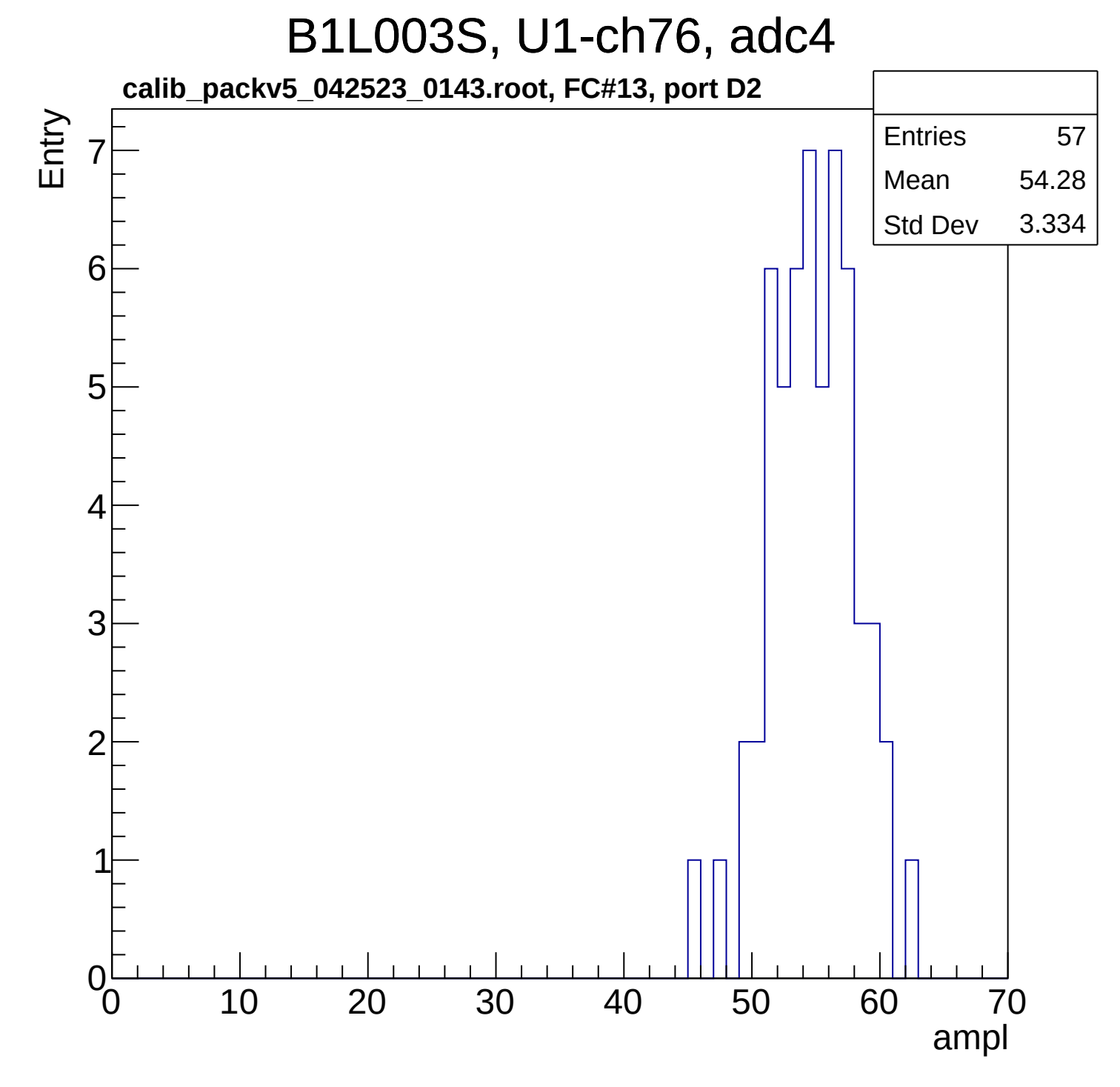

B1L003S, U1-ch75, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

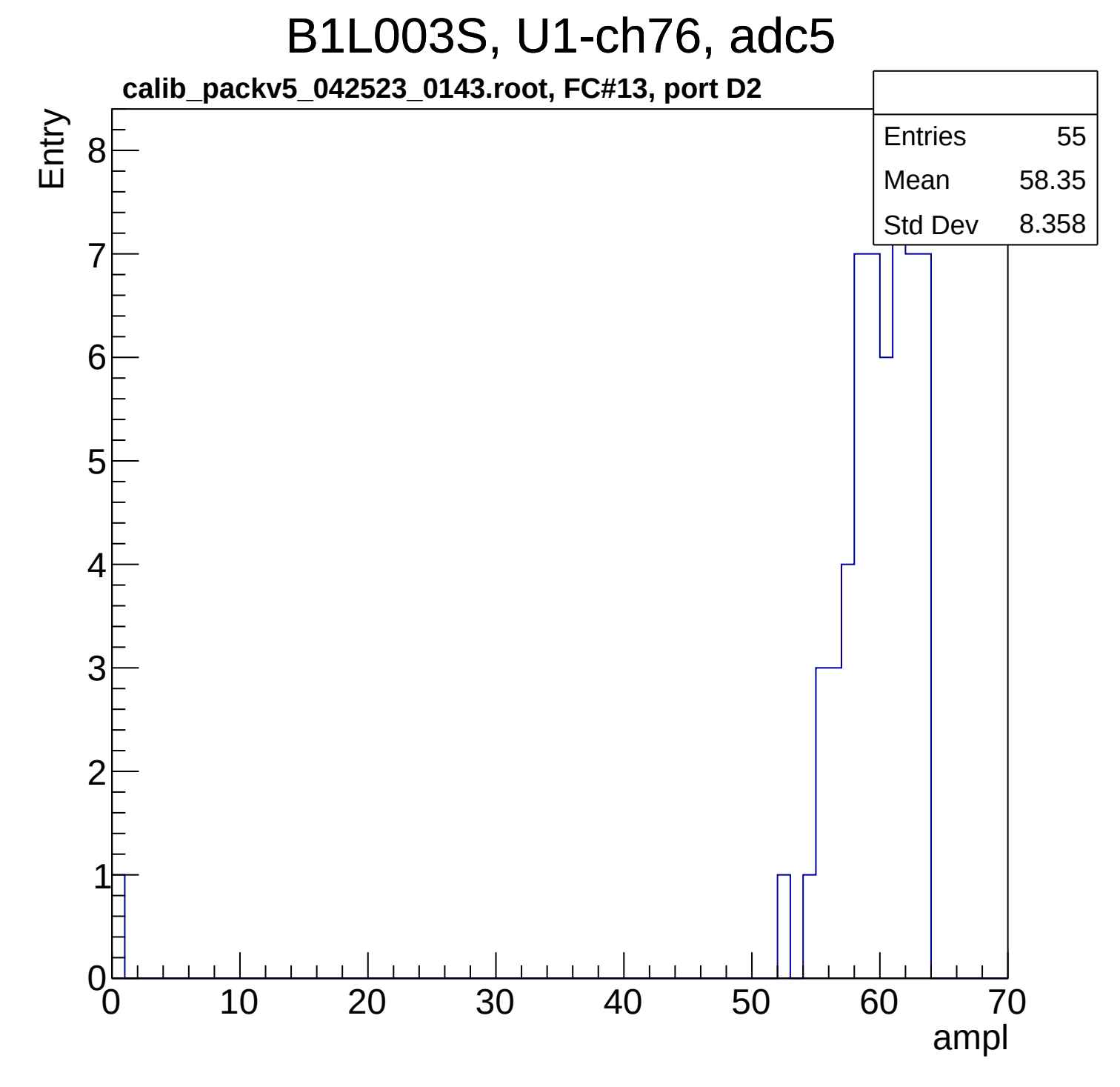

B1L003S, U1-ch76, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.65
Std Dev 4.955
Gaus mean : 29.3821
Gaus Width: 4.0758
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

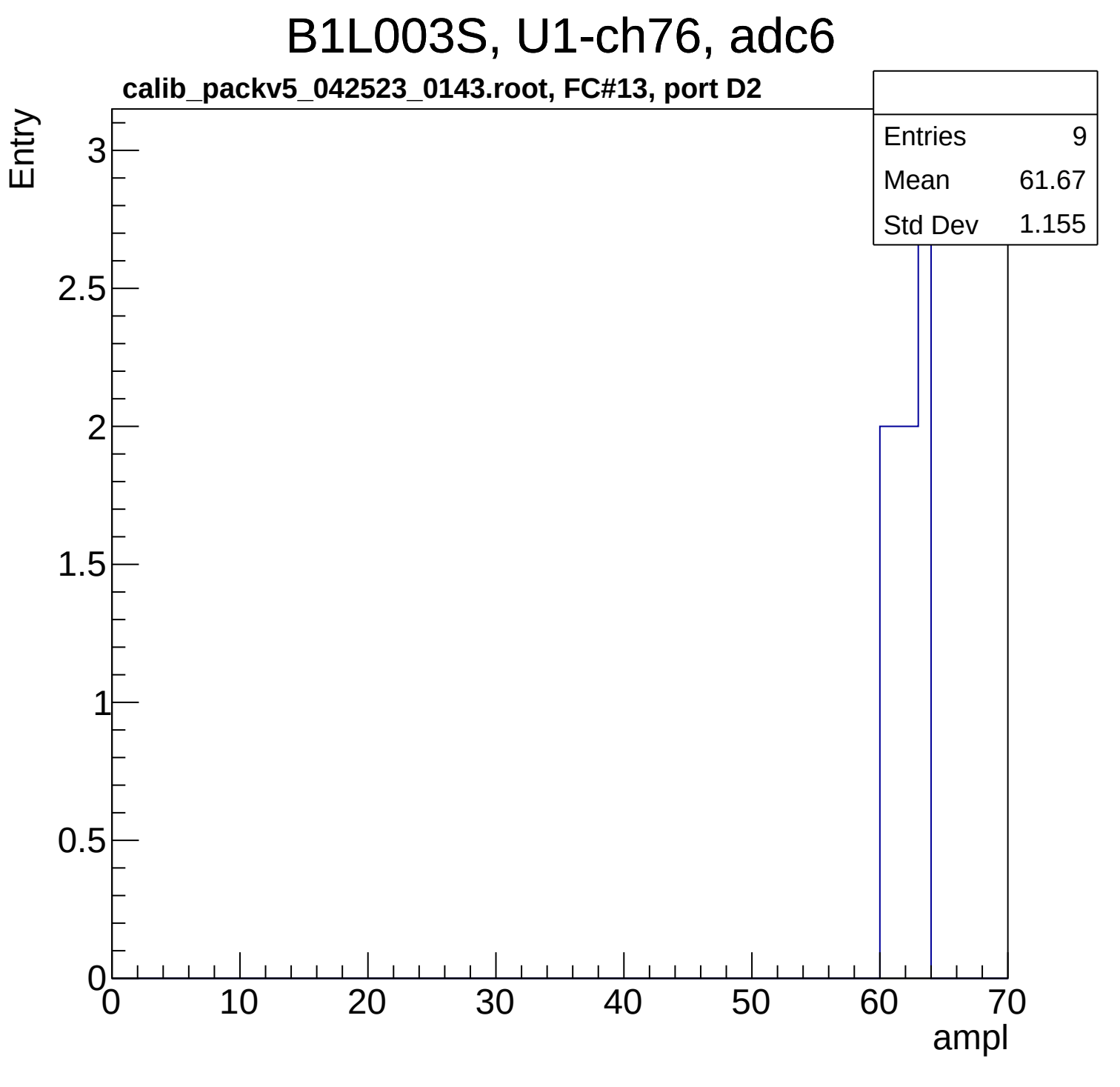

B1L003S, U1-ch76, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 35.24
Std Dev 3.191
Gaus mean : 35.7416
Gaus Width: 3.4270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

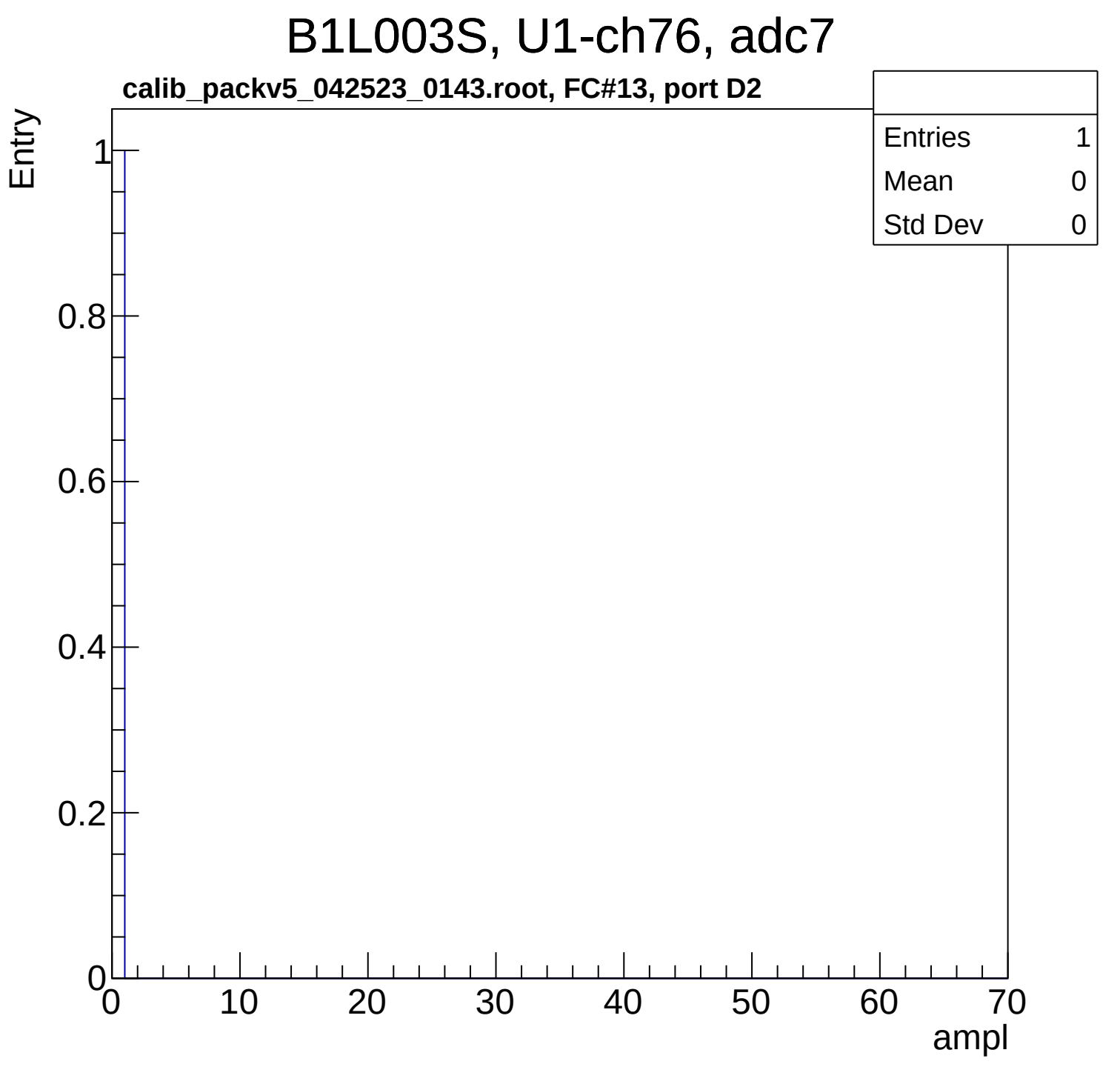

B1L003S, U1-ch76, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 42.29
Std Dev 3.283
Gaus mean : 42.4557
Gaus Width: 3.5005
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

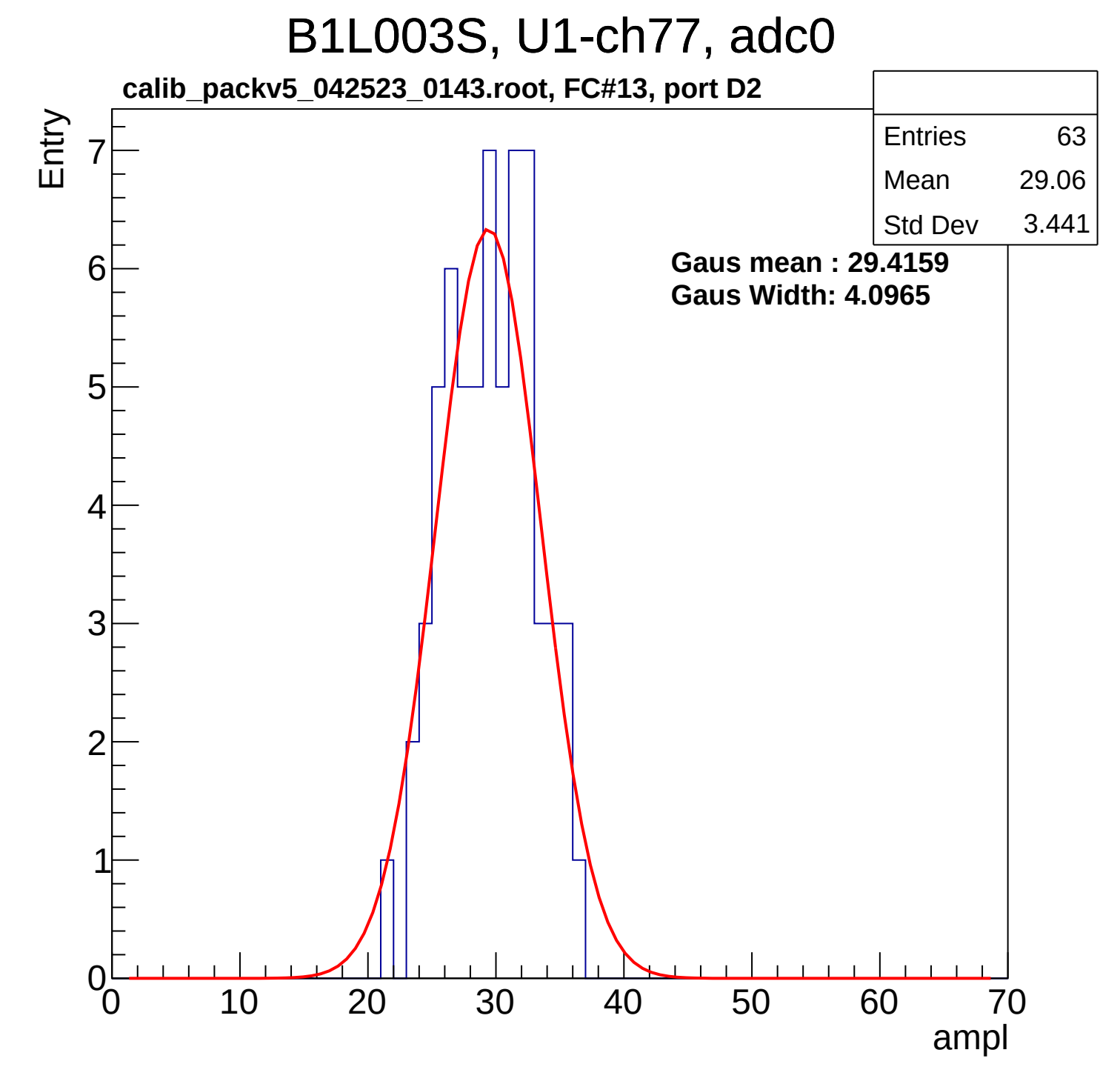

B1L003S, U1-ch76, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.76
Std Dev 3.69
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

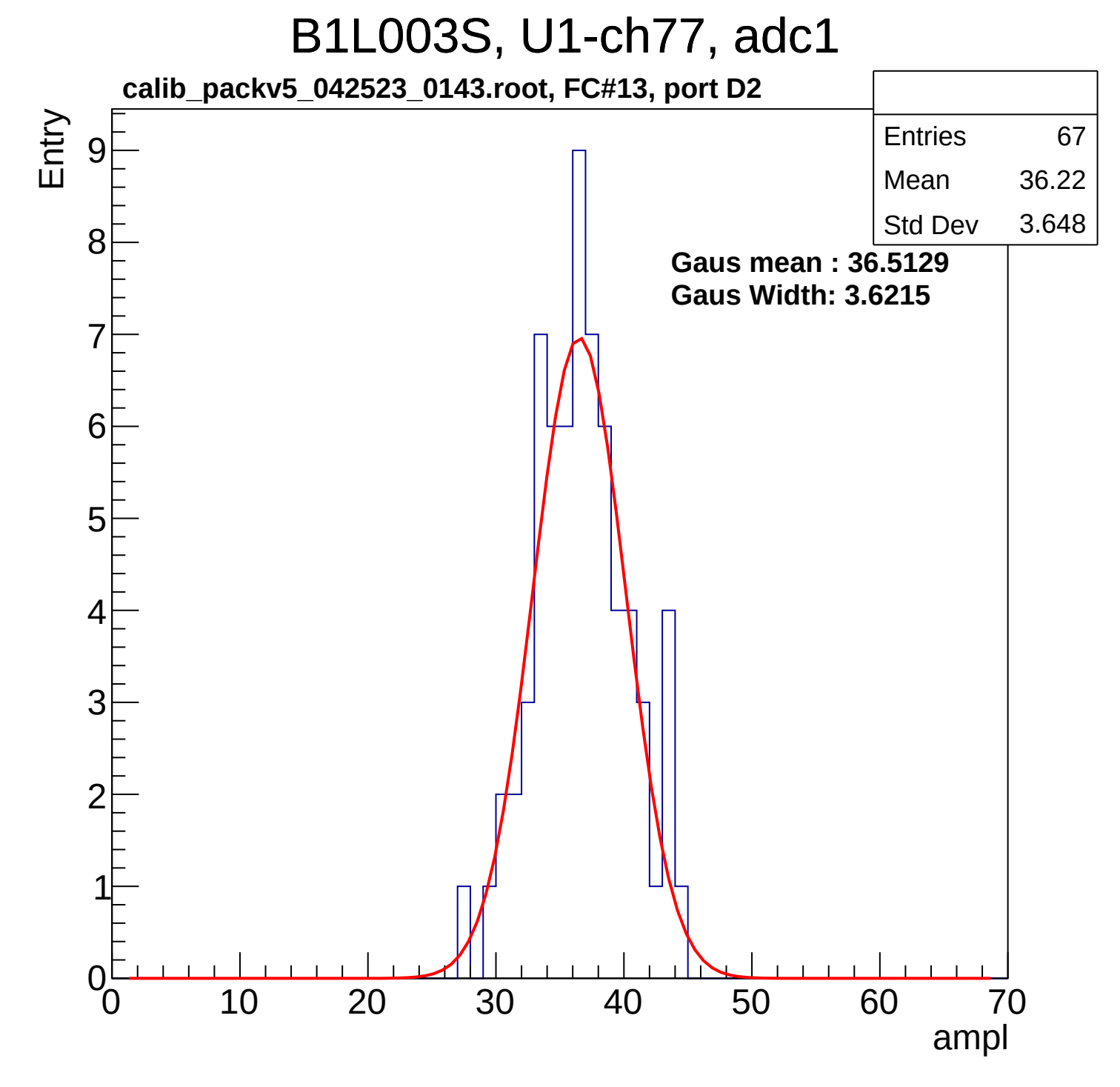

B1L003S, U1-ch76, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.28
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

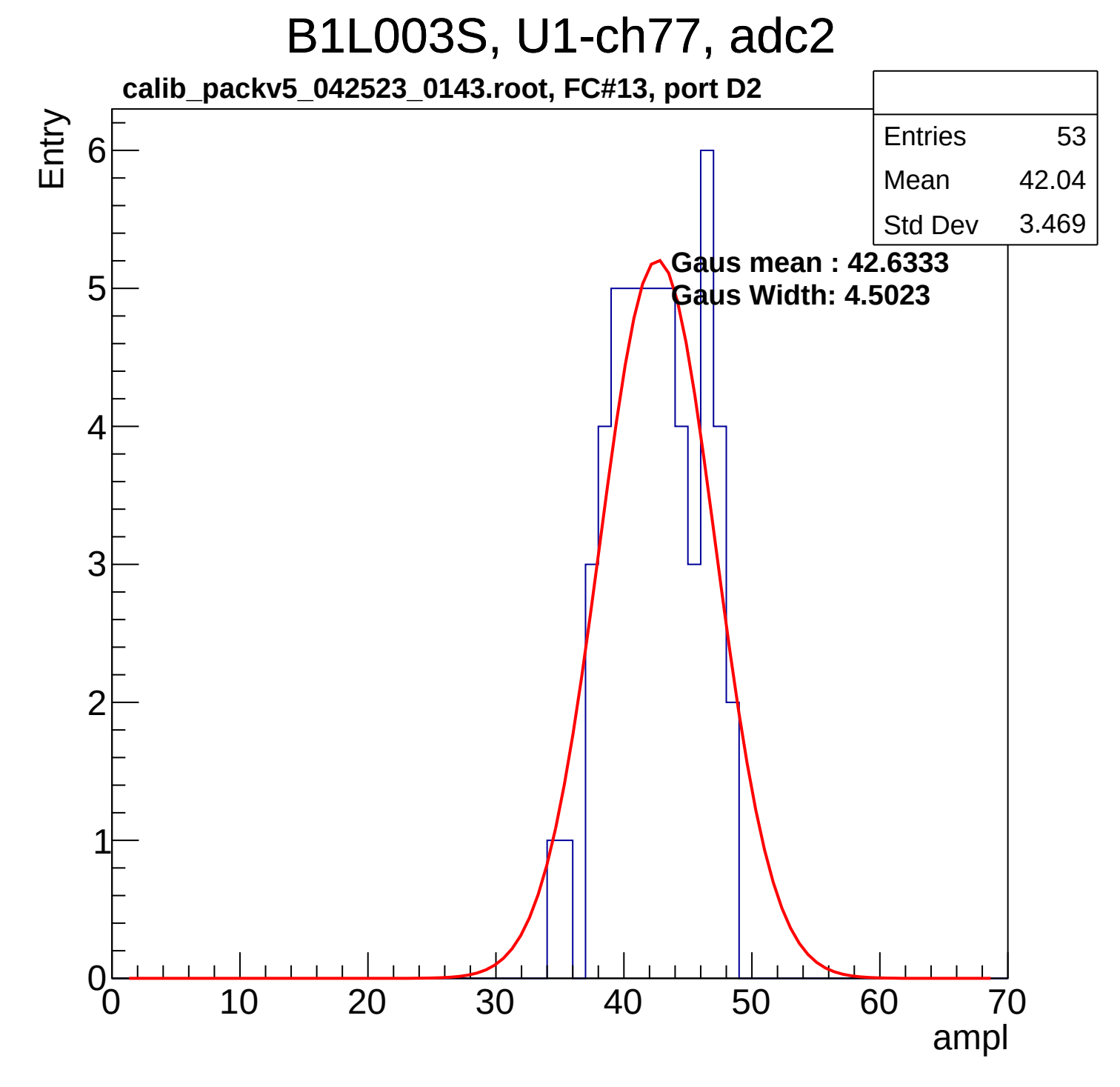

B1L003S, U1-ch76, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 58.35
Std Dev 8.358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

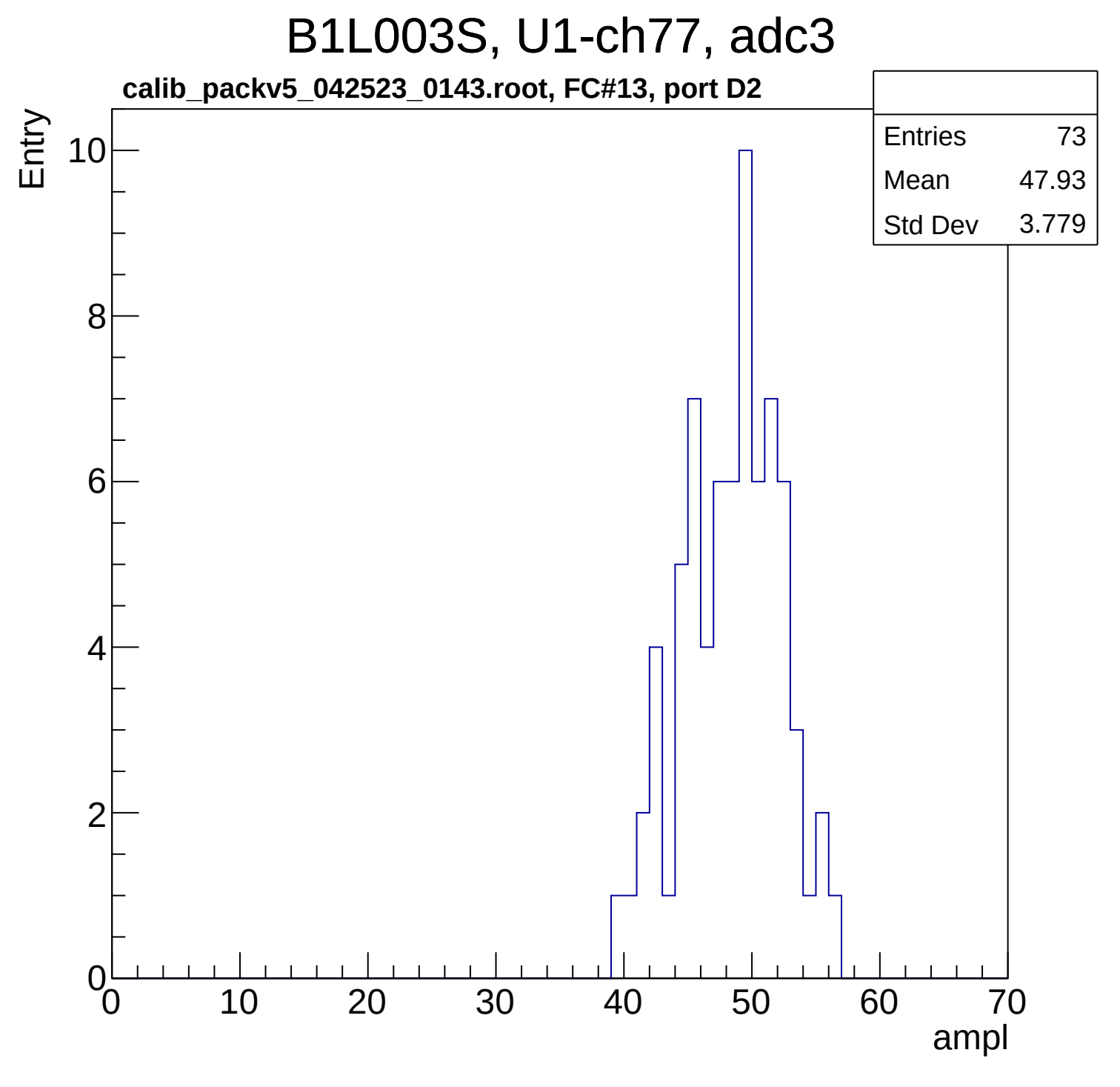

B1L003S, U1-ch76, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.67
Std Dev 1.155
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

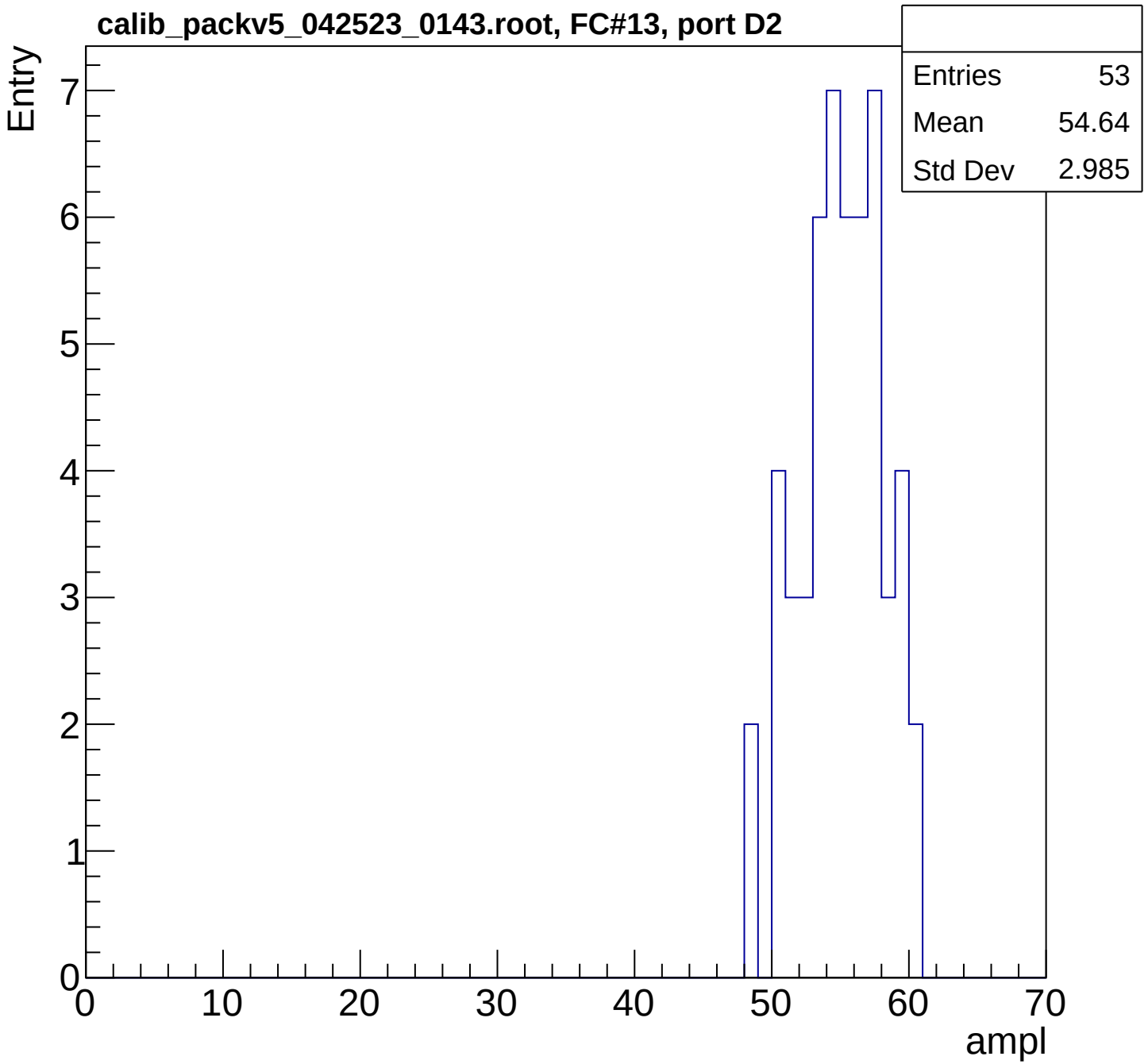

B1L003S, U1-ch76, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

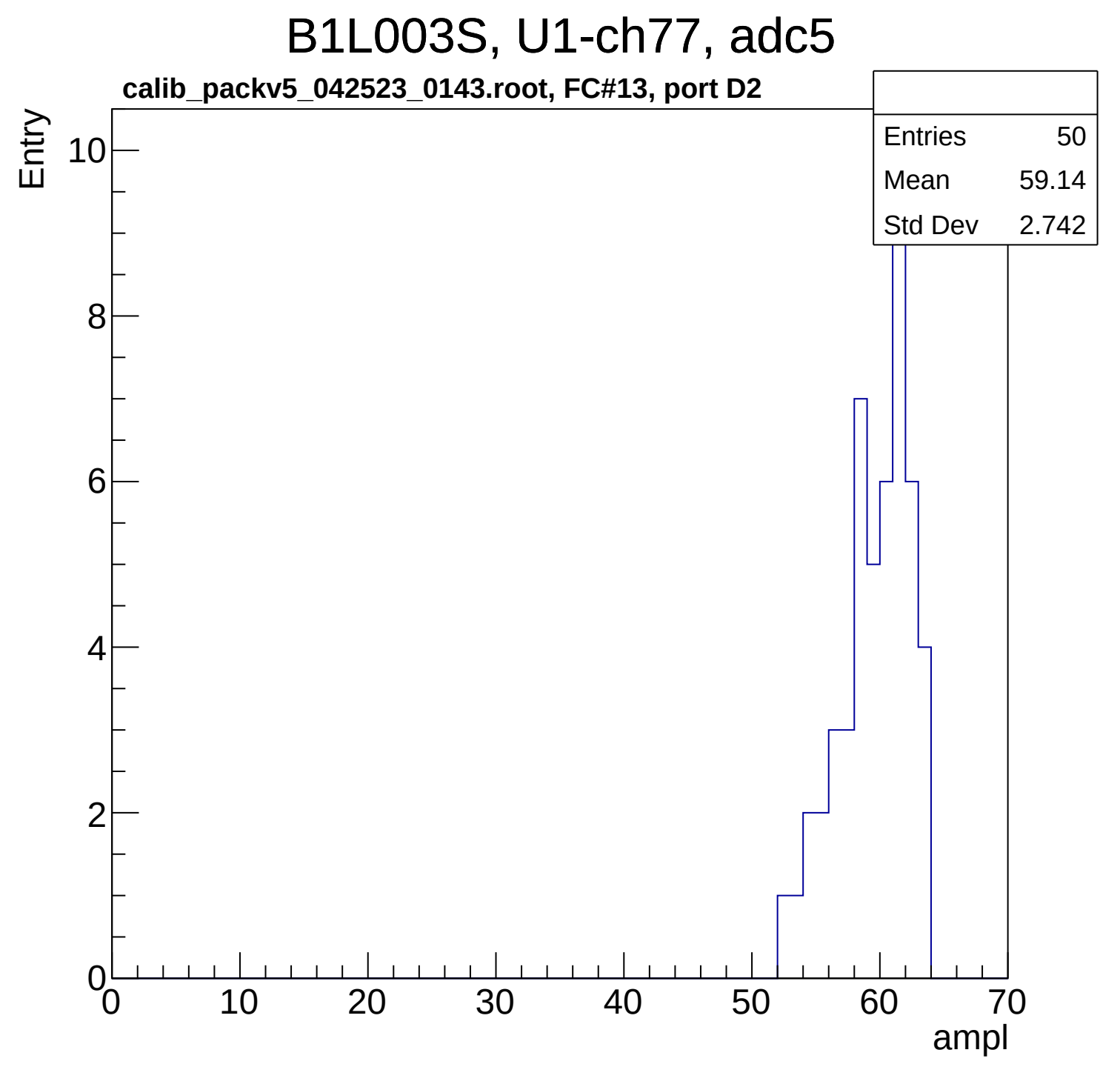

B1L003S, U1-ch77, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 29.06
Std Dev 3.441
Gaus mean : 29.4159
Gaus Width: 4.0965
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

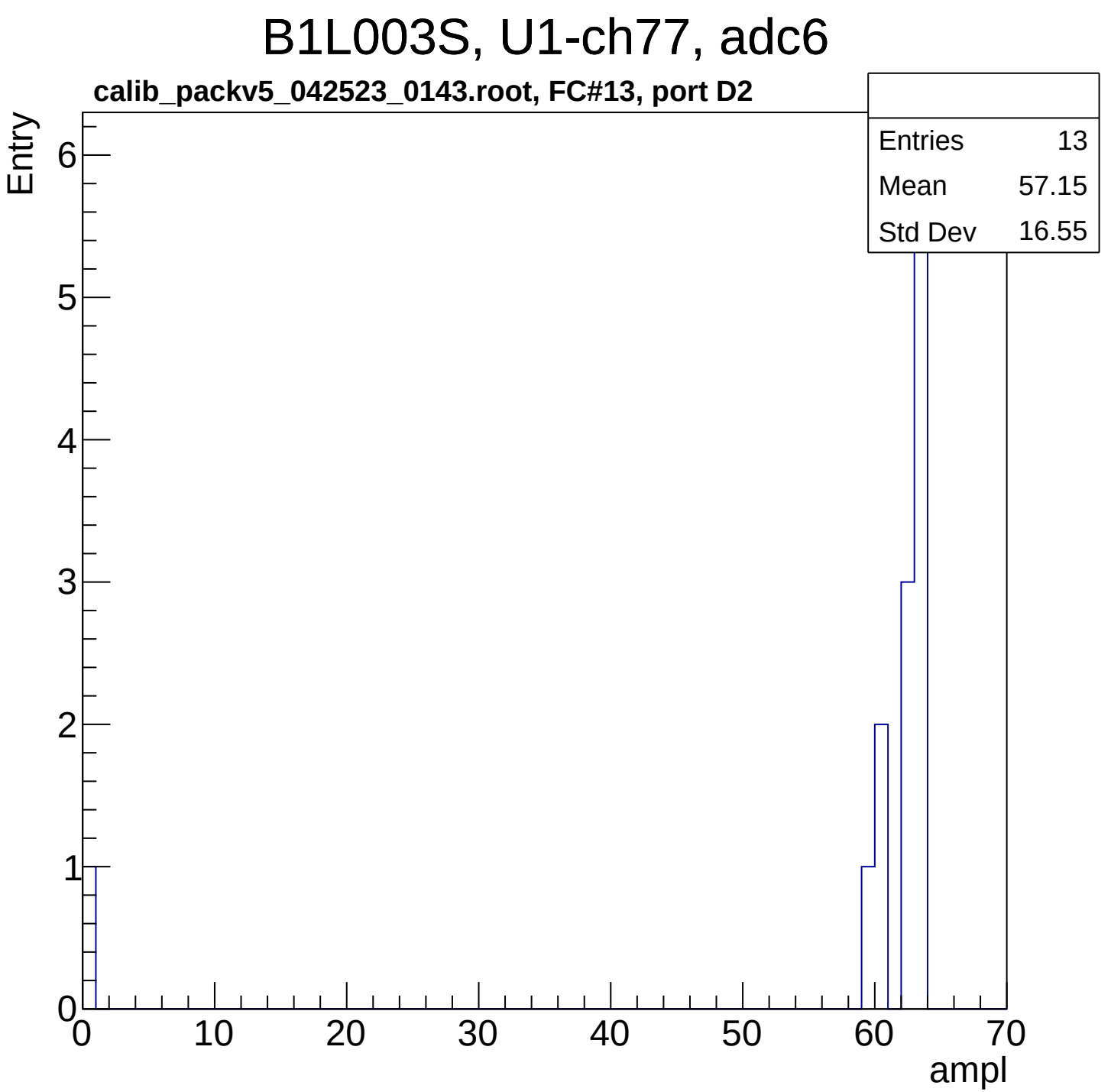

B1L003S, U1-ch77, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 36.22
Std Dev 3.648
Gaus mean : 36.5129
Gaus Width: 3.6215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

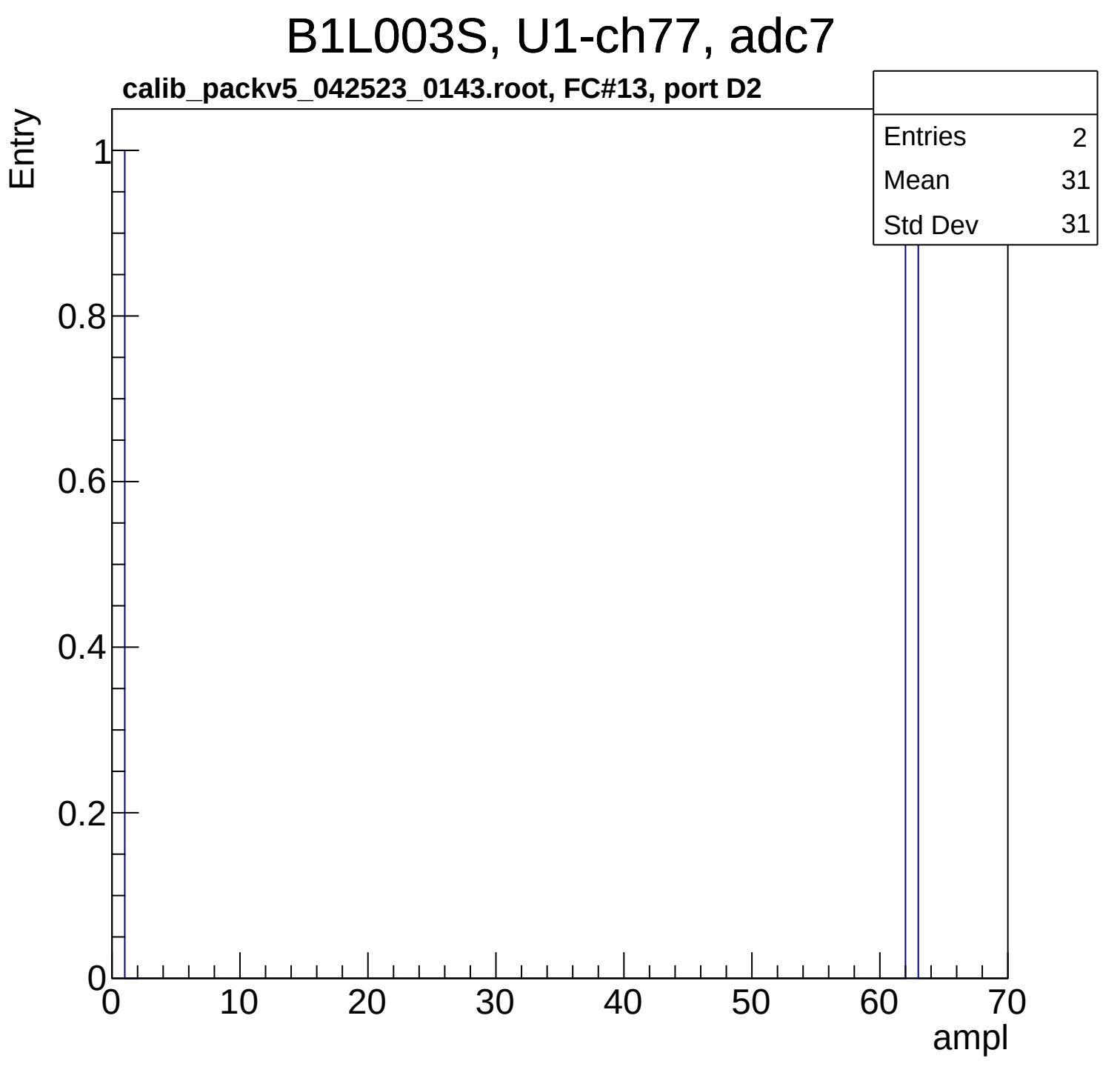

B1L003S, U1-ch77, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.04
Std Dev 3.469
Gaus mean : 42.6333
Gaus Width: 4.5023
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

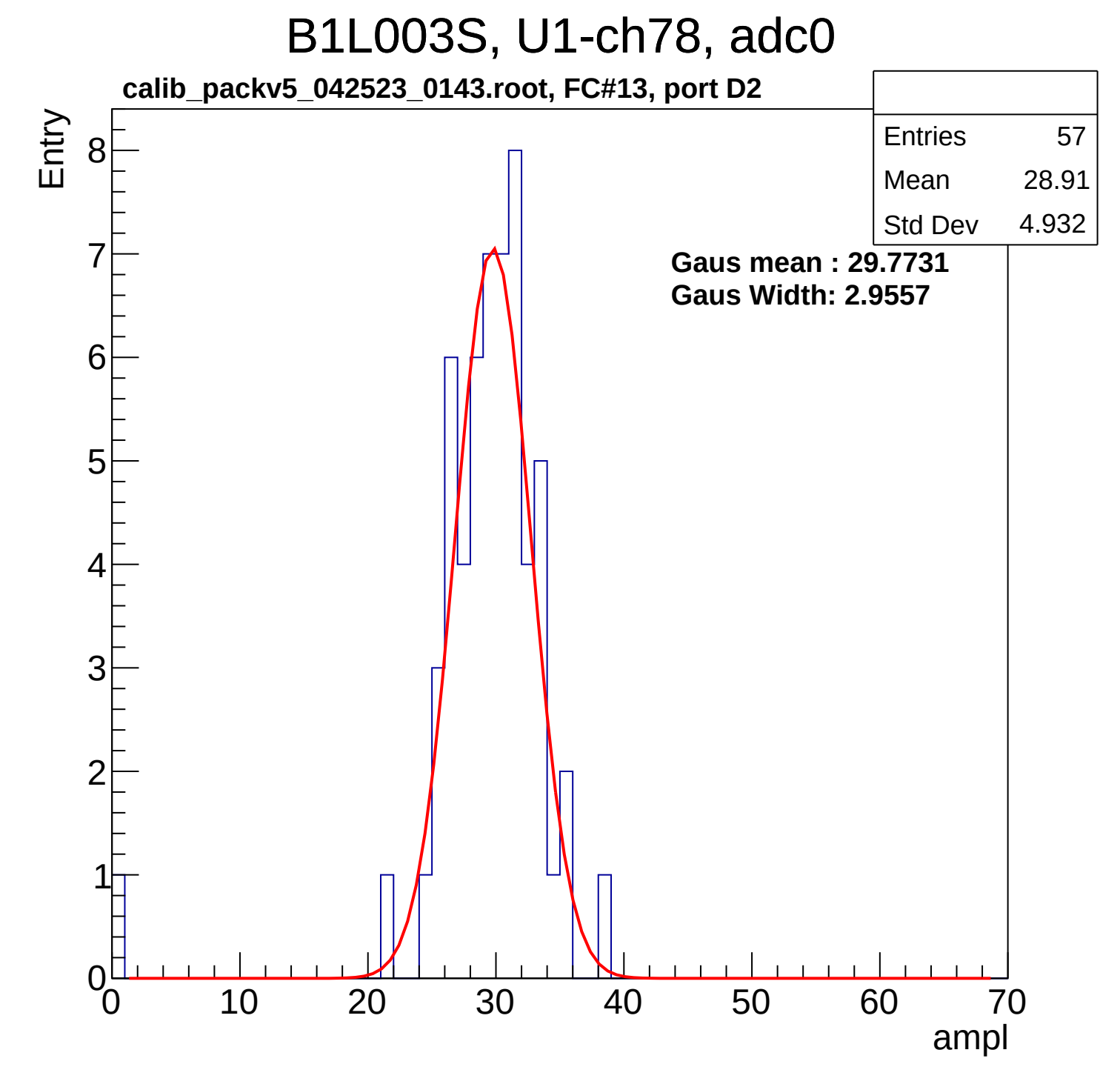

B1L003S, U1-ch77, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 47.93
Std Dev 3.779
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

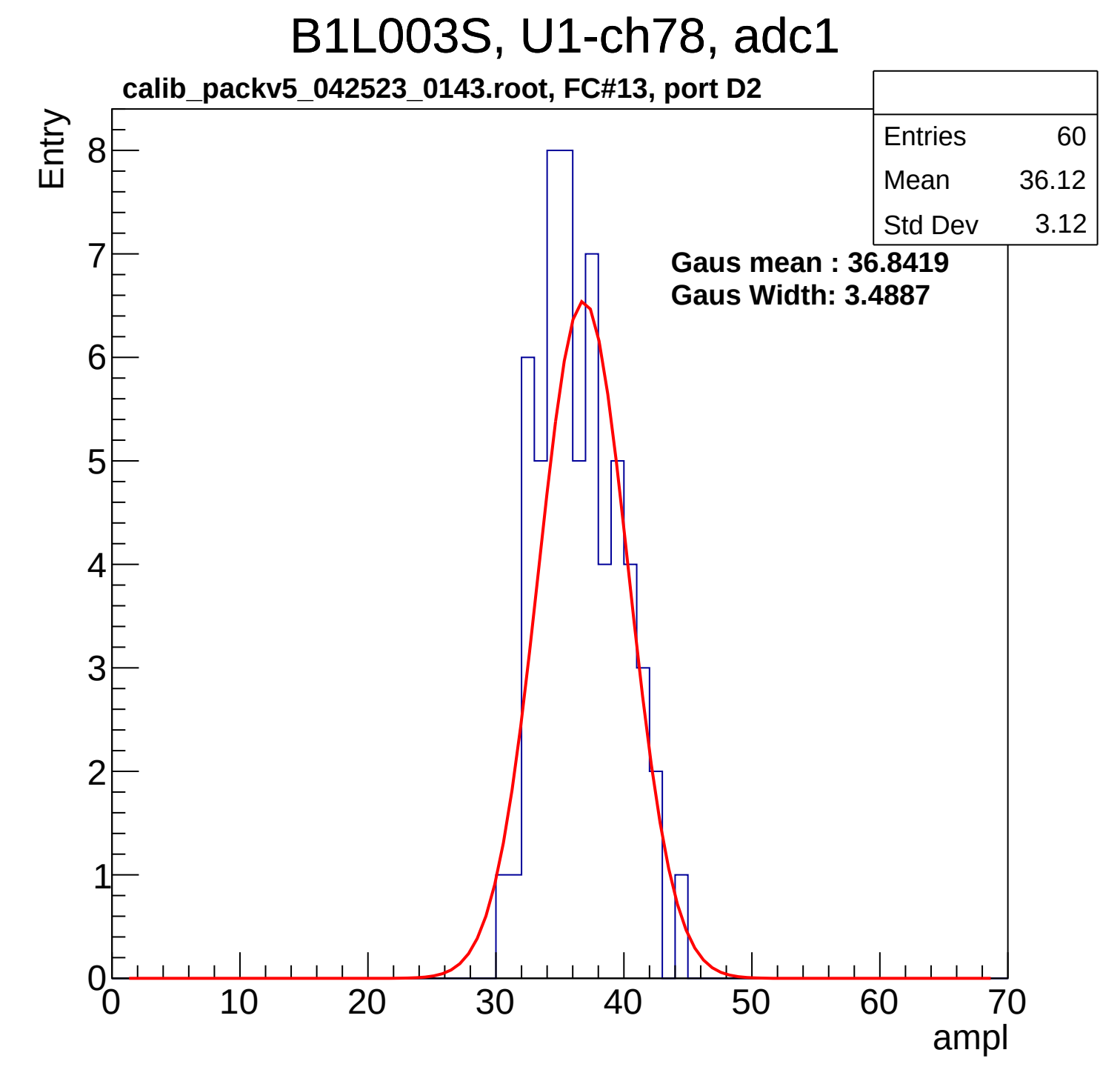

B1L003S, U1-ch77, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.64
Std Dev 2.985
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

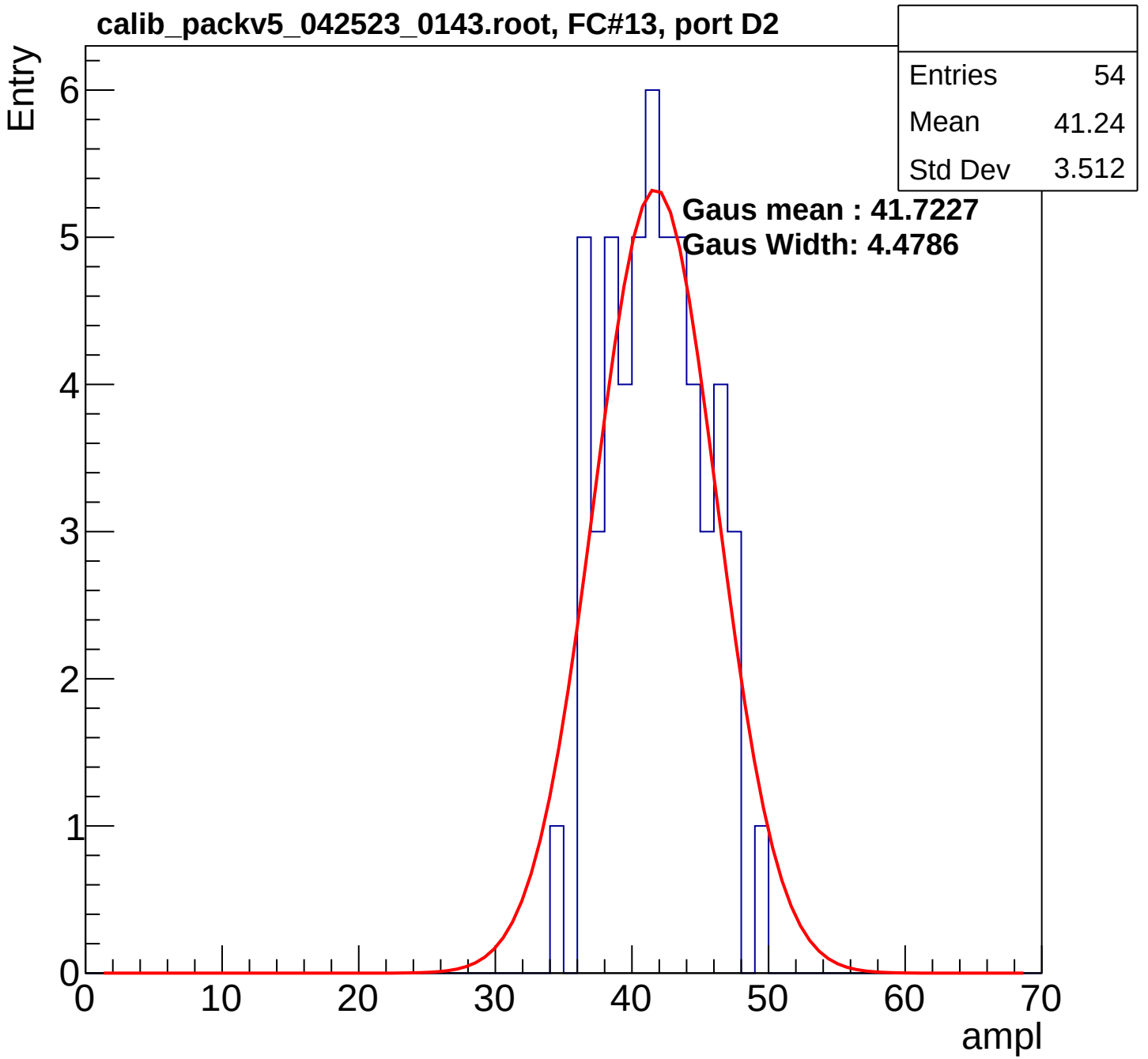

B1L003S, U1-ch77, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.14
Std Dev 2.742
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

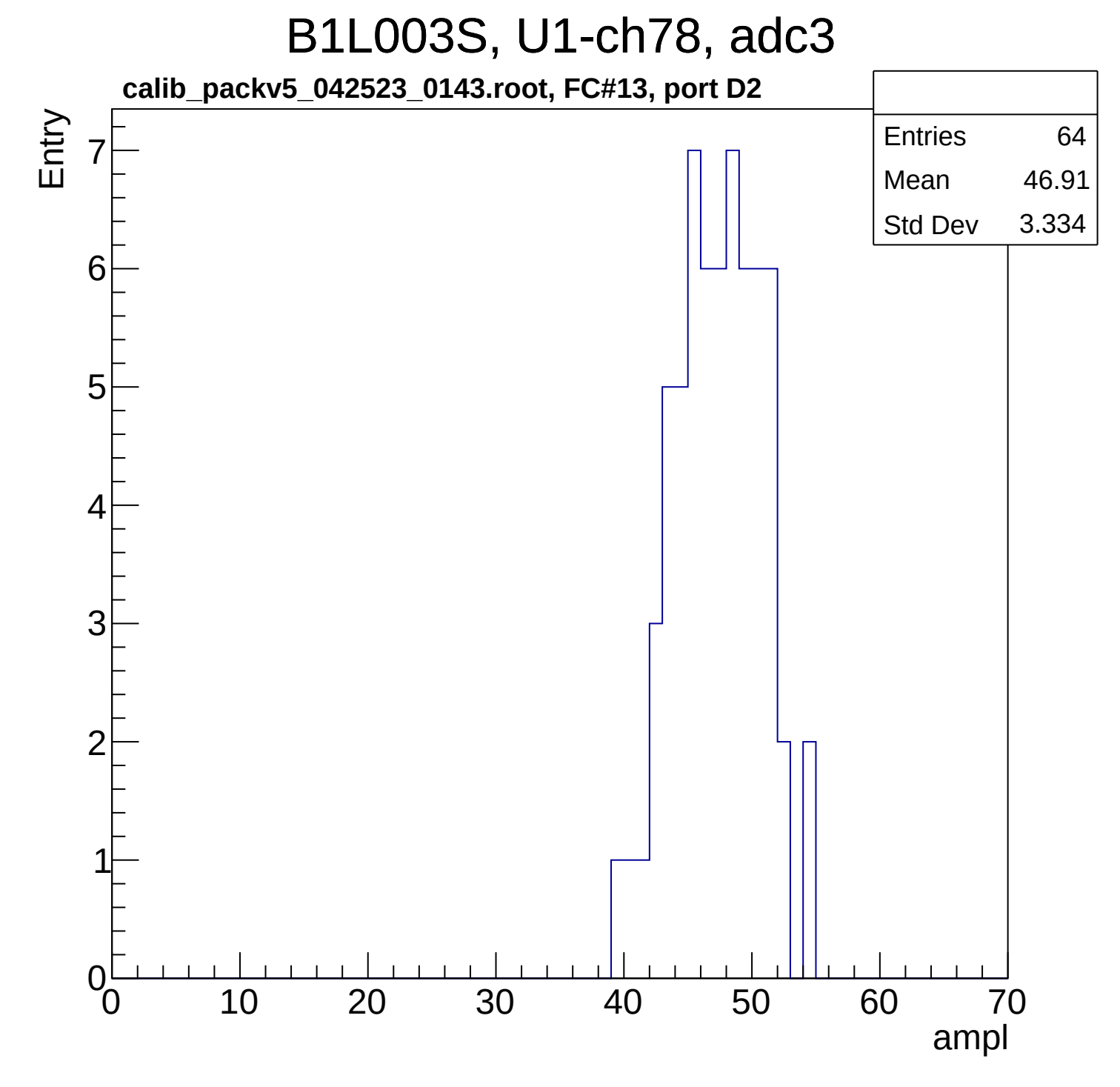

B1L003S, U1-ch77, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 57.15
Std Dev 16.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

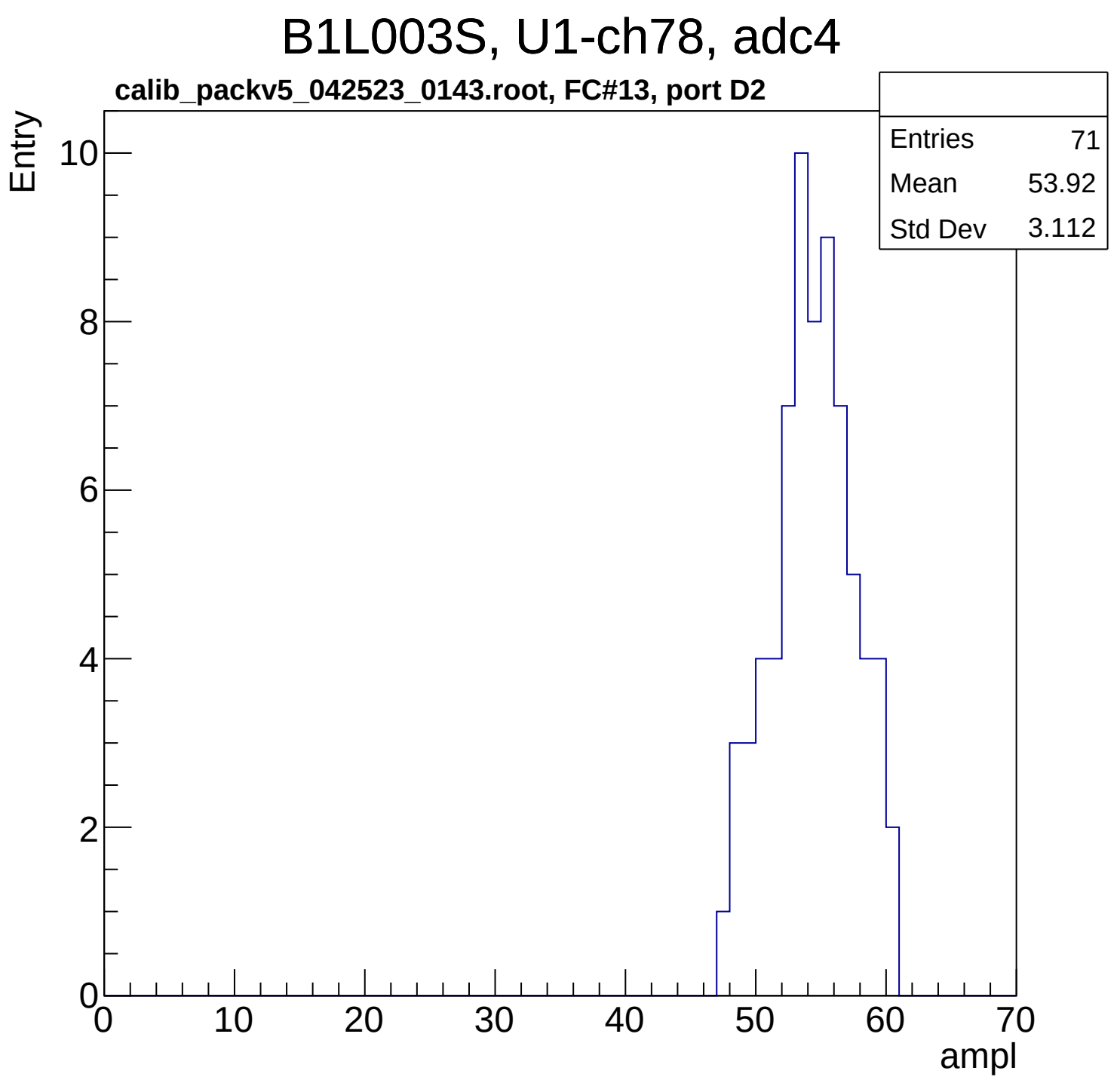

B1L003S, U1-ch77, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

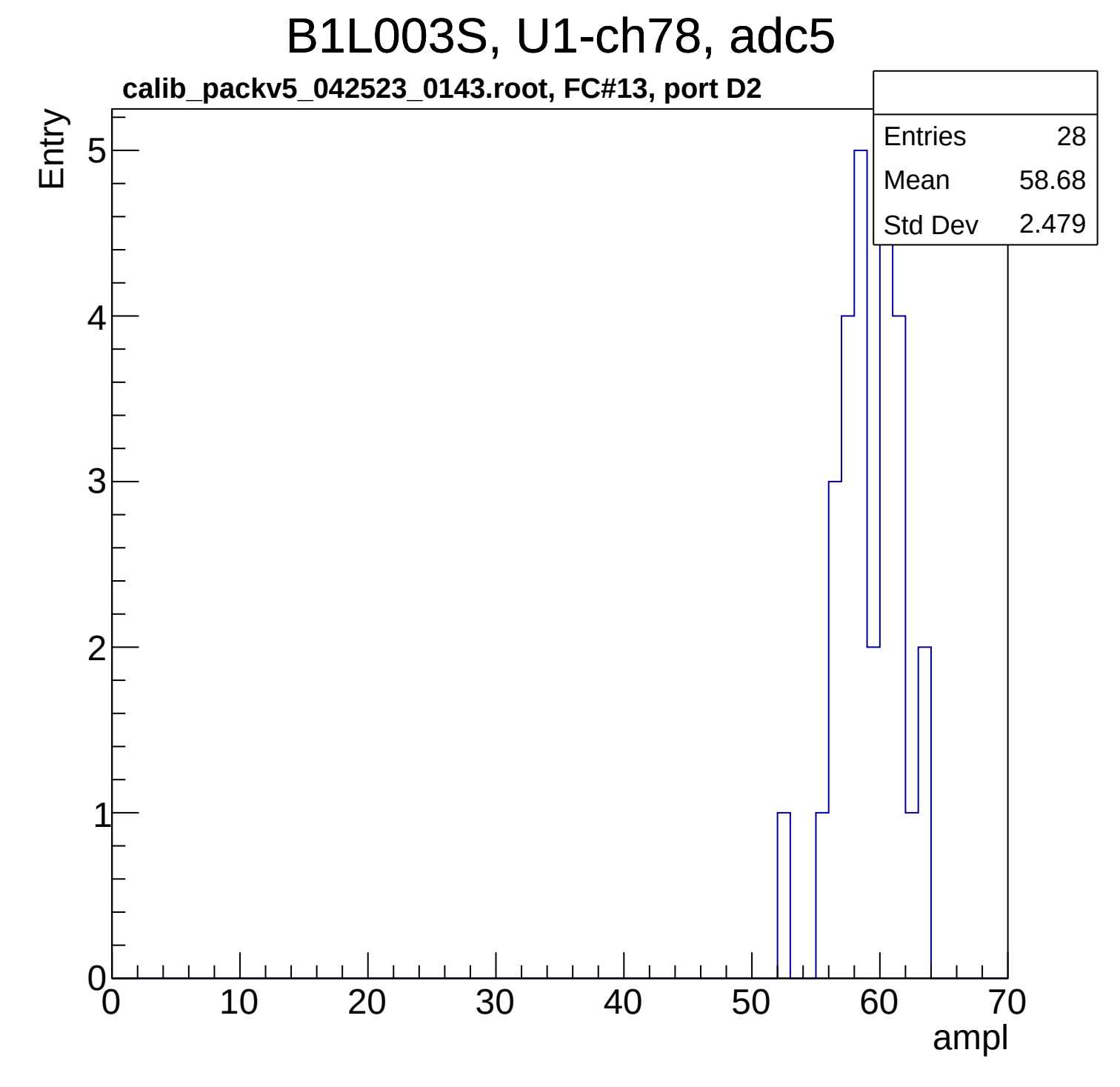

B1L003S, U1-ch78, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 28.91
Std Dev 4.932
Gaus mean : 29.7731
Gaus Width: 2.9557
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

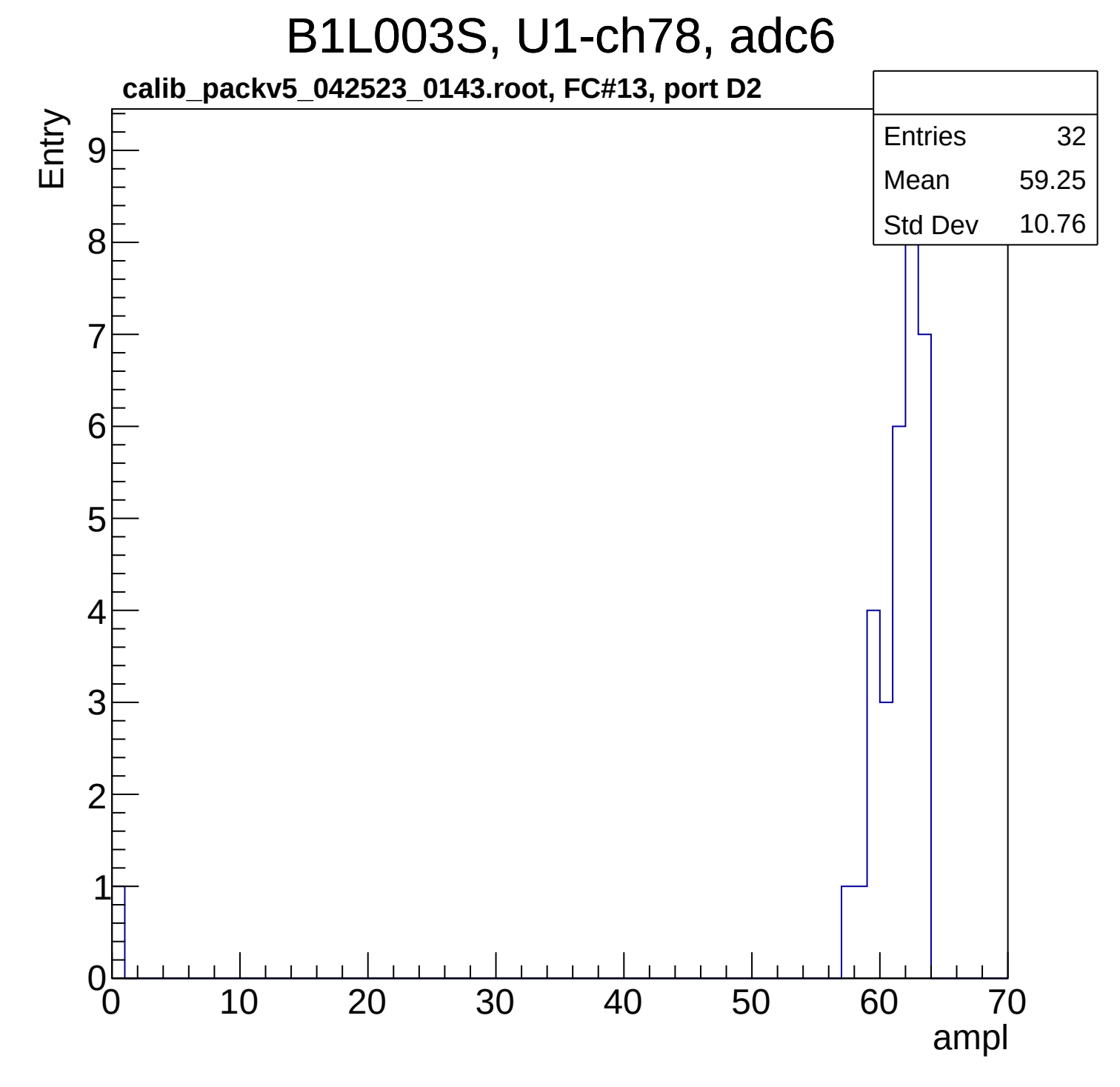

B1L003S, U1-ch78, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.12
Std Dev 3.12
Gaus mean : 36.8419
Gaus Width: 3.4887
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch78, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.24
Std Dev 3.512
Gaus mean : 41.7227
Gaus Width: 4.4786
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch78, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 46.91
Std Dev 3.334
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch78, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 53.92
Std Dev 3.112
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch78, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 28
Mean 58.68
Std Dev 2.479
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L003S, U1-ch78, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 32
Mean 59.25
Std Dev 10.76
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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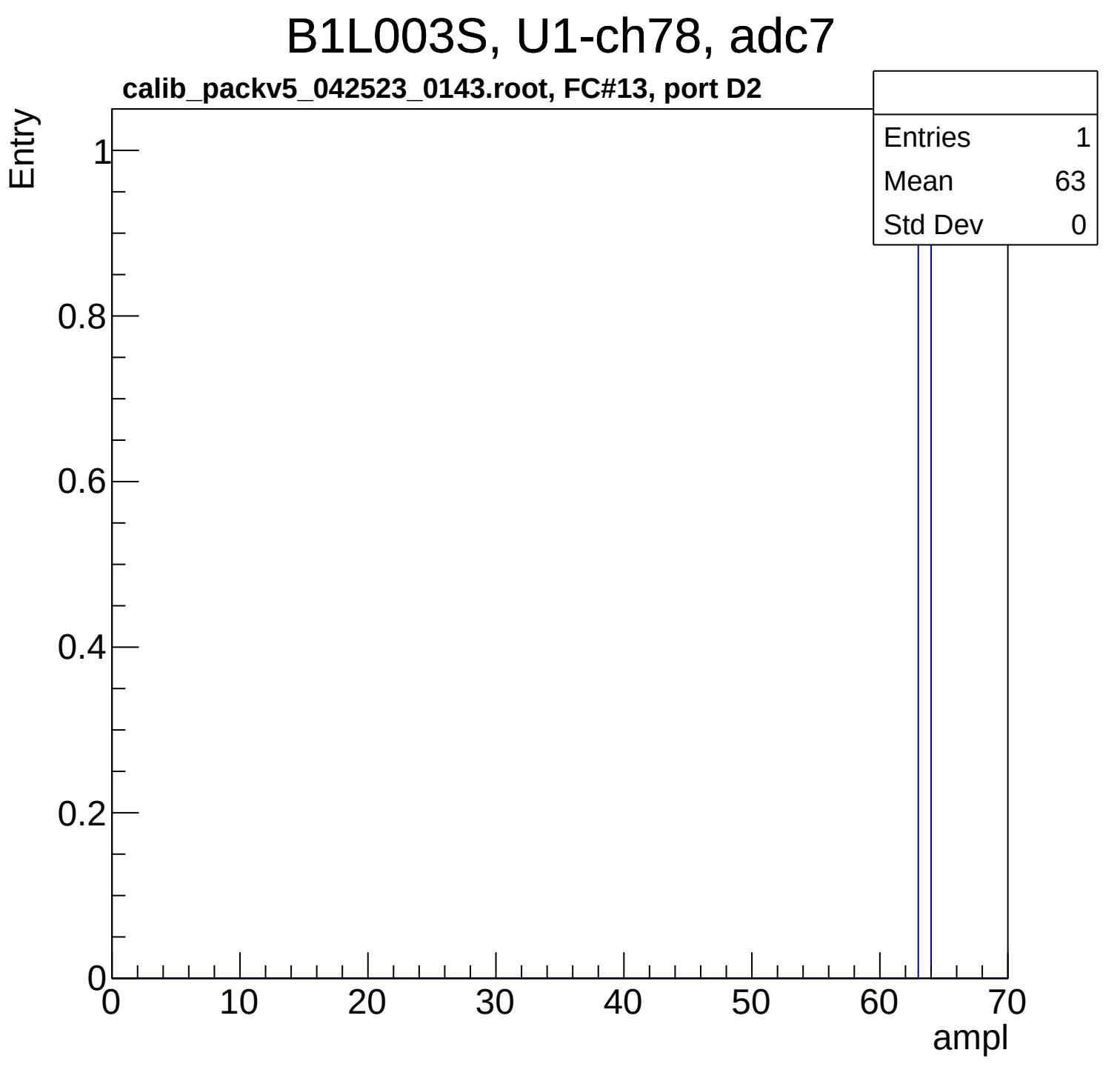
40
50
60
70


B1L003S, U1-ch78, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
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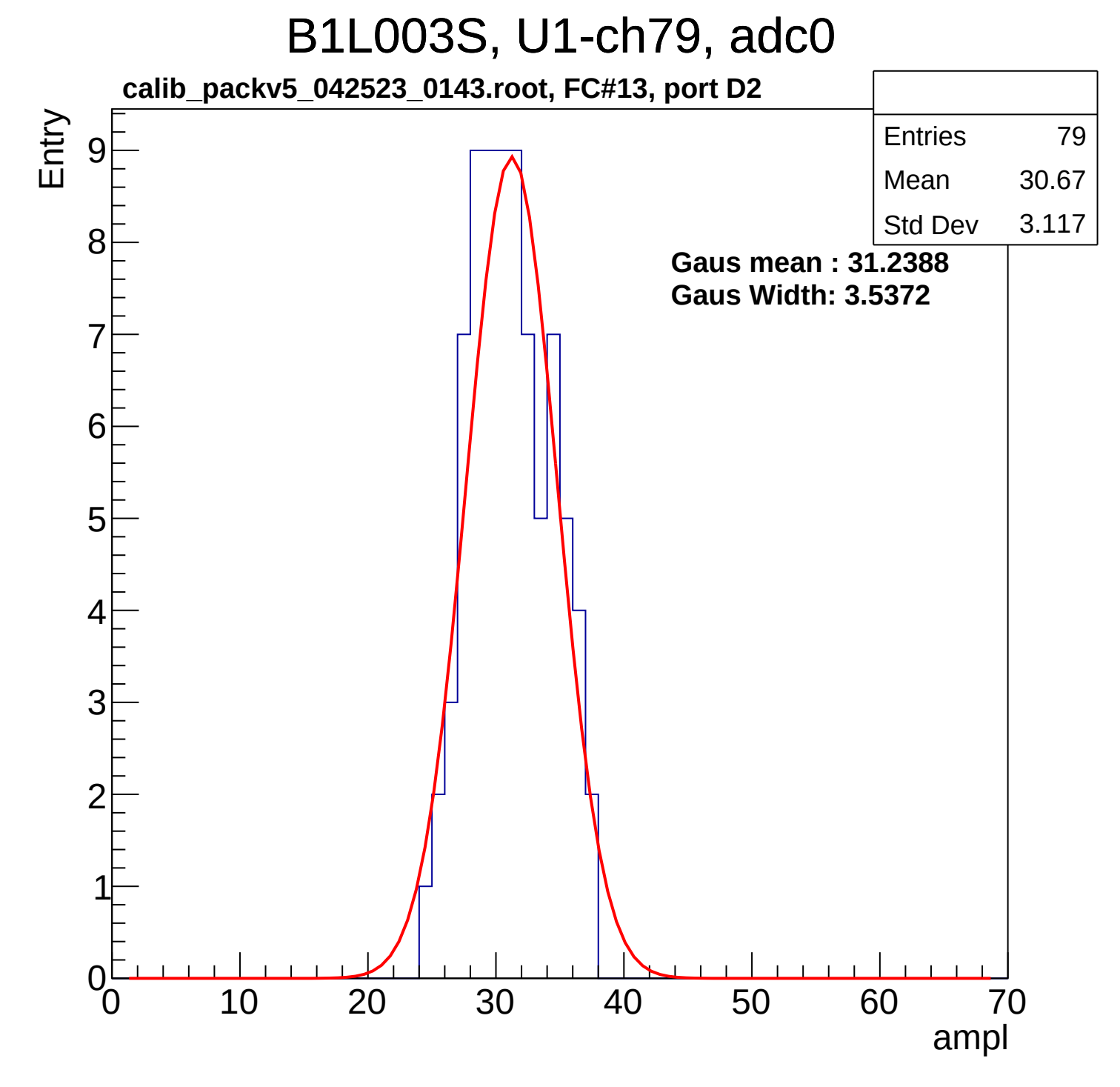
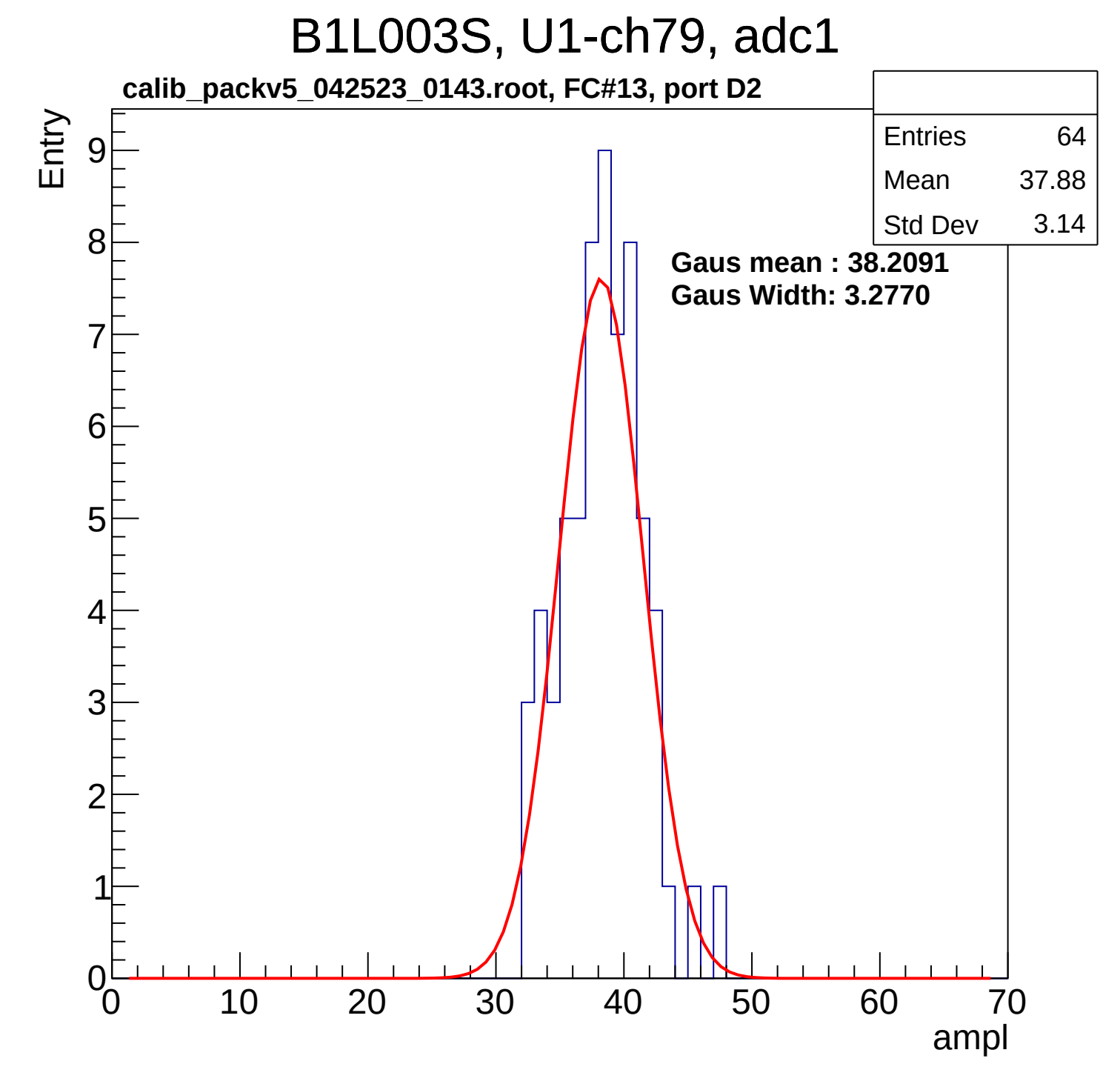
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch79, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 30.67
Std Dev 3.117
Gaus mean : 31.2388
Gaus Width: 3.5372
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch79, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 37.88
Std Dev 3.14
Gaus mean : 38.2091
Gaus Width: 3.2770
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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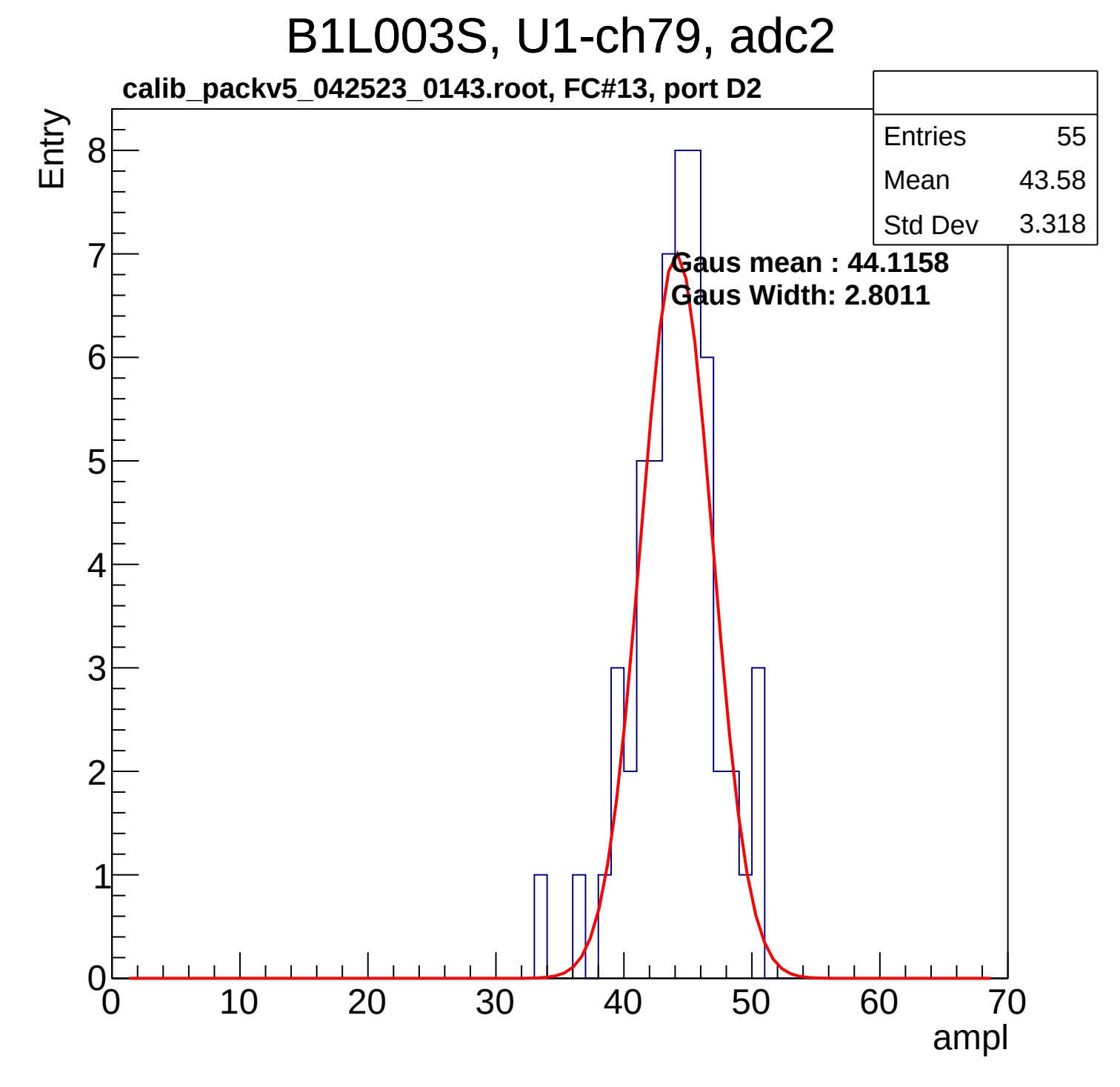
B1L003S, U1-ch79, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 43.58
Std Dev 3.318
Gaus mean : 44.1158
Gaus Width: 2.8011
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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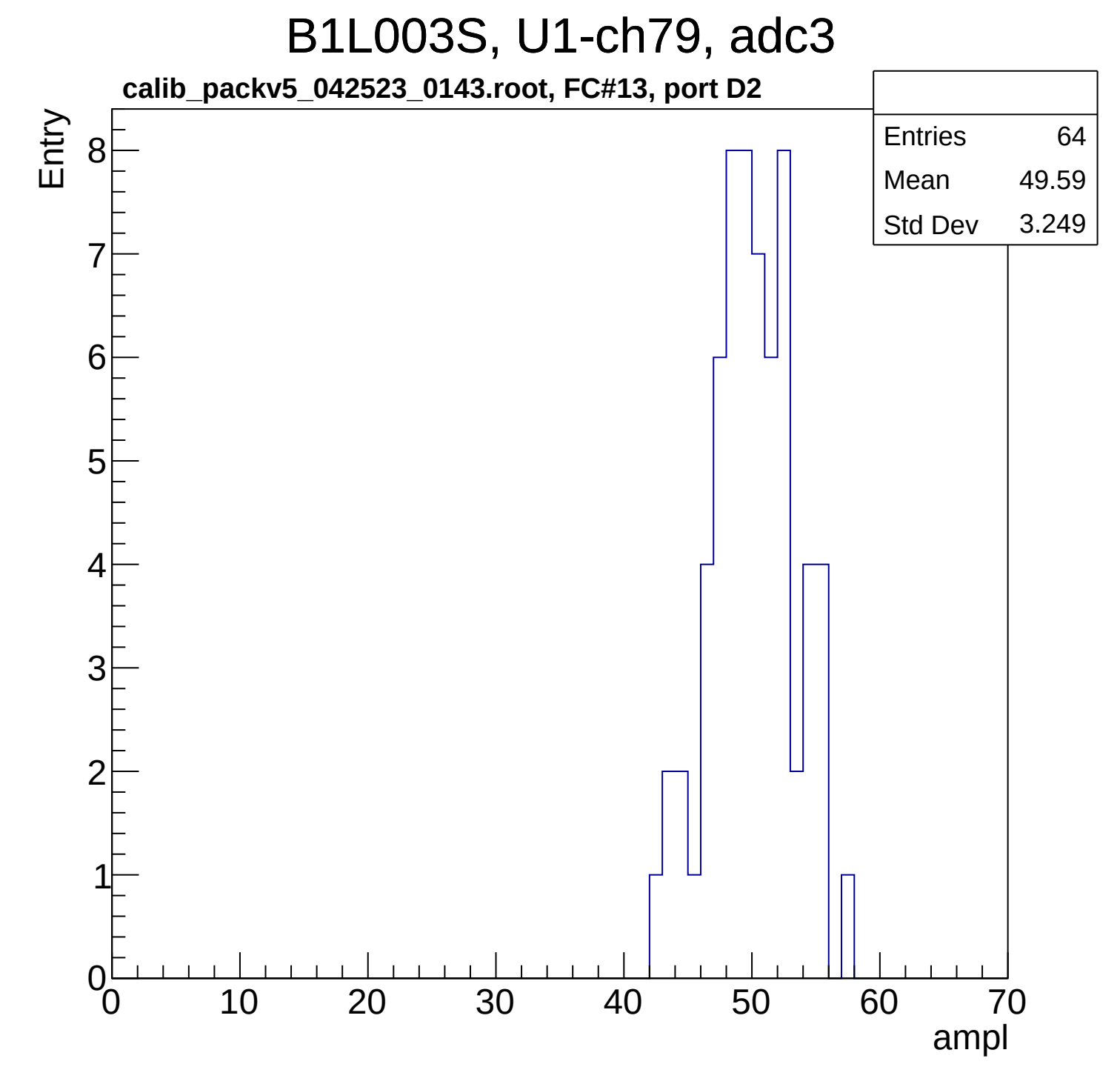
B1L003S, U1-ch79, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 49.59
Std Dev 3.249
Entry
ampl
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1
2
3
4
5
6
7
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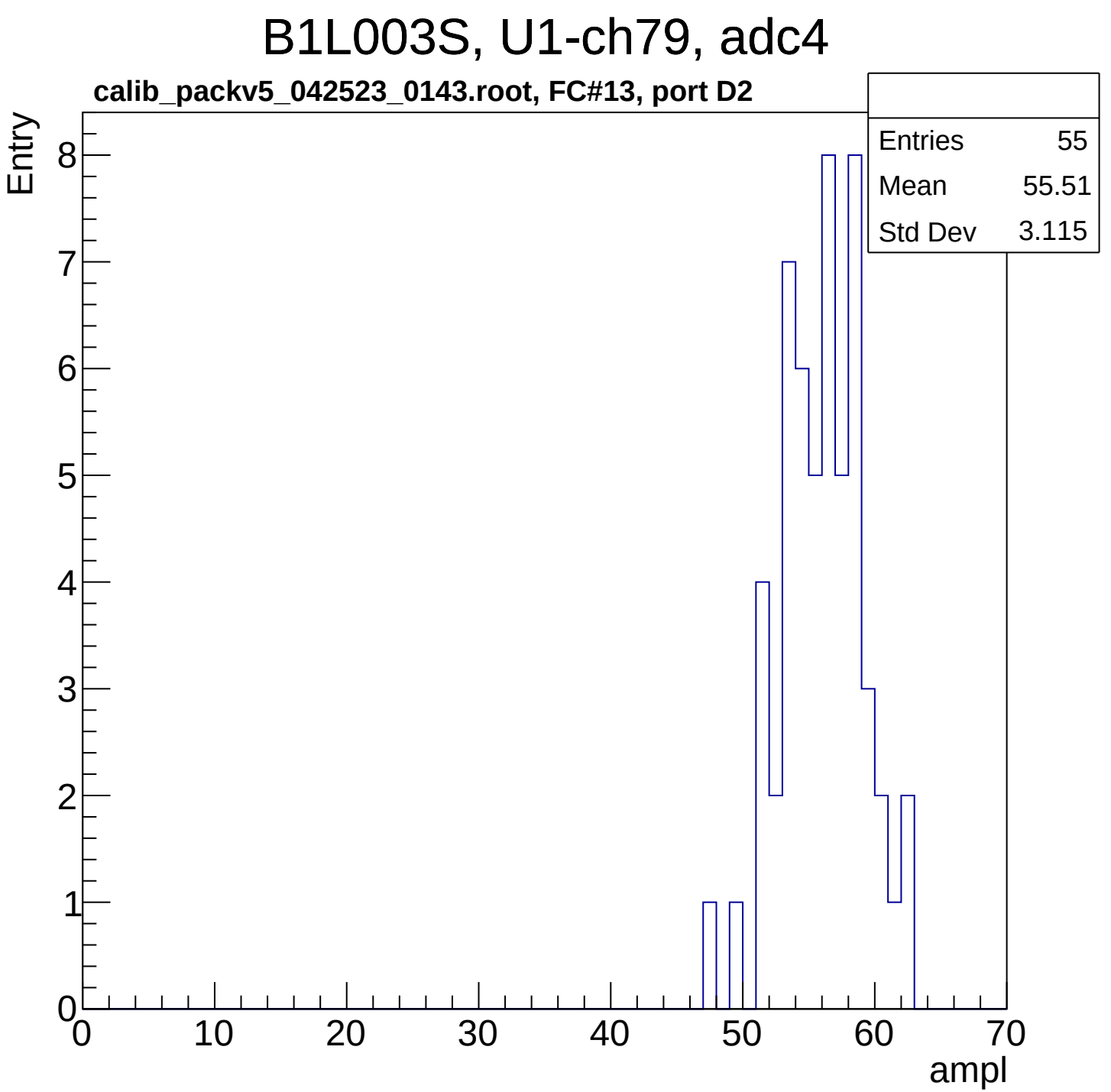
0
10
20
30
40
50
60
70


B1L003S, U1-ch79, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 55.51
Std Dev 3.115
Entry
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2
3
4
5
6
7
8
0
10
20
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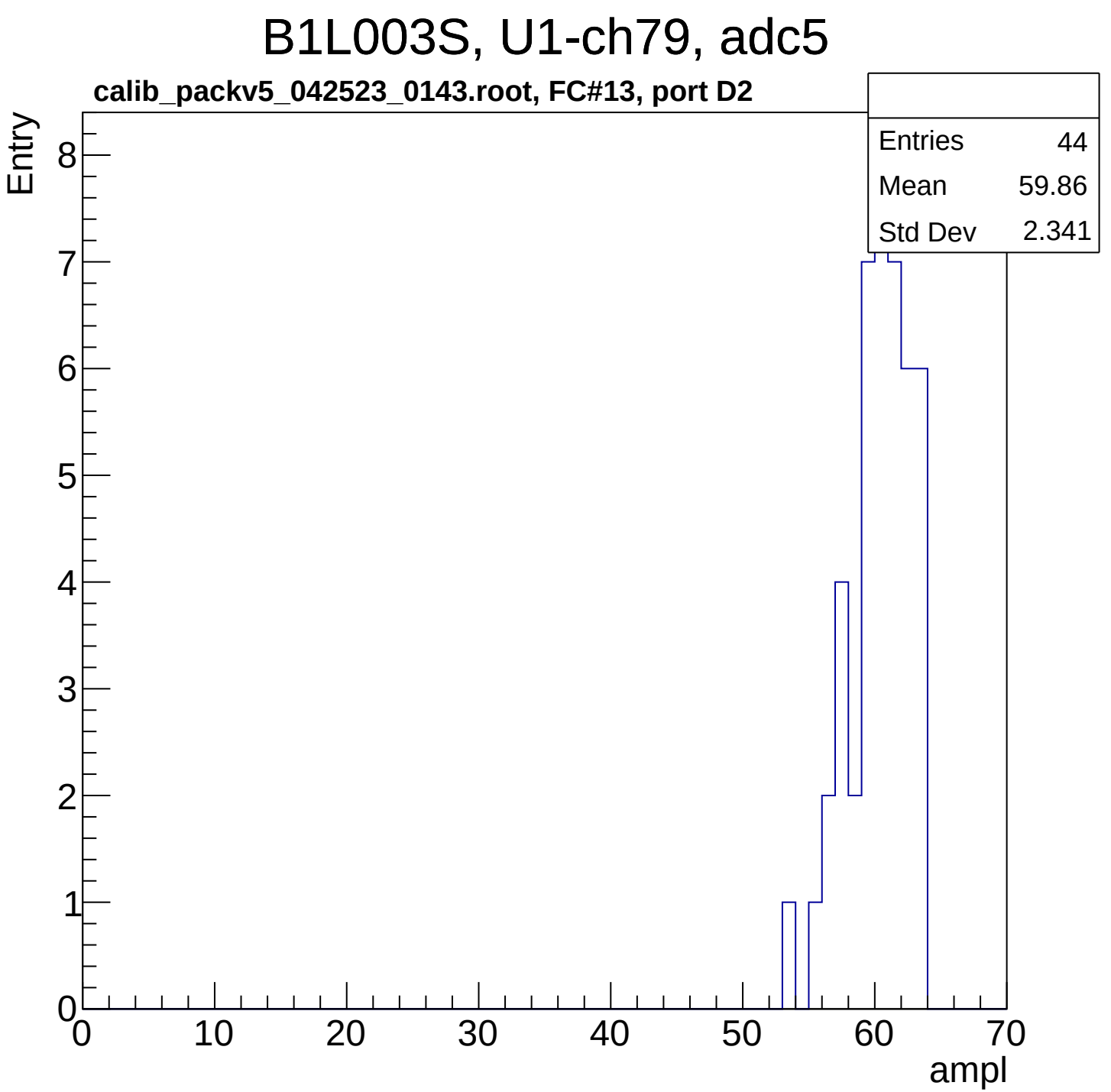
40
50
60
70
ampl


B1L003S, U1-ch79, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 44
Mean 59.86
Std Dev 2.341
Entry
ampl
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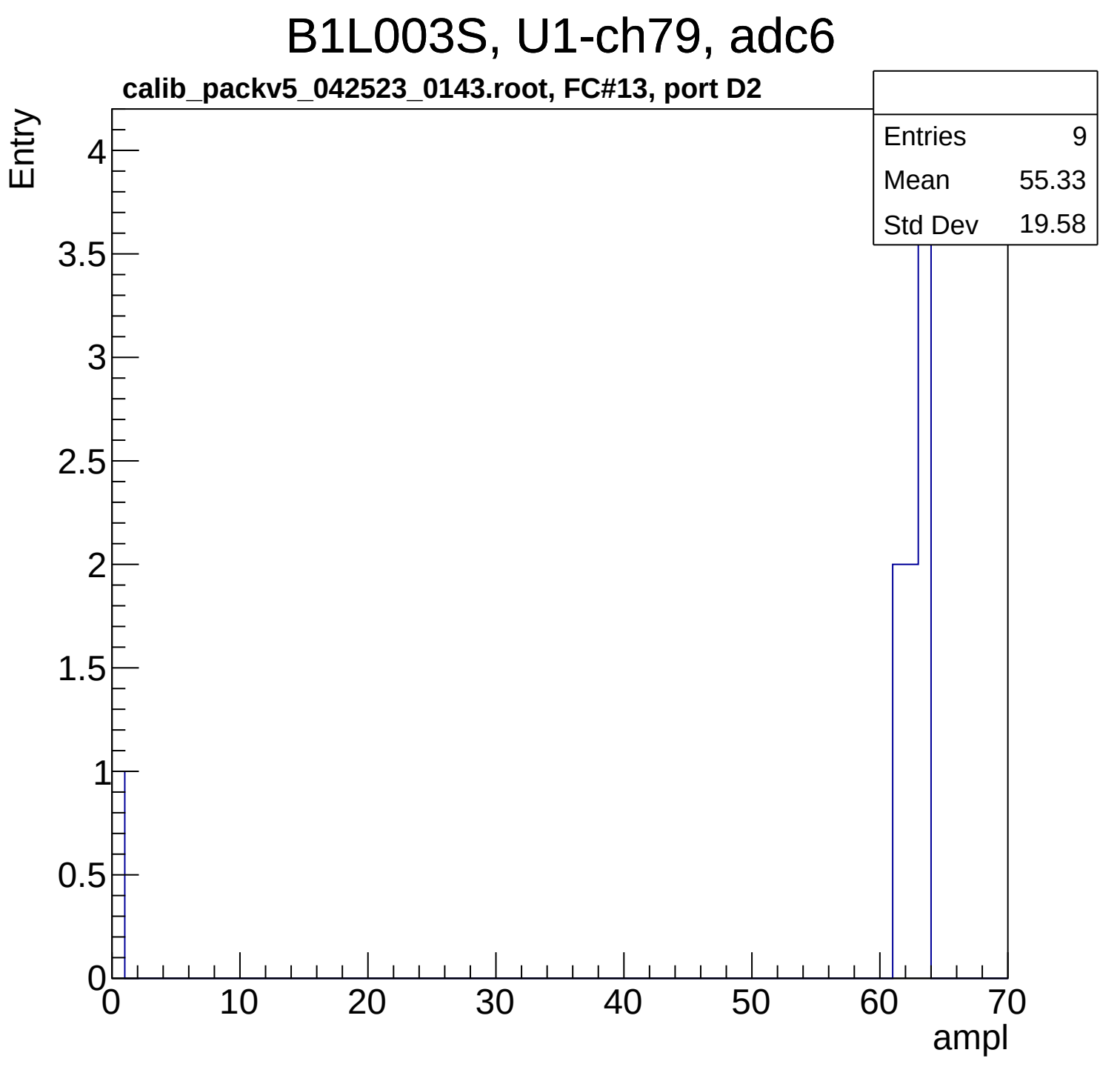
B1L003S, U1-ch79, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 55.33
Std Dev 19.58
Entry
ampl
0
0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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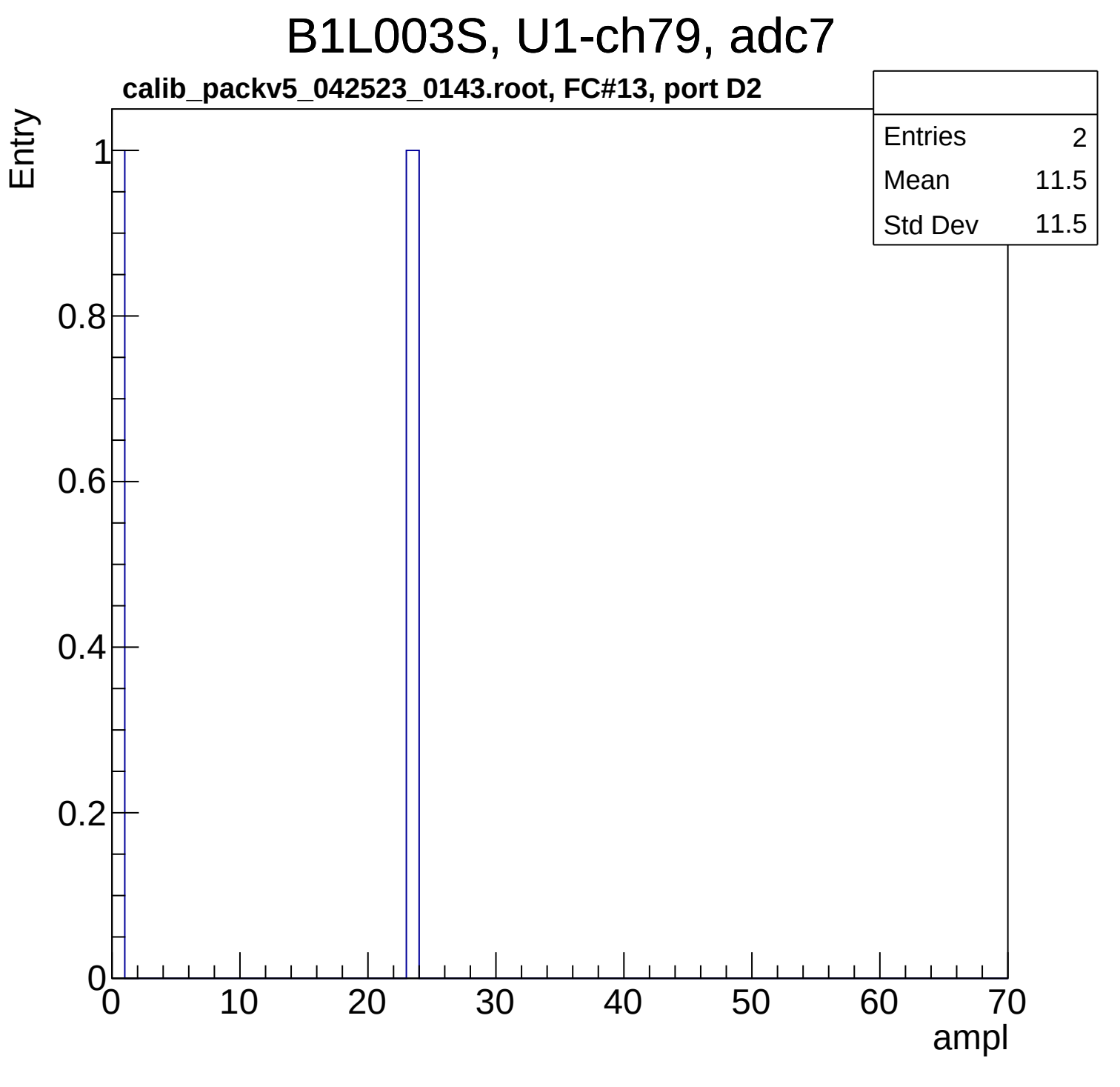
B1L003S, U1-ch79, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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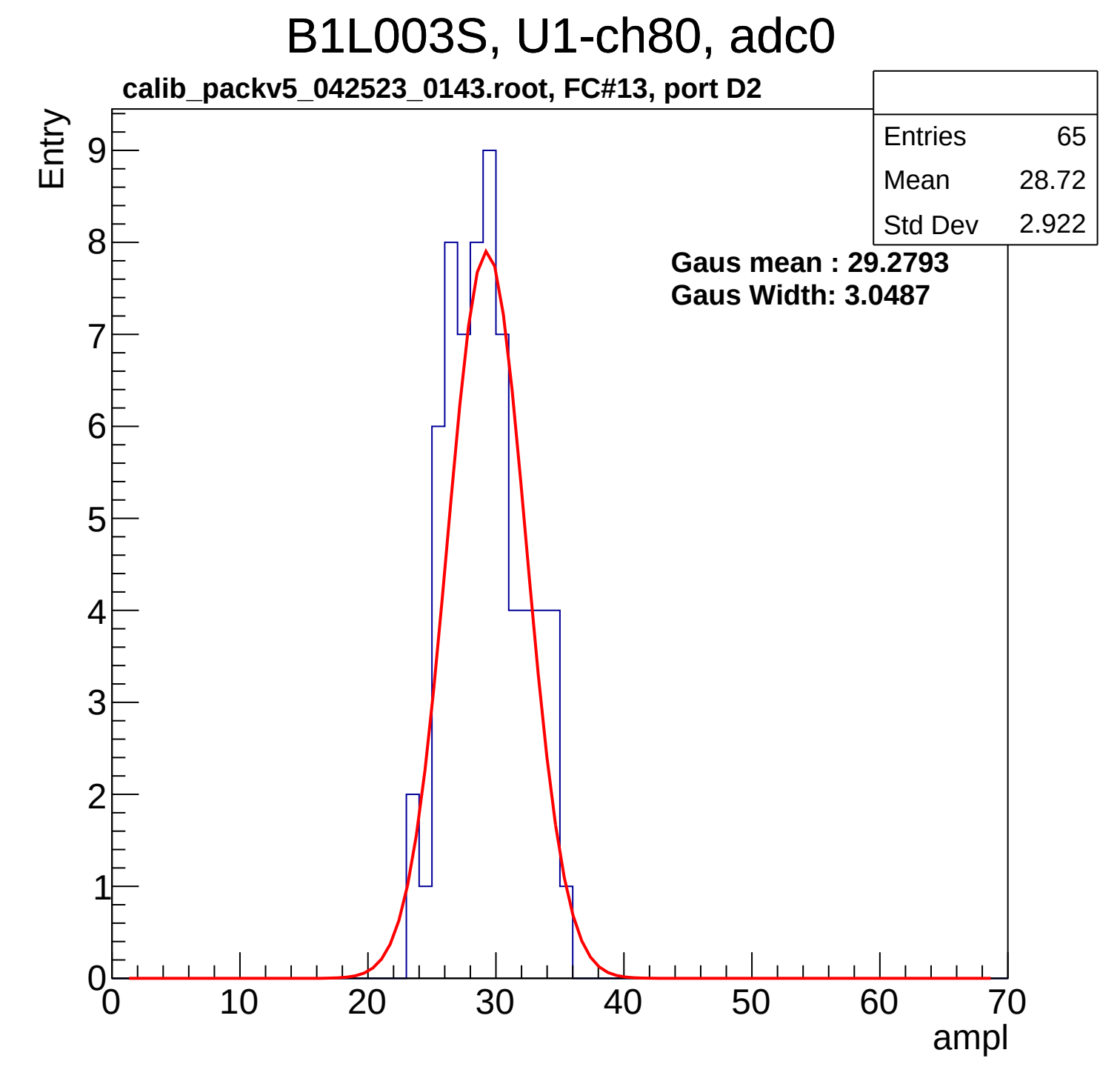
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch80, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.72
Std Dev 2.922
Gaus mean : 29.2793
Gaus Width: 3.0487
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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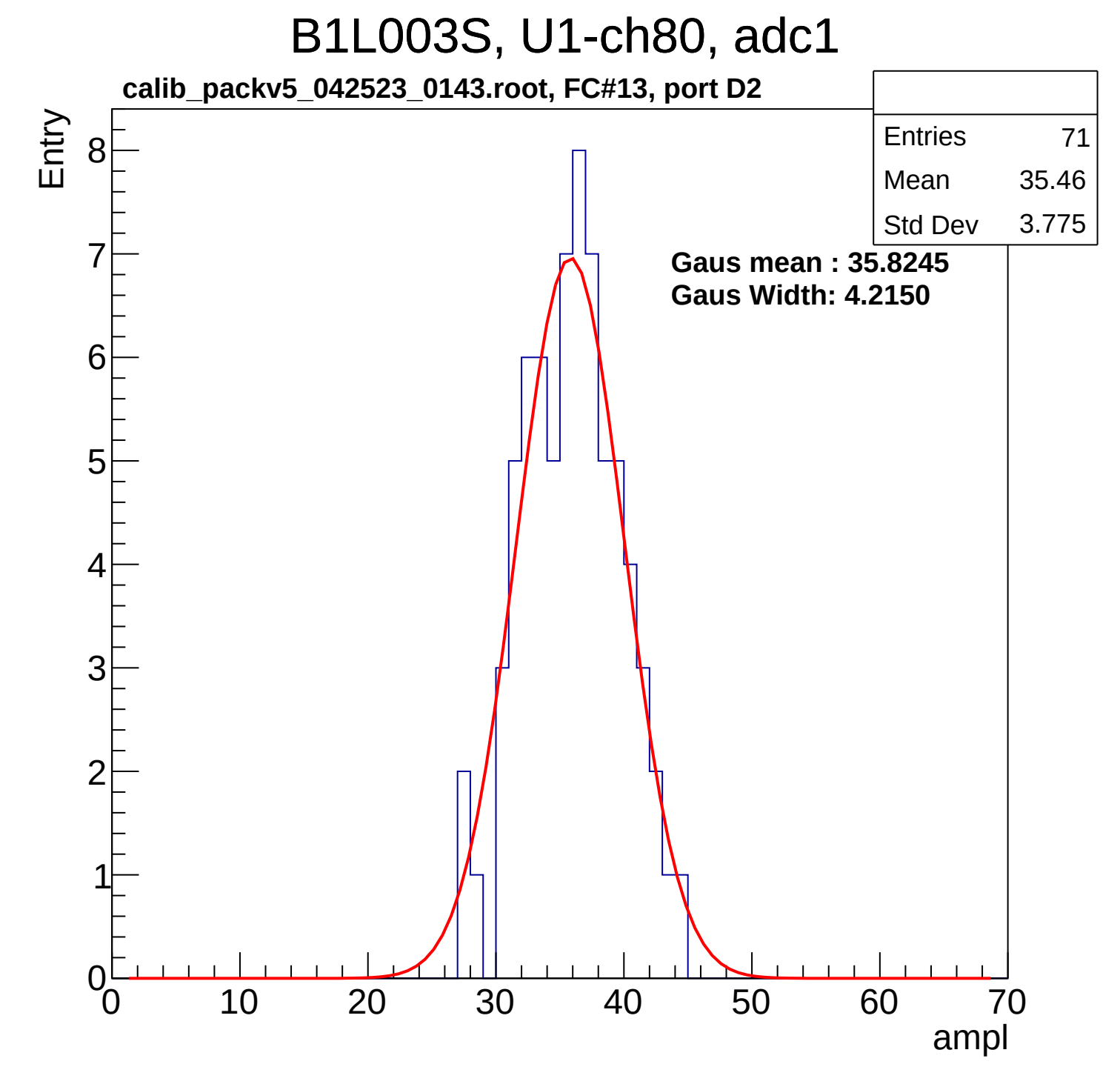
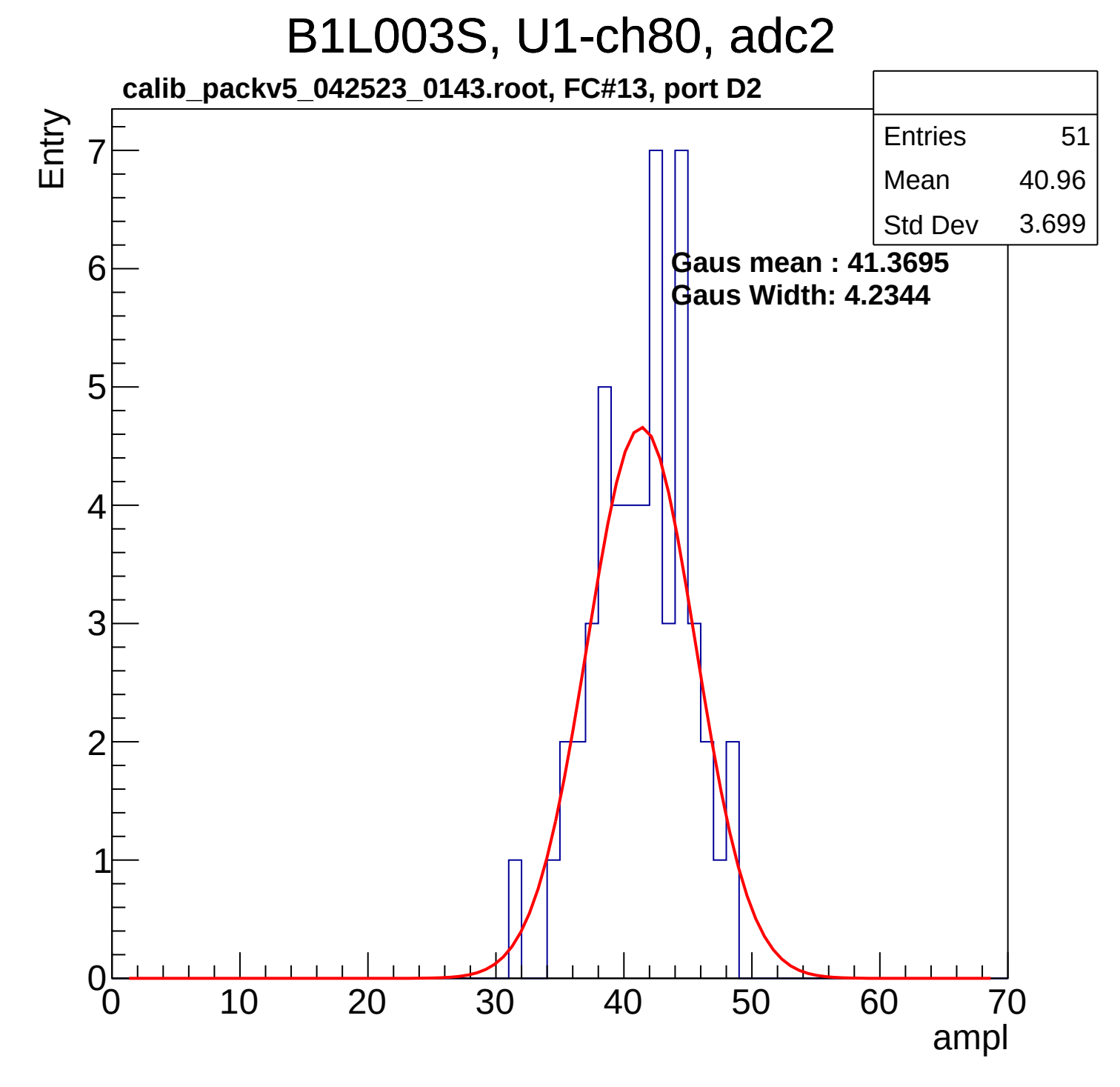

B1L003S, U1-ch80, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 35.46
Std Dev 3.775
Gaus mean : 35.8245
Gaus Width: 4.2150
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch80, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 40.96
Std Dev 3.699
Gaus mean : 41.3695
Gaus Width: 4.2344
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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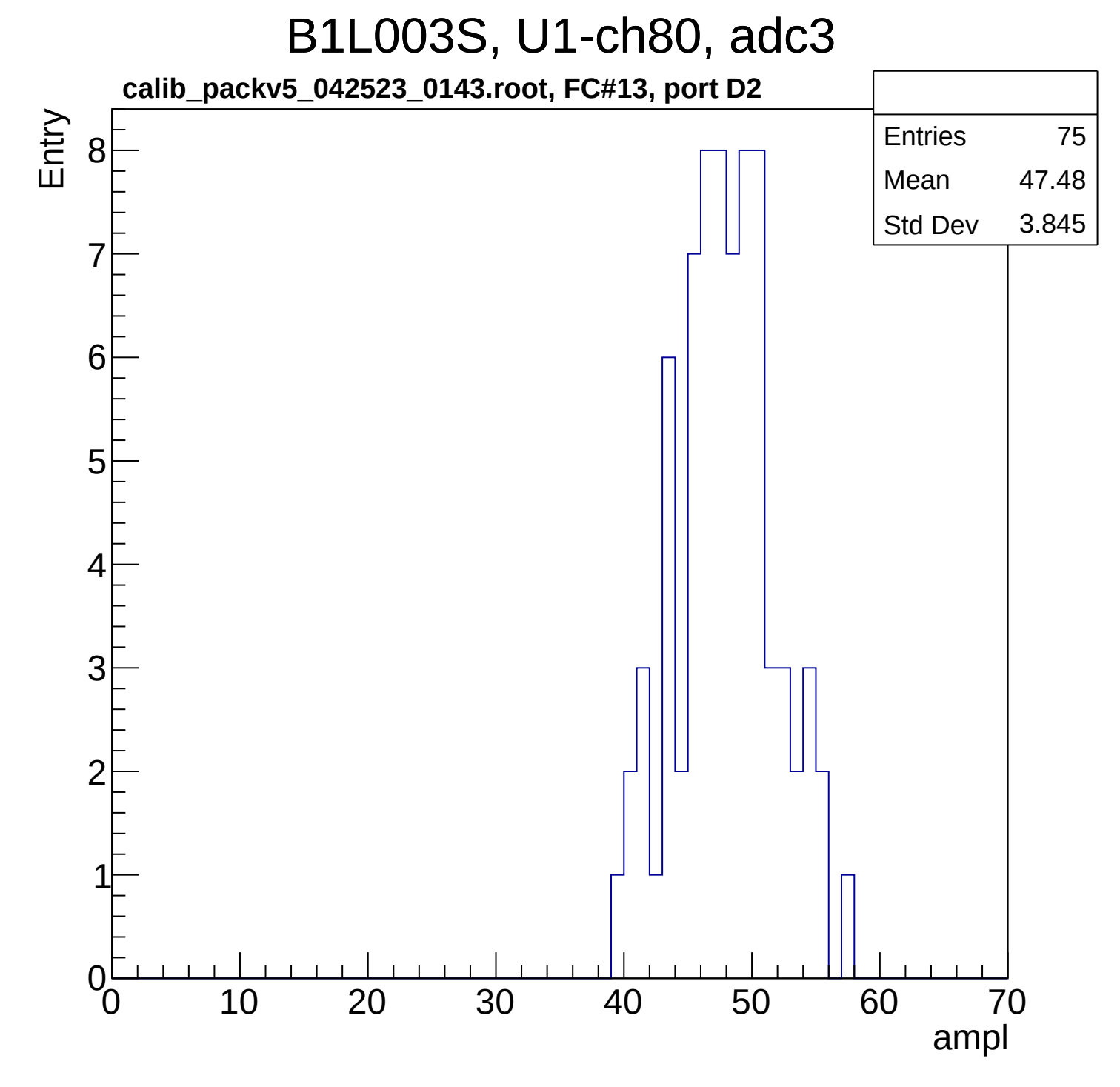
B1L003S, U1-ch80, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 47.48
Std Dev 3.845
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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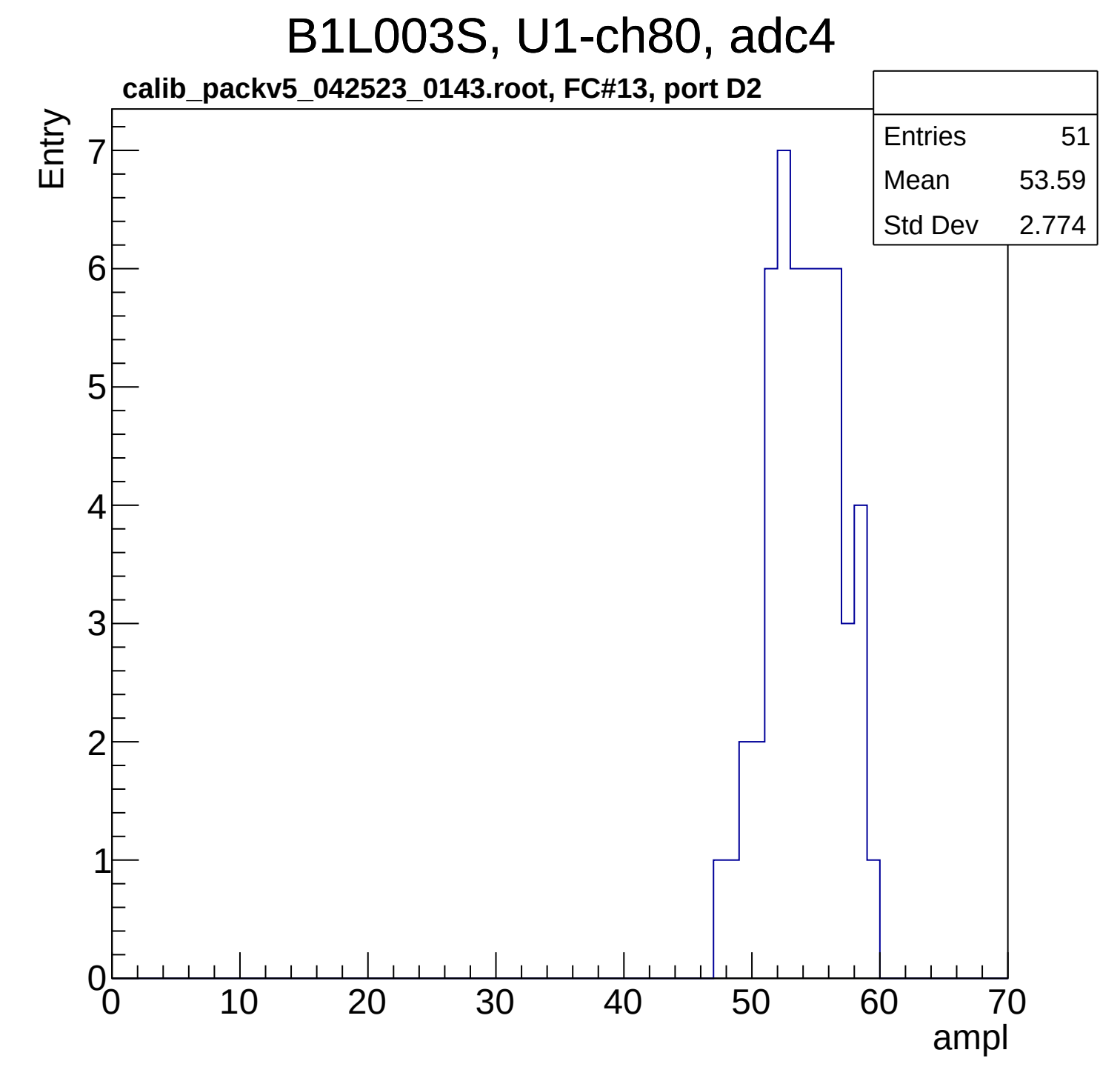
B1L003S, U1-ch80, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 53.59
Std Dev 2.774
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
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60
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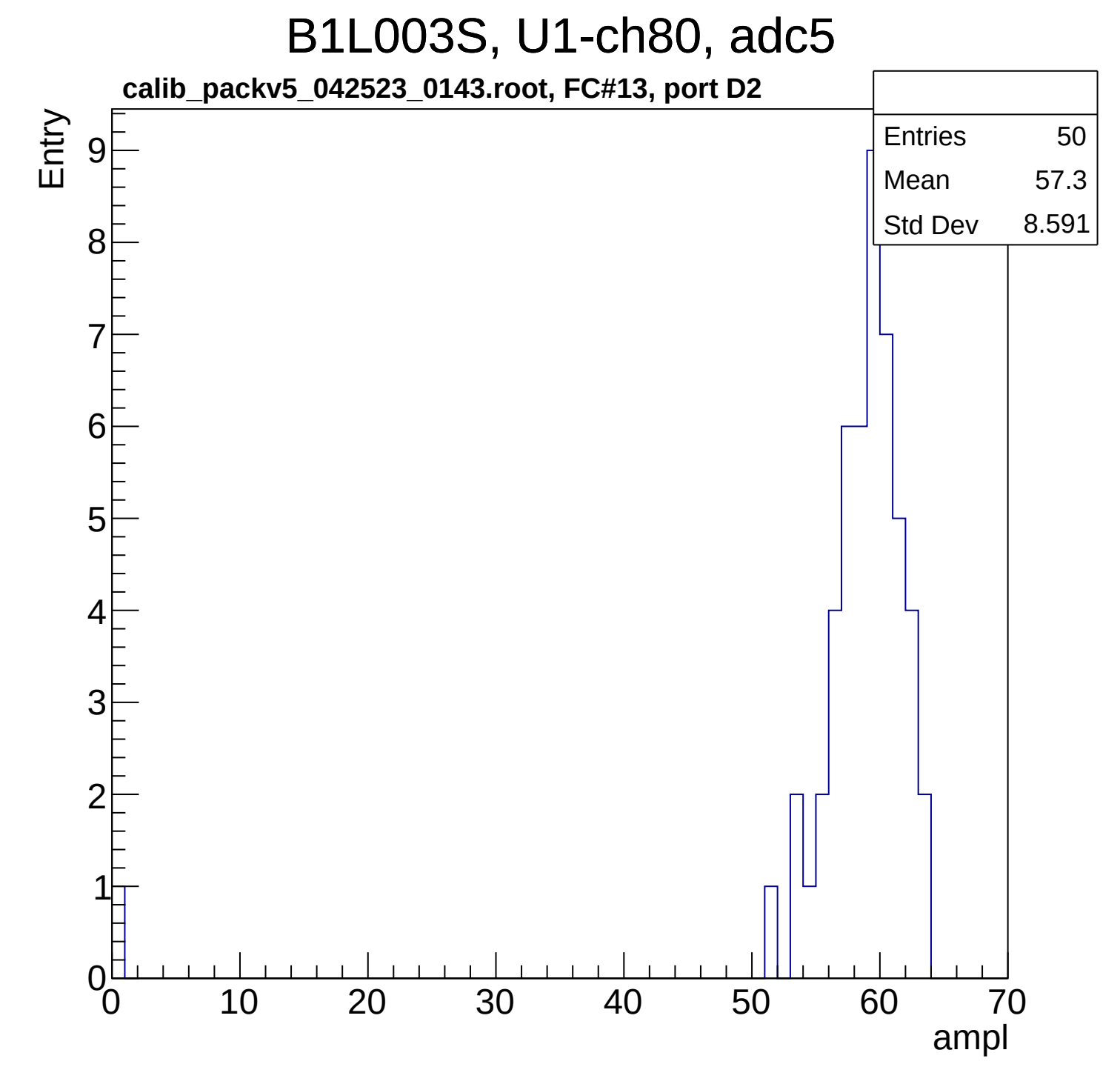
B1L003S, U1-ch80, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 57.3
Std Dev 8.591
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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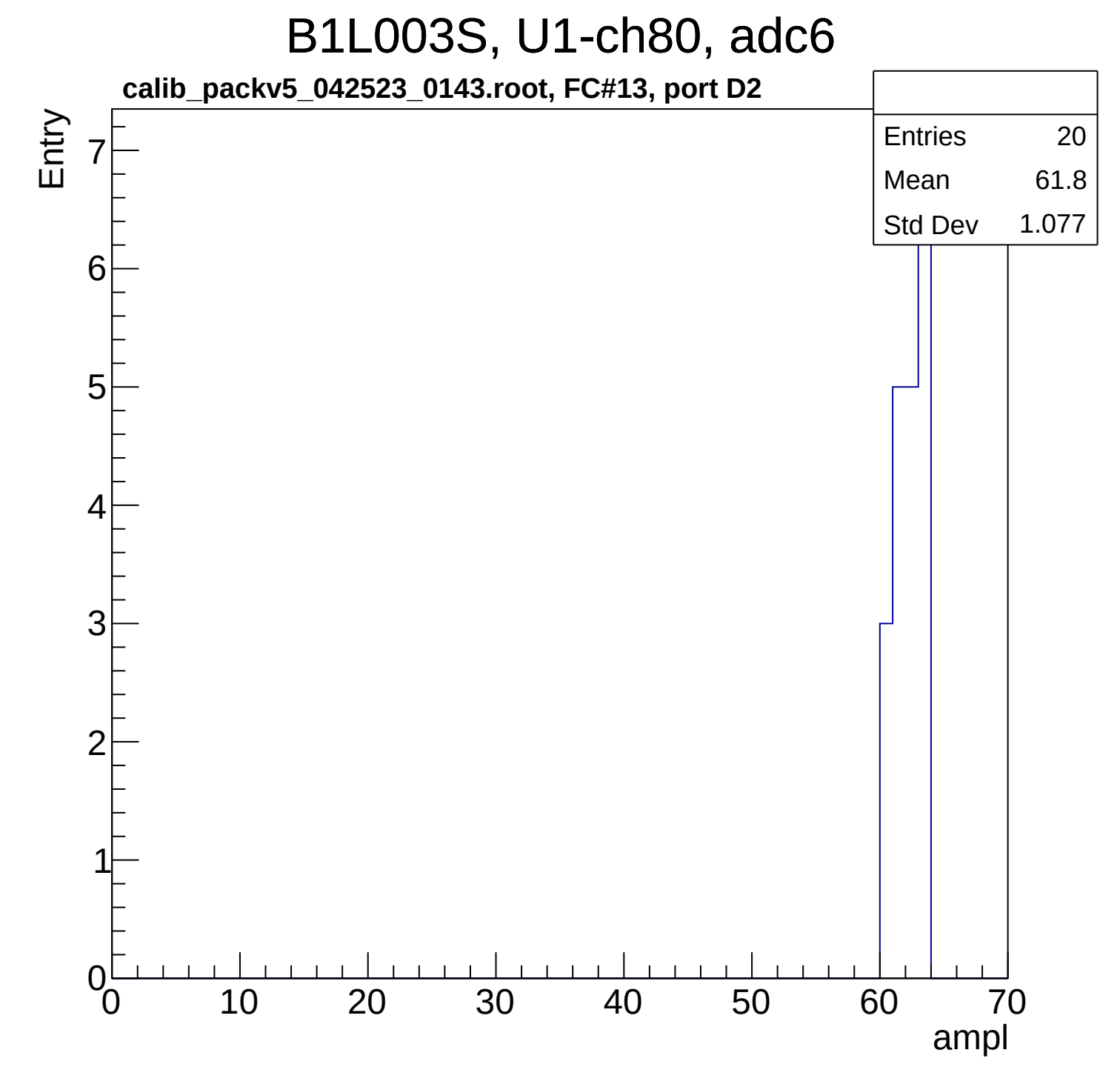
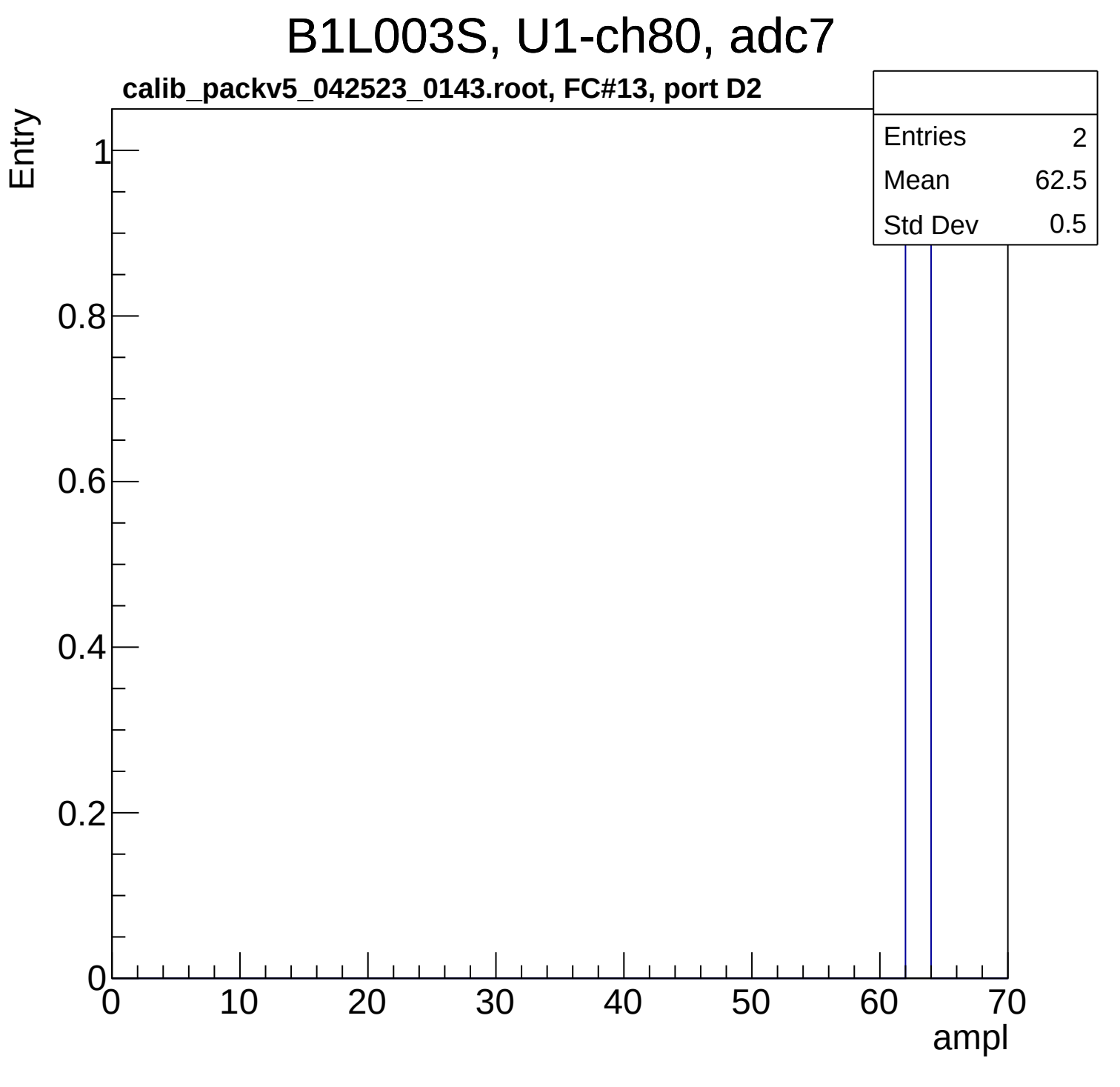
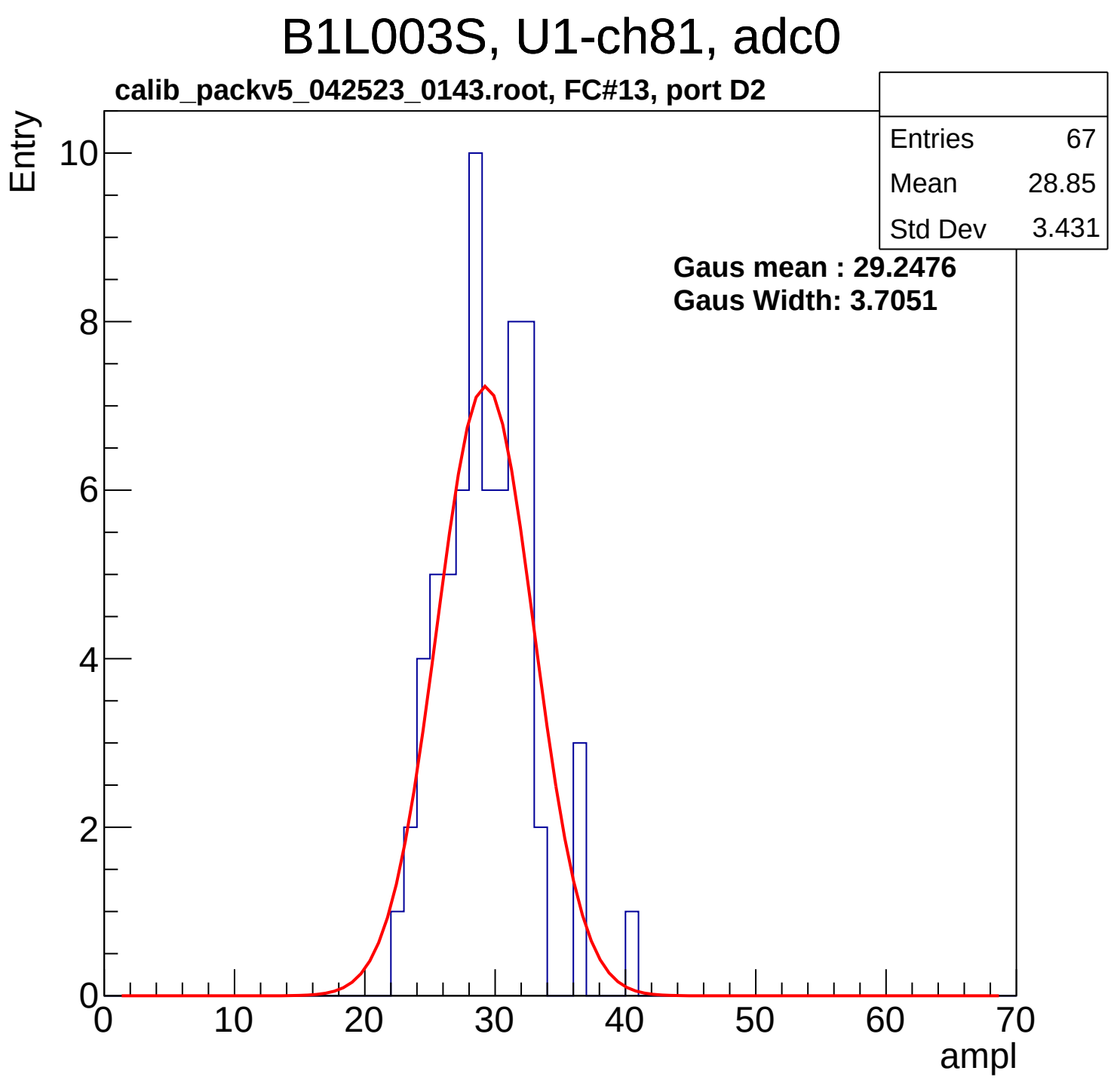
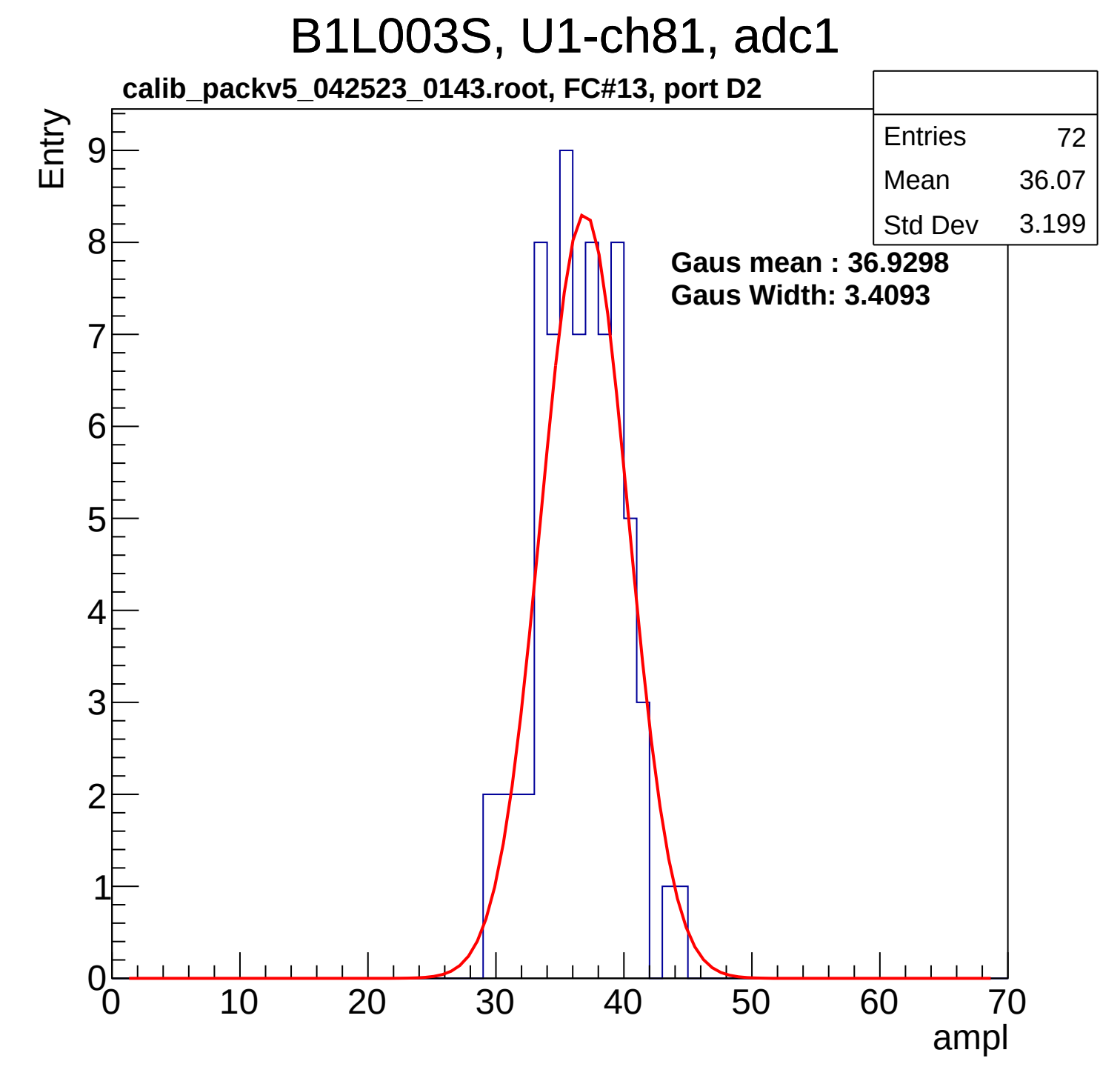
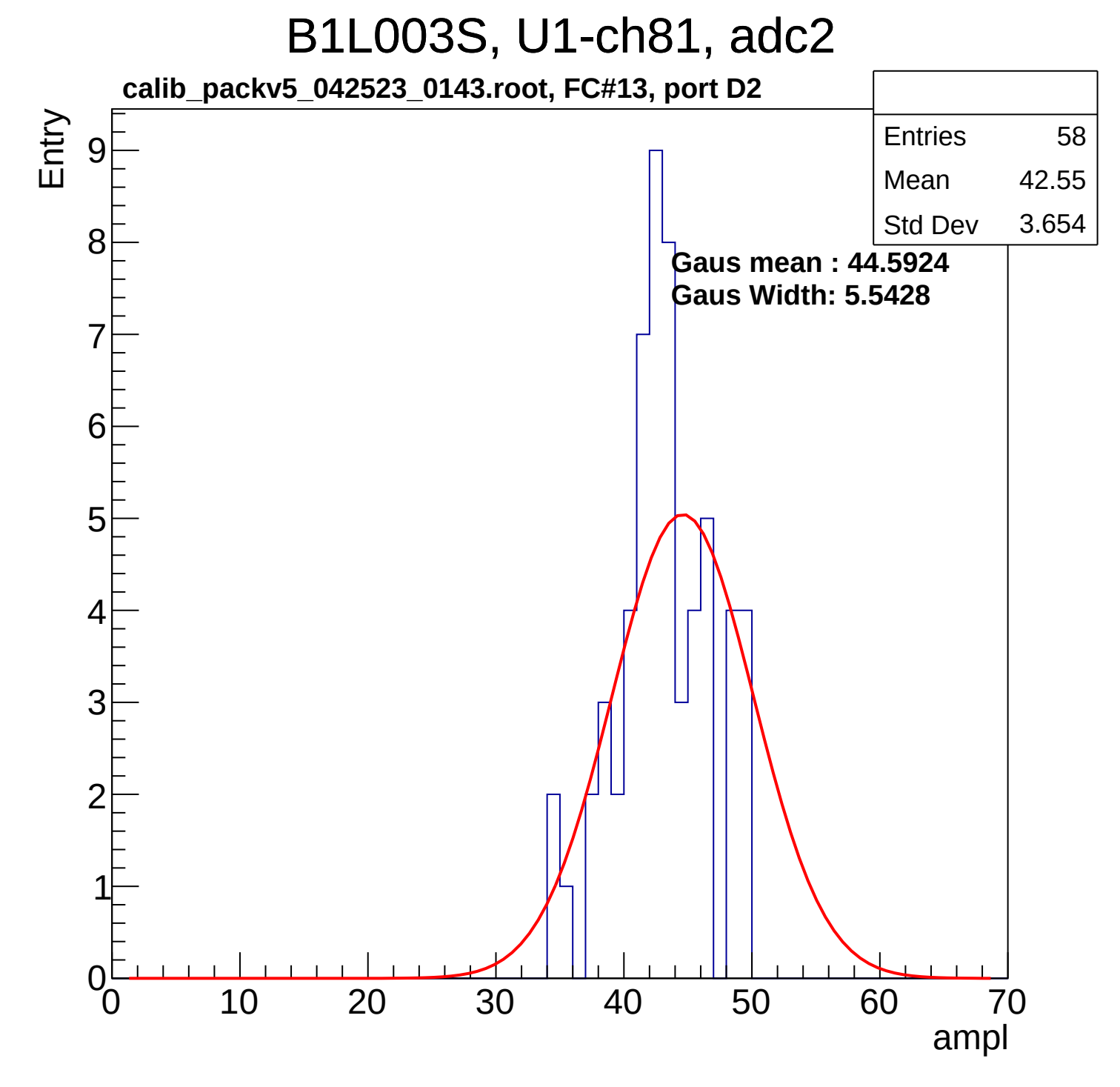

B1L003S, U1-ch80, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 20
Mean 61.8
Std Dev 1.077
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch80, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch81, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 28.85
Std Dev 3.431
Gaus mean : 29.2476
Gaus Width: 3.7051
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch81, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 36.07
Std Dev 3.199
Gaus mean : 36.9298
Gaus Width: 3.4093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch81, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.55
Std Dev 3.654
Gaus mean : 44.5924
Gaus Width: 5.5428
Entry
ampl
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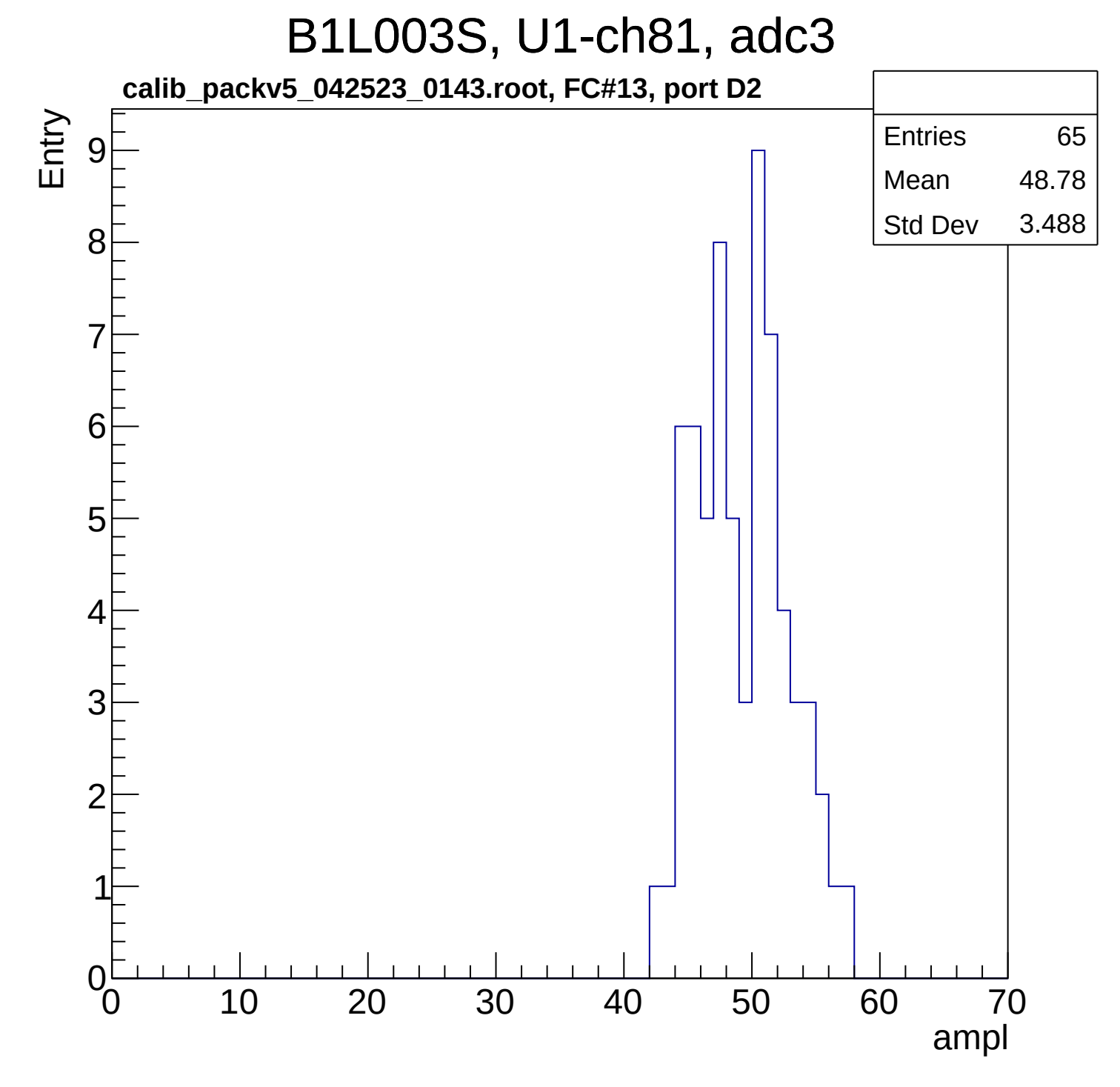
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch81, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 48.78
Std Dev 3.488
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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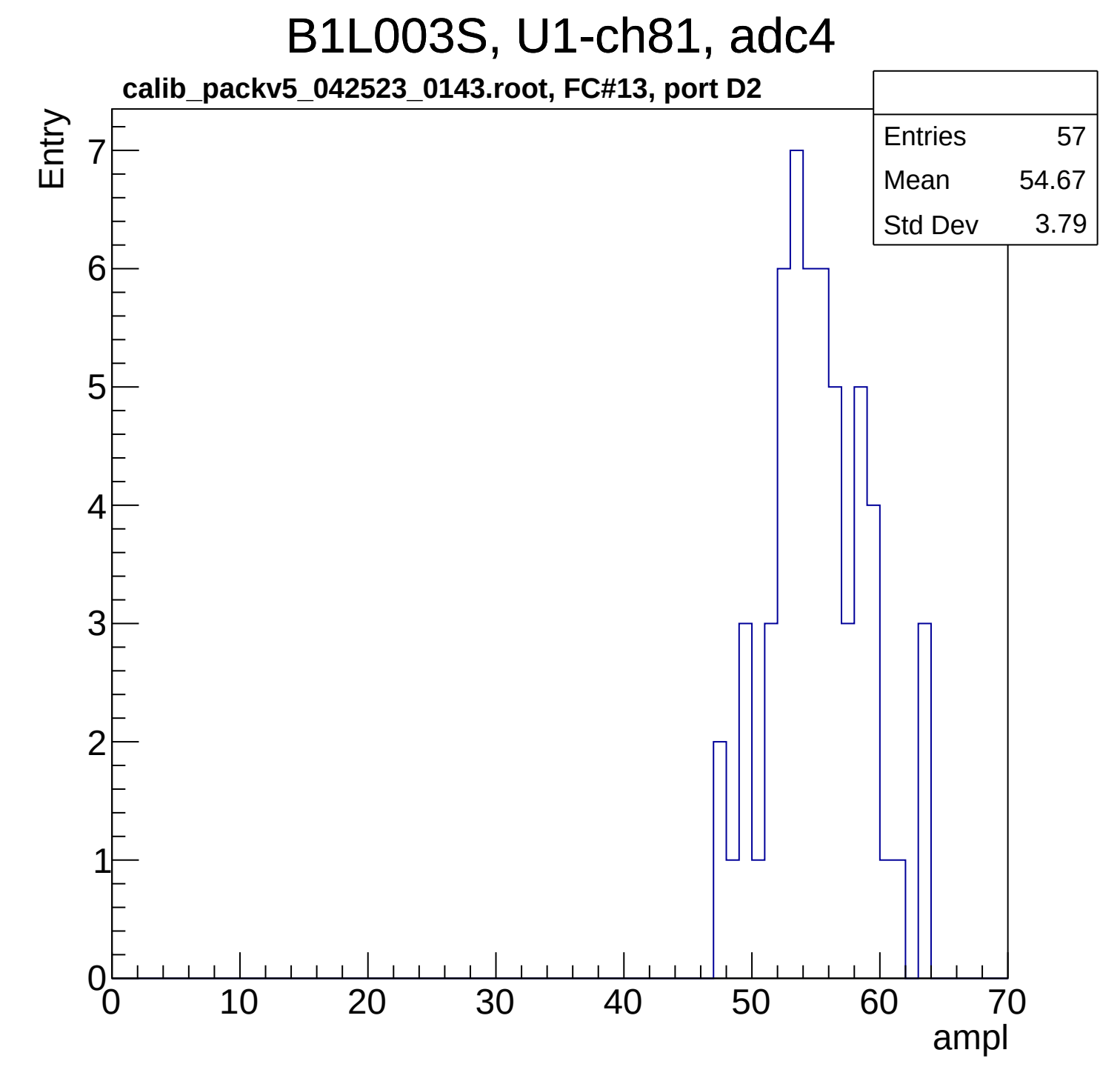
B1L003S, U1-ch81, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.67
Std Dev 3.79
Entry
ampl
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1
2
3
4
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6
7
0
10
20
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40
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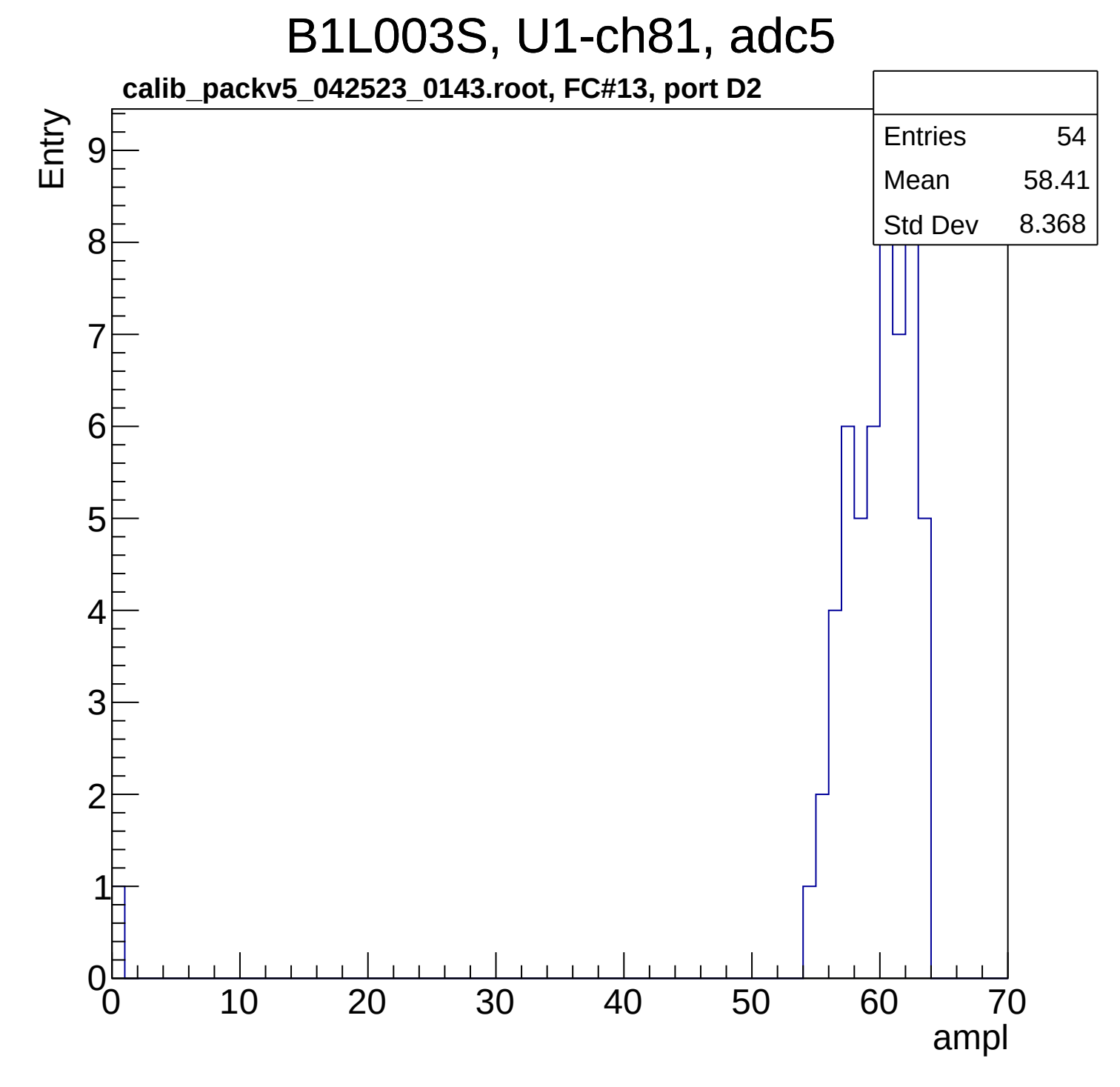
B1L003S, U1-ch81, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.41
Std Dev 8.368
Entry
ampl
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2
3
4
5
6
7
8
9
0
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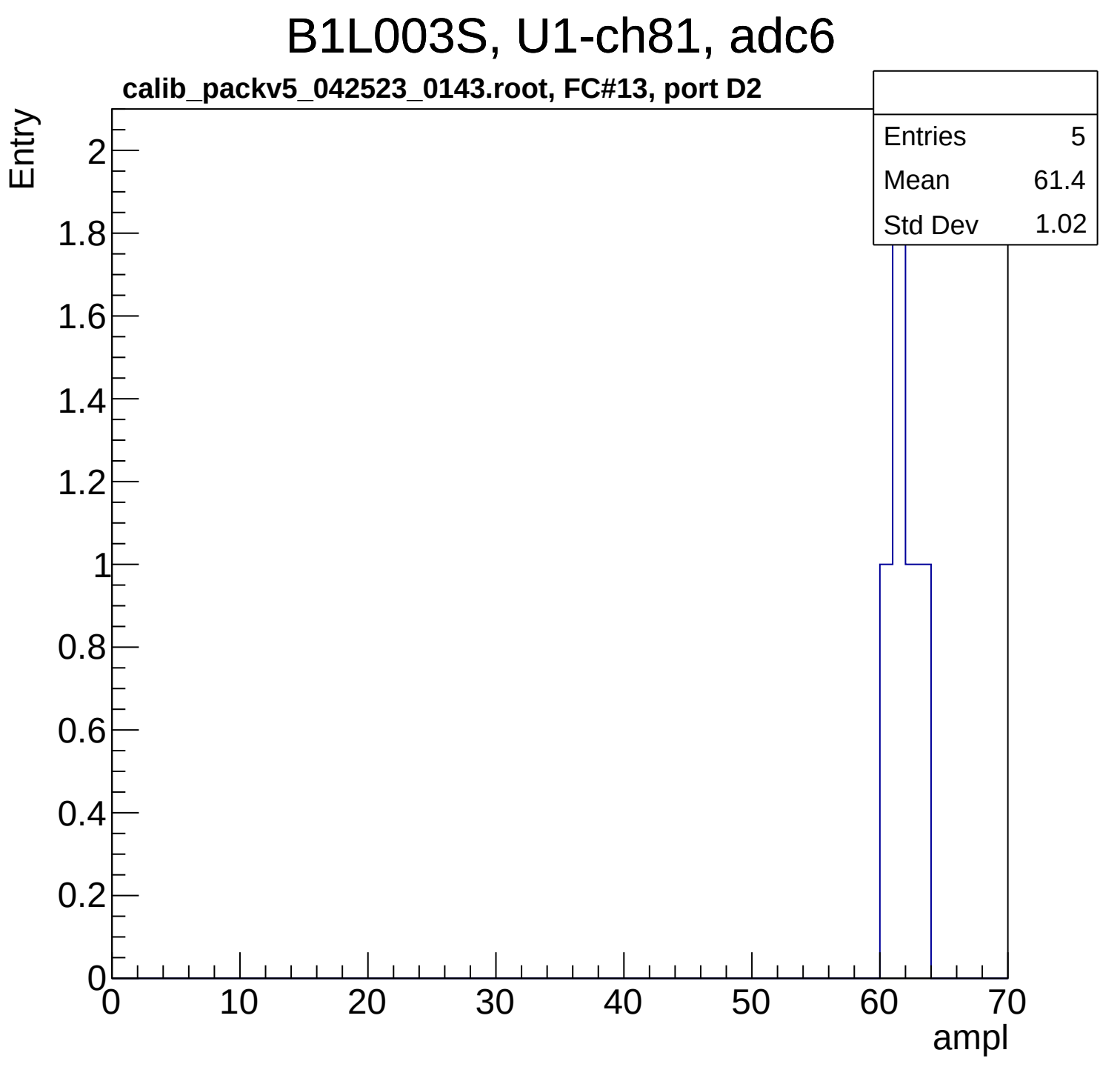
60
70


B1L003S, U1-ch81, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 61.4
Std Dev 1.02
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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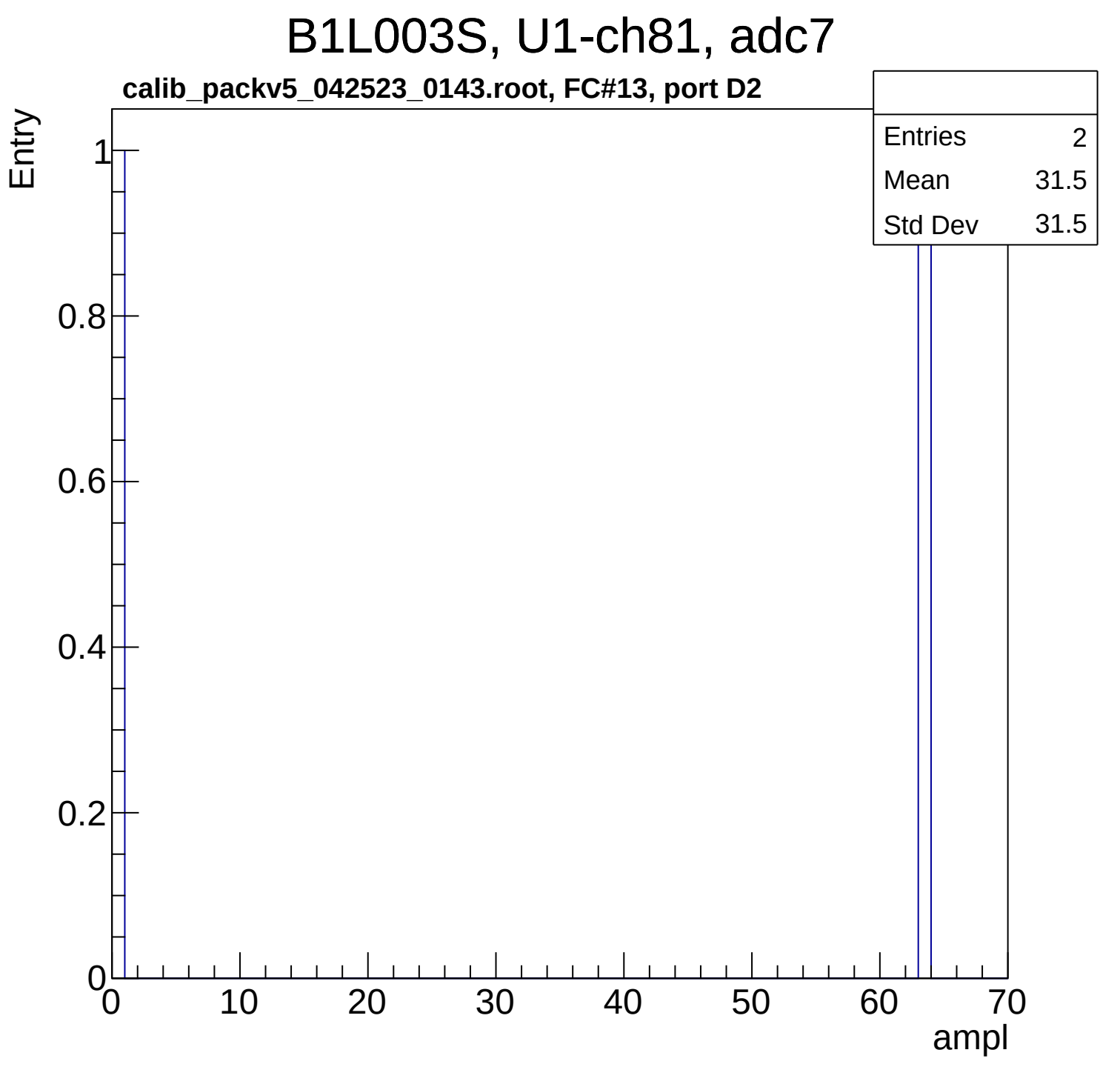
40
50
60
70


B1L003S, U1-ch81, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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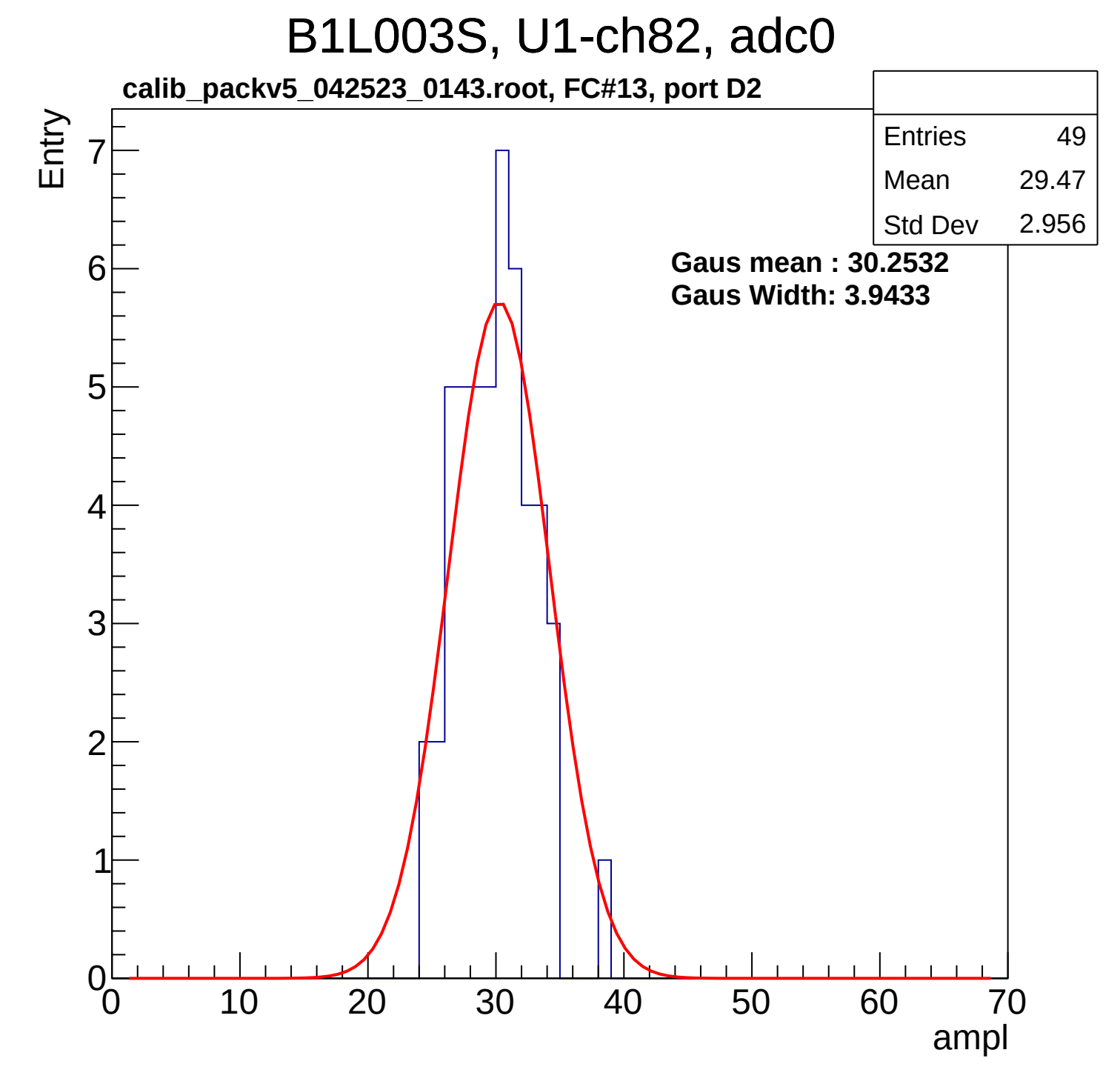
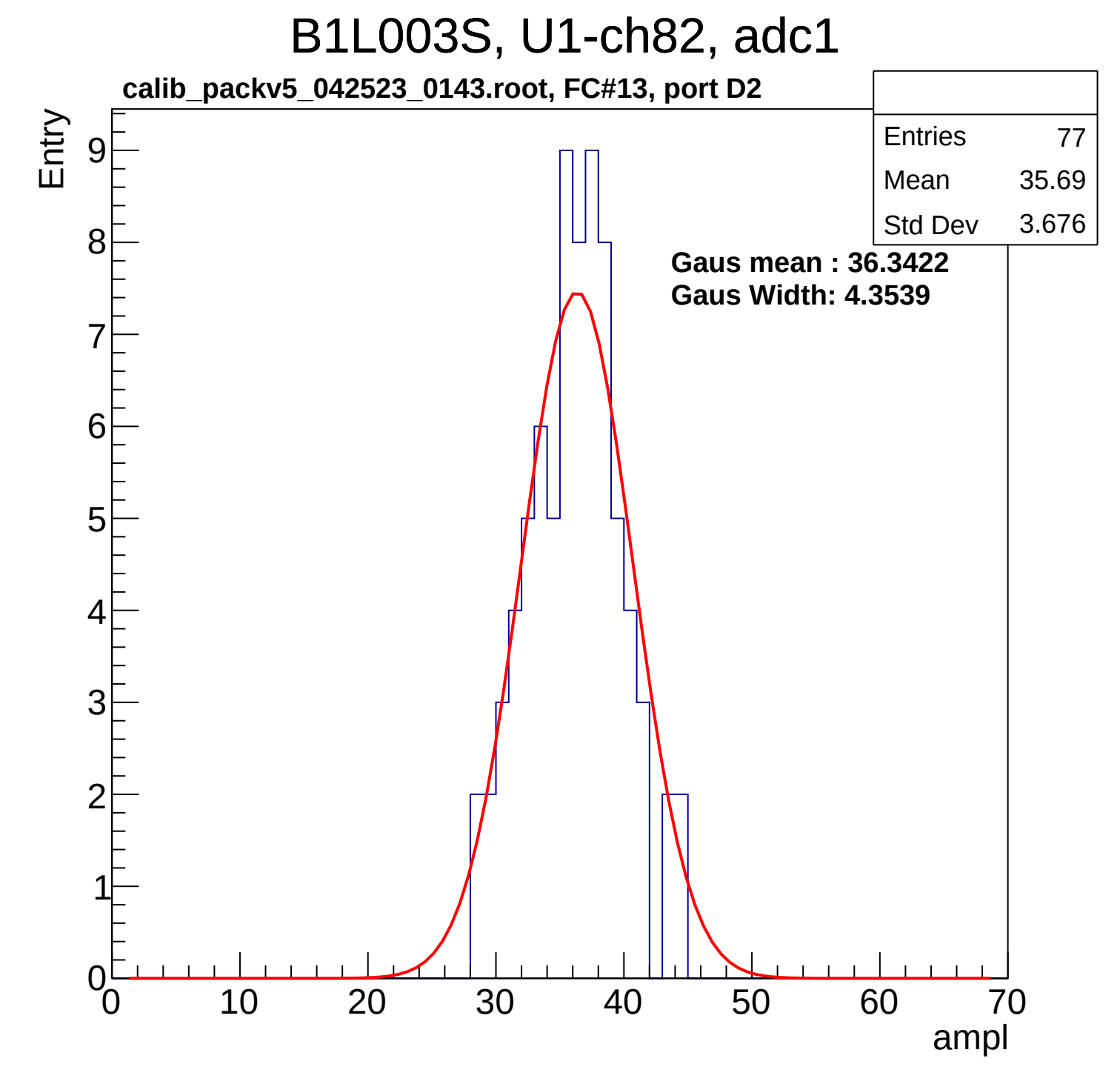
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch82, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 29.47
Std Dev 2.956
Gaus mean : 30.2532
Gaus Width: 3.9433
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch82, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 35.69
Std Dev 3.676
Gaus mean : 36.3422
Gaus Width: 4.3539
Entry
ampl
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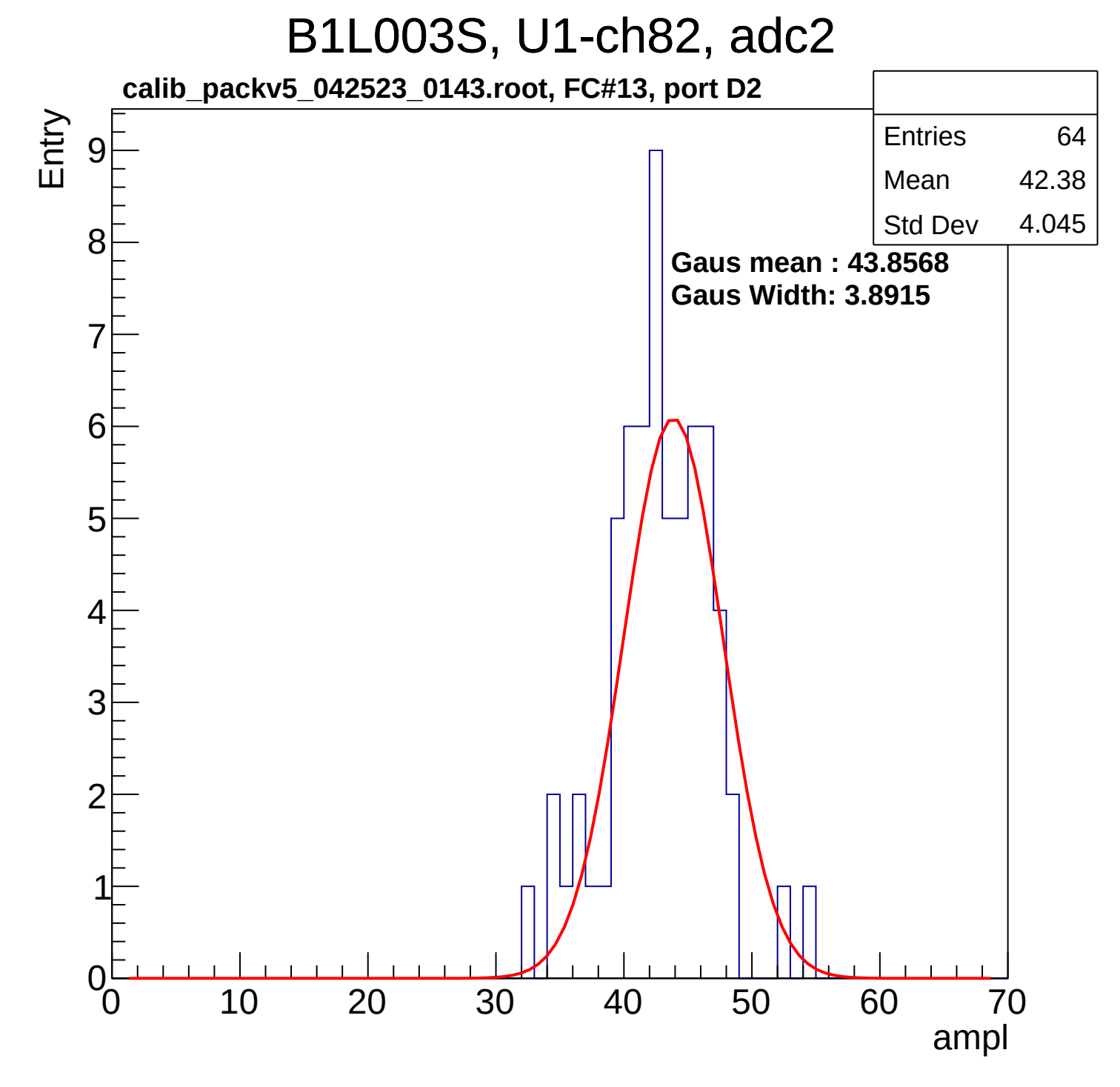
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch82, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 42.38
Std Dev 4.045
Gaus mean : 43.8568
Gaus Width: 3.8915
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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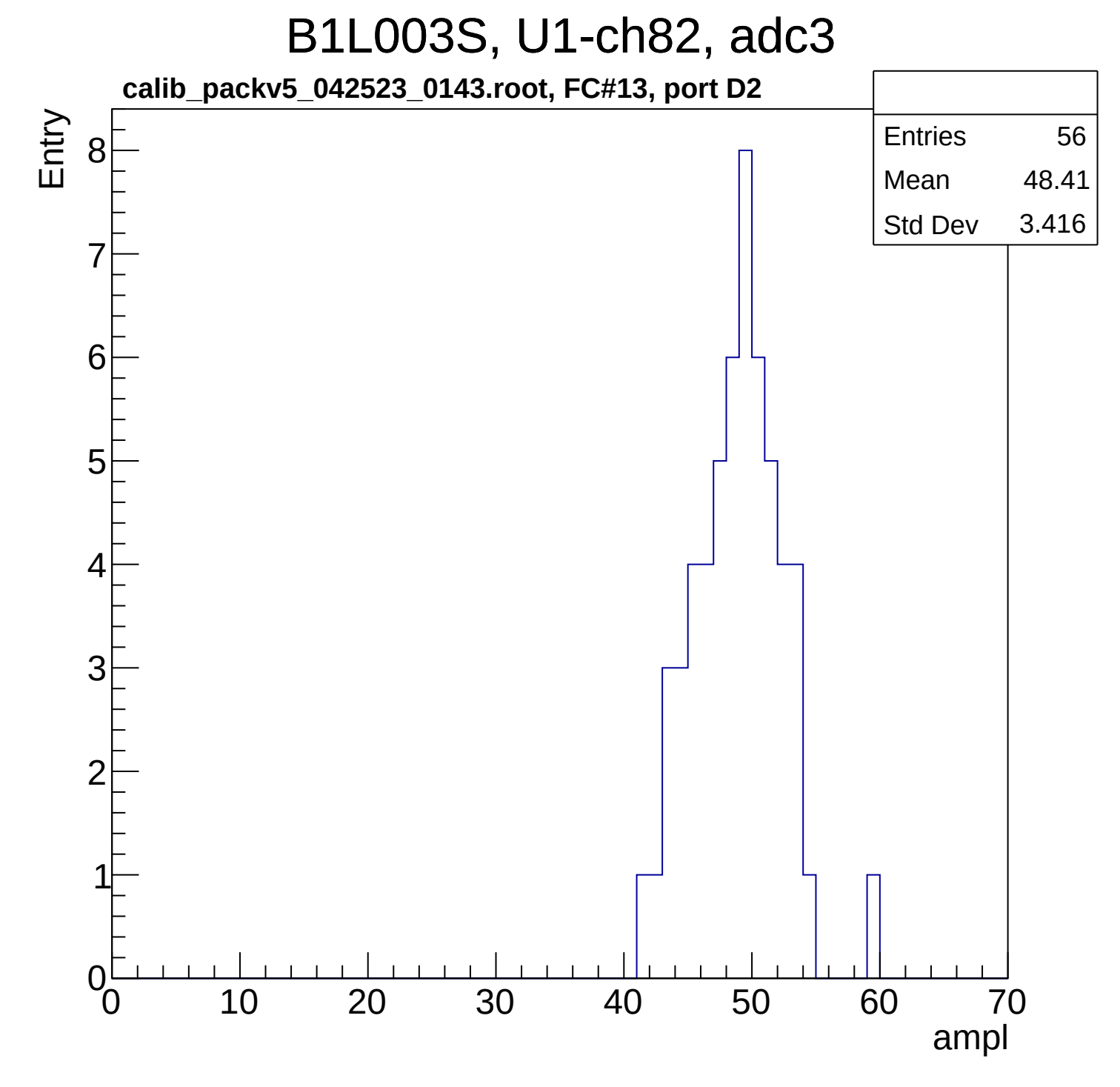
B1L003S, U1-ch82, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 48.41
Std Dev 3.416
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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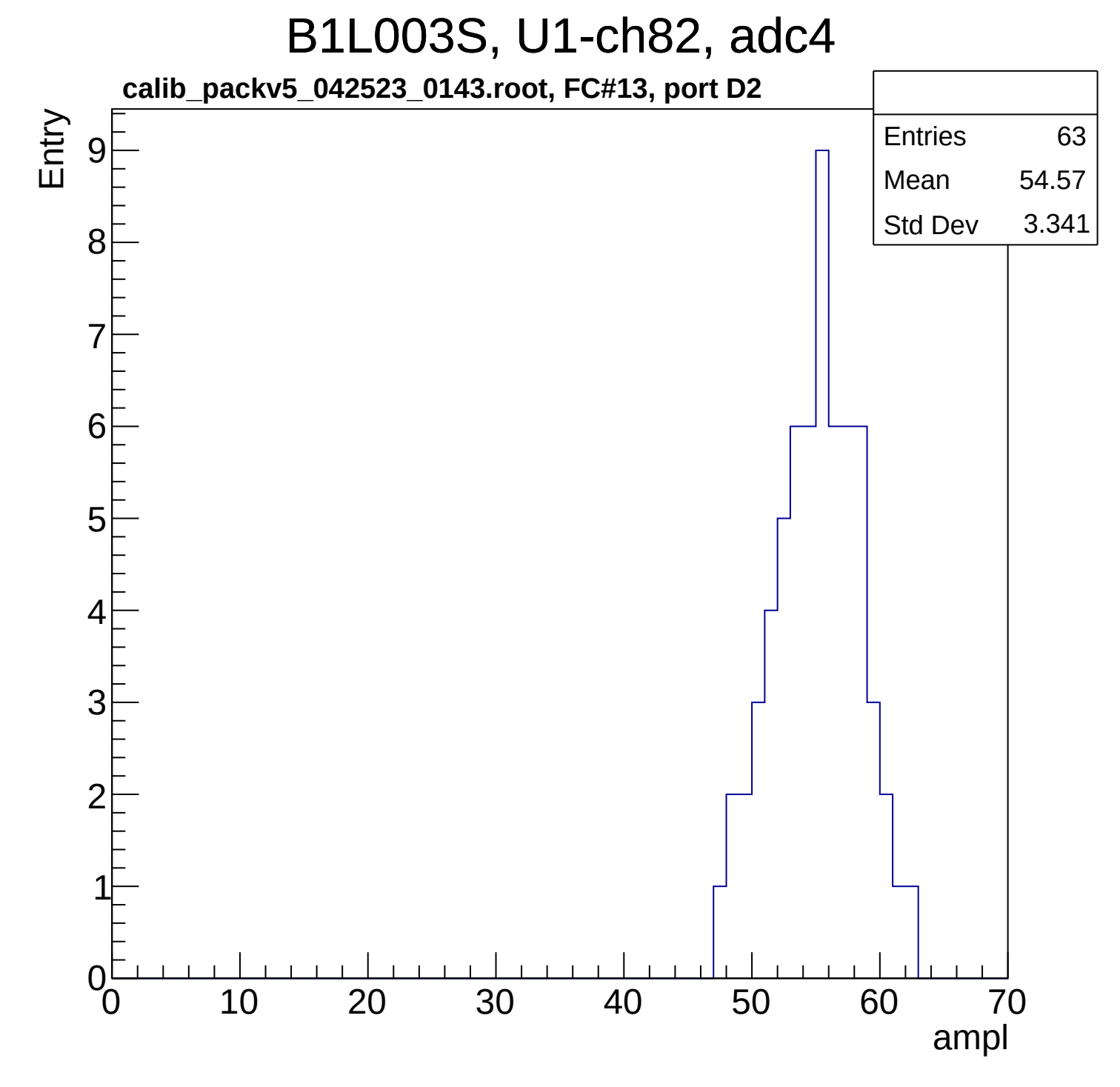
B1L003S, U1-ch82, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.57
Std Dev 3.341
Entry
ampl
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4
5
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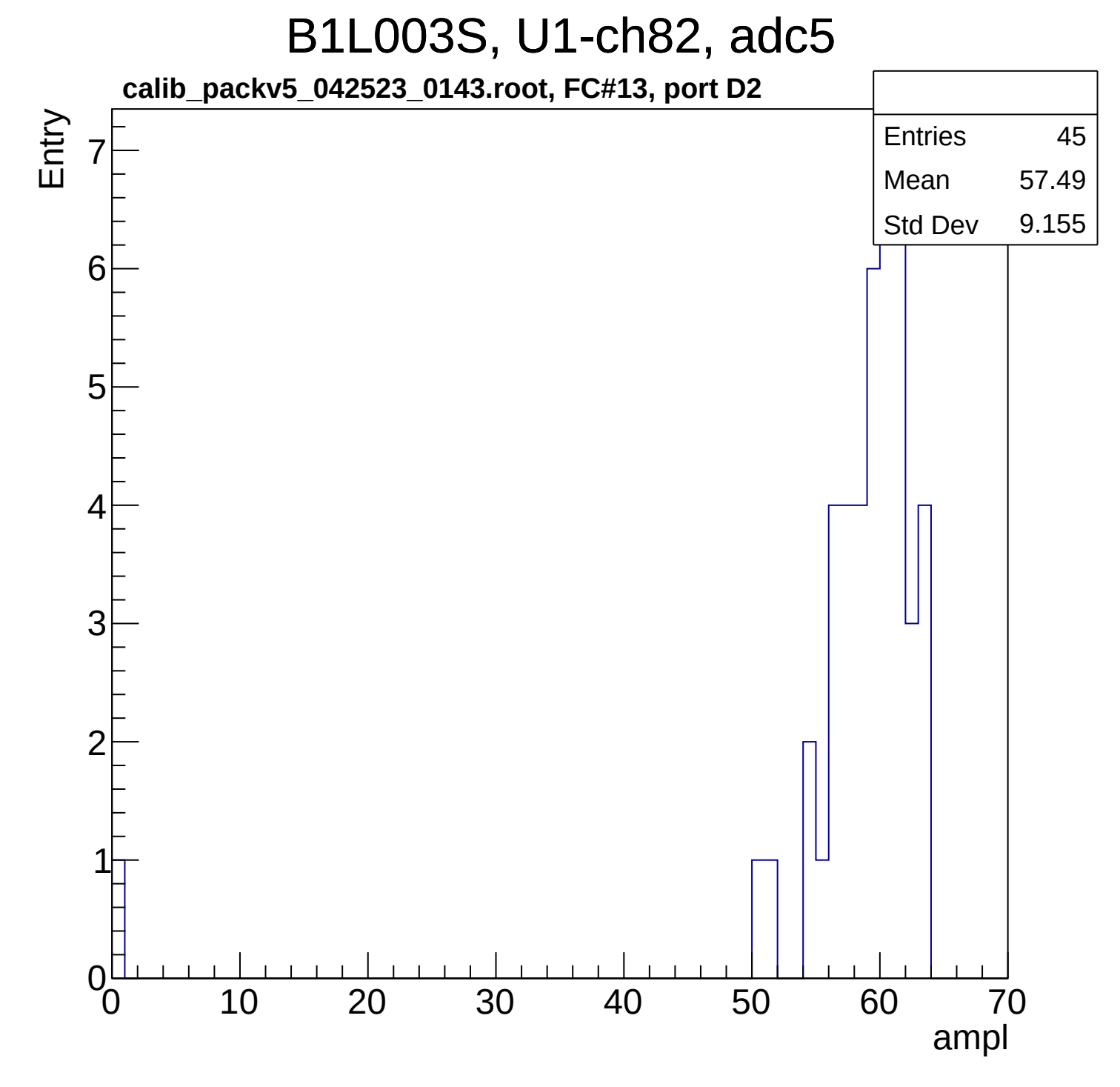
B1L003S, U1-ch82, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 57.49
Std Dev 9.155
Entry
ampl
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1
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3
4
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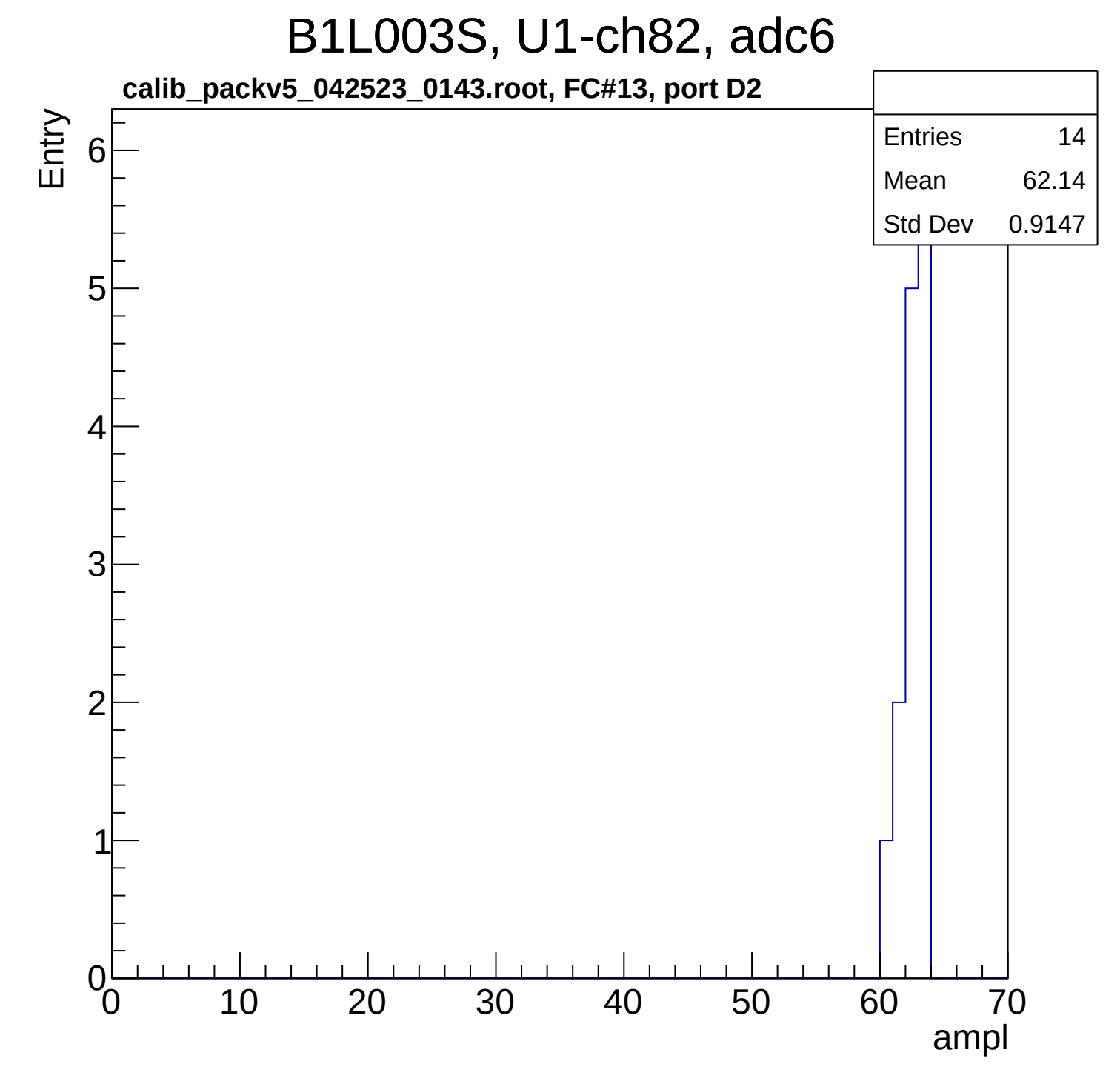
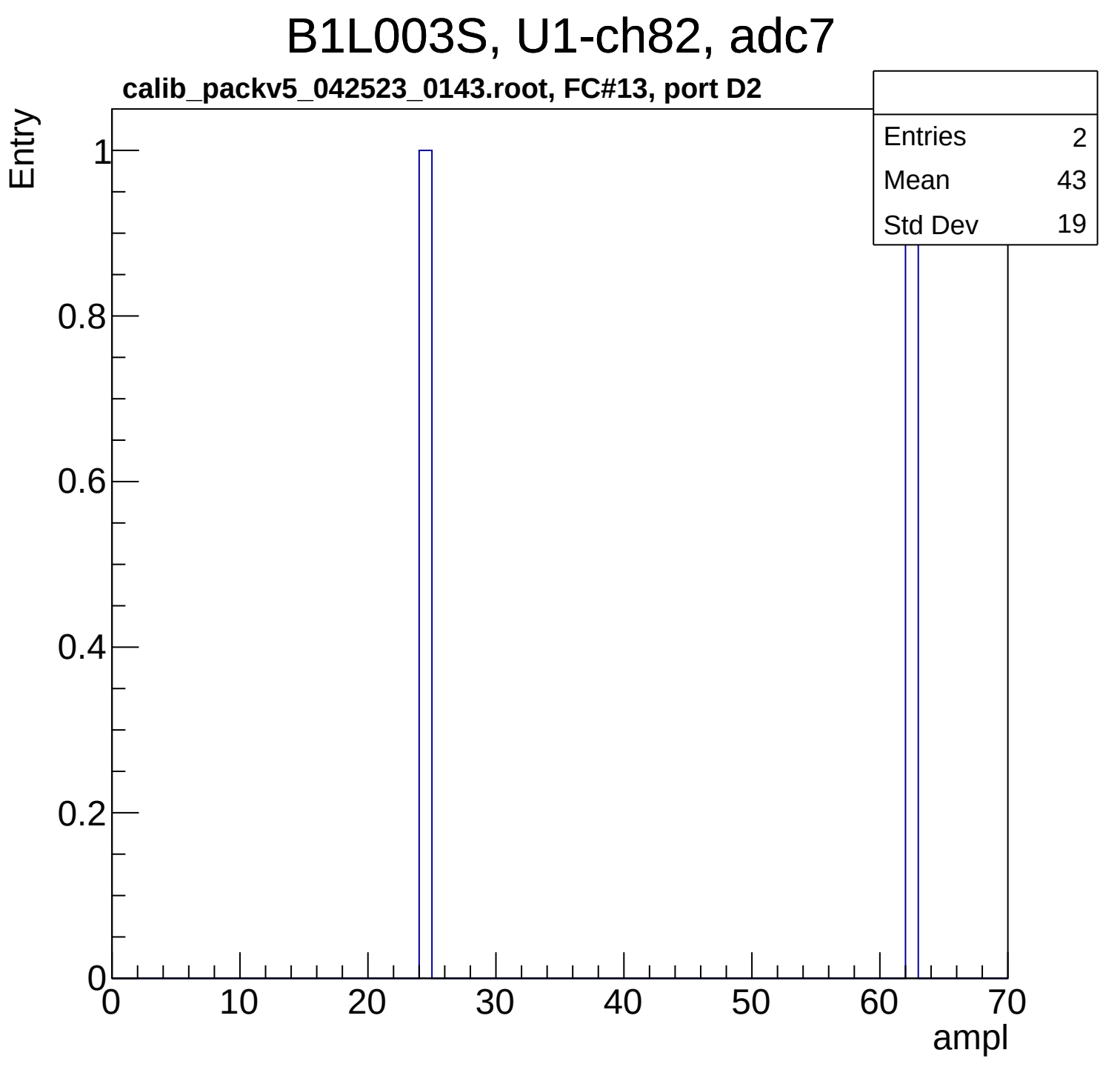

B1L003S, U1-ch82, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 62.14
Std Dev 0.9147
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch82, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 43
Std Dev 19
Entry
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0.8
0.6
0.4
0.2
0
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10
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50
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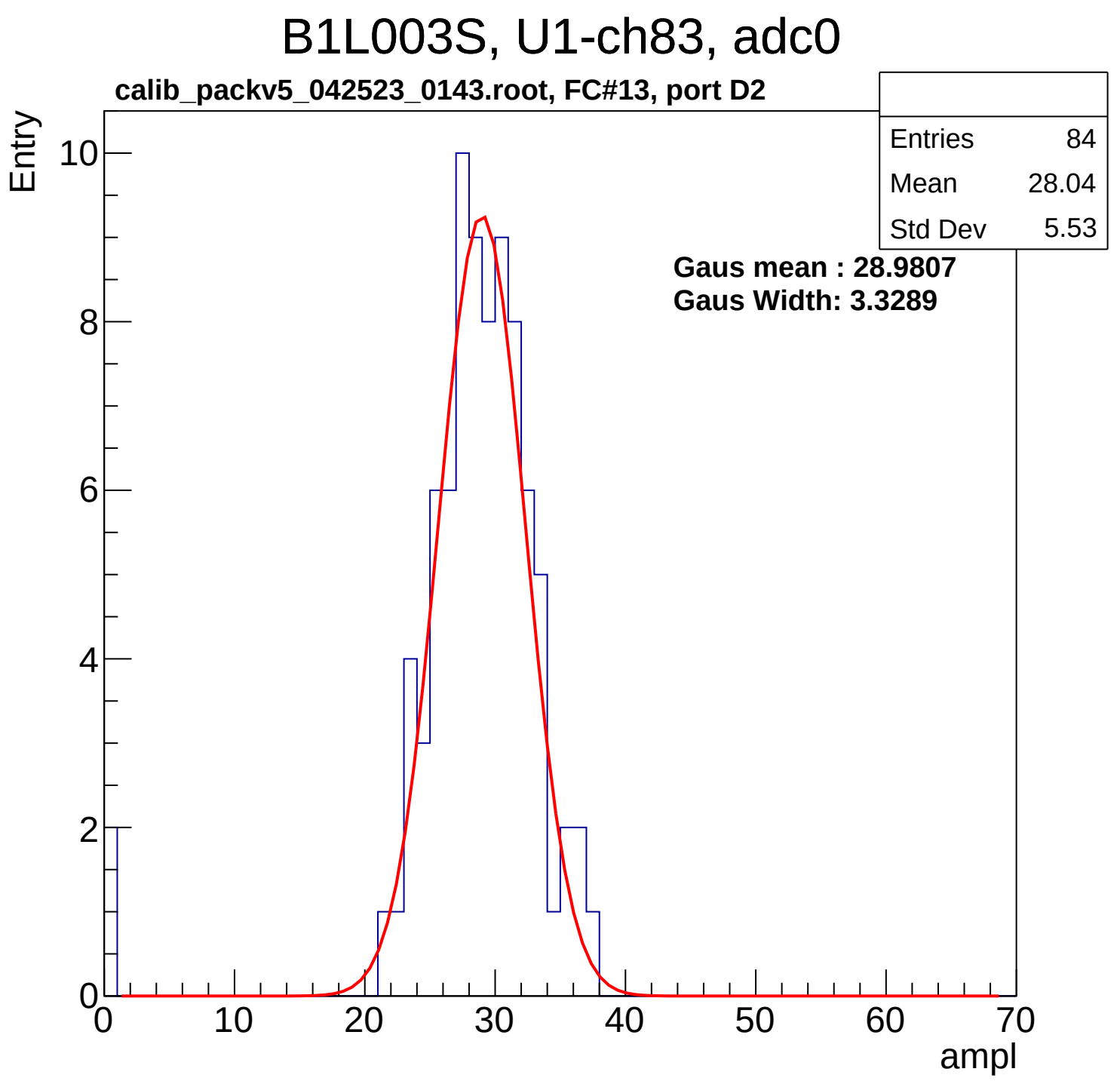
70
ampl


B1L003S, U1-ch83, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 28.04
Std Dev 5.53
Gaus mean : 28.9807
Gaus Width: 3.3289
Entry
ampl
0
2
4
6
8
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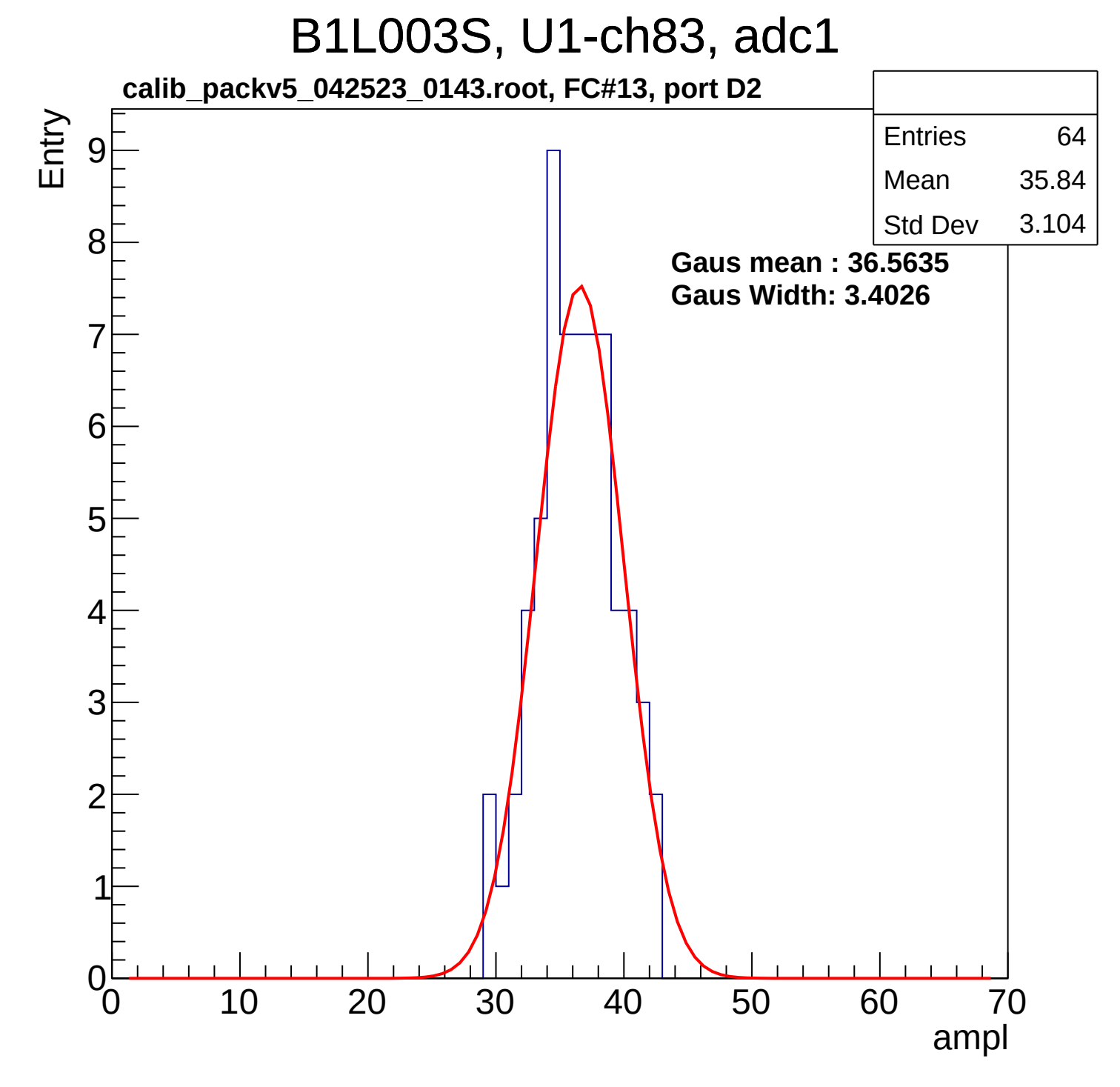
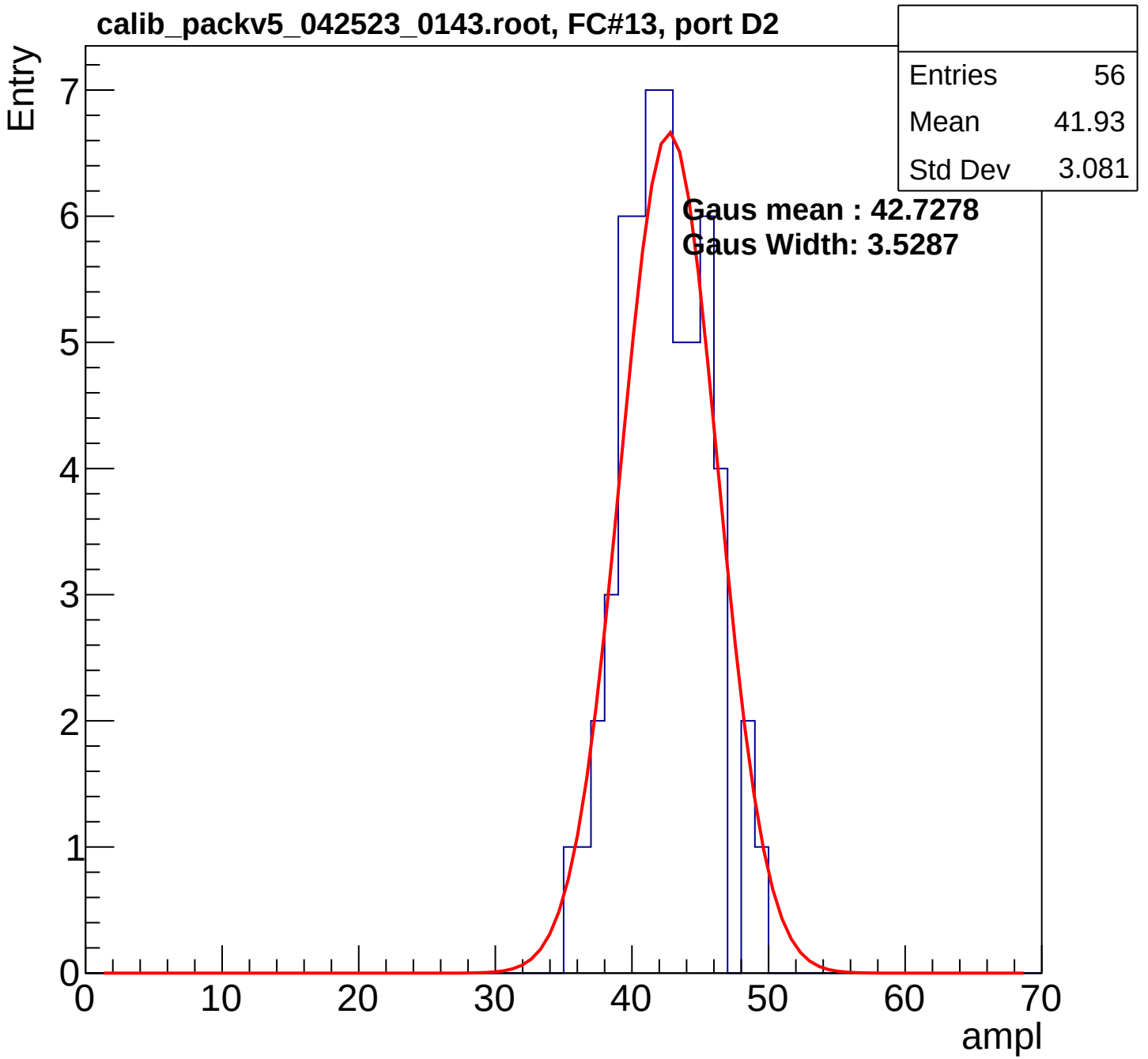
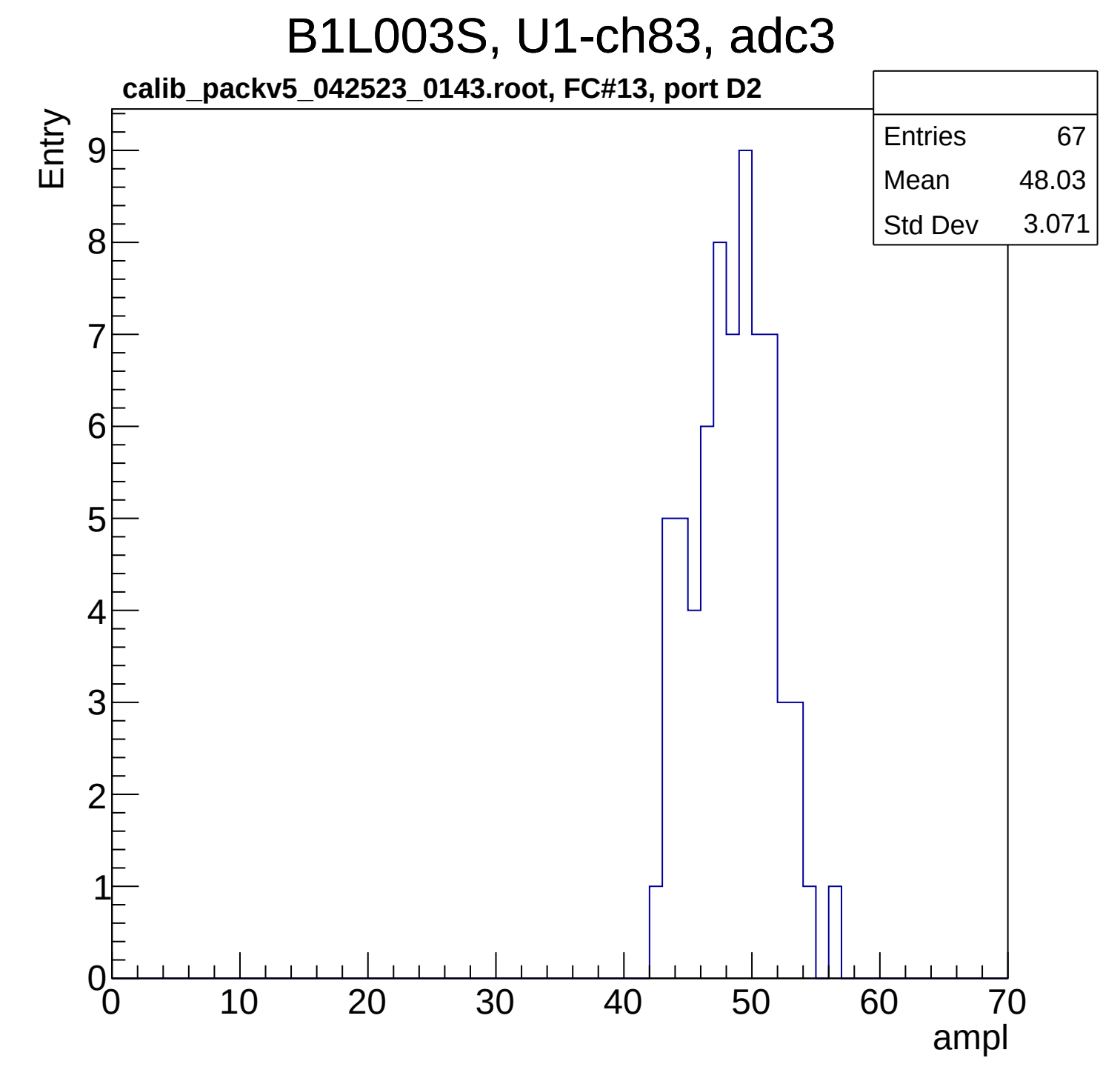
0
10
20
30
40
50
60
70


B1L003S, U1-ch83, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.84
Std Dev 3.104
Gaus mean : 36.5635
Gaus Width: 3.4026
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch83, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 41.93
Std Dev 3.081
Gaus mean : 42.7278
Gaus Width: 3.5287
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch83, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 48.03
Std Dev 3.071
Entry
ampl
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3
4
5
6
7
8
9
0
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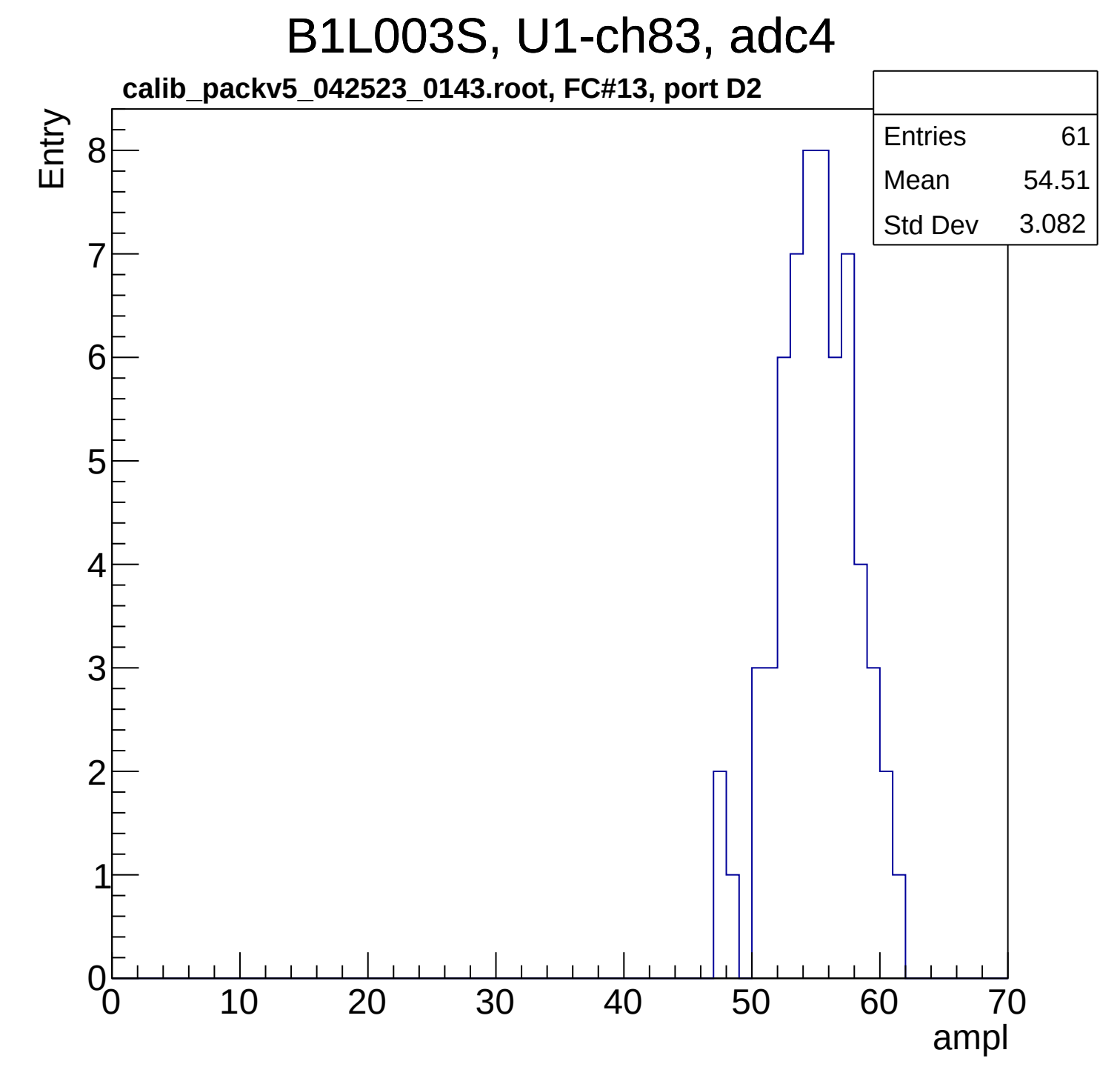
B1L003S, U1-ch83, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 54.51
Std Dev 3.082
Entry
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2
3
4
5
6
7
8
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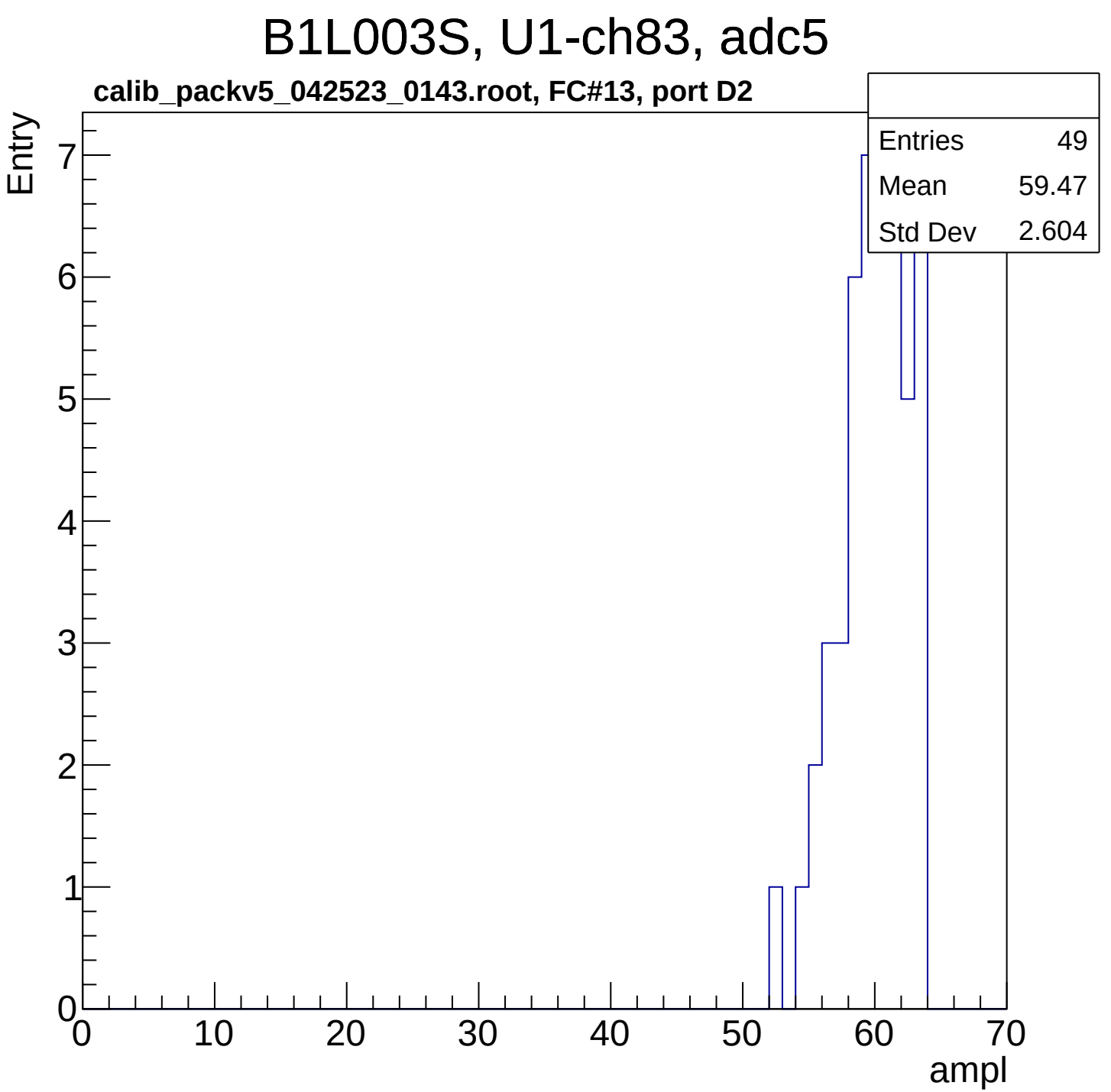
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch83, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 59.47
Std Dev 2.604
Entry
ampl
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1
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4
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6
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0
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40
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60
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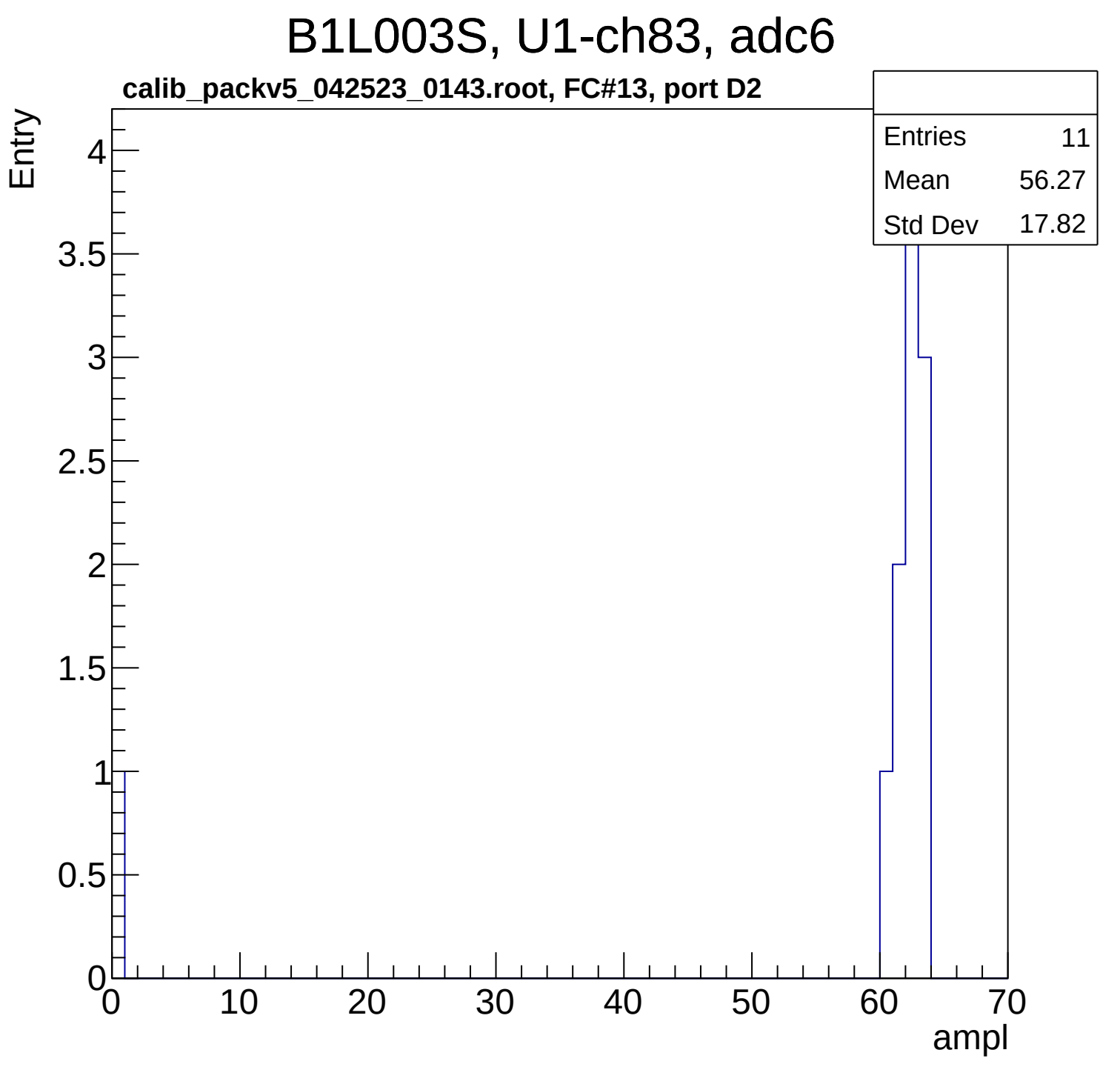
B1L003S, U1-ch83, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 56.27
Std Dev 17.82
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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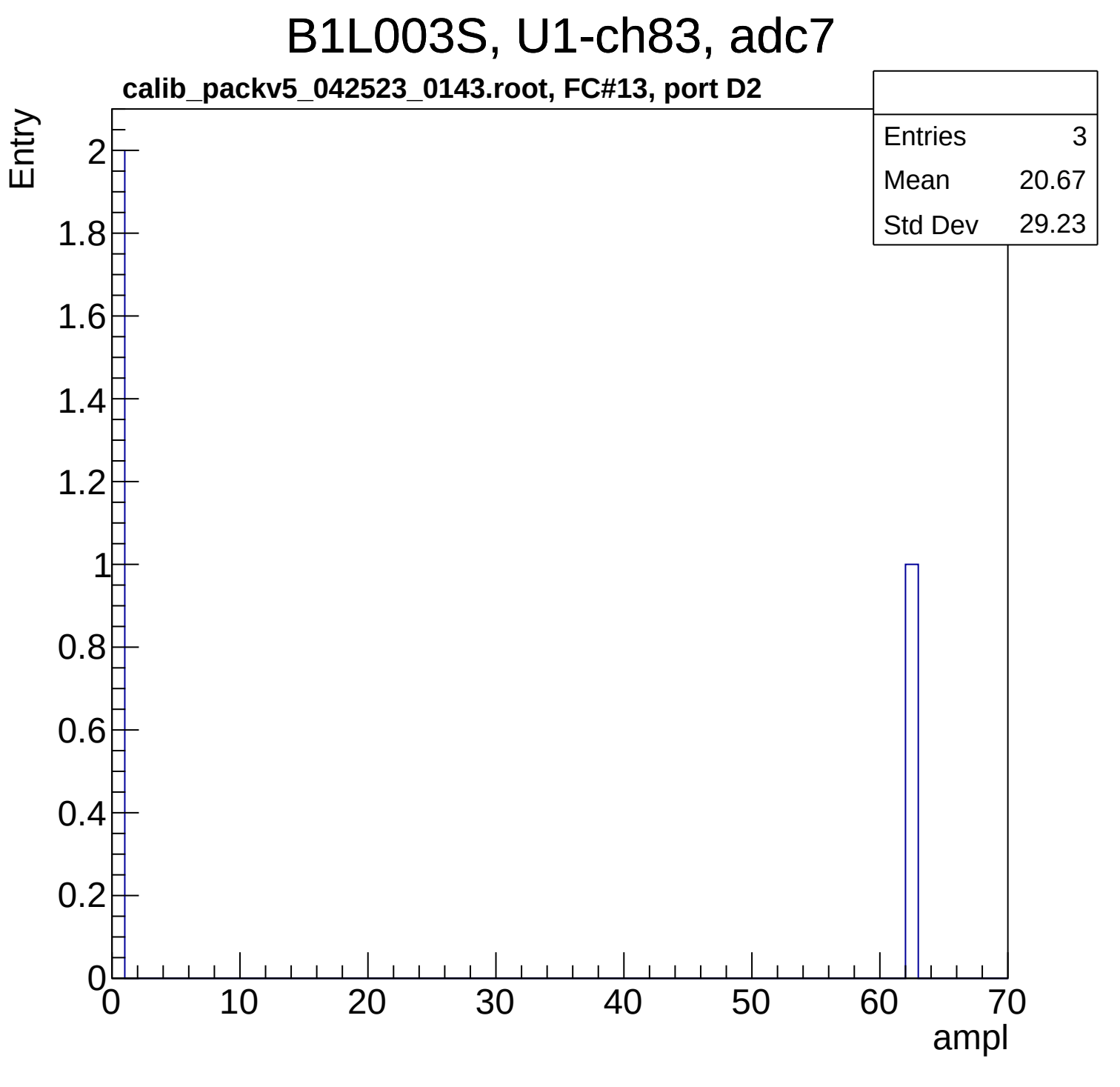
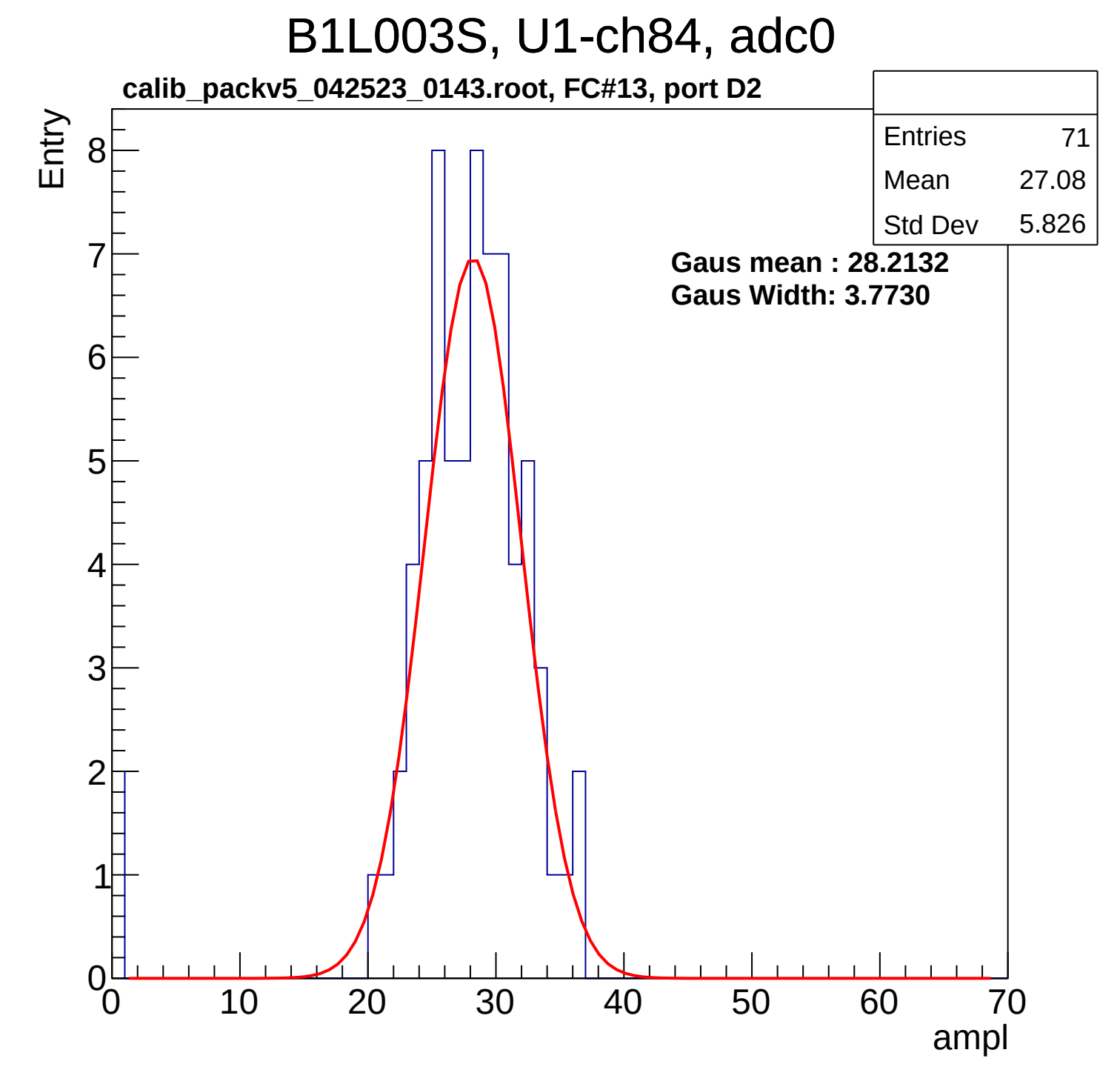
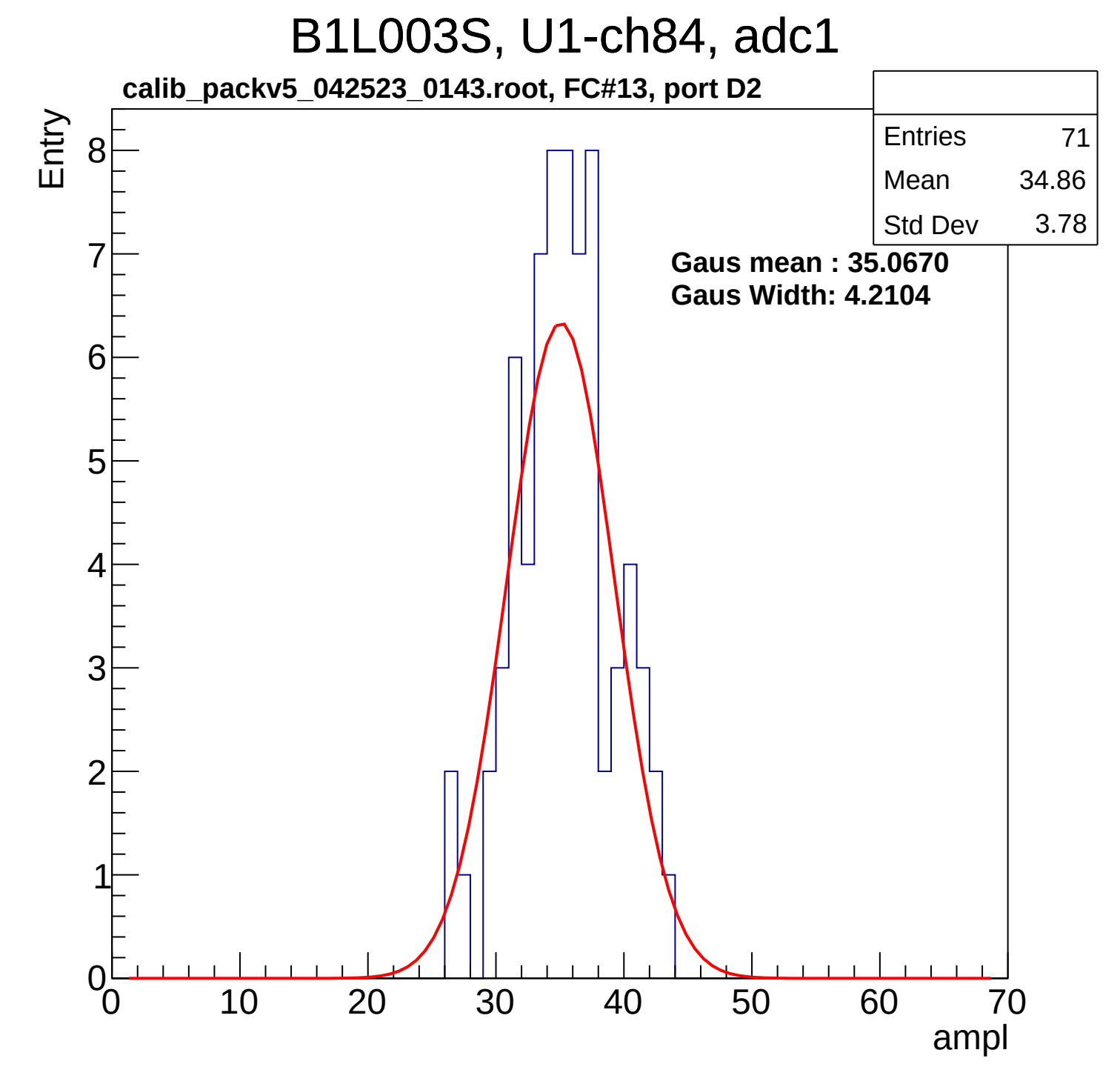

B1L003S, U1-ch83, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch84, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 27.08
Std Dev 5.826
Gaus mean : 28.2132
Gaus Width: 3.7730
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch84, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 34.86
Std Dev 3.78
Gaus mean : 35.0670
Gaus Width: 4.2104
Entry
ampl
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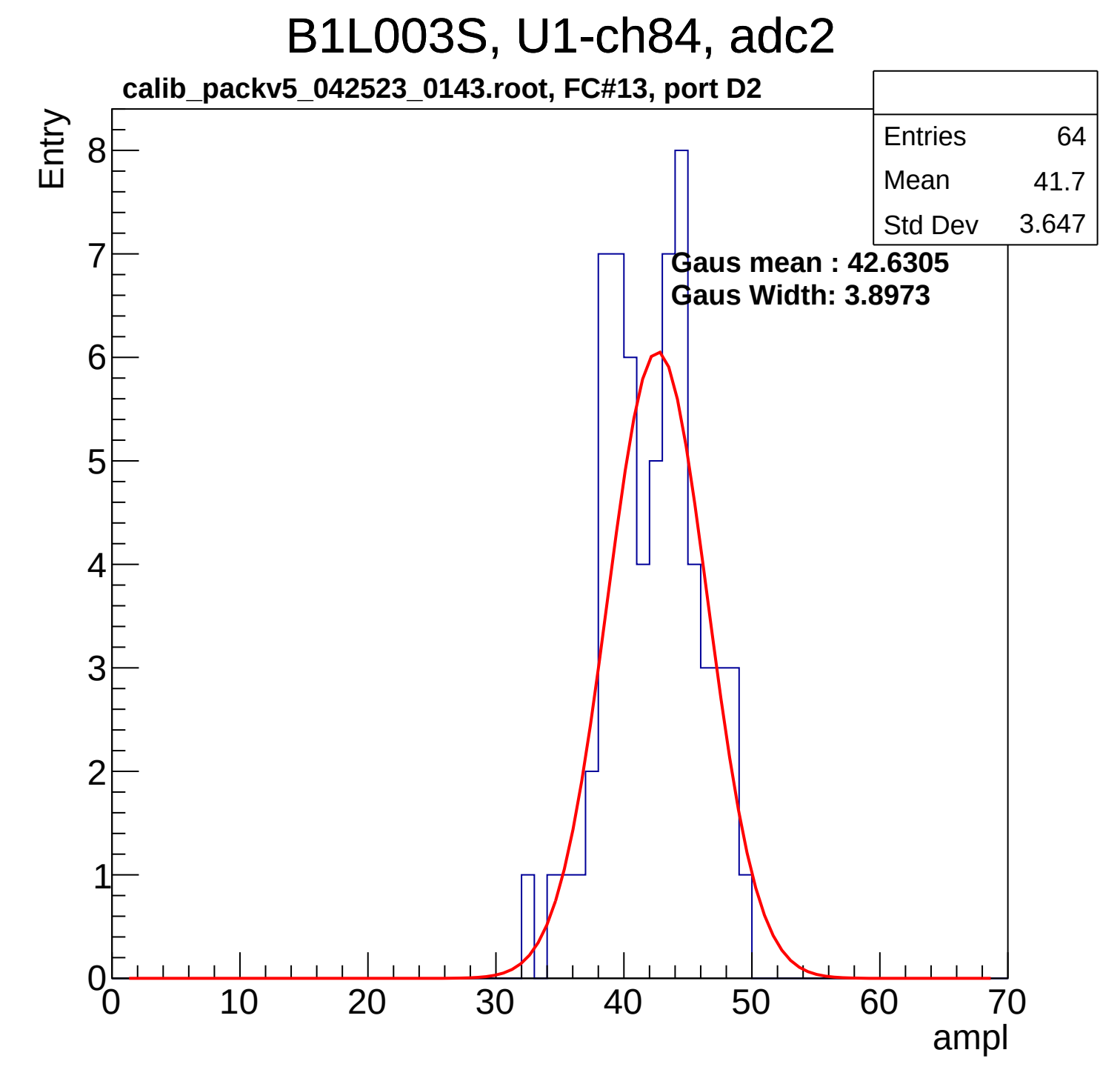
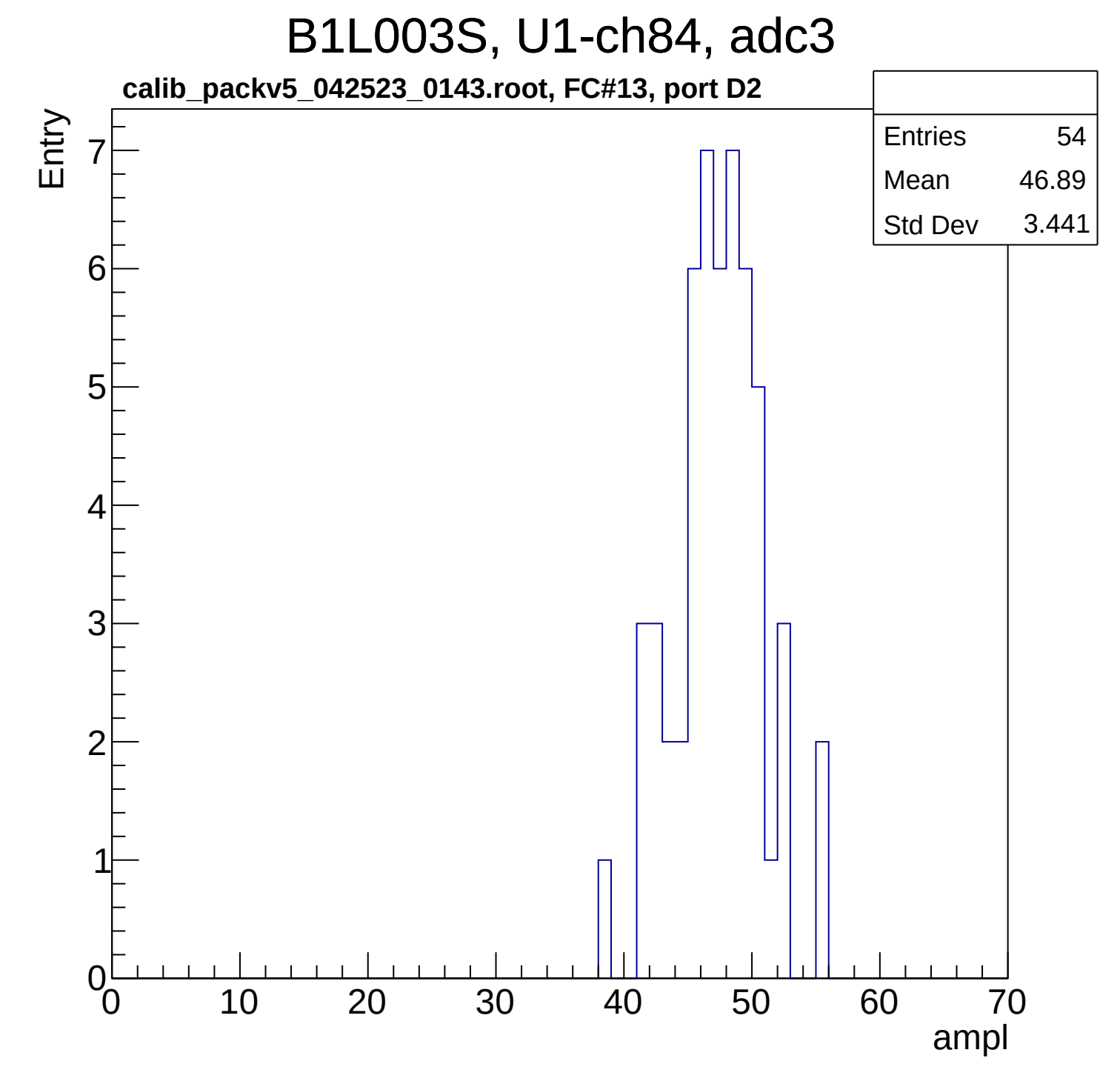
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch84, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.7
Std Dev 3.647
Gaus mean : 42.6305
Gaus Width: 3.8973
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch84, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 46.89
Std Dev 3.441
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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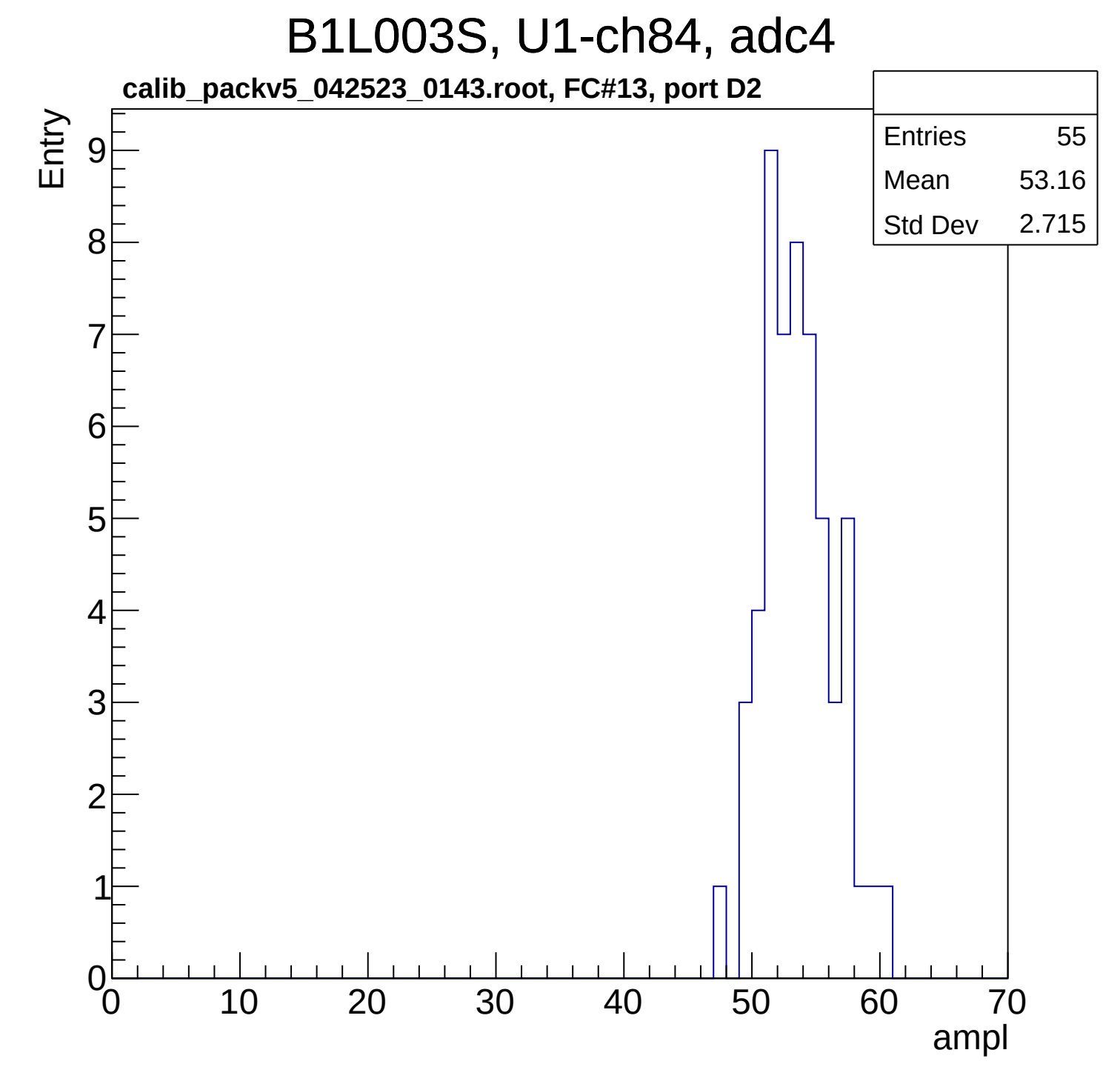
B1L003S, U1-ch84, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 53.16
Std Dev 2.715
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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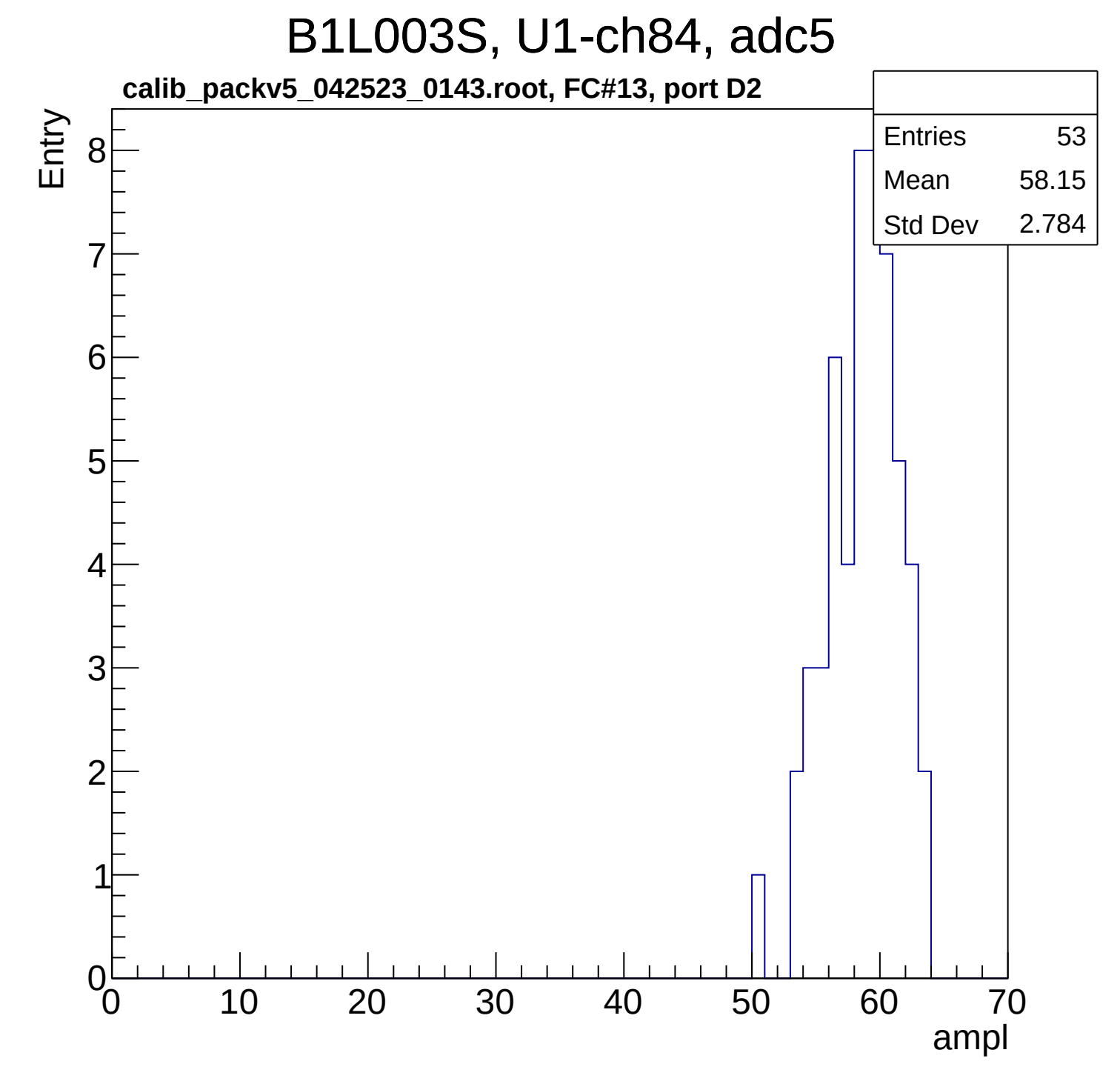
40
50
60
70


B1L003S, U1-ch84, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 58.15
Std Dev 2.784
Entry
ampl
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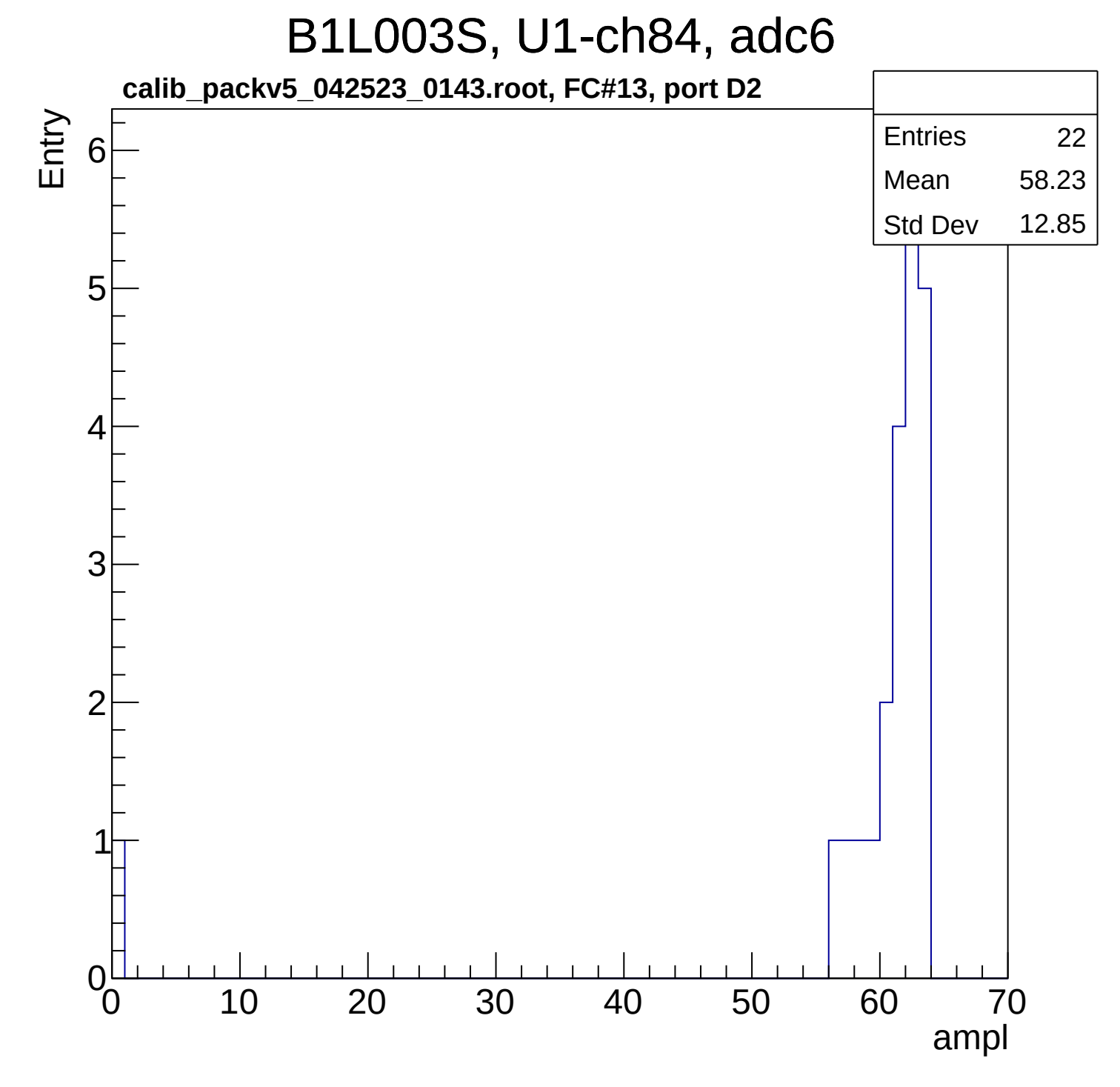
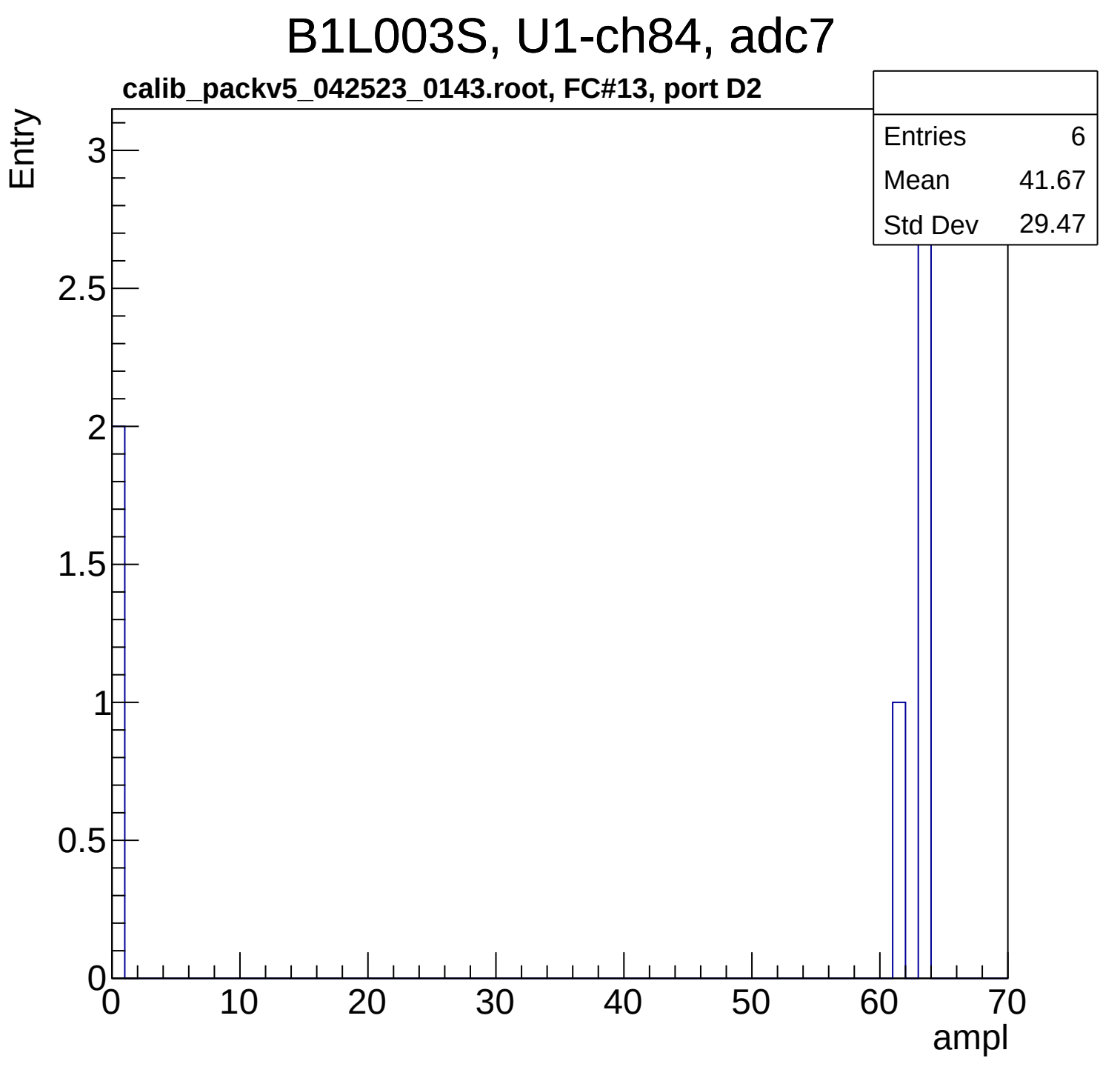
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch84, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.23
Std Dev 12.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch84, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 41.67
Std Dev 29.47
Entry
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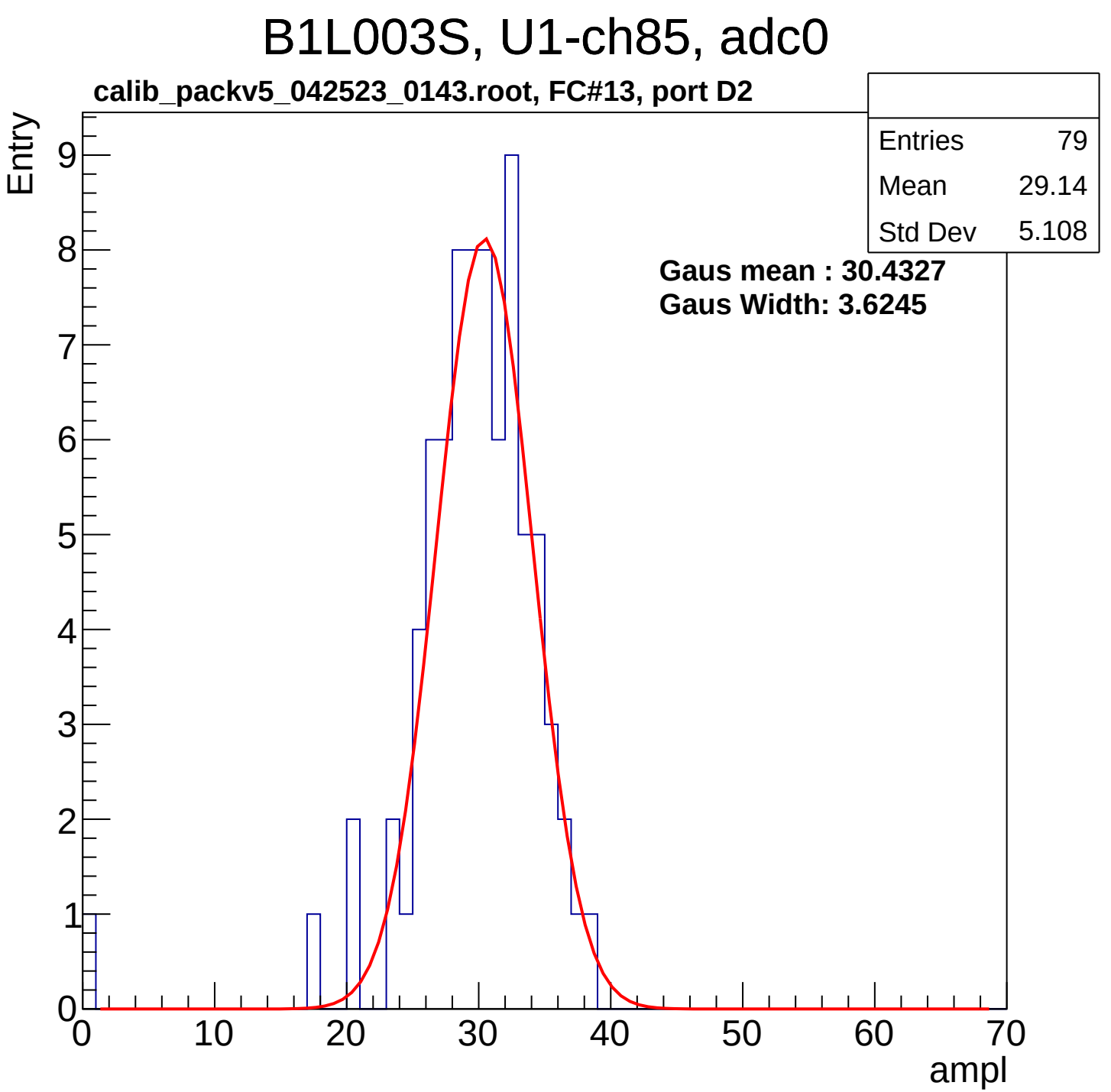
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch85, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 29.14
Std Dev 5.108
Gaus mean : 30.4327
Gaus Width: 3.6245
Entry
ampl
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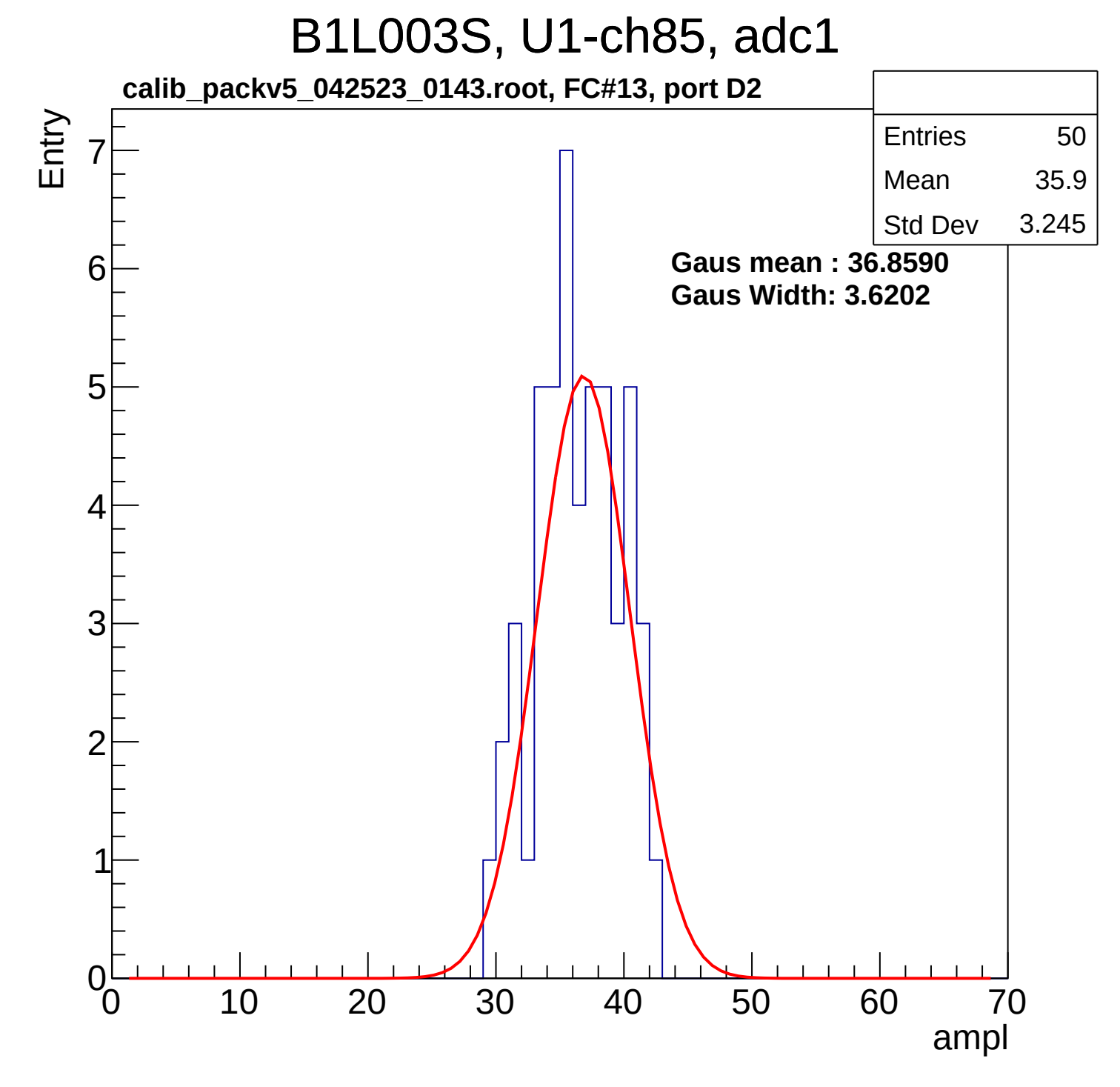
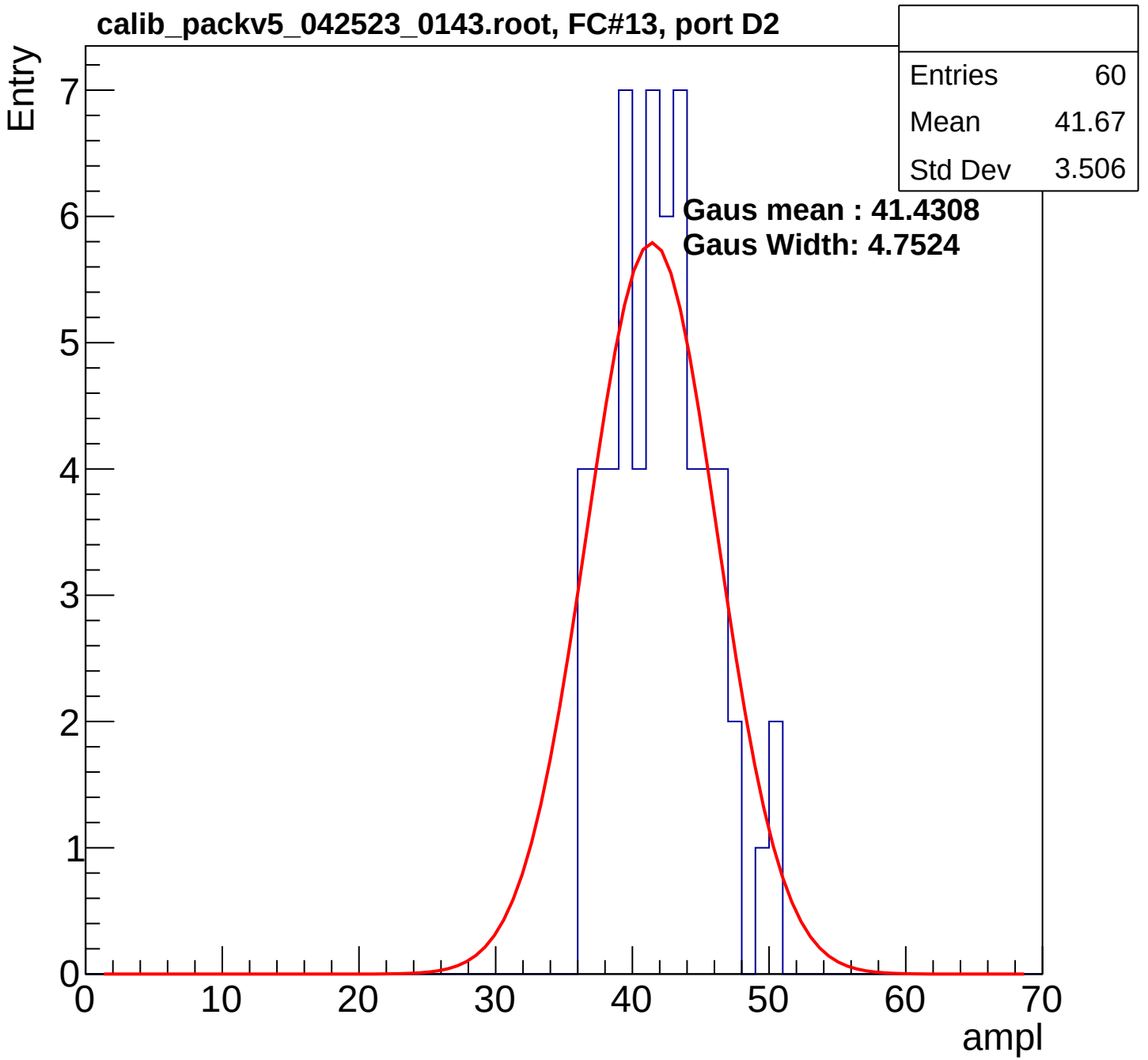
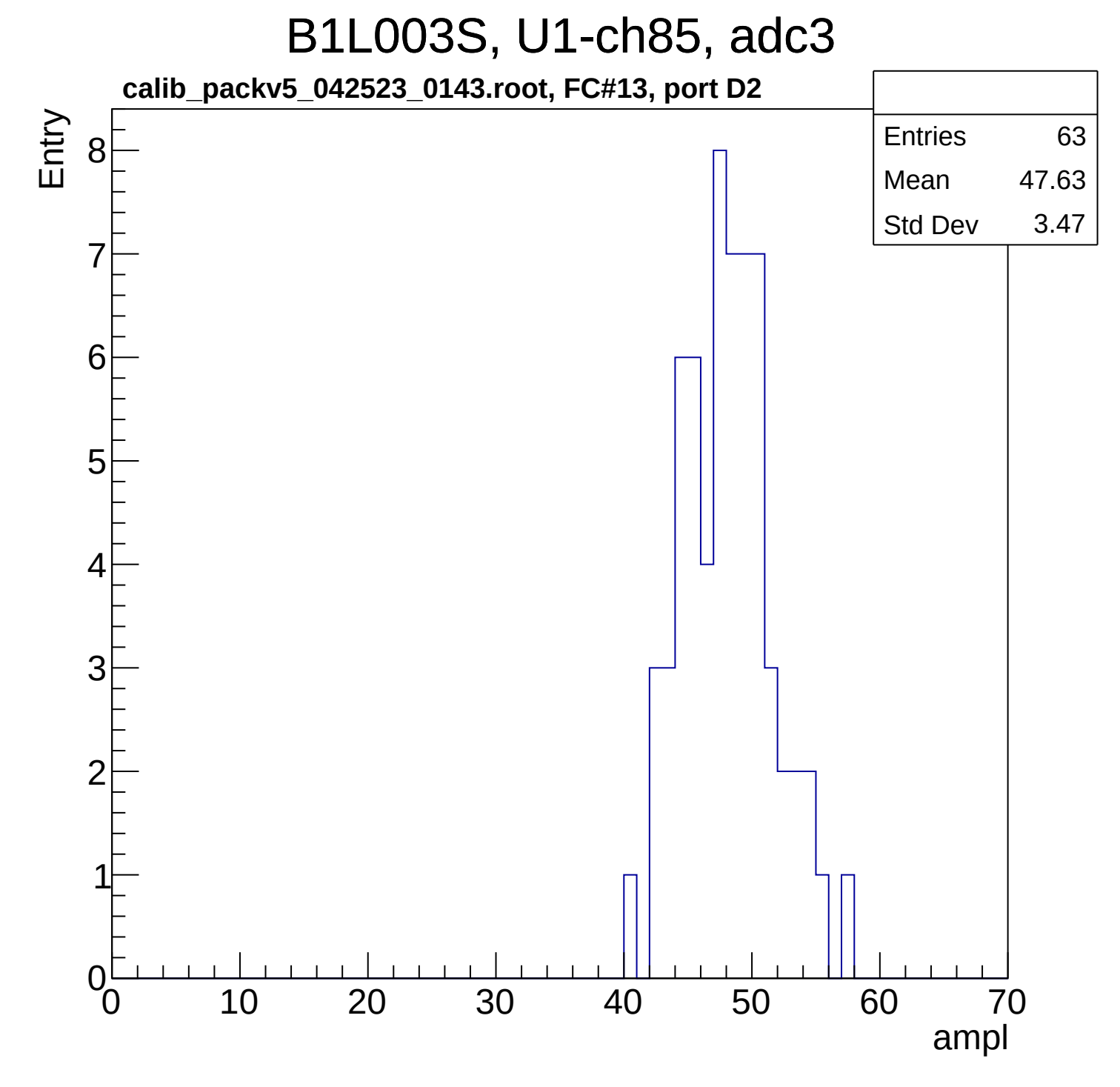
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch85, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 35.9
Std Dev 3.245
Gaus mean : 36.8590
Gaus Width: 3.6202
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch85, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 41.67
Std Dev 3.506
Gaus mean : 41.4308
Gaus Width: 4.7524
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch85, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 47.63
Std Dev 3.47
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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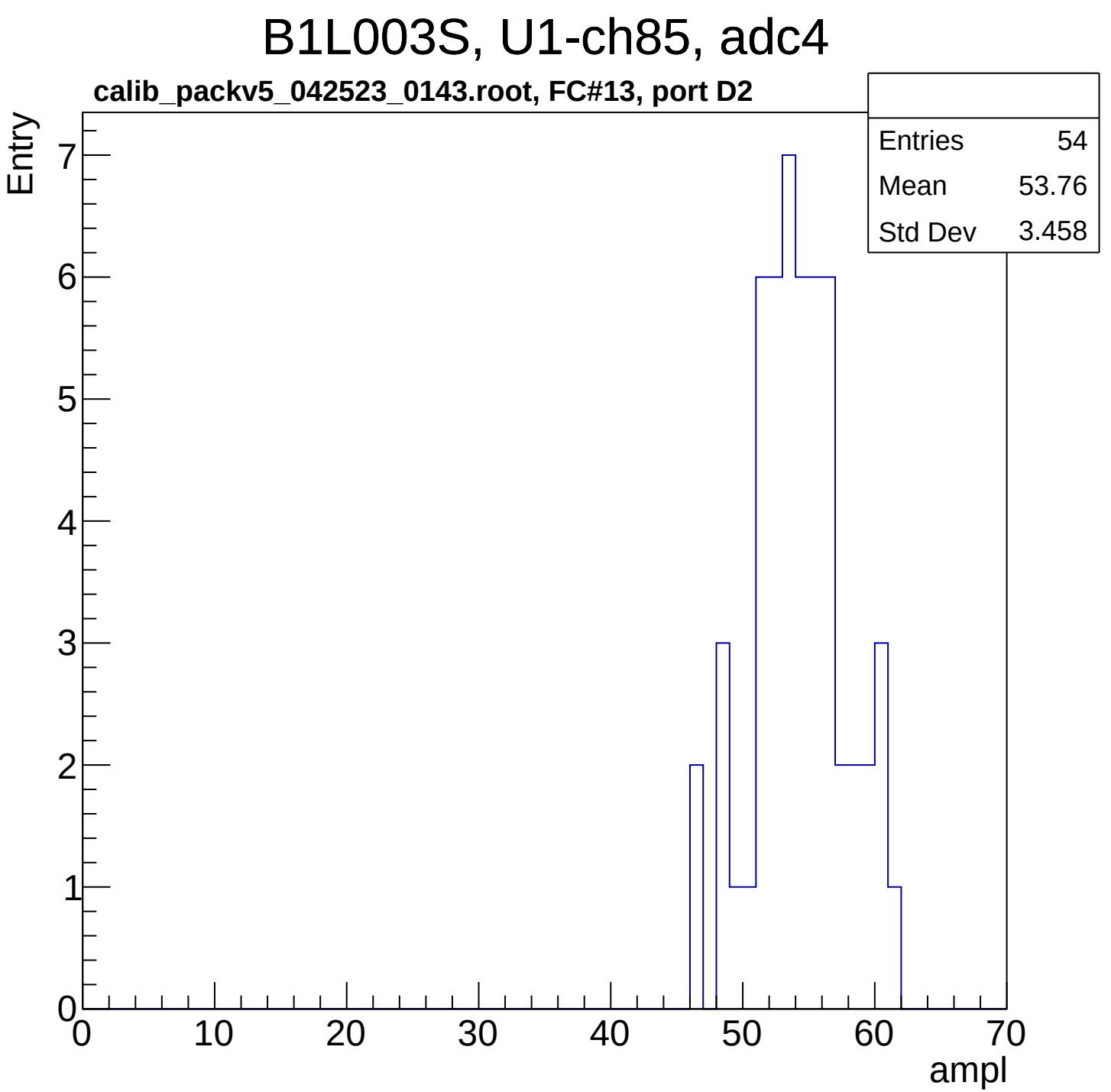
B1L003S, U1-ch85, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.76
Std Dev 3.458
Entry
ampl
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1
2
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4
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7
0
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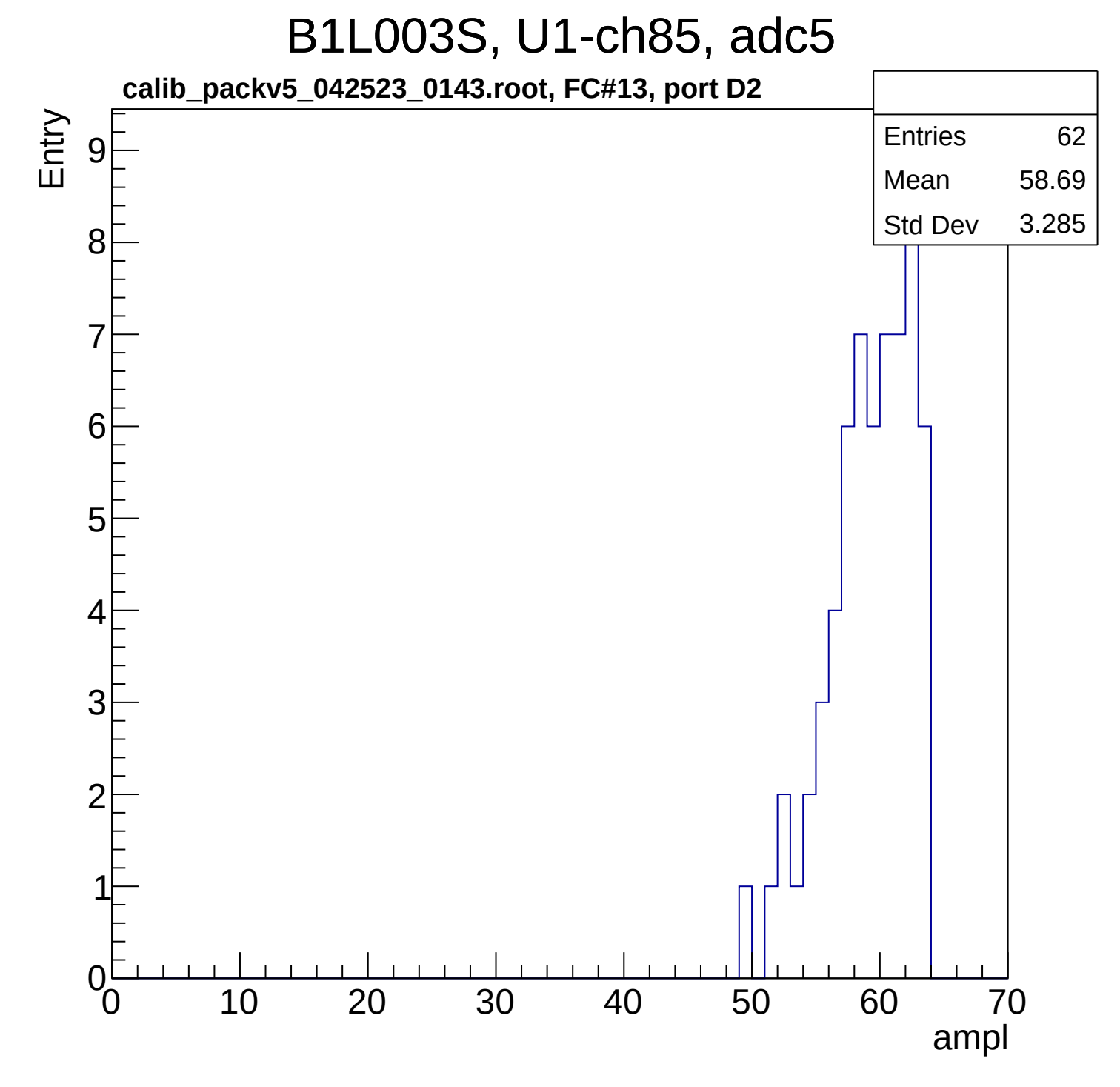
B1L003S, U1-ch85, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 58.69
Std Dev 3.285
Entry
ampl
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6
7
8
9
0
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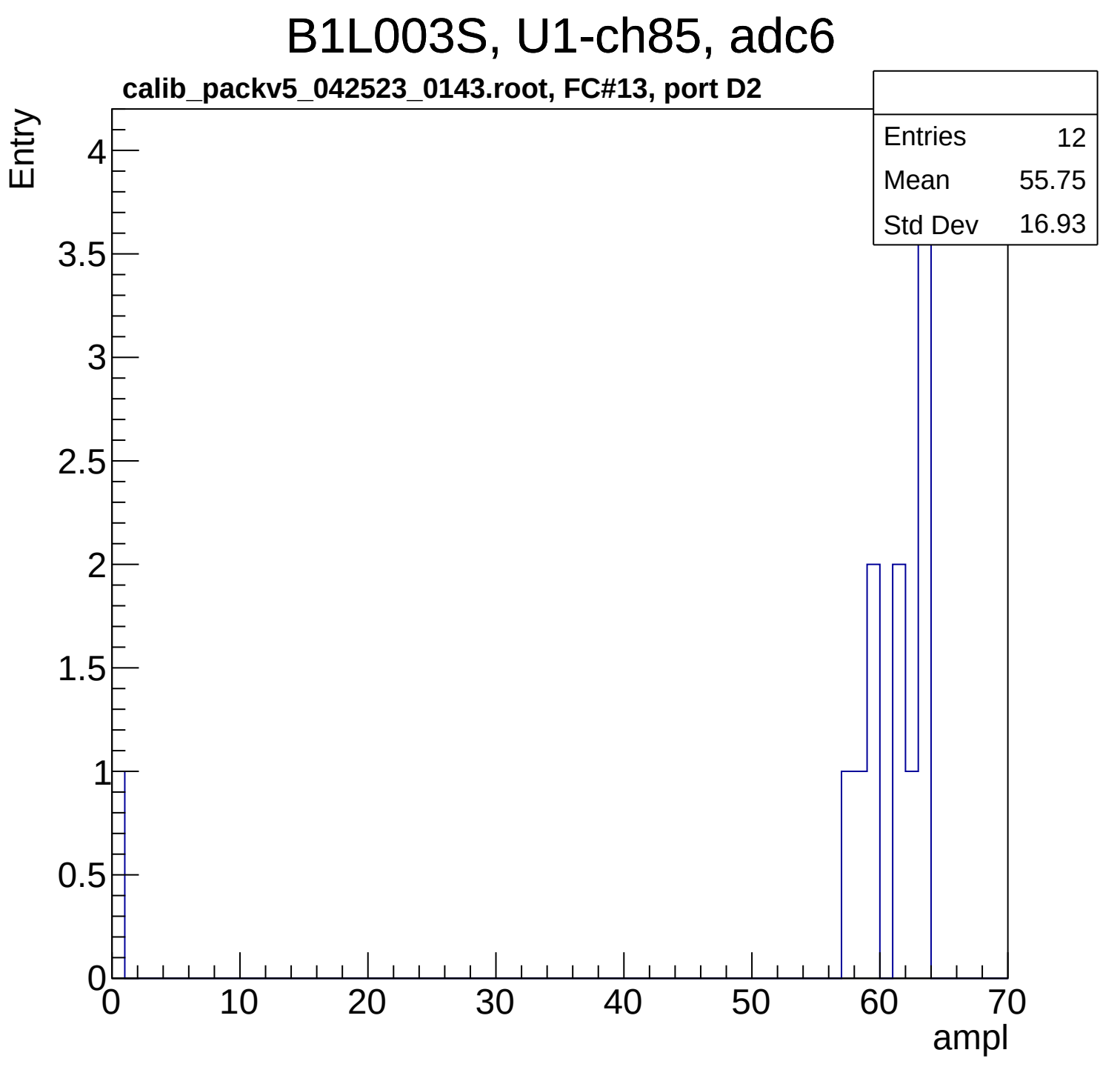
B1L003S, U1-ch85, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 55.75
Std Dev 16.93
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
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60
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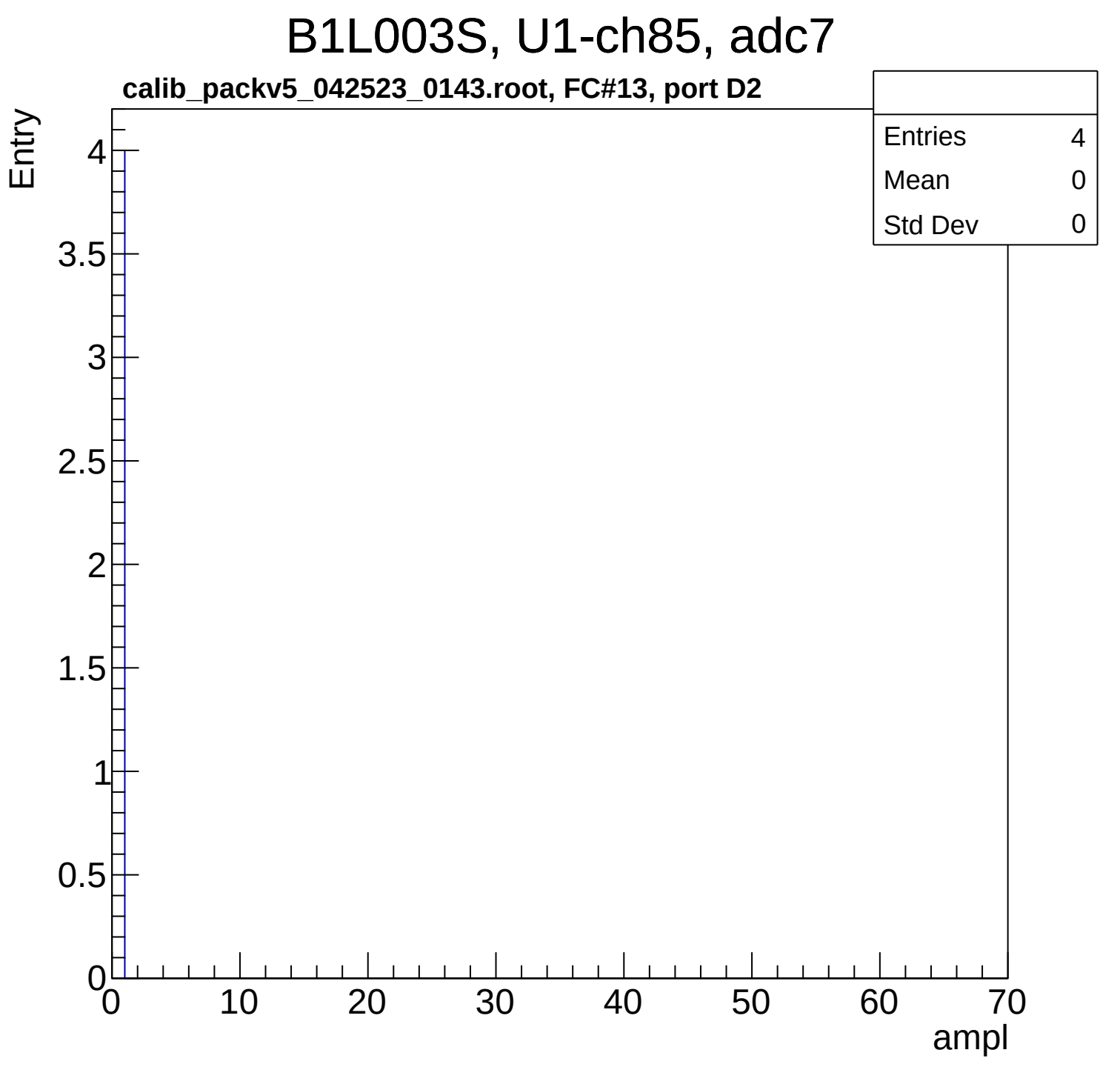
B1L003S, U1-ch85, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 0
Std Dev 0
Entry
0
0.5
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2
2.5
3
3.5
4
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10
20
30
40
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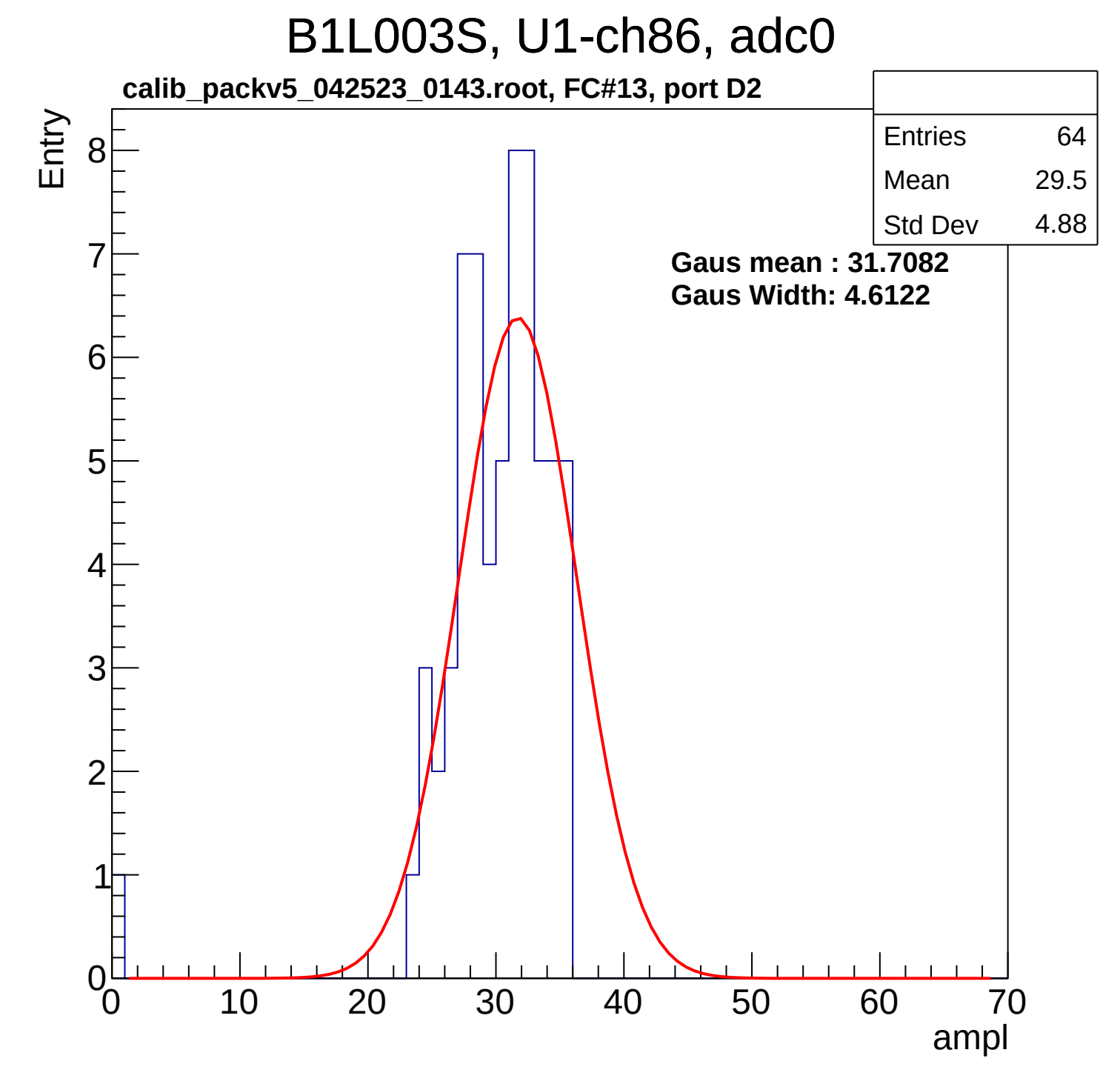
60
70
ampl


B1L003S, U1-ch86, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.5
Std Dev 4.88
Gaus mean : 31.7082
Gaus Width: 4.6122
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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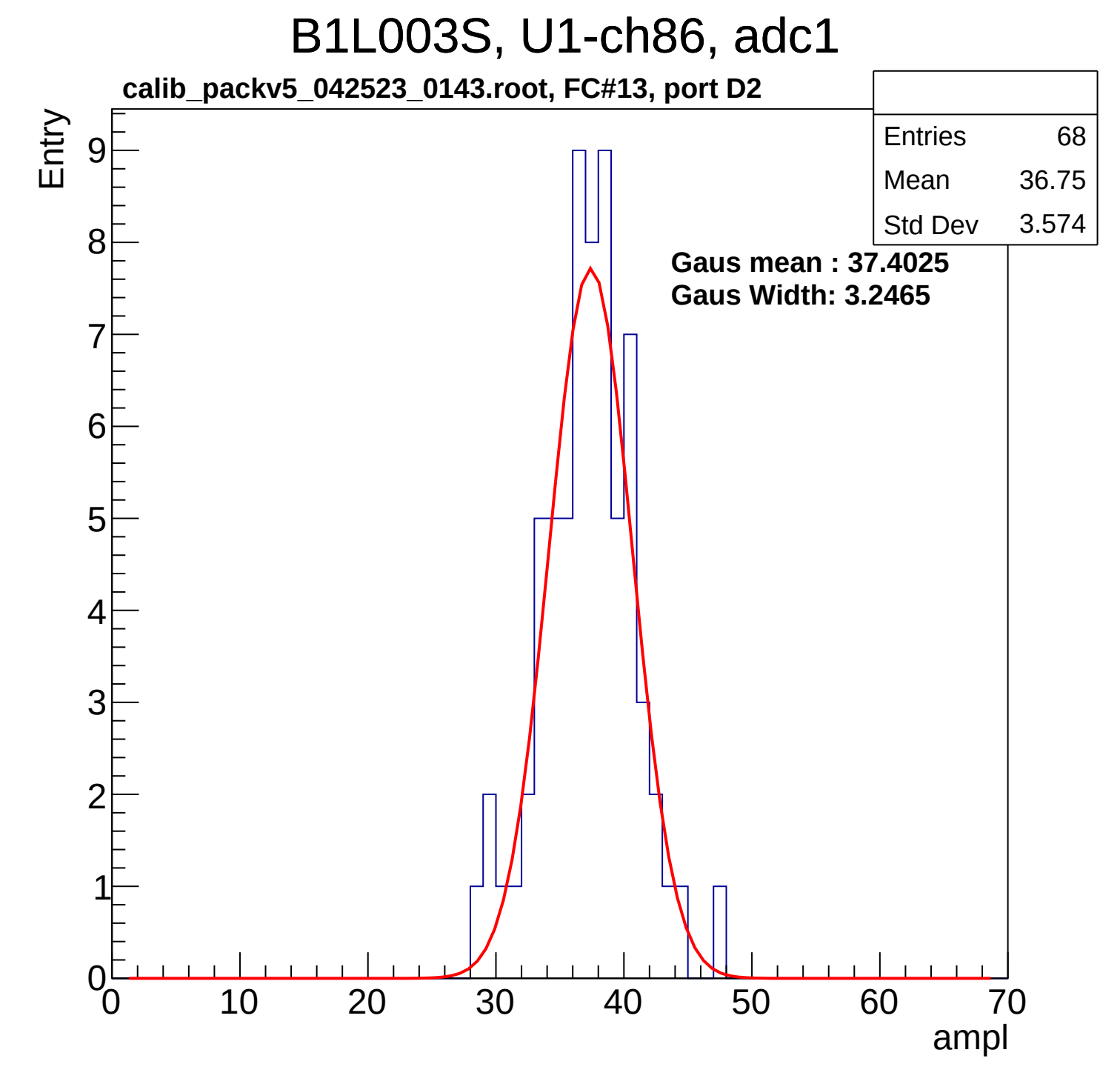
B1L003S, U1-ch86, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 36.75
Std Dev 3.574
Gaus mean : 37.4025
Gaus Width: 3.2465
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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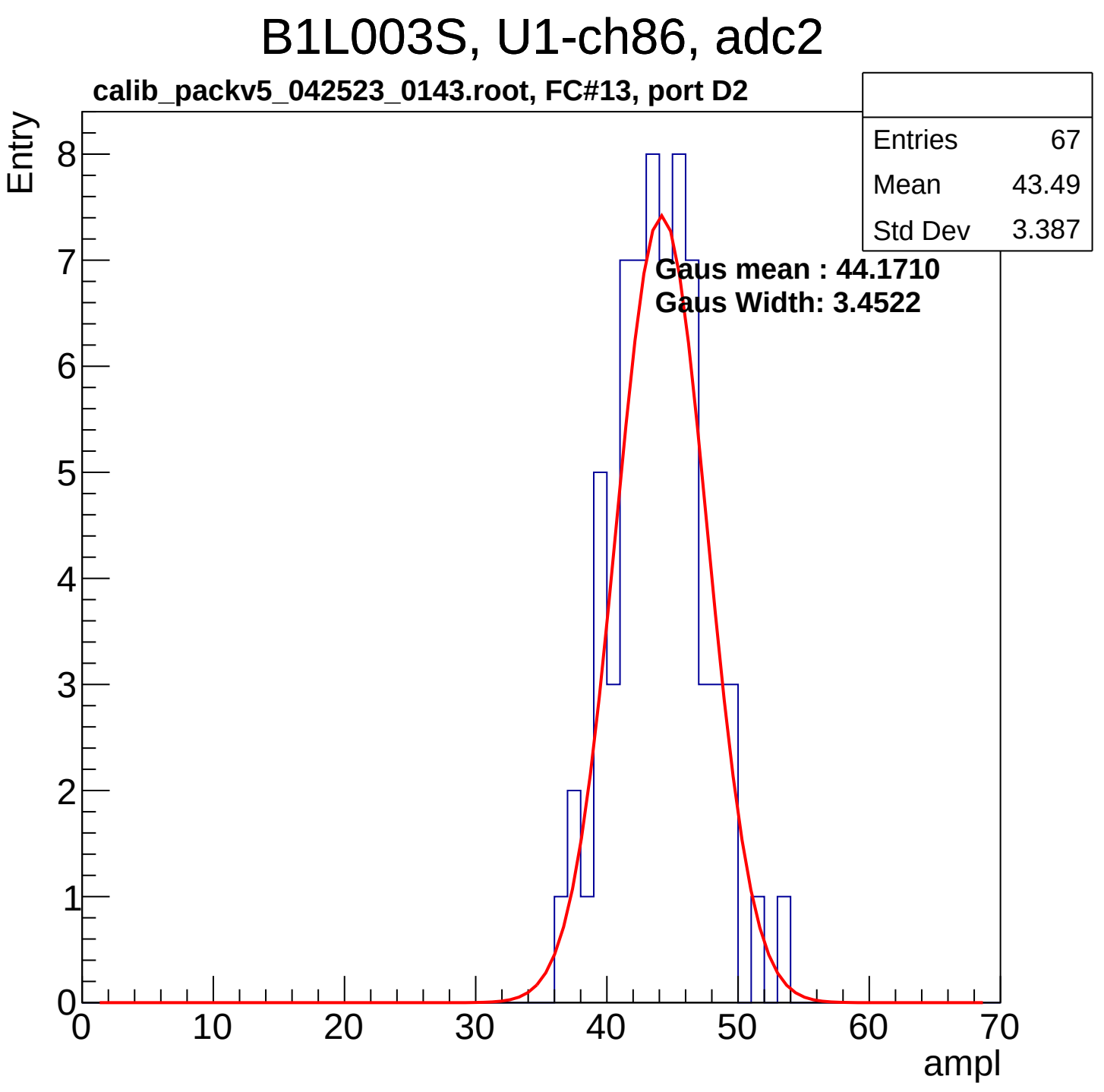
40
50
60
70


B1L003S, U1-ch86, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 43.49
Std Dev 3.387
Gaus mean : 44.1710
Gaus Width: 3.4522
Entry
ampl
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1
2
3
4
5
6
7
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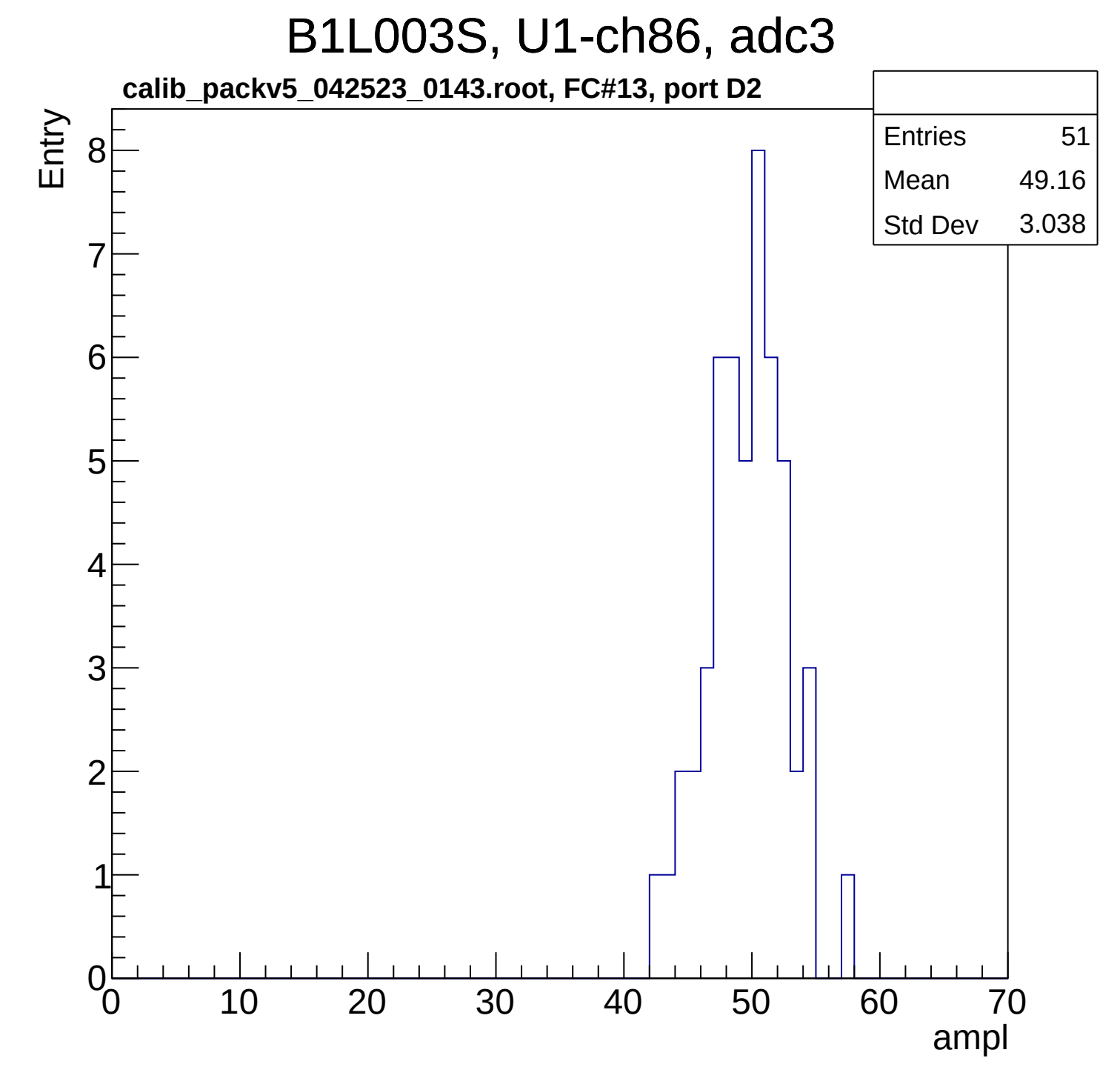
B1L003S, U1-ch86, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 49.16
Std Dev 3.038
Entry
ampl
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1
2
3
4
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0
10
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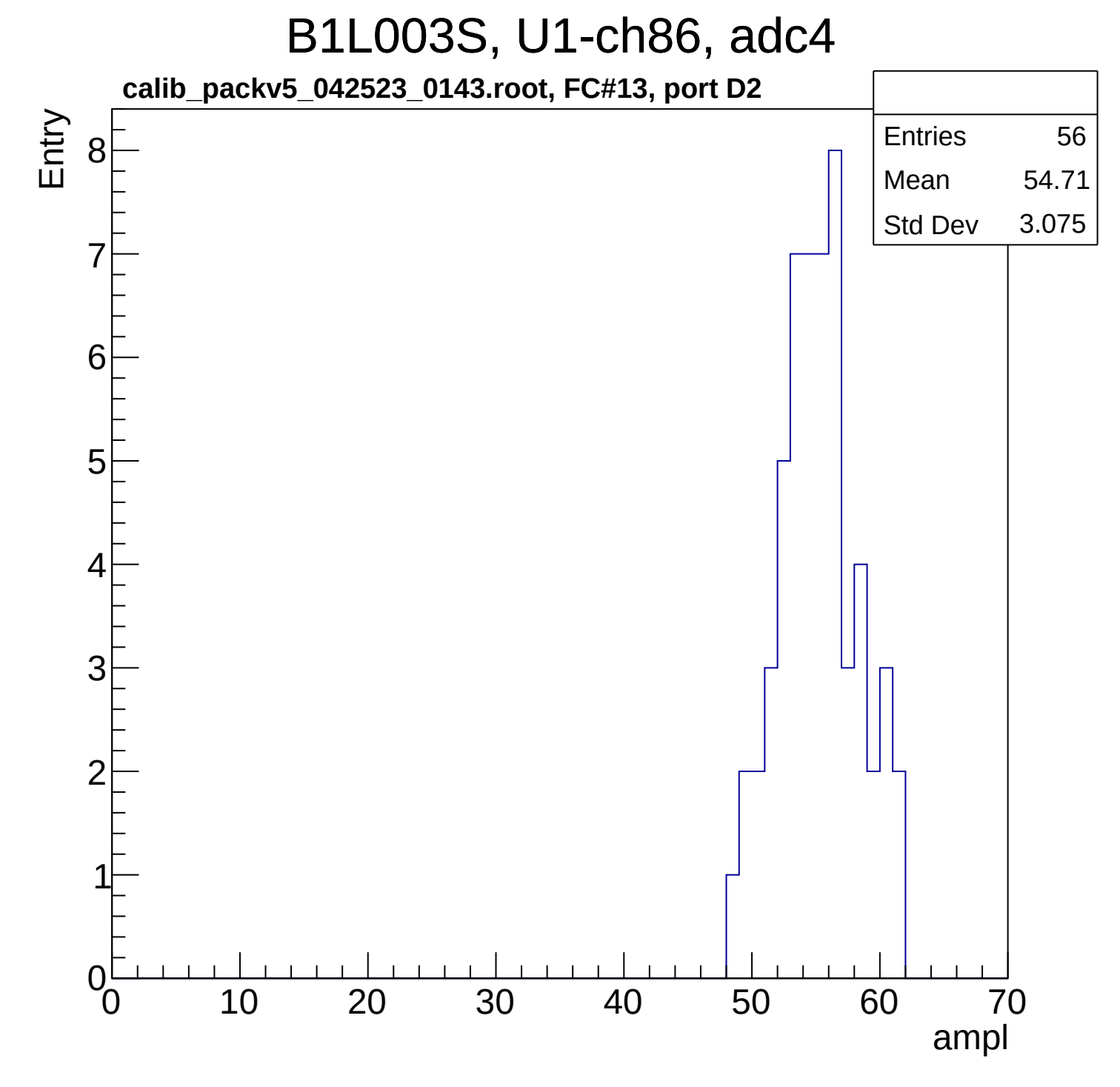
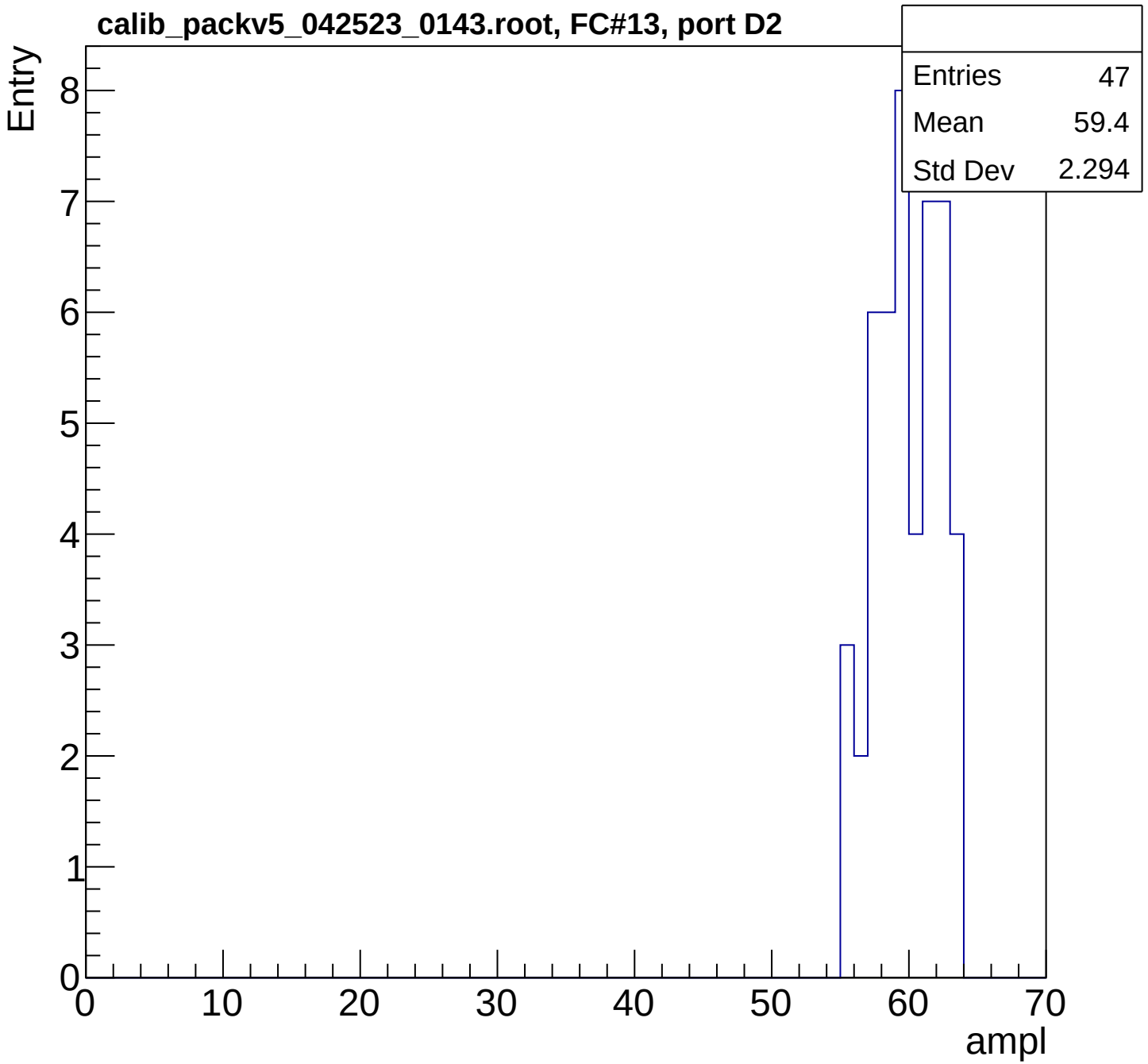

B1L003S, U1-ch86, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.71
Std Dev 3.075
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch86, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 59.4
Std Dev 2.294
Entry
0
1
2
3
4
5
6
7
8
0
10
20
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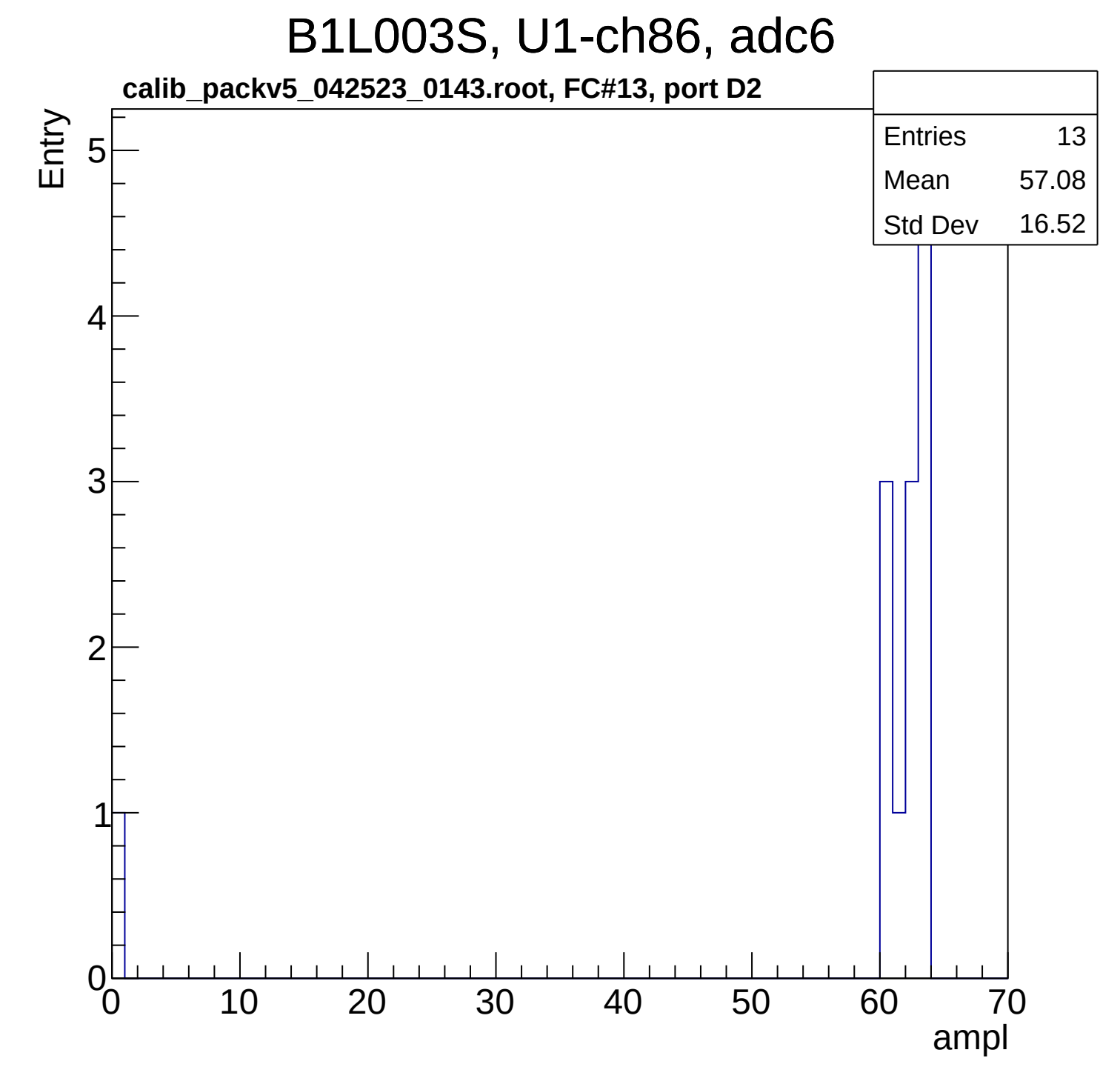
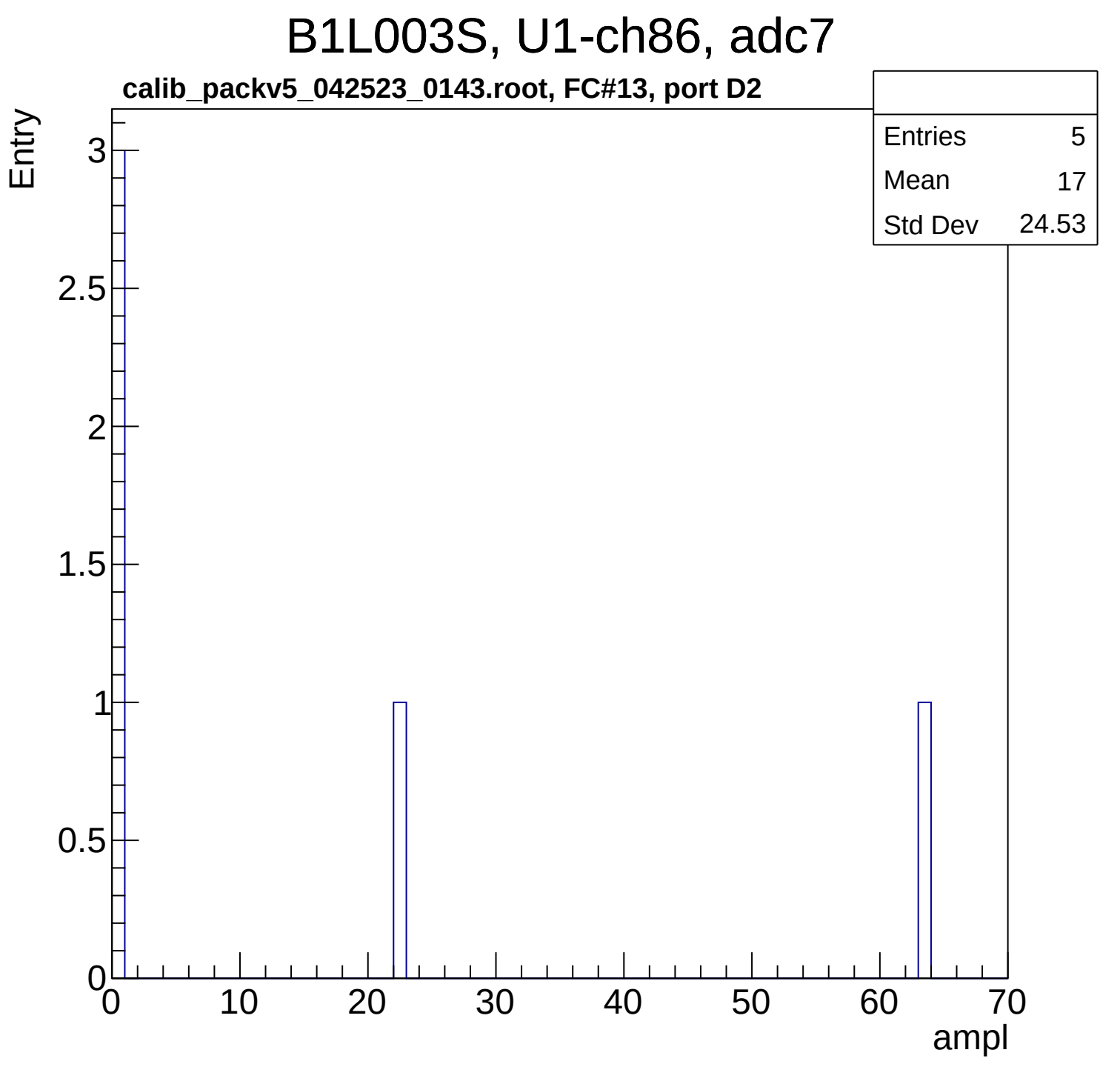
50
60
70
ampl


B1L003S, U1-ch86, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 57.08
Std Dev 16.52
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch86, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 17
Std Dev 24.53
Entry
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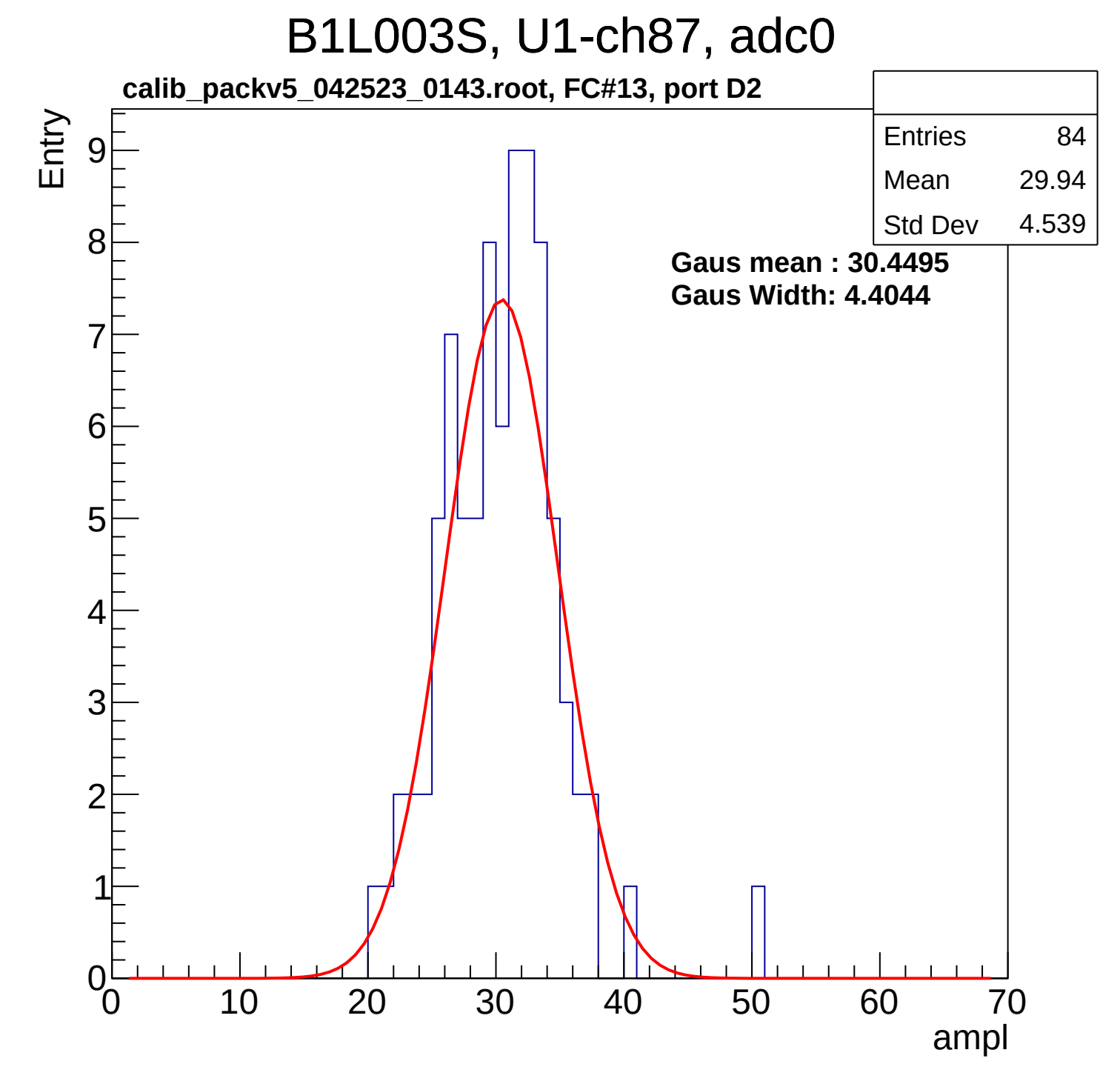
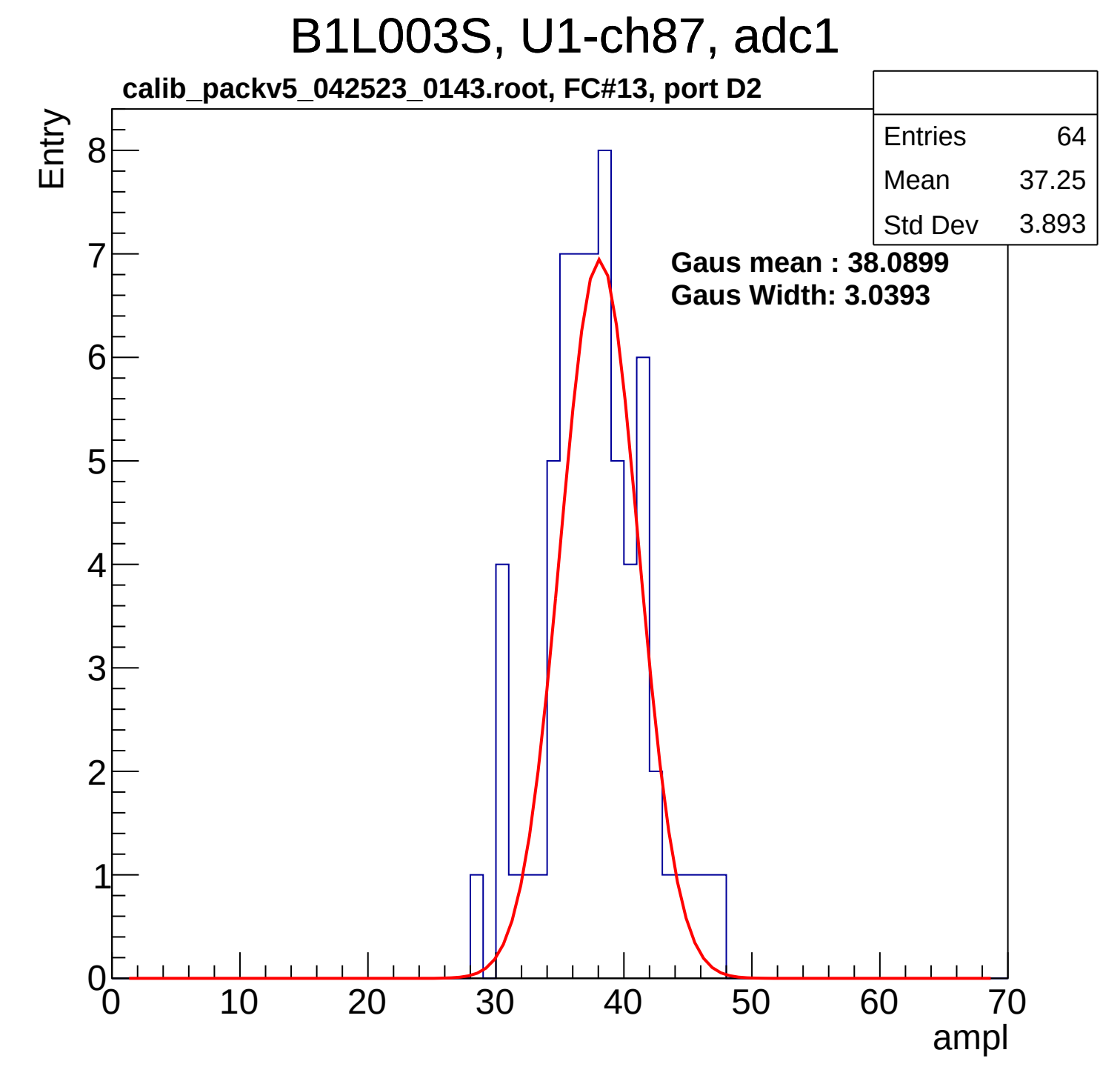
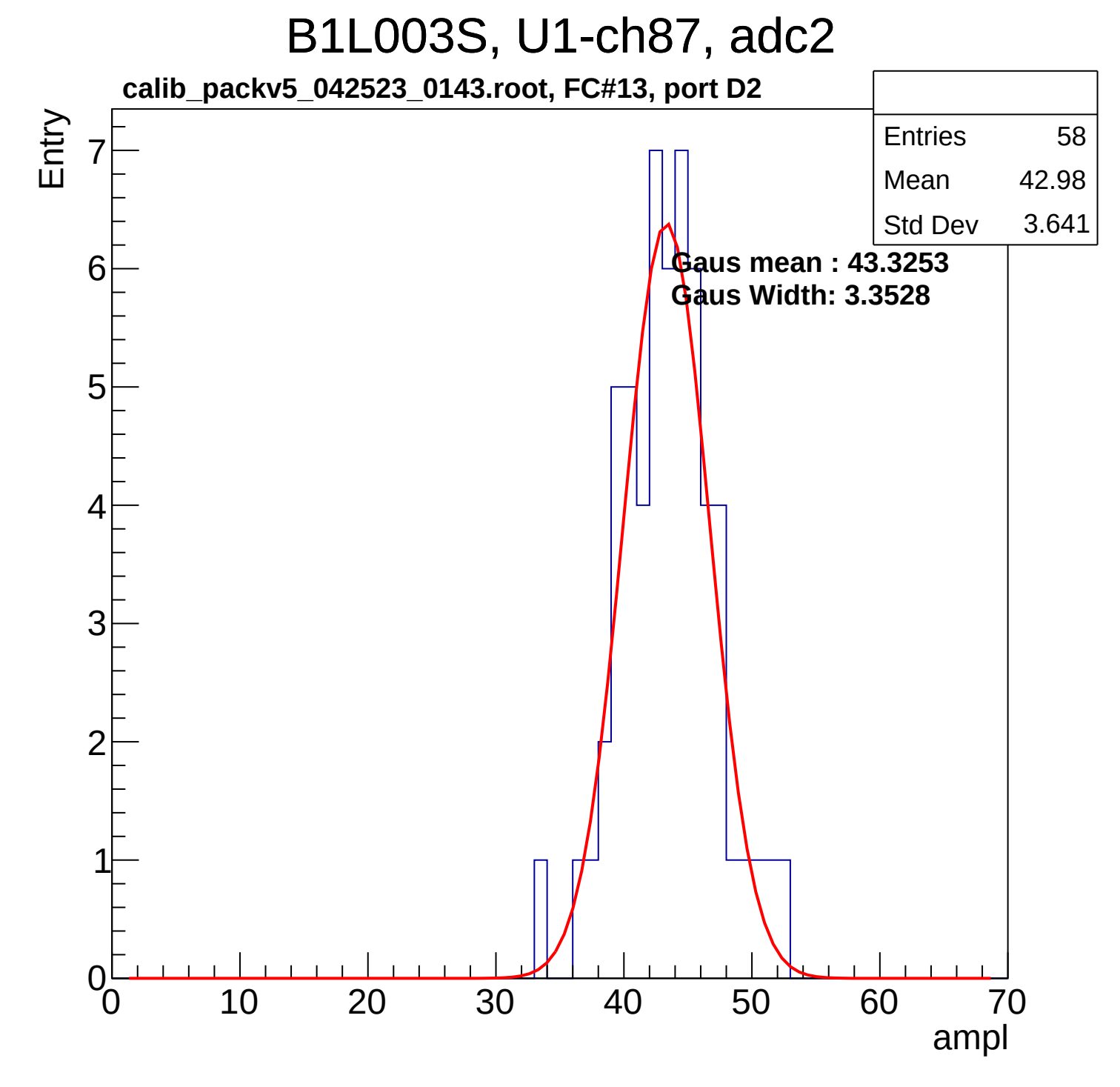
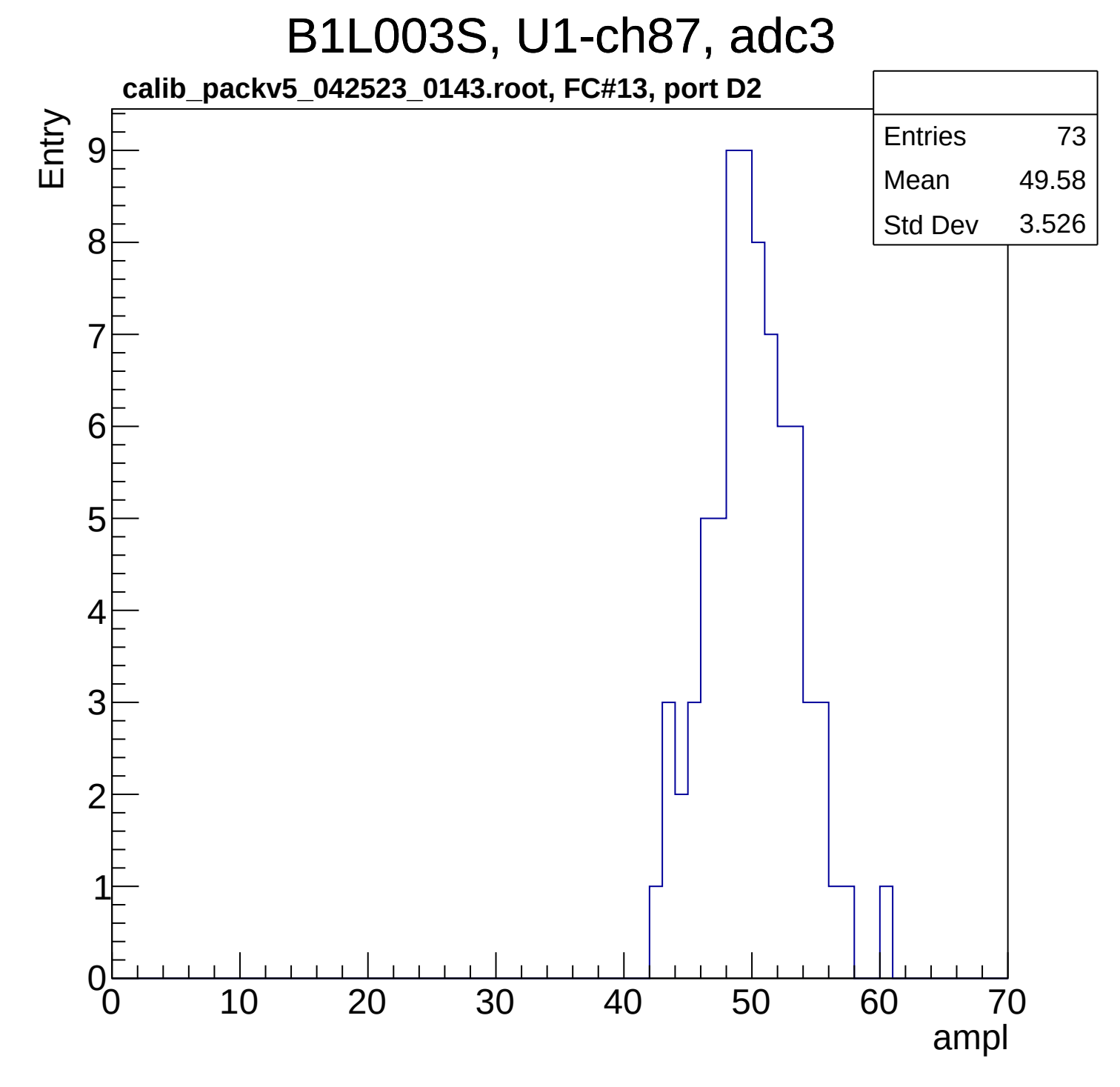
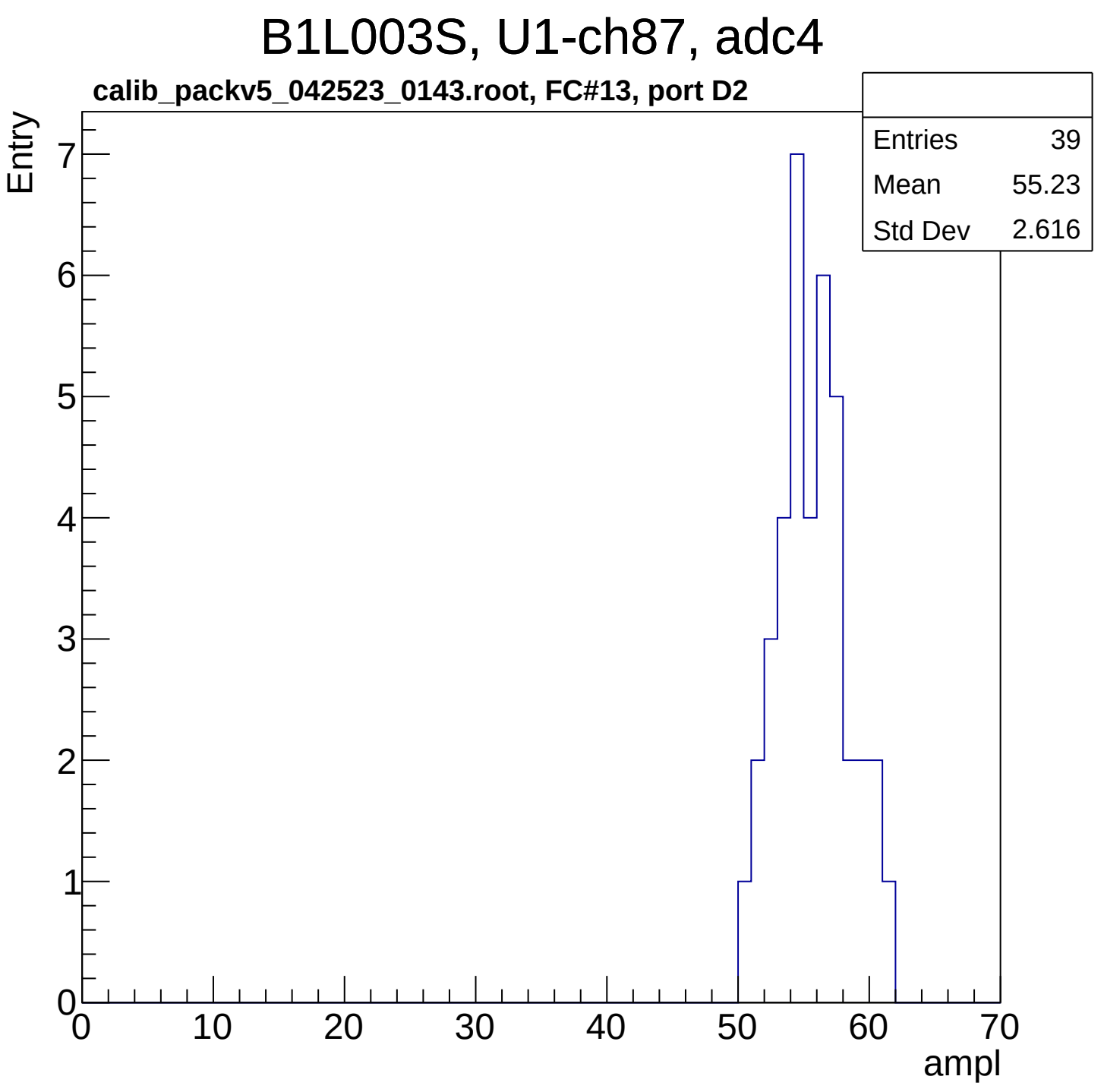
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

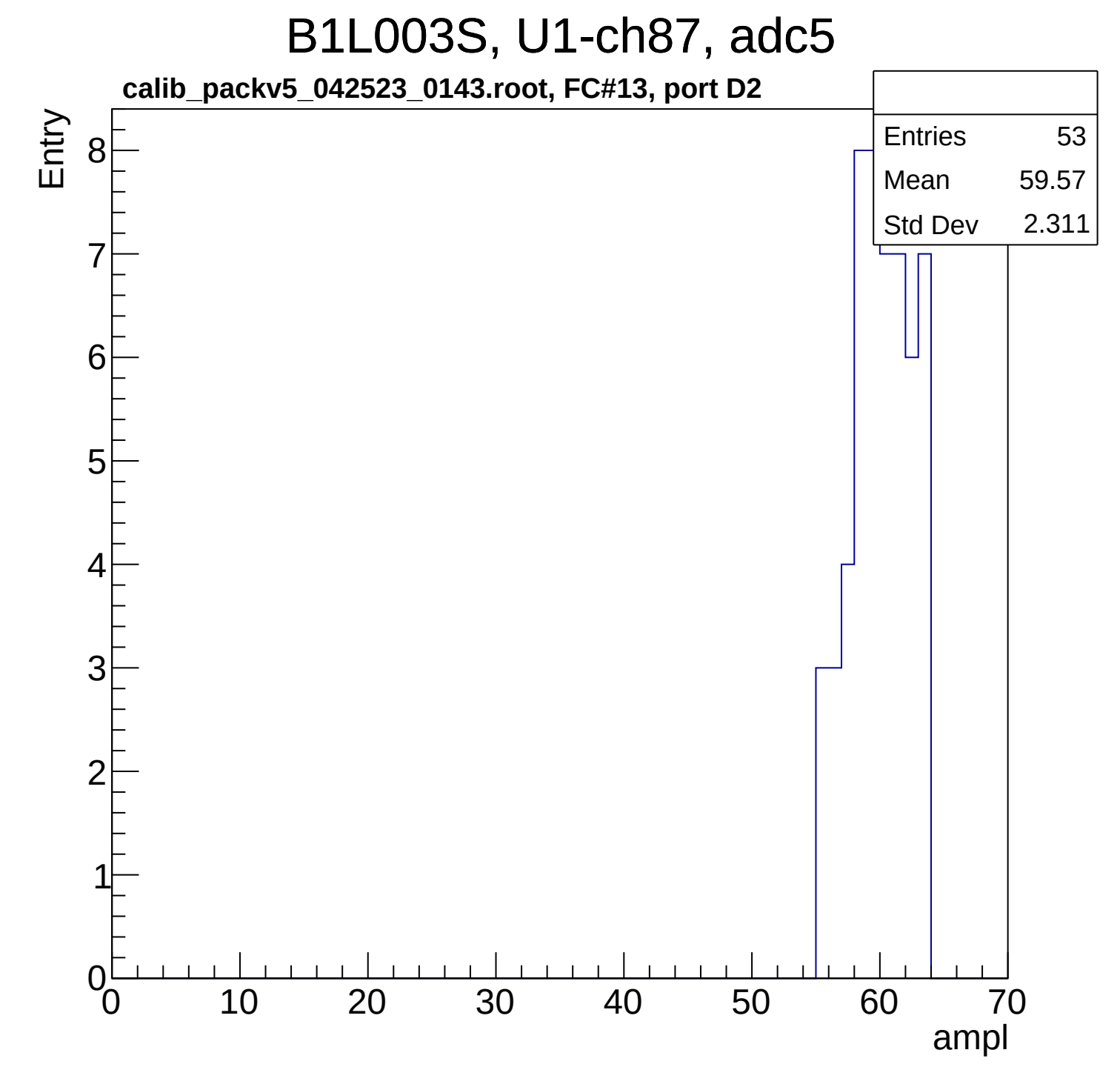

B1L003S, U1-ch87, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 29.94
Std Dev 4.539
Gaus mean : 30.4495
Gaus Width: 4.4044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

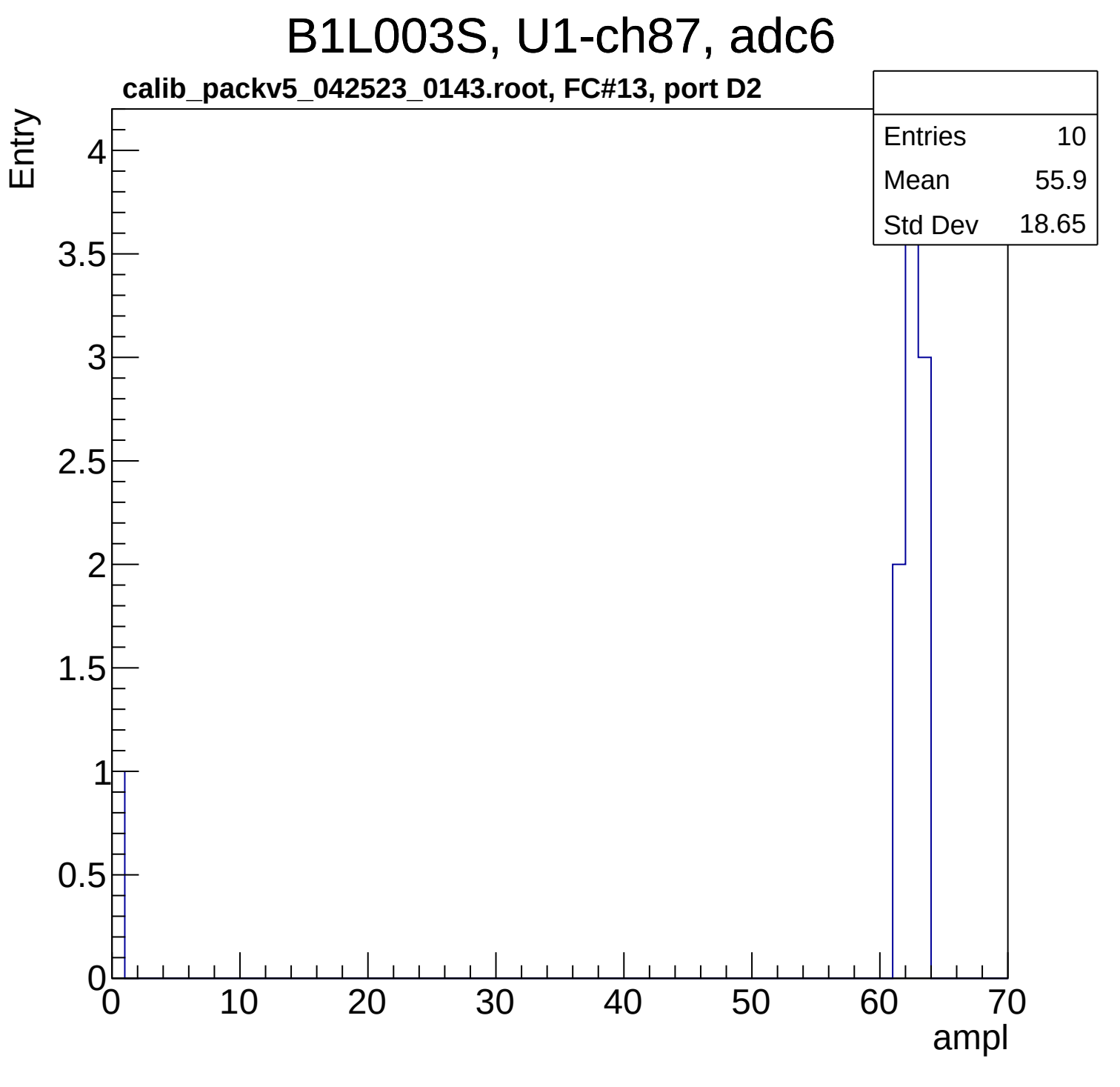

B1L003S, U1-ch87, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 37.25
Std Dev 3.893
Gaus mean : 38.0899
Gaus Width: 3.0393
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

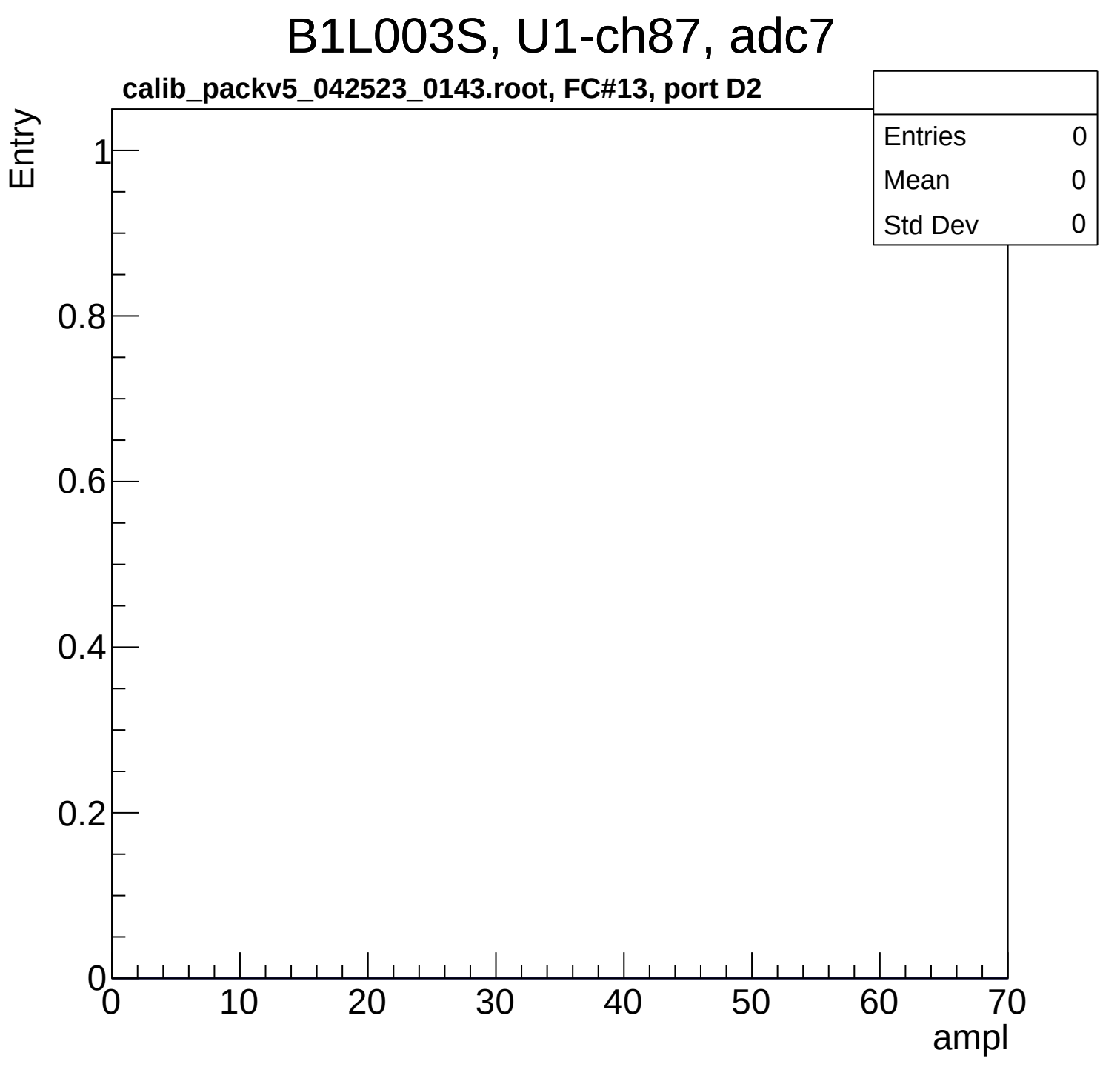

B1L003S, U1-ch87, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.98
Std Dev 3.641
Gaus mean : 43.3253
Gaus Width: 3.3528
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

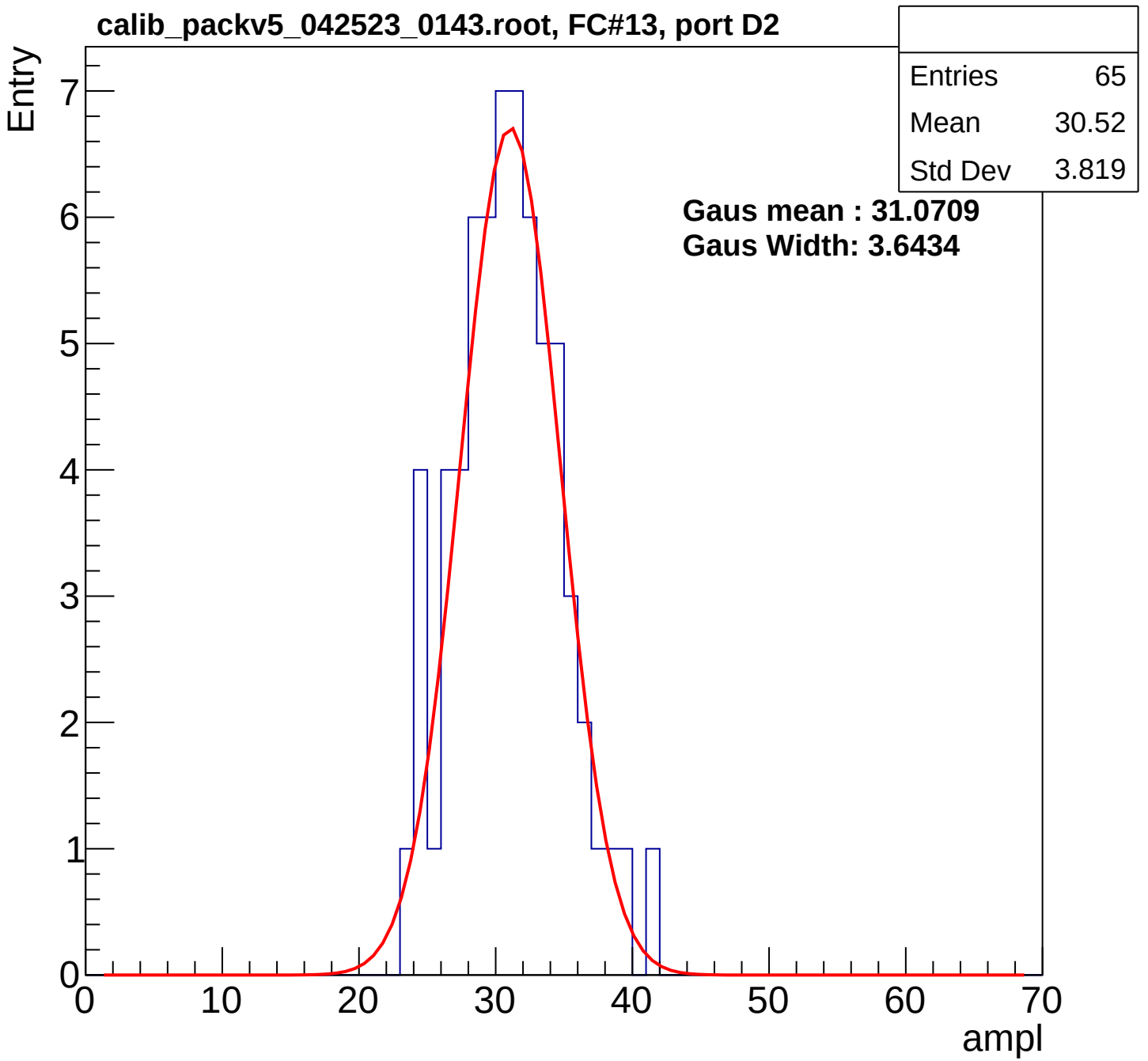

B1L003S, U1-ch87, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 49.58
Std Dev 3.526
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

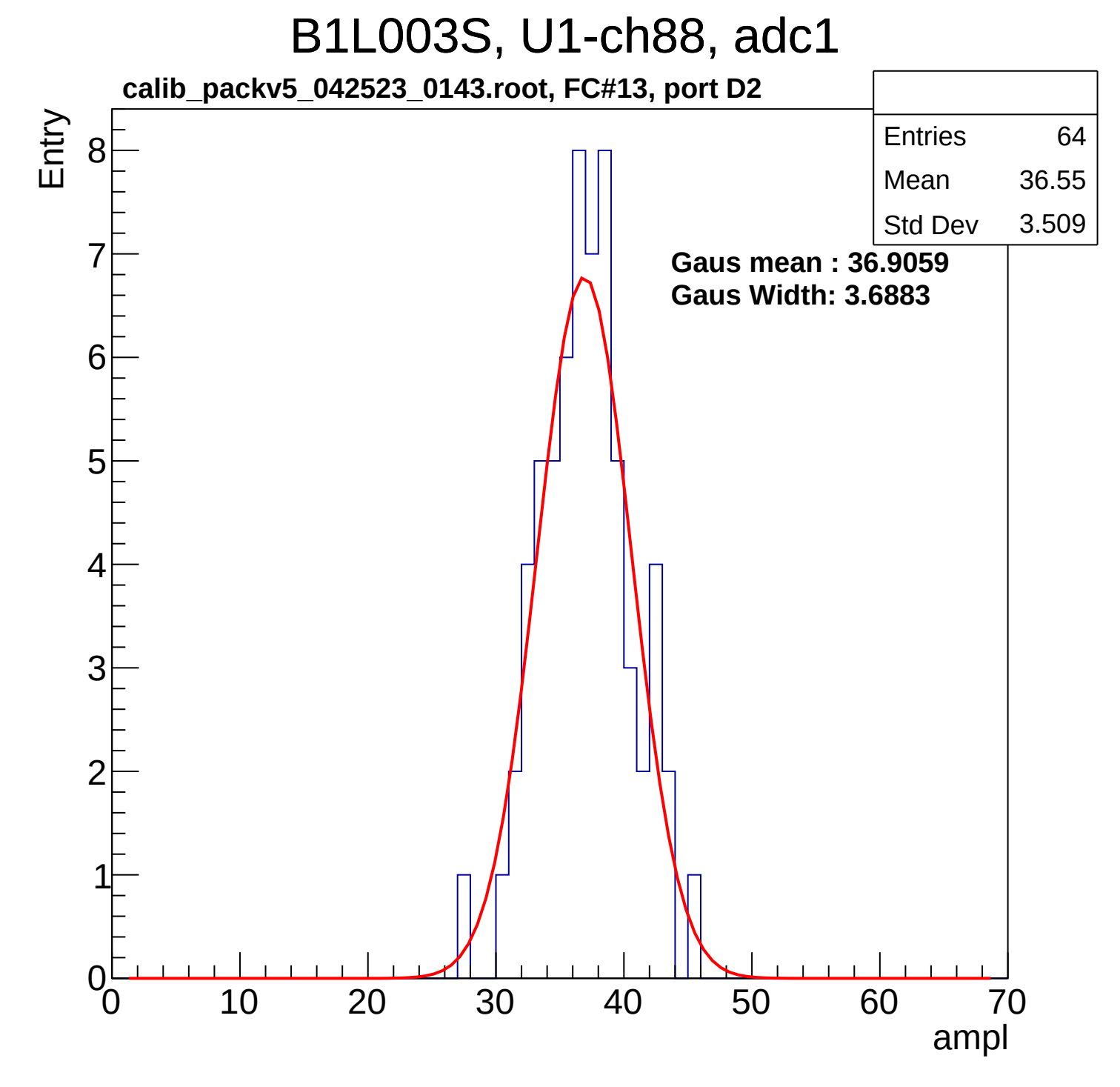

B1L003S, U1-ch87, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 55.23
Std Dev 2.616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

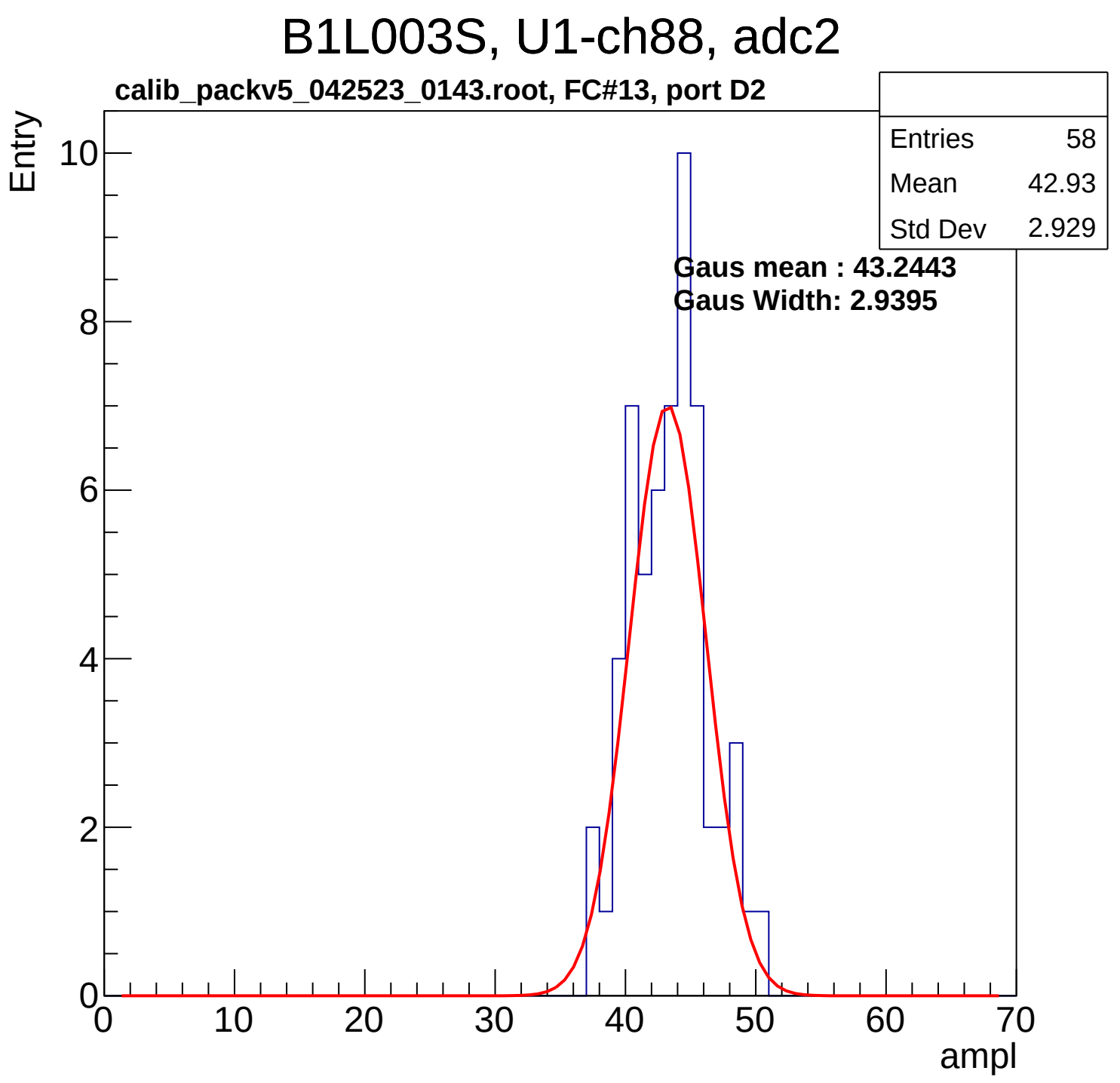

B1L003S, U1-ch87, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.57
Std Dev 2.311
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

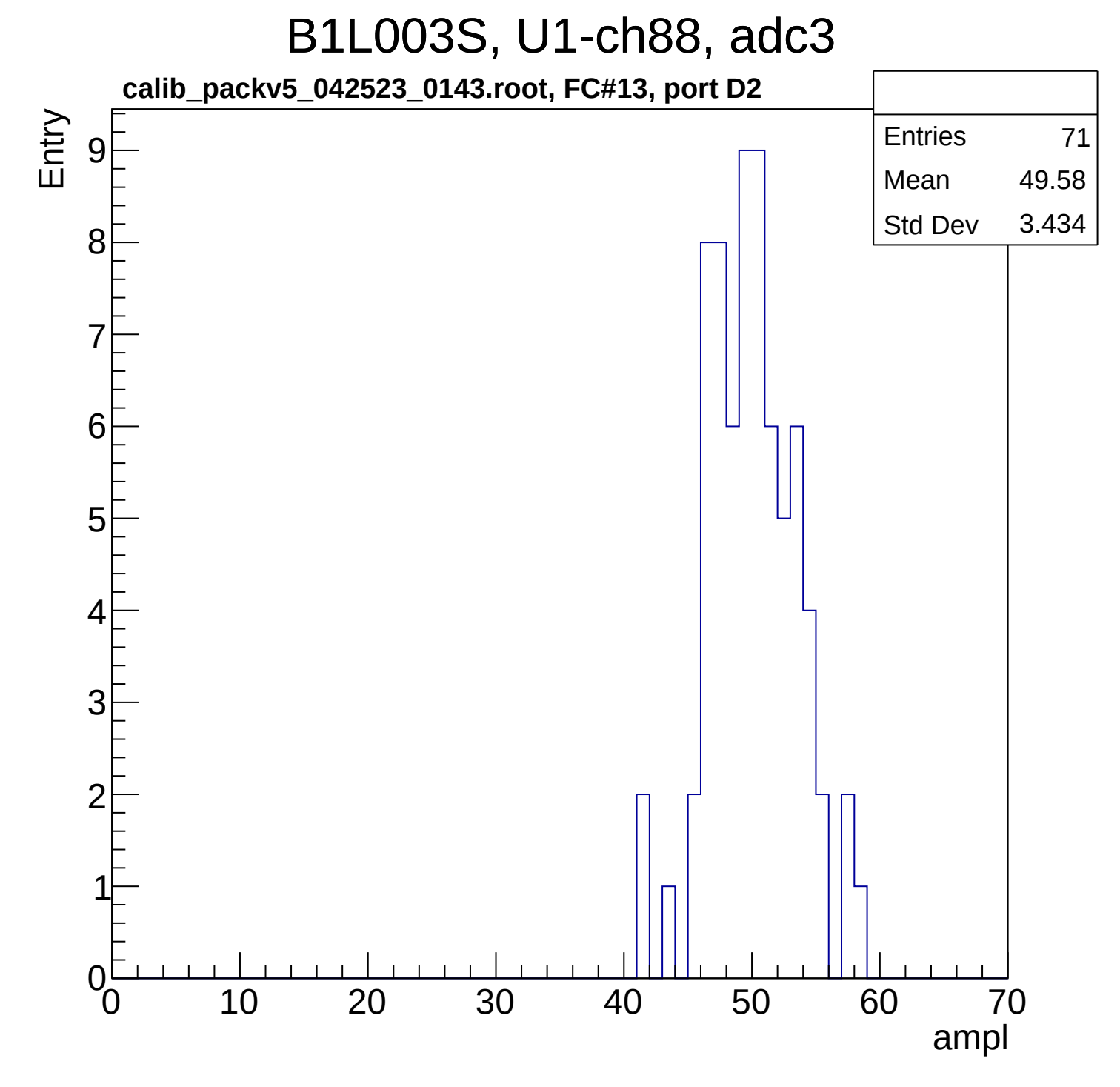

B1L003S, U1-ch87, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.9
Std Dev 18.65
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch87, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch88, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.52
Std Dev 3.819
Gaus mean : 31.0709
Gaus Width: 3.6434
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch88, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 36.55
Std Dev 3.509
Gaus mean : 36.9059
Gaus Width: 3.6883
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch88, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 42.93
Std Dev 2.929
Gaus mean : 43.2443
Gaus Width: 2.9395
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch88, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.58
Std Dev 3.434
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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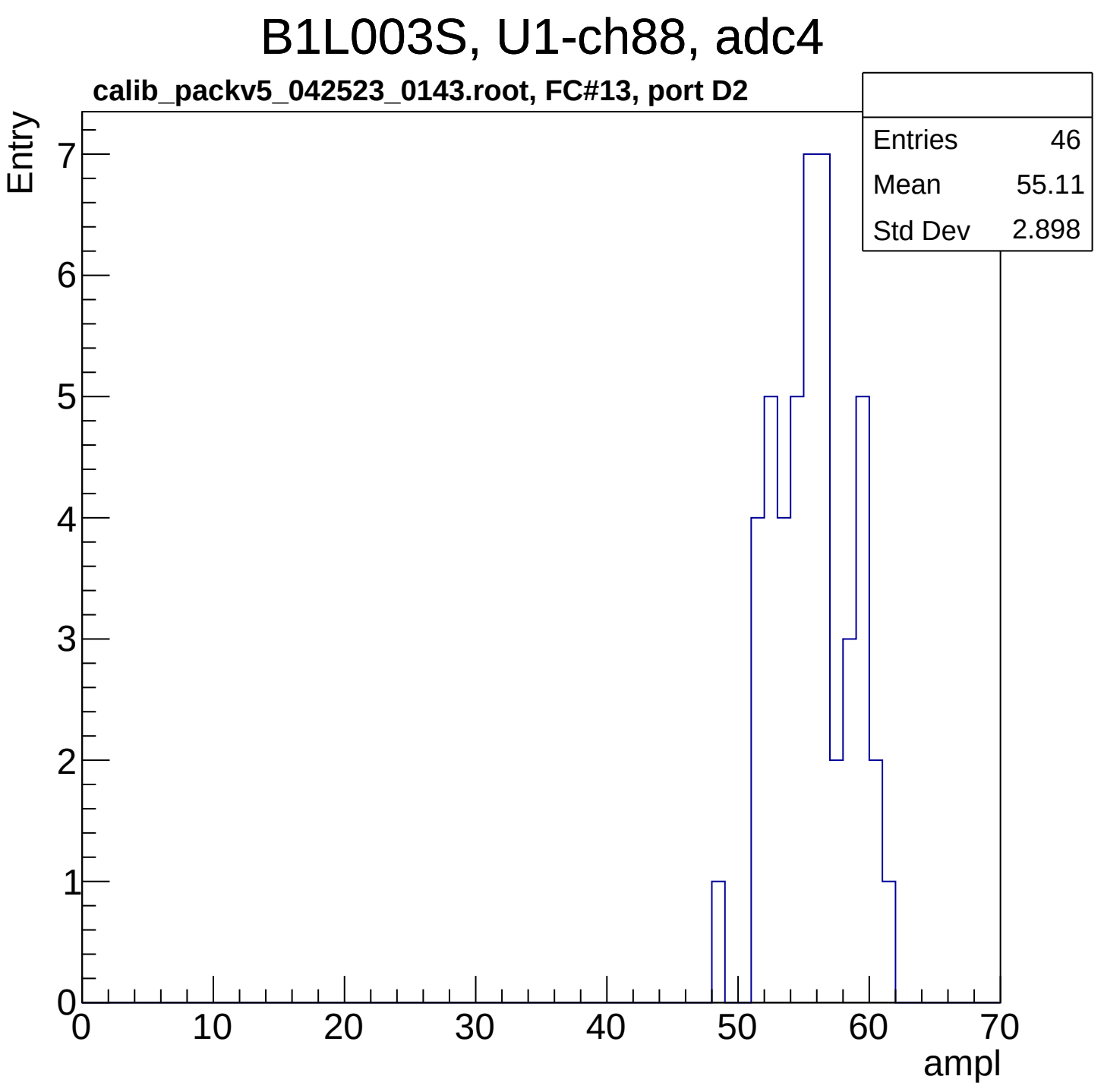
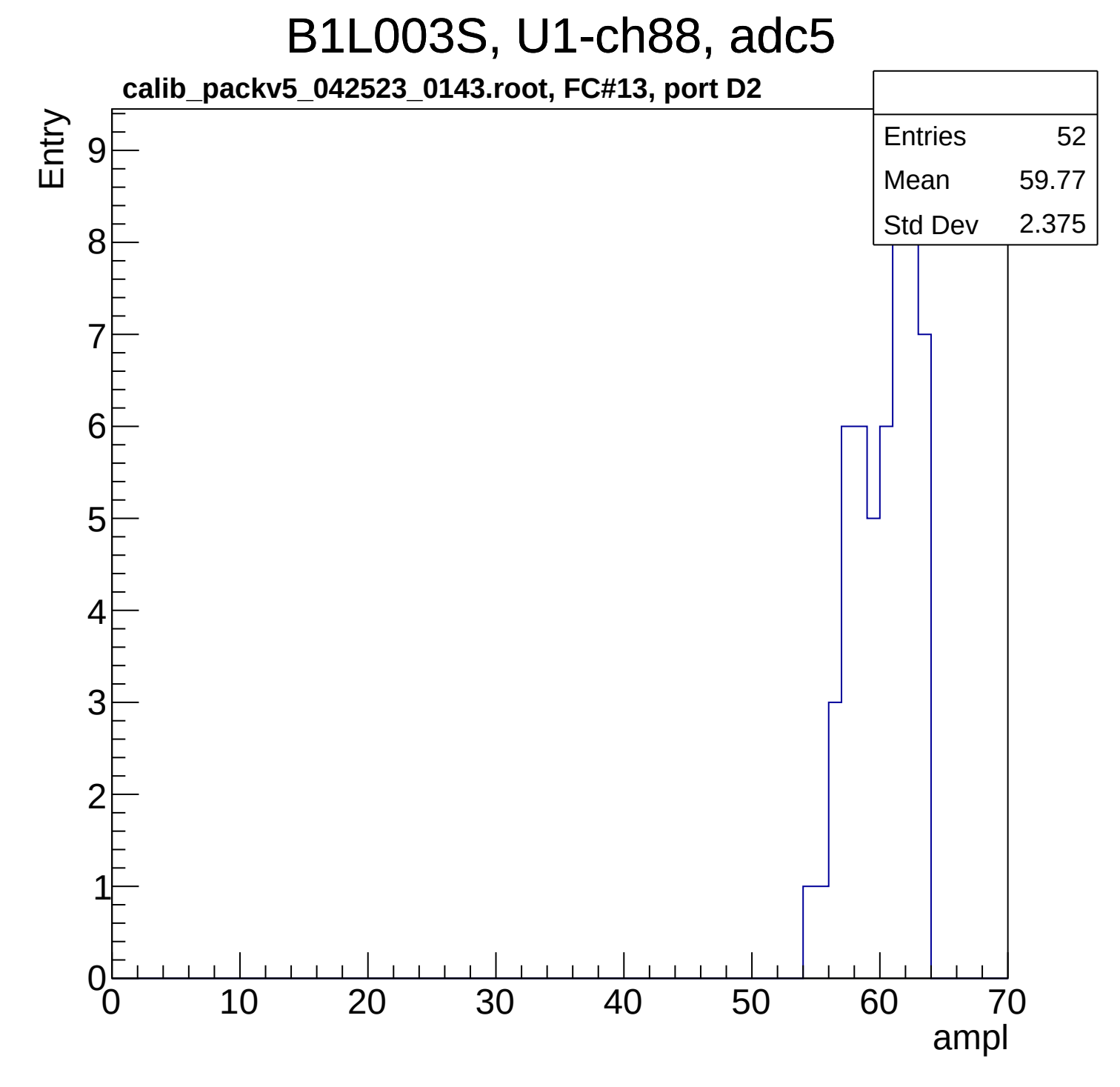

B1L003S, U1-ch88, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 55.11
Std Dev 2.898
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch88, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 59.77
Std Dev 2.375
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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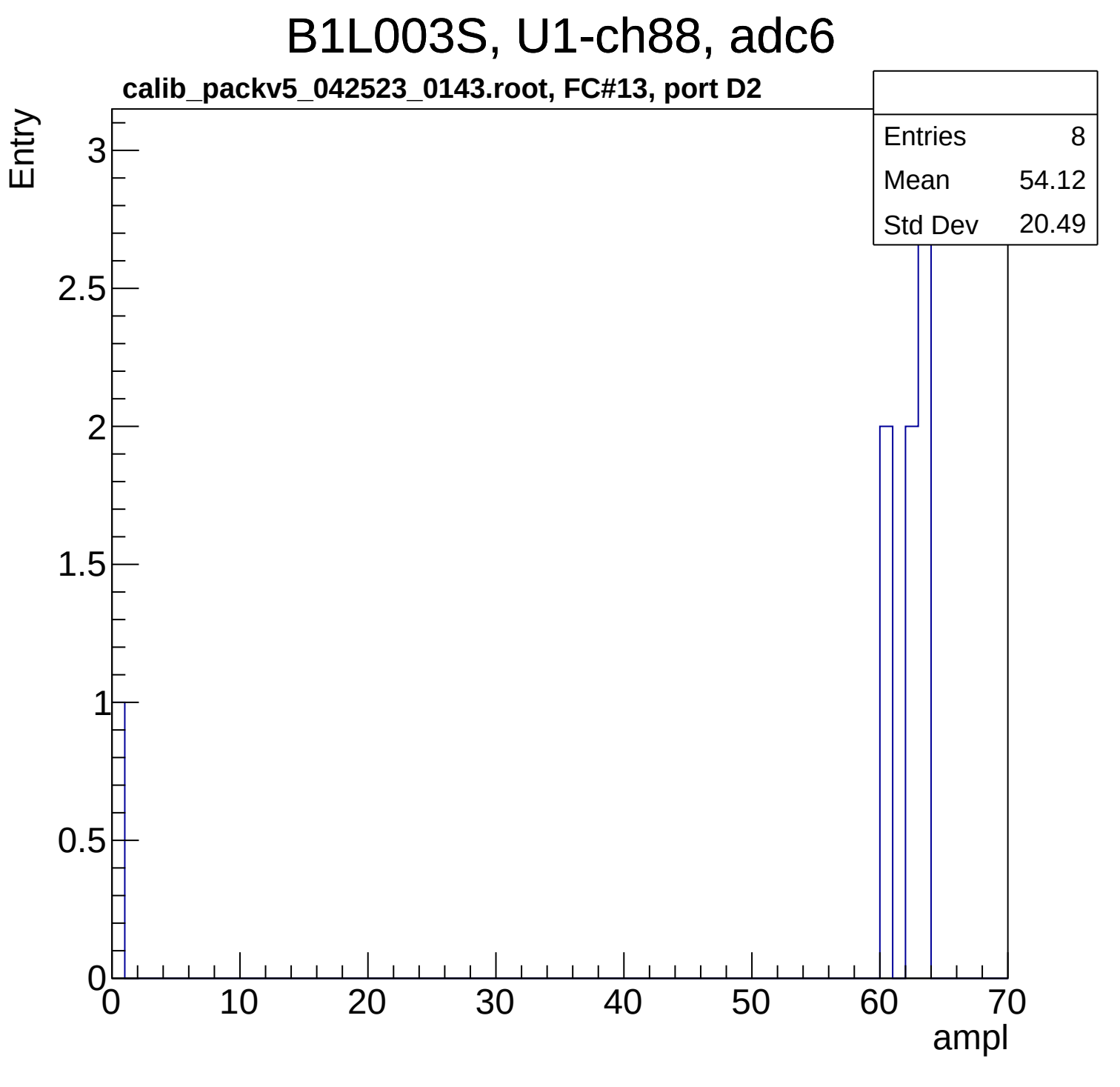
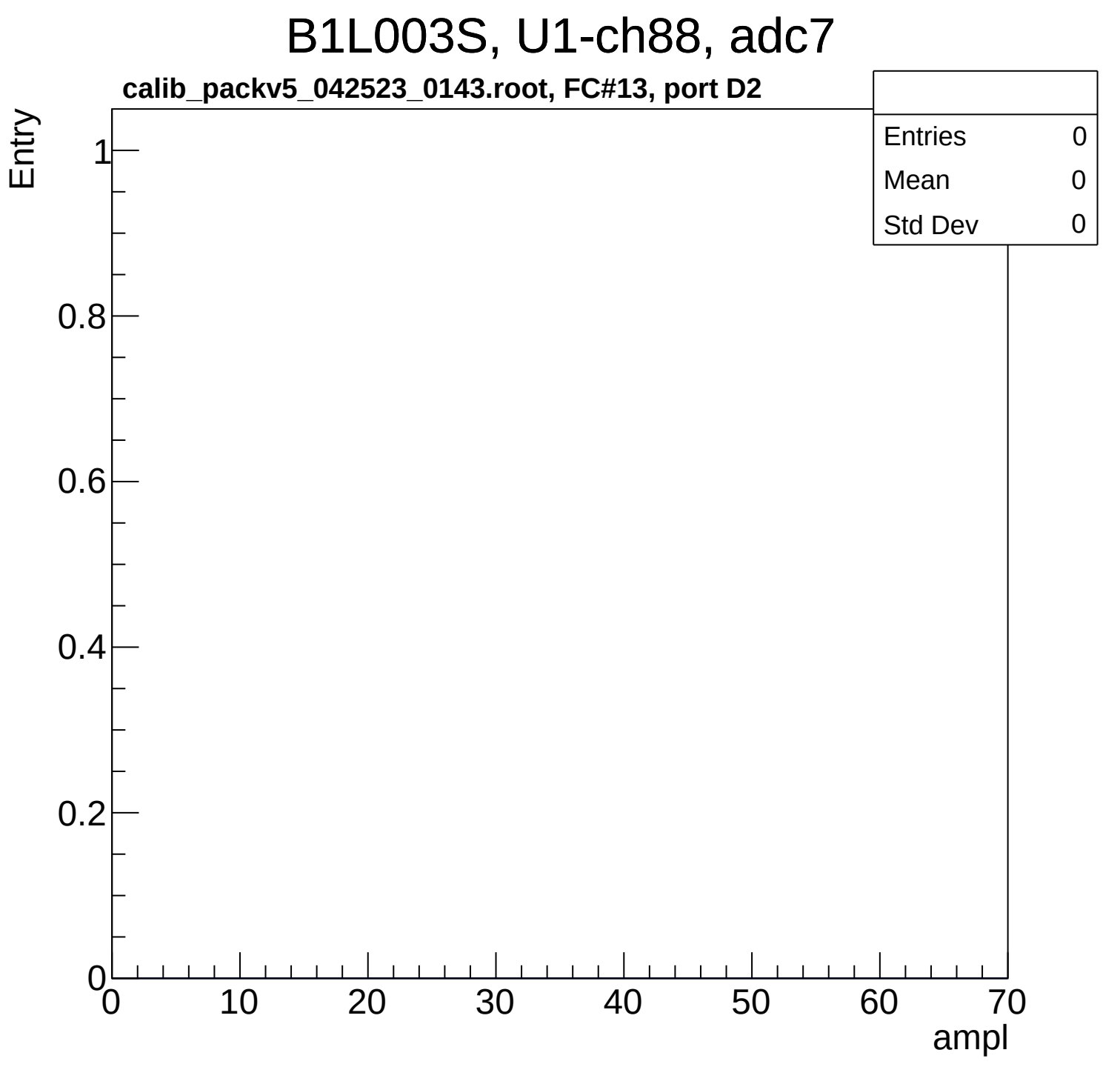
40
50
60
70


B1L003S, U1-ch88, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.12
Std Dev 20.49
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch88, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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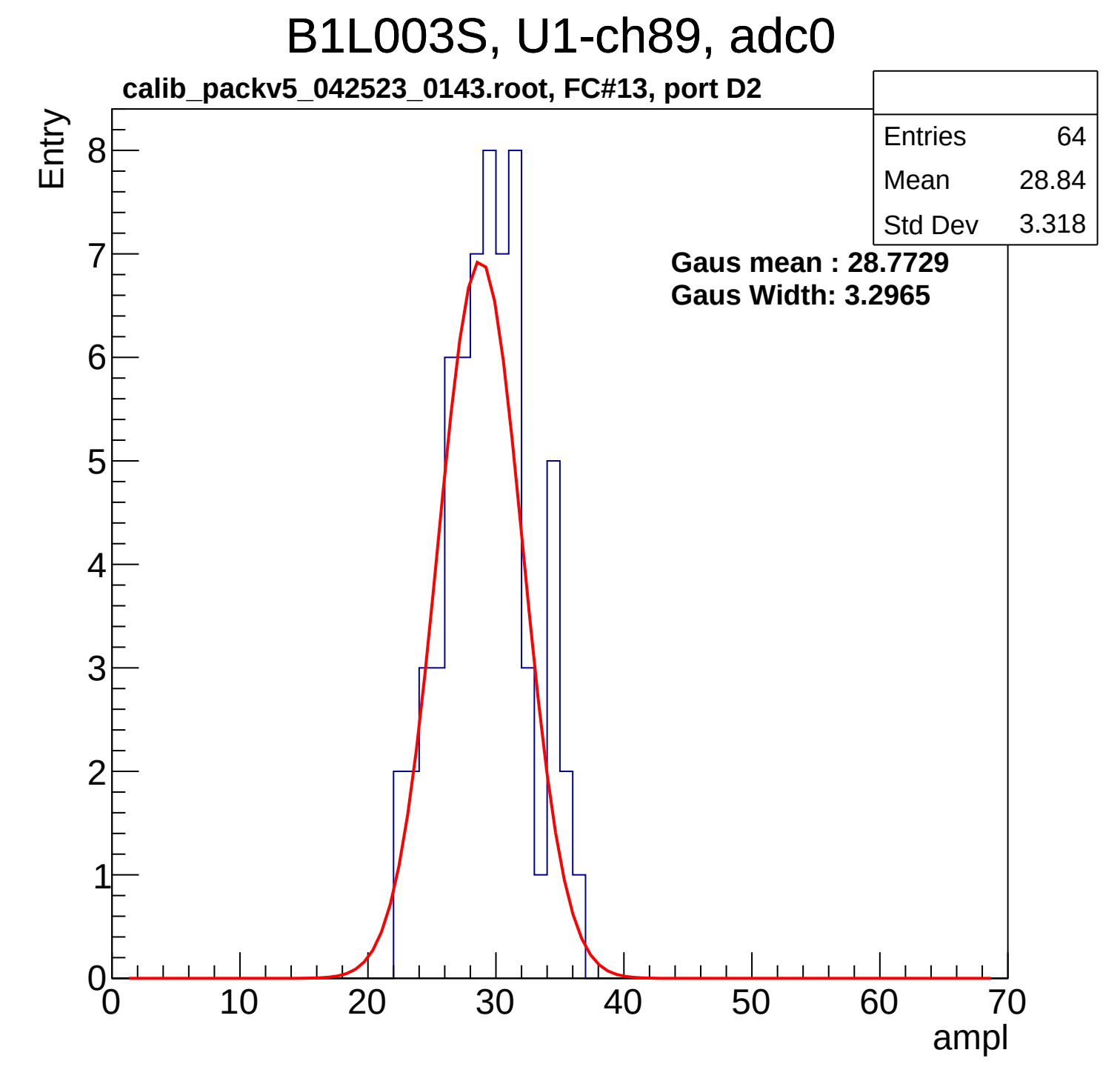
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch89, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 28.84
Std Dev 3.318
Gaus mean : 28.7729
Gaus Width: 3.2965
Entry
ampl
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1
2
3
4
5
6
7
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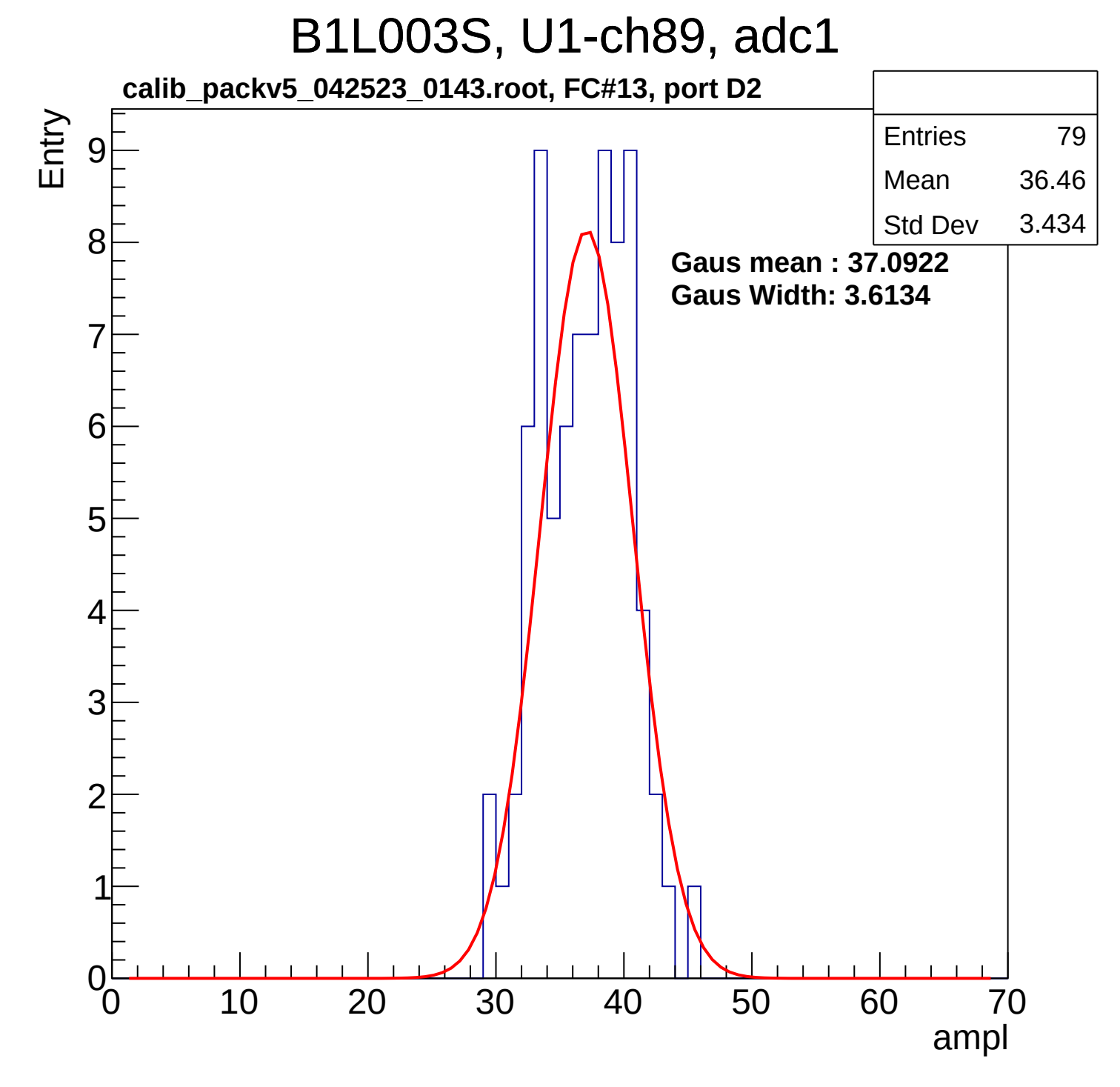
0
10
20
30
40
50
60
70


B1L003S, U1-ch89, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 79
Mean 36.46
Std Dev 3.434
Gaus mean : 37.0922
Gaus Width: 3.6134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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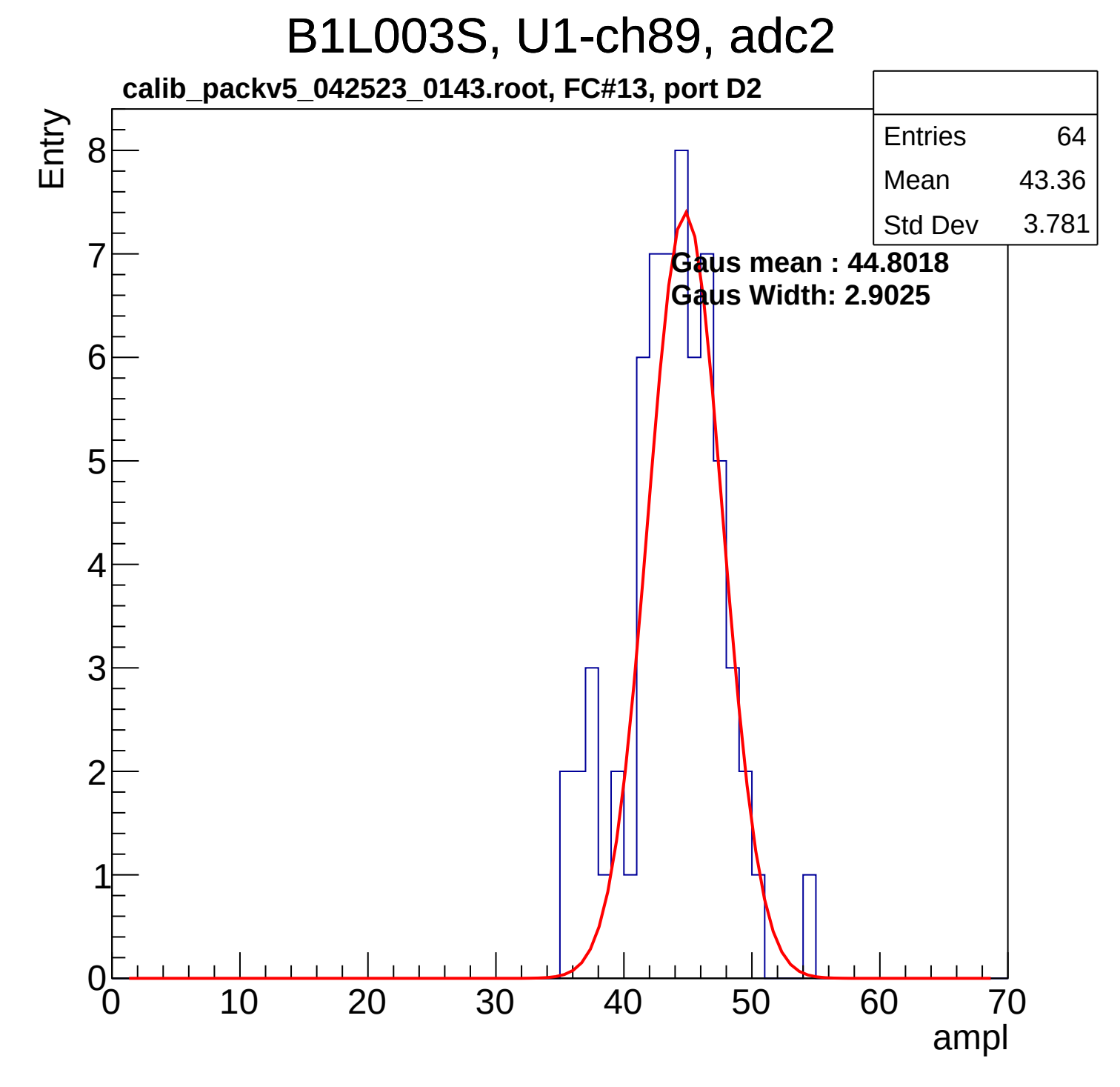
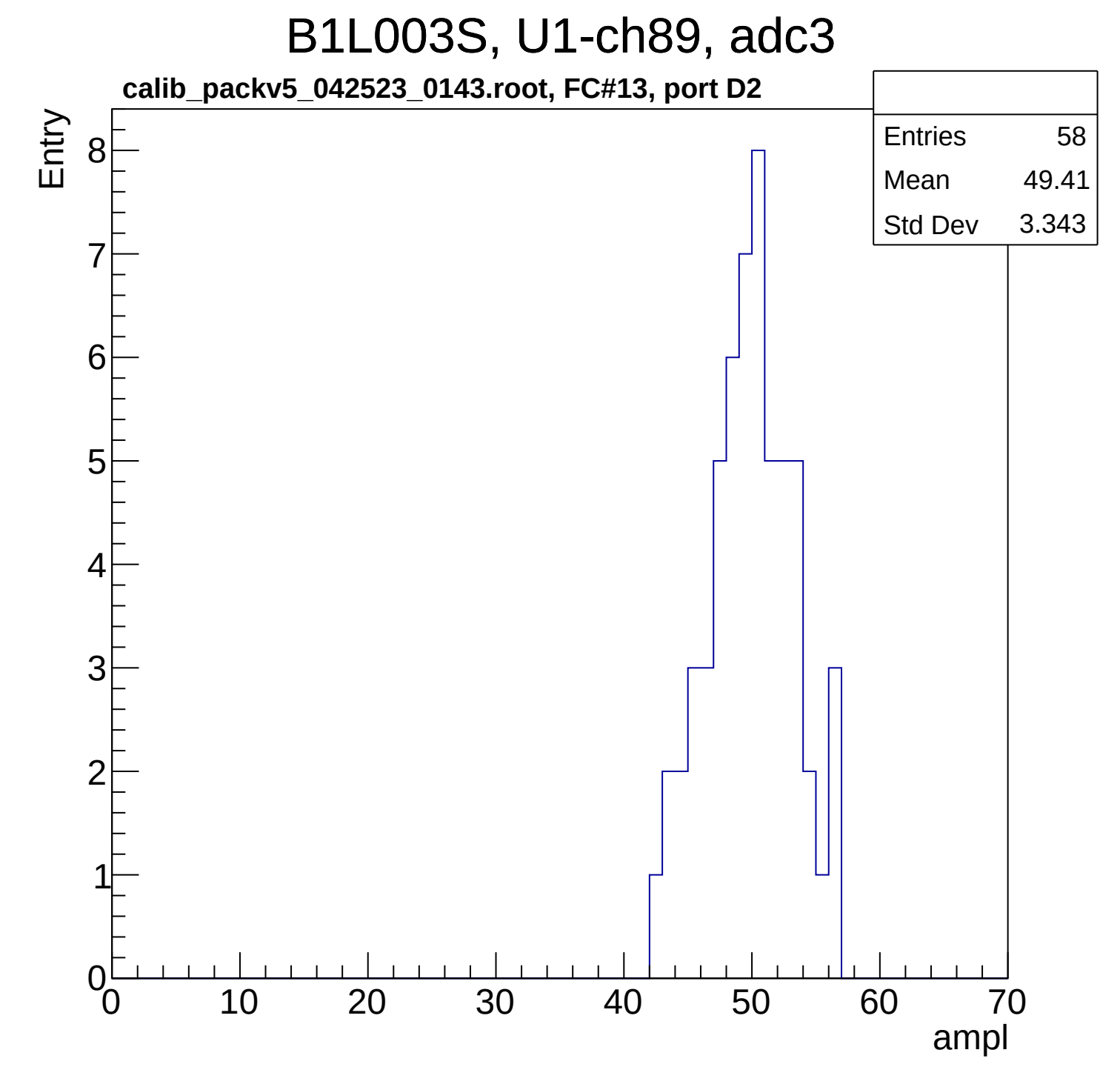

B1L003S, U1-ch89, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 43.36
Std Dev 3.781
Gaus mean : 44.8018
Gaus Width: 2.9025
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch89, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 49.41
Std Dev 3.343
Entry
0
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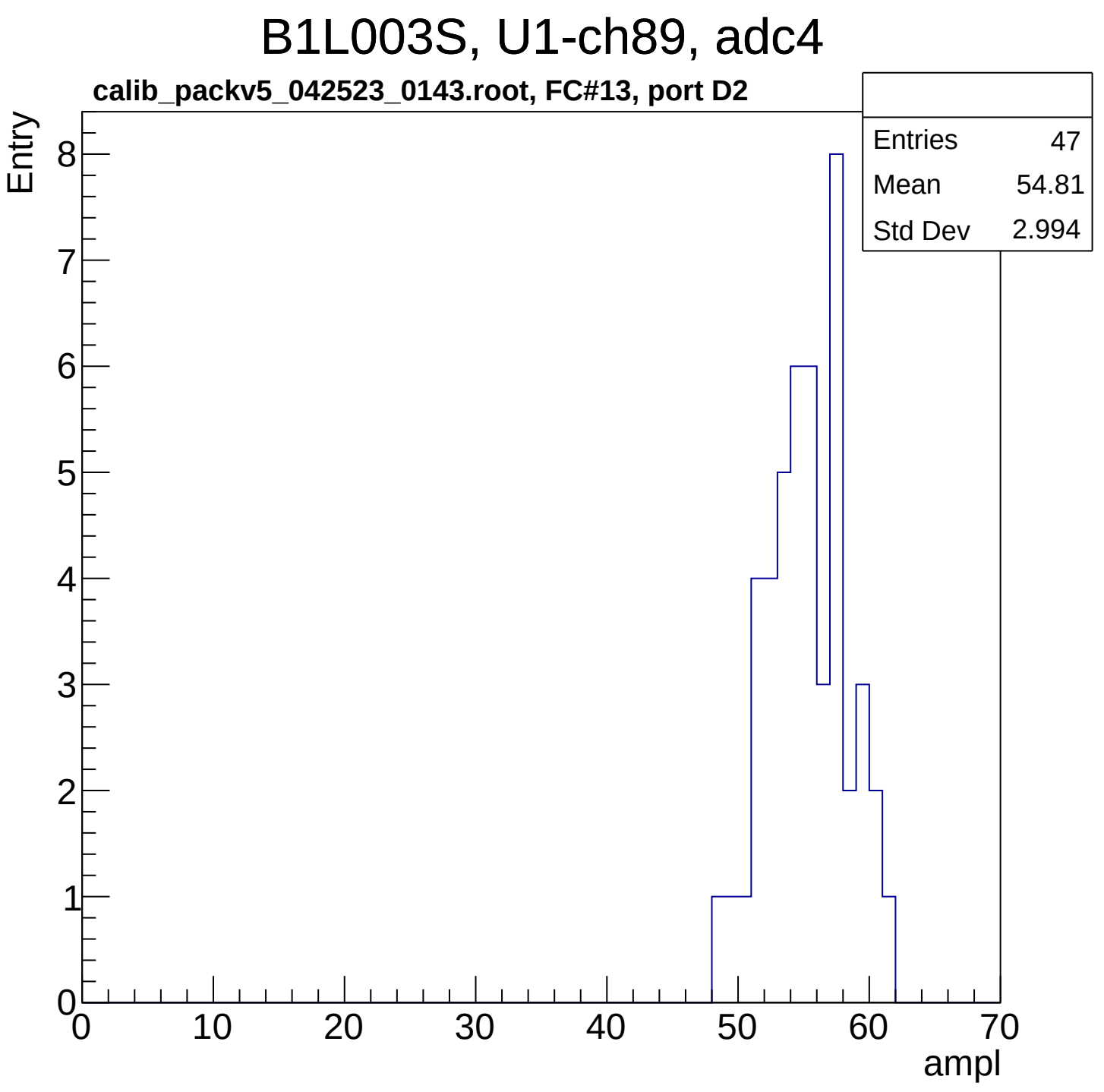
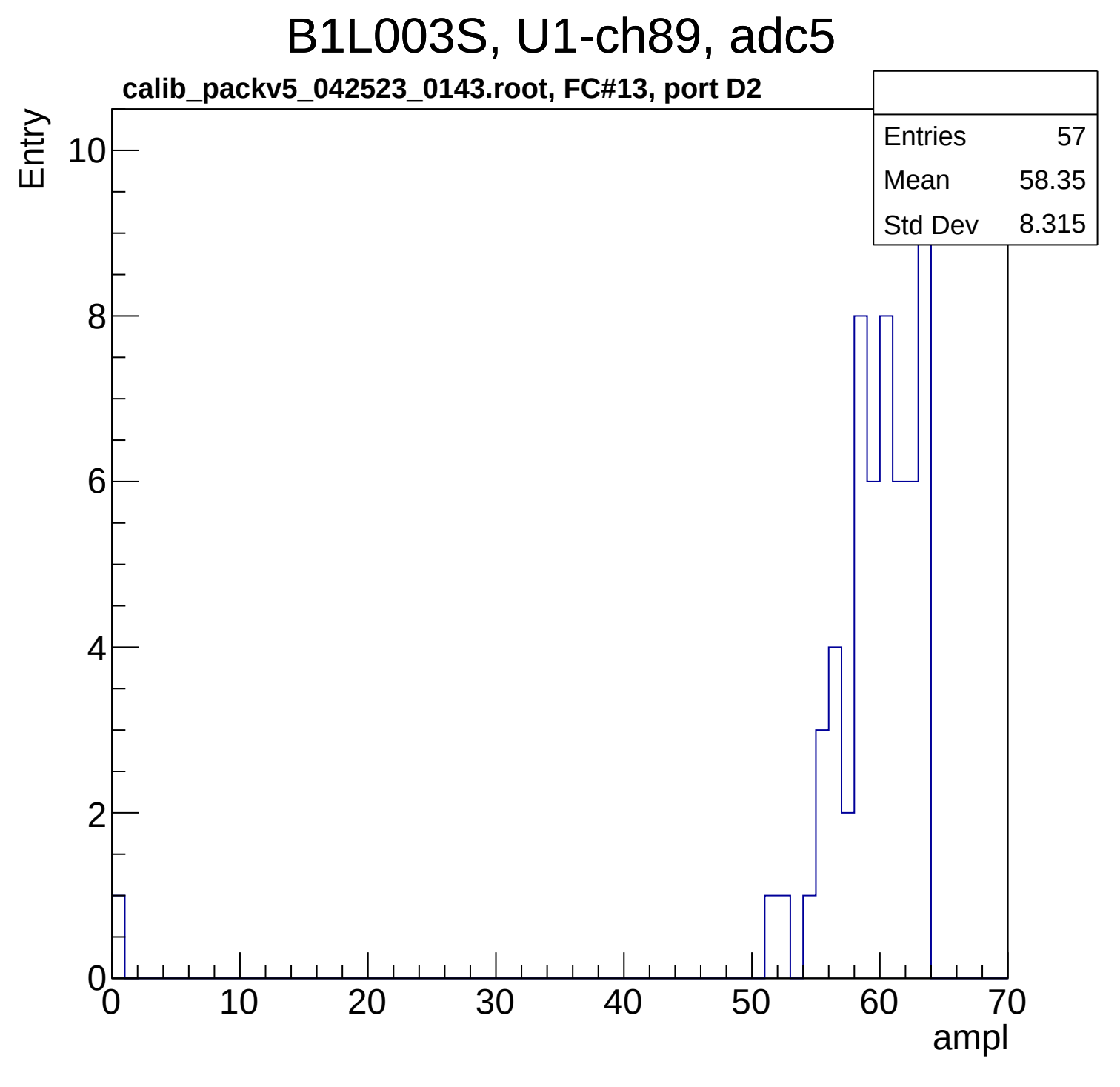
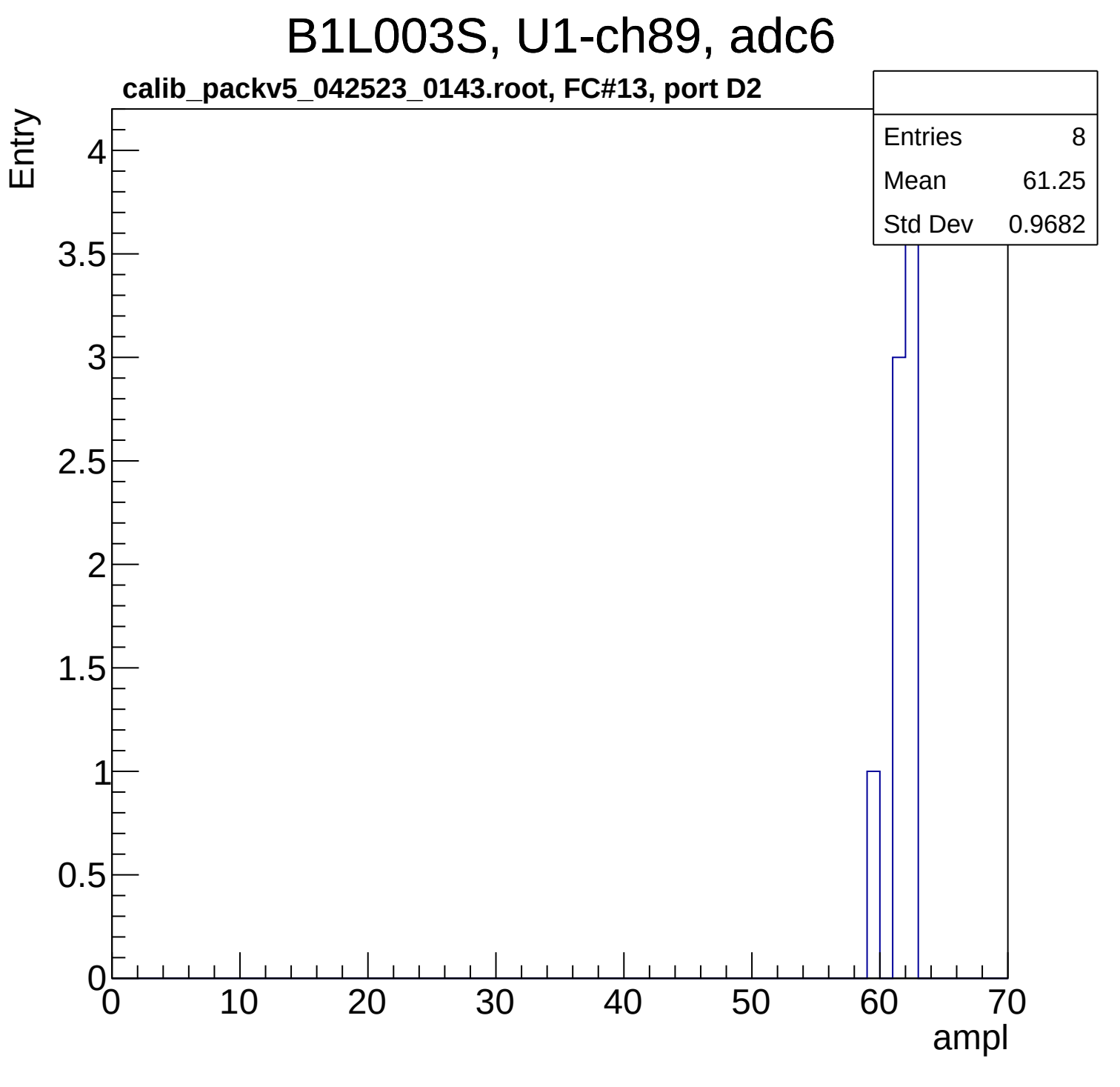
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch89, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 54.81
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch89, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 58.35
Std Dev 8.315
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch89, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 61.25
Std Dev 0.9682
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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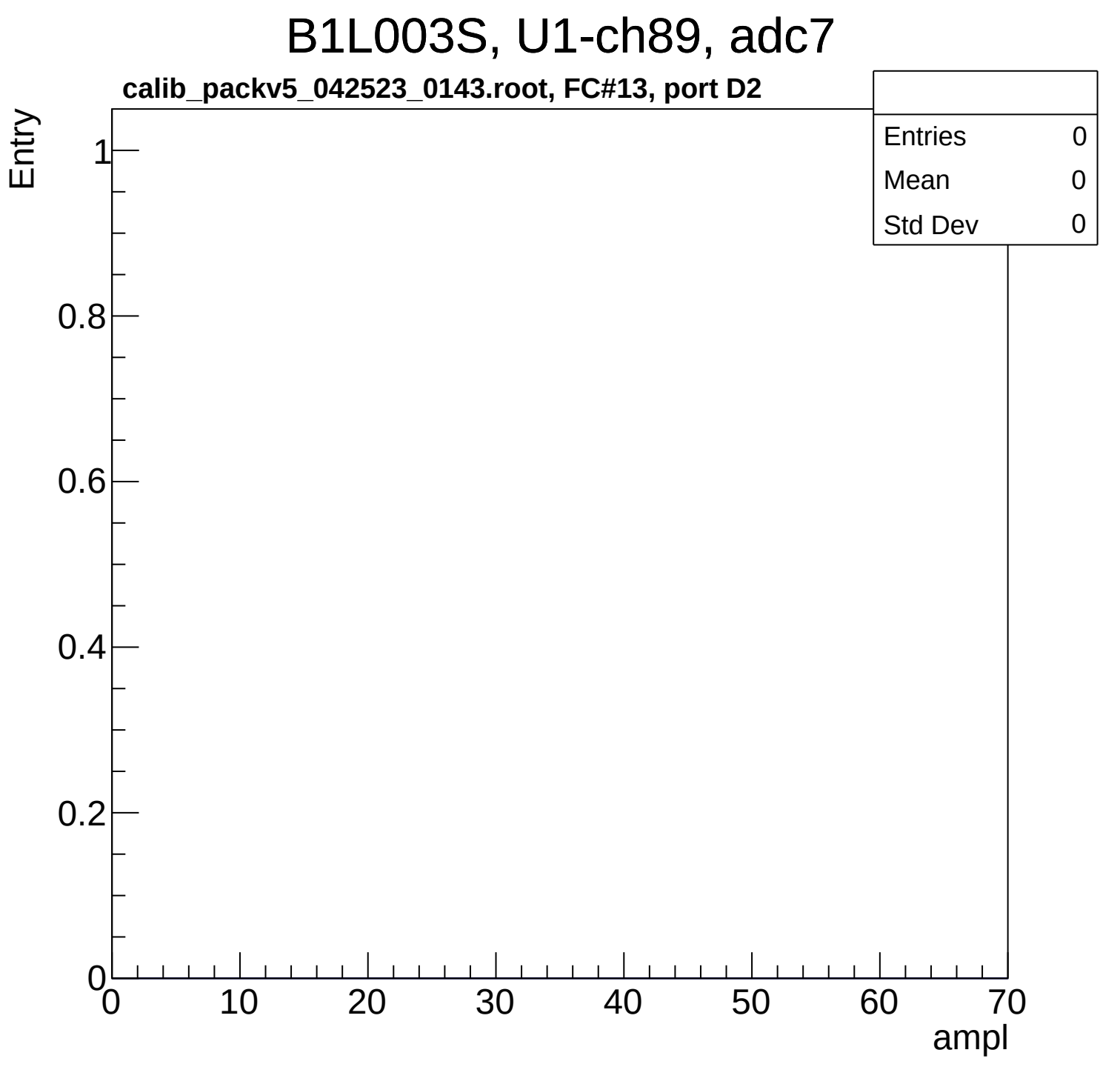
40
50
60
70


B1L003S, U1-ch89, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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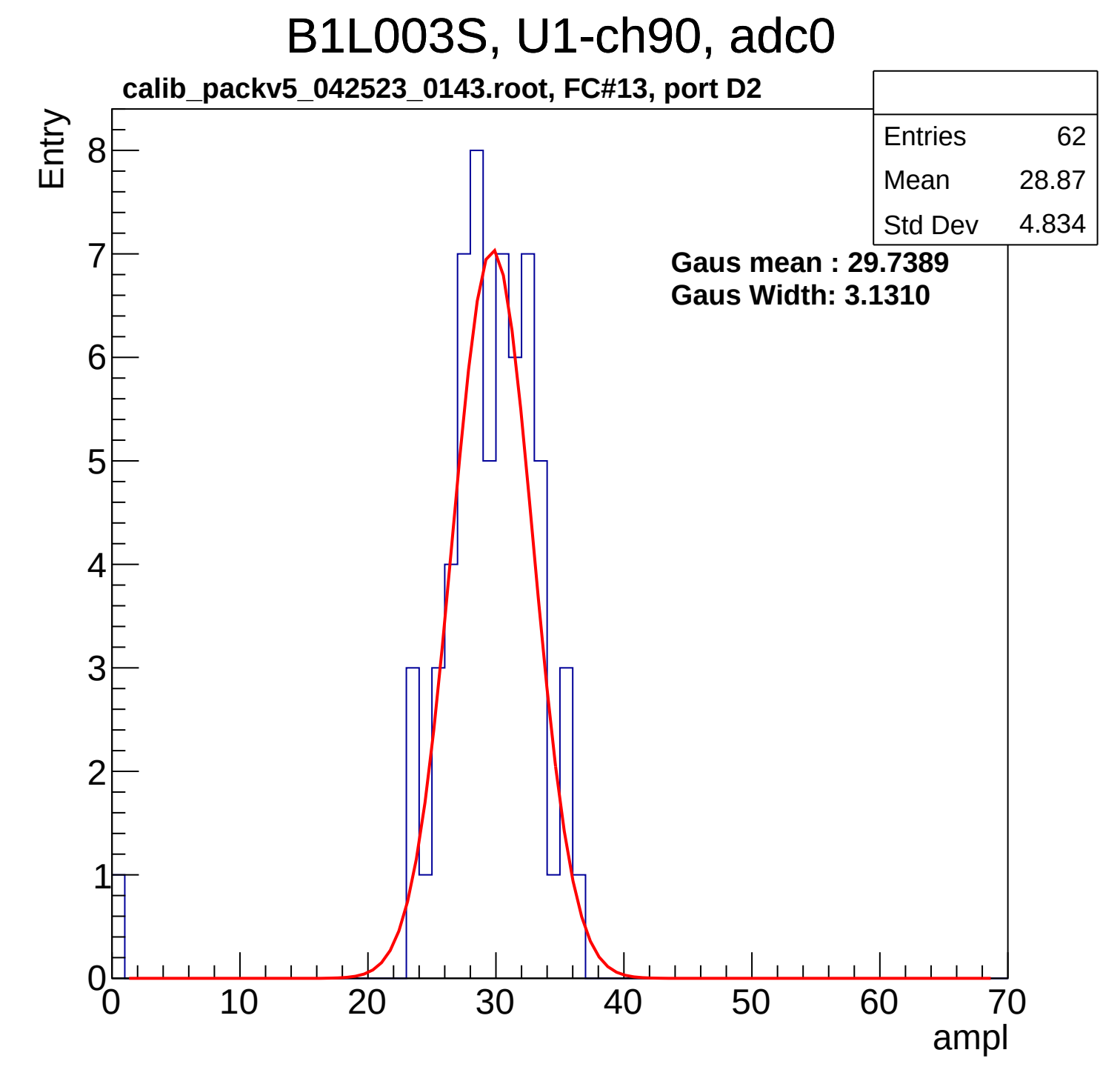
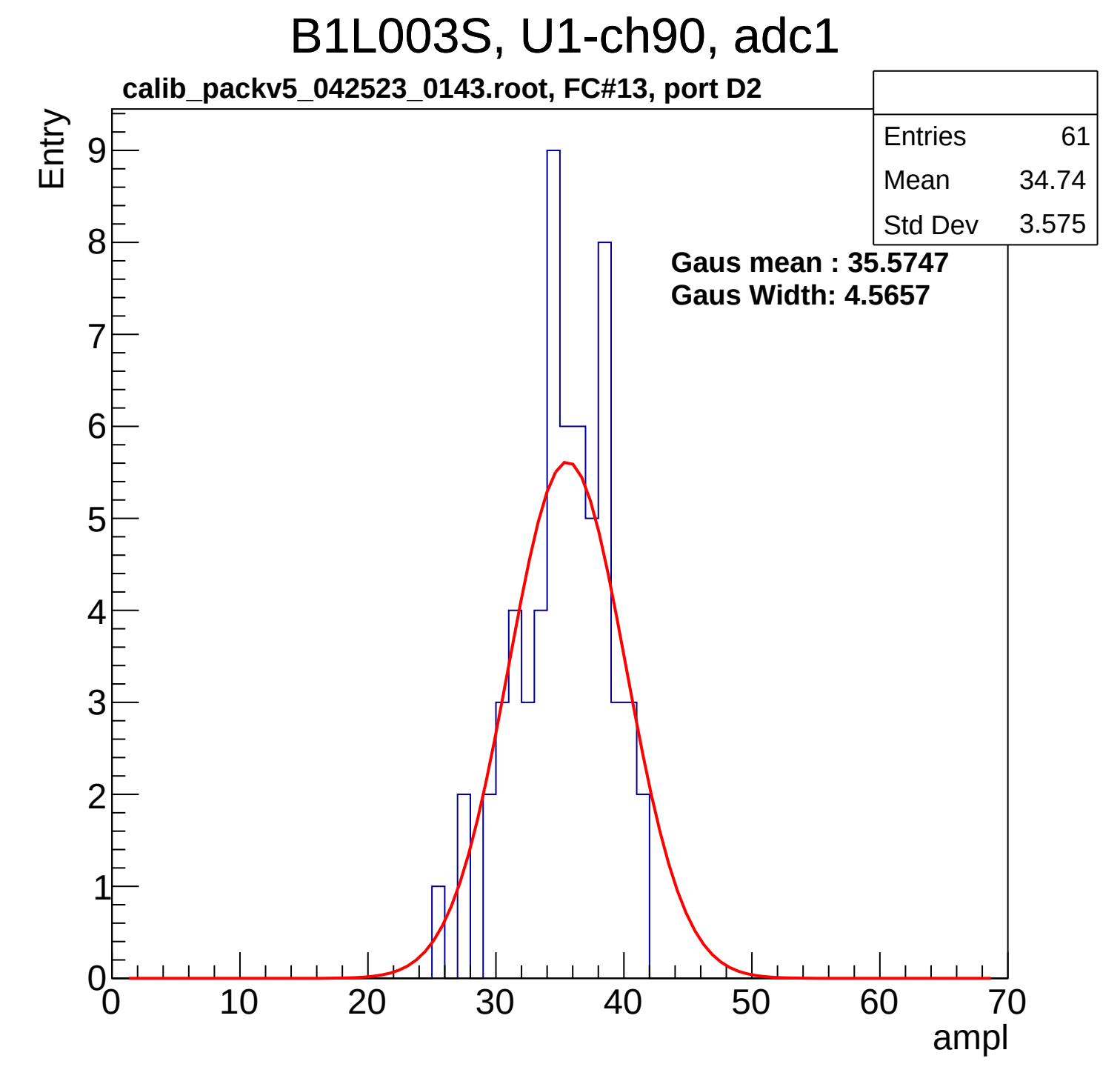
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch90, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 28.87
Std Dev 4.834
Gaus mean : 29.7389
Gaus Width: 3.1310
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch90, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 34.74
Std Dev 3.575
Gaus mean : 35.5747
Gaus Width: 4.5657
Entry
ampl
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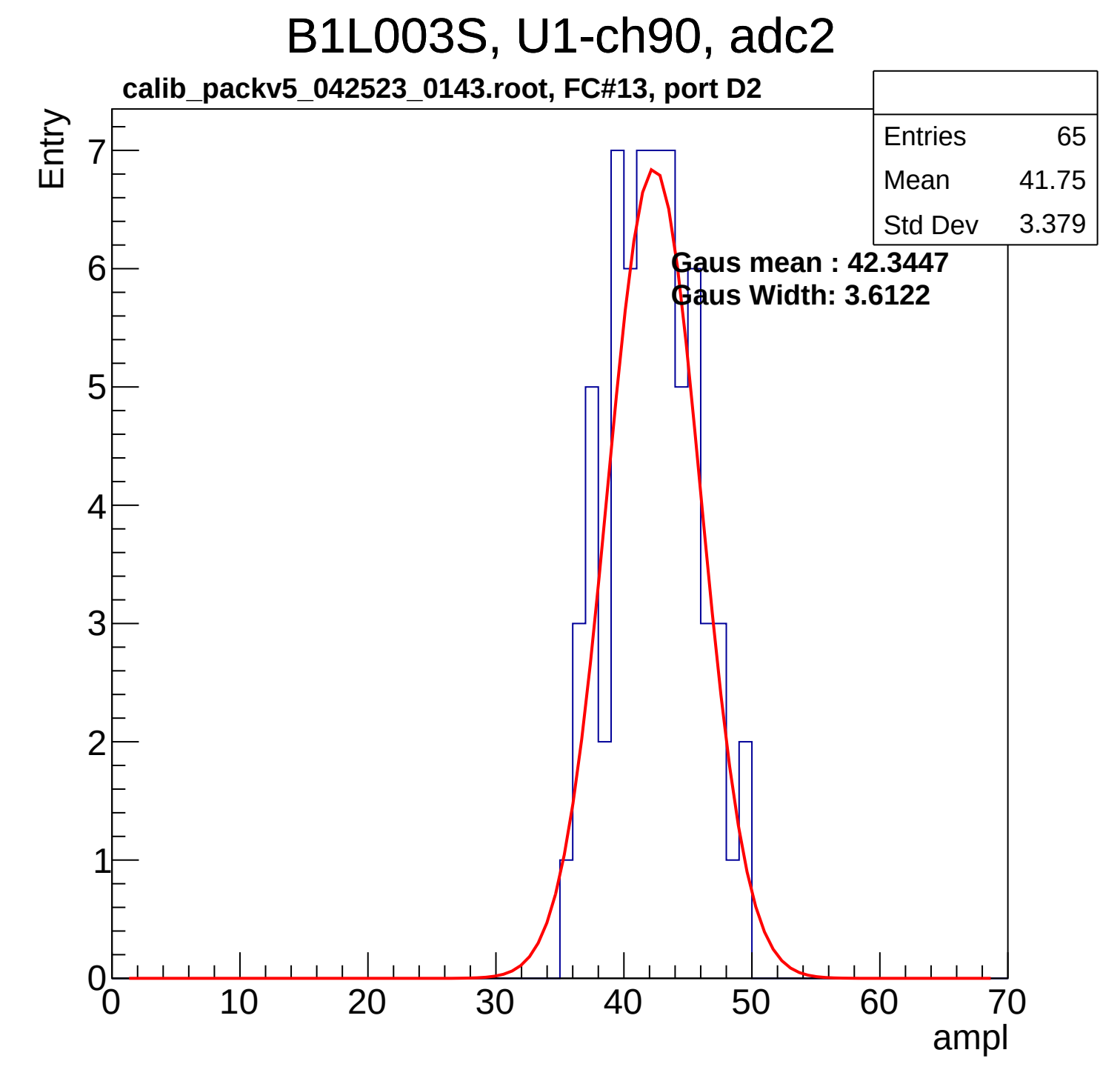
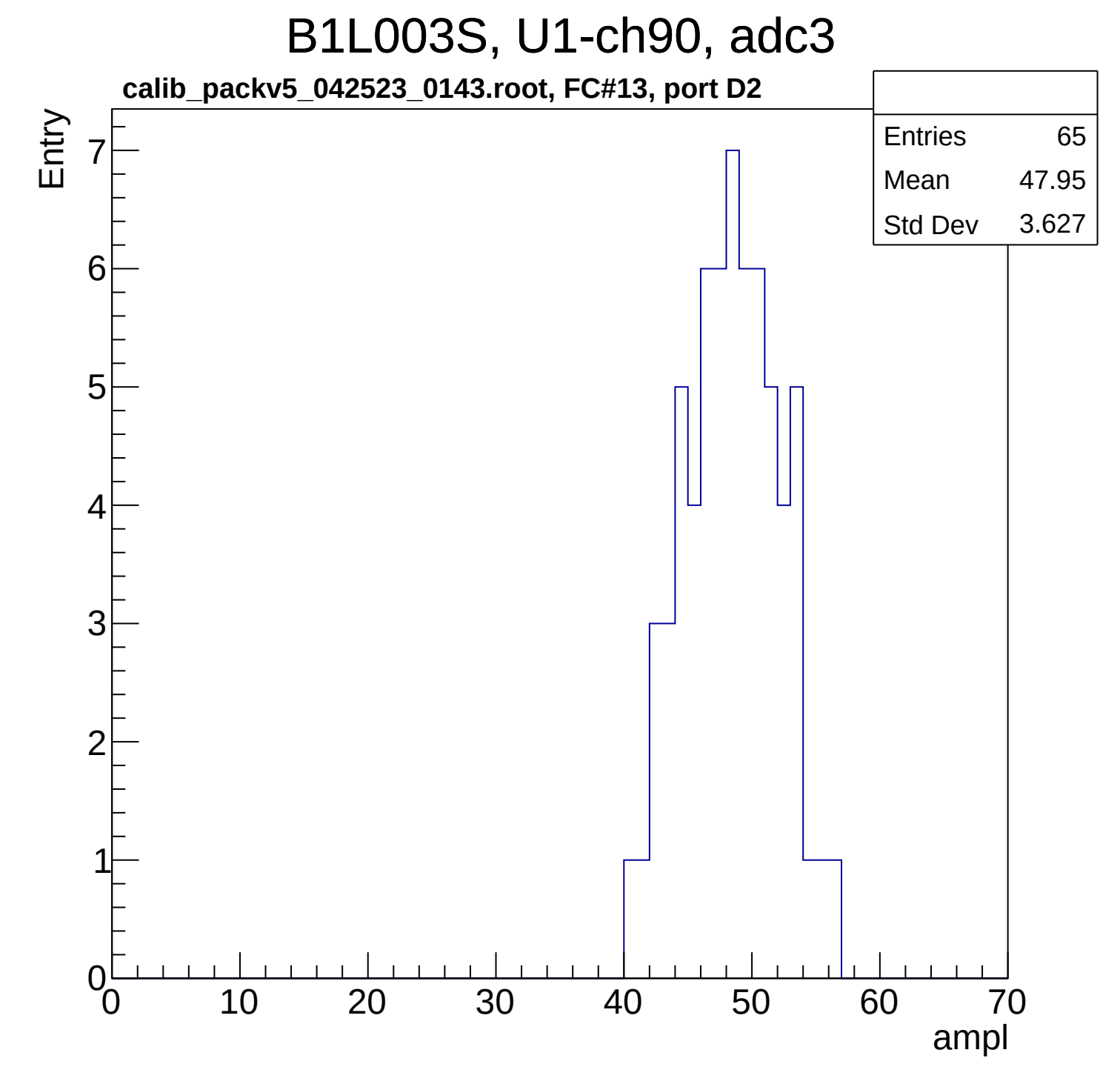
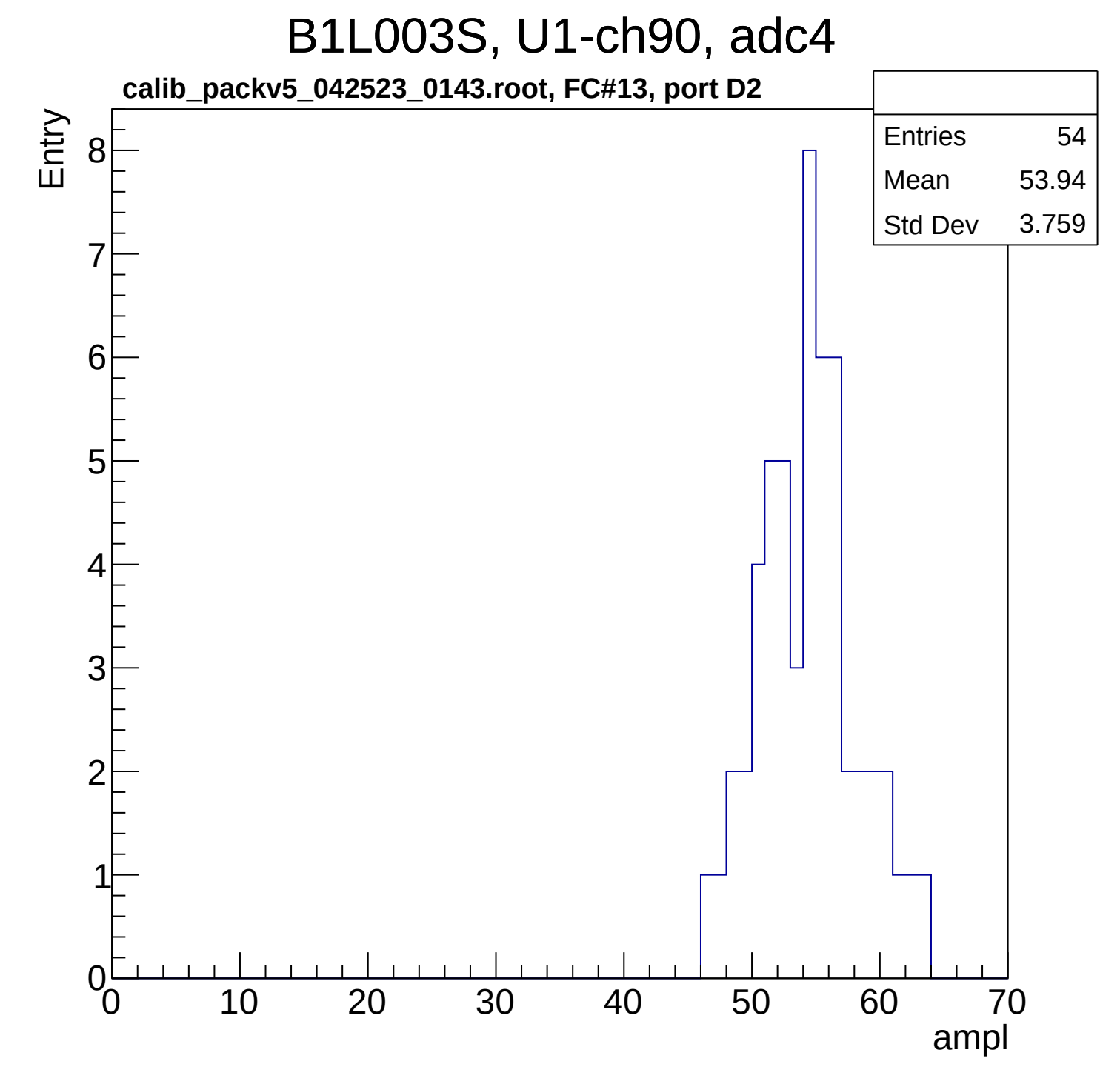
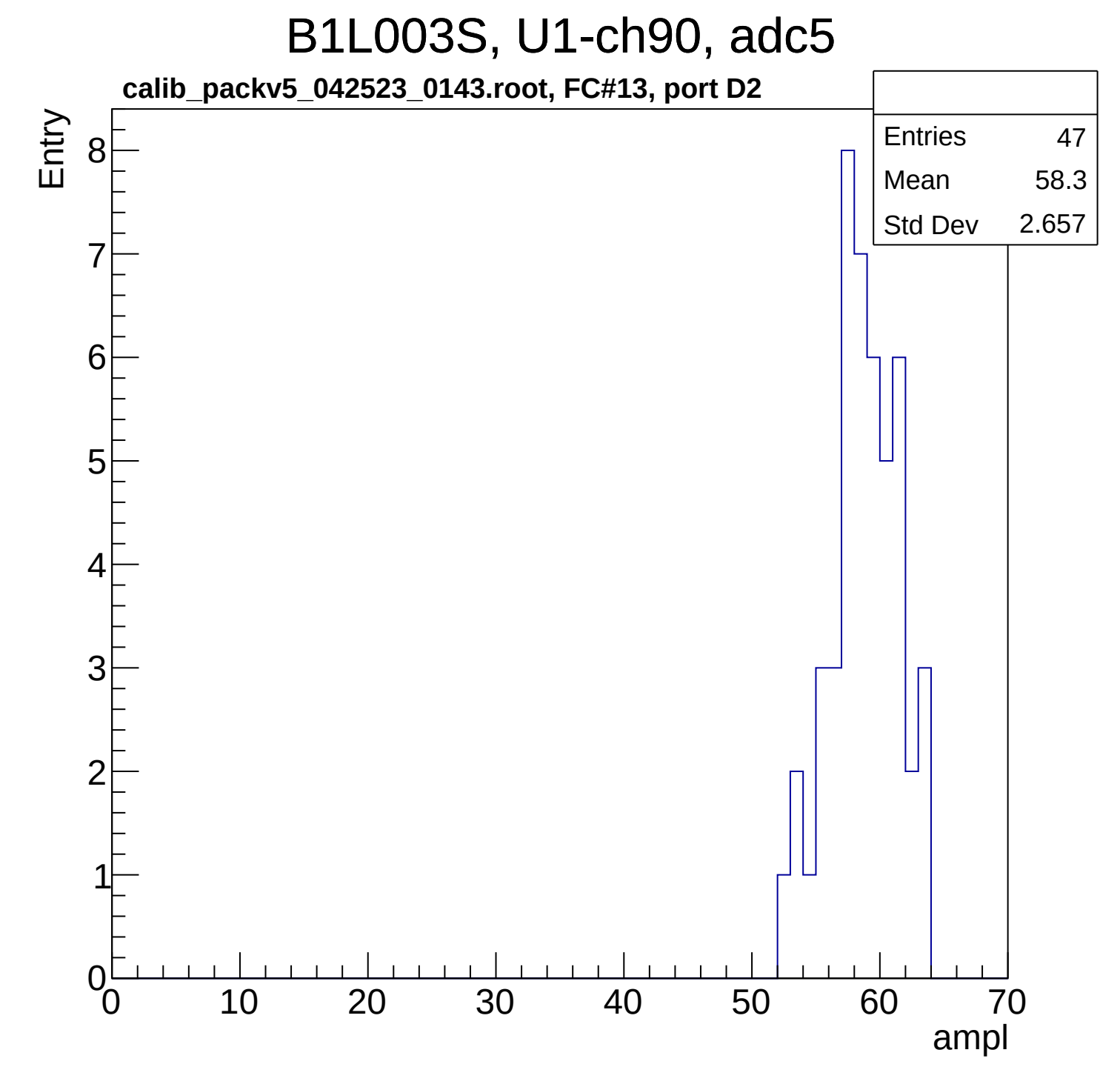
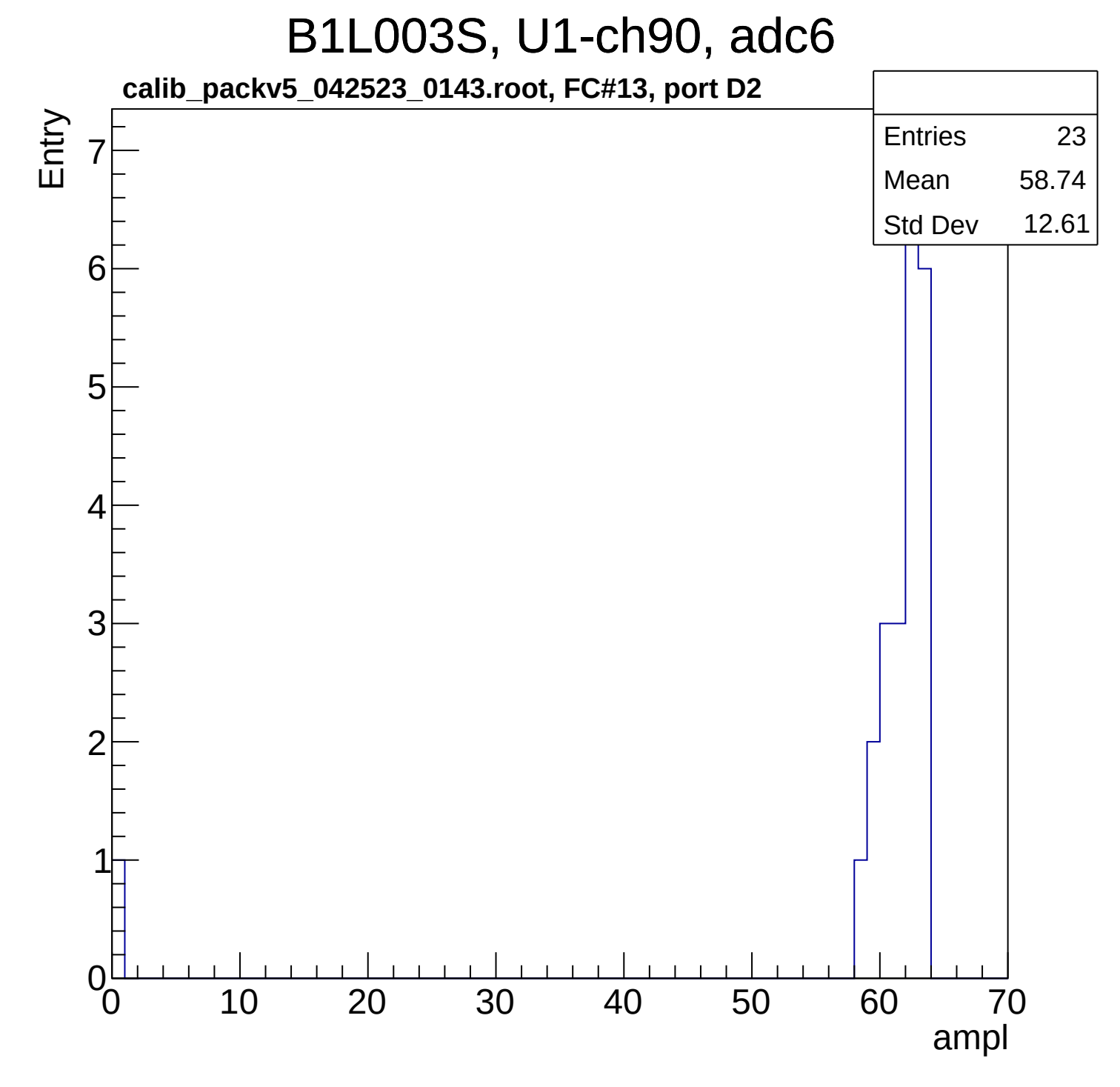
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

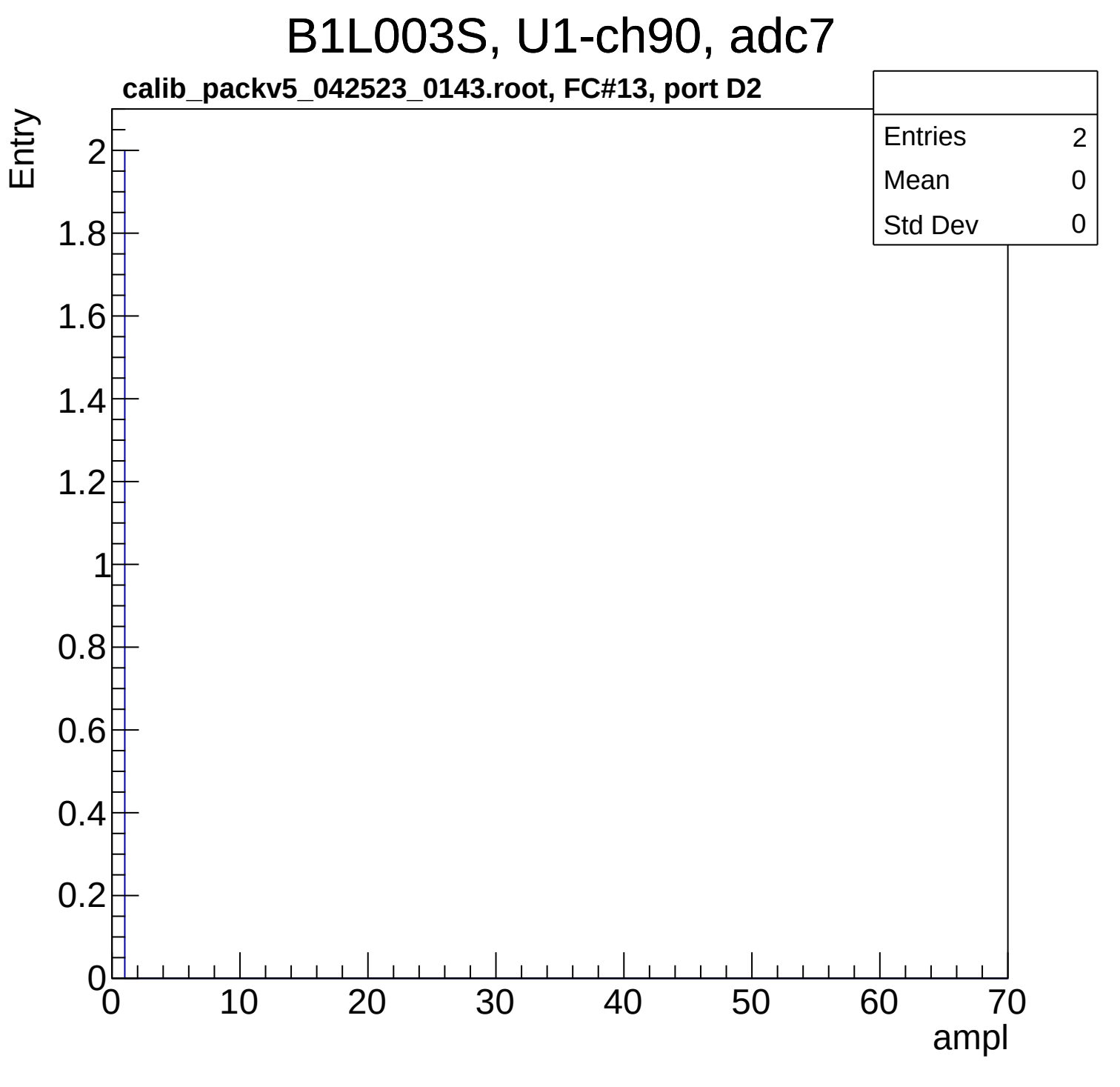

B1L003S, U1-ch90, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.75
Std Dev 3.379
Gaus mean : 42.3447
Gaus Width: 3.6122
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

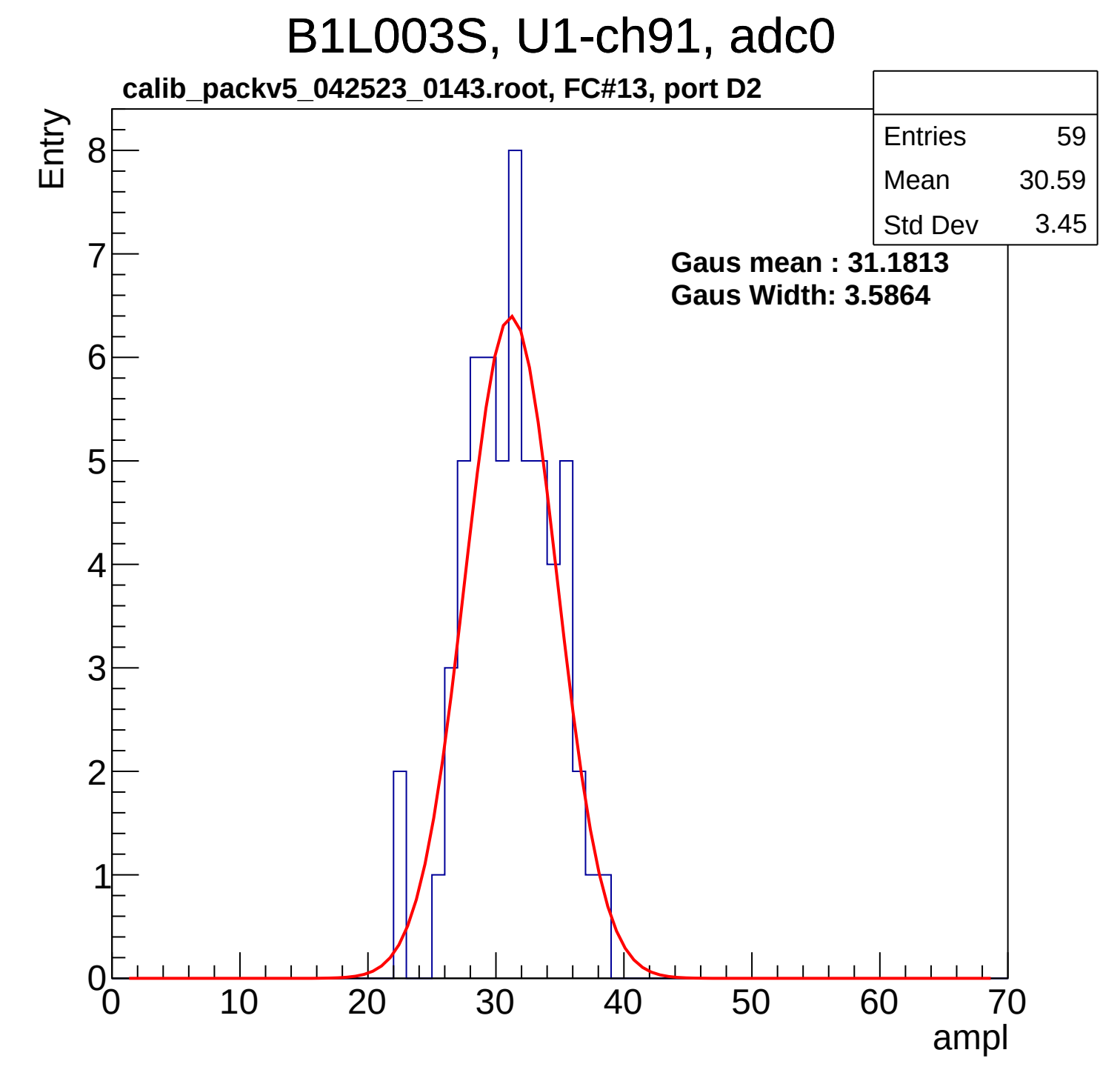

B1L003S, U1-ch90, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.95
Std Dev 3.627
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

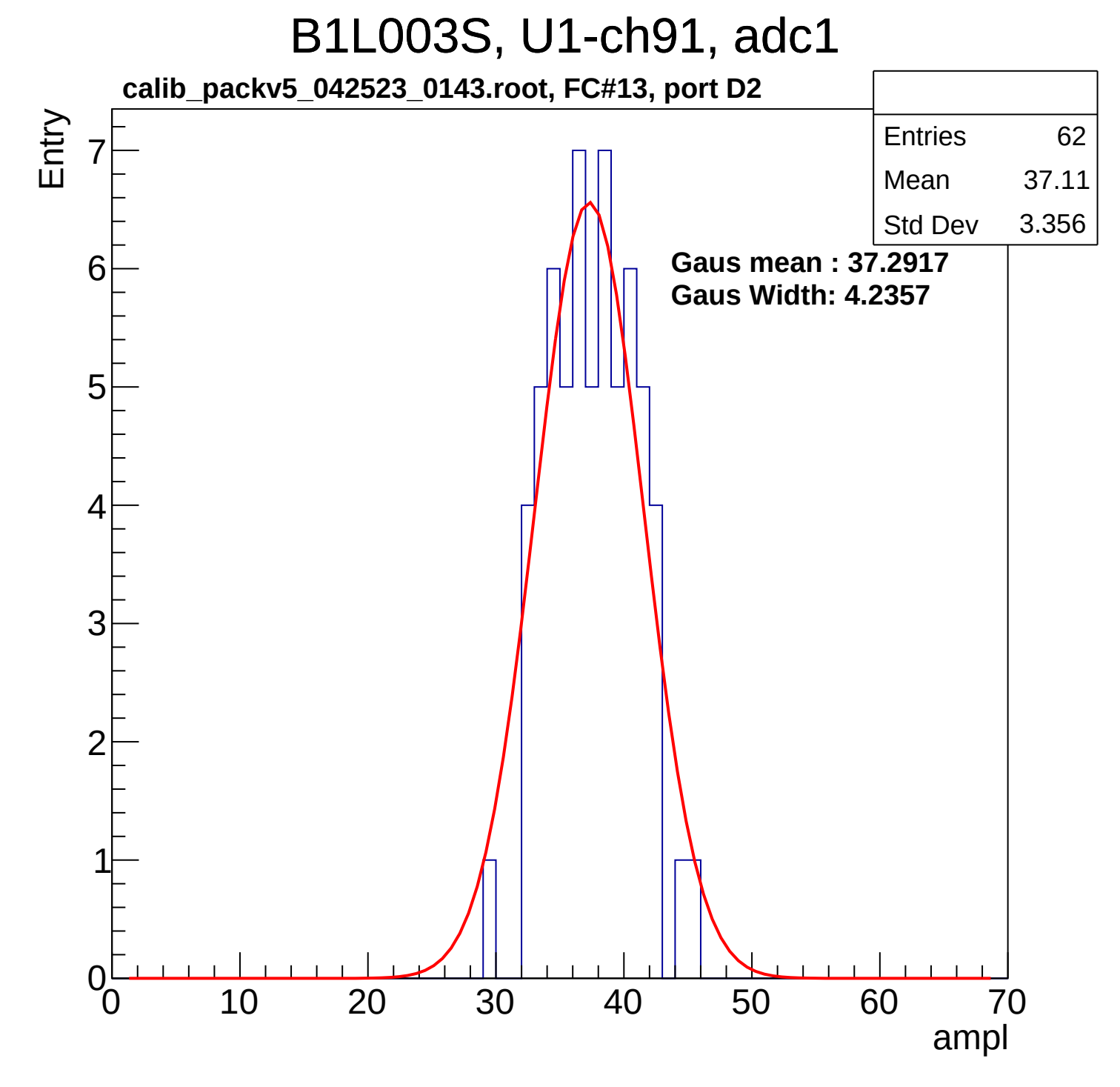

B1L003S, U1-ch90, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 53.94
Std Dev 3.759
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

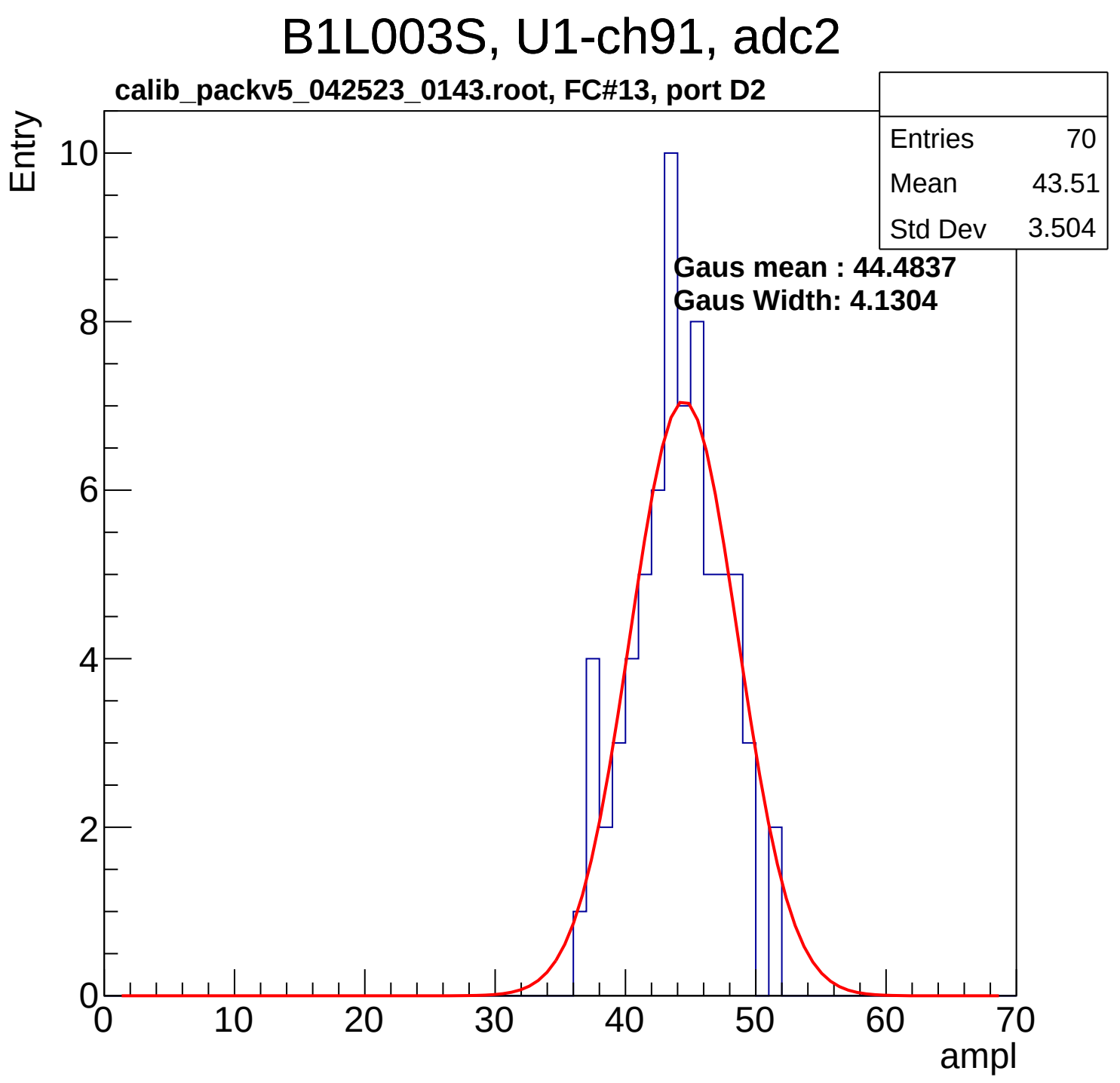

B1L003S, U1-ch90, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.3
Std Dev 2.657
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

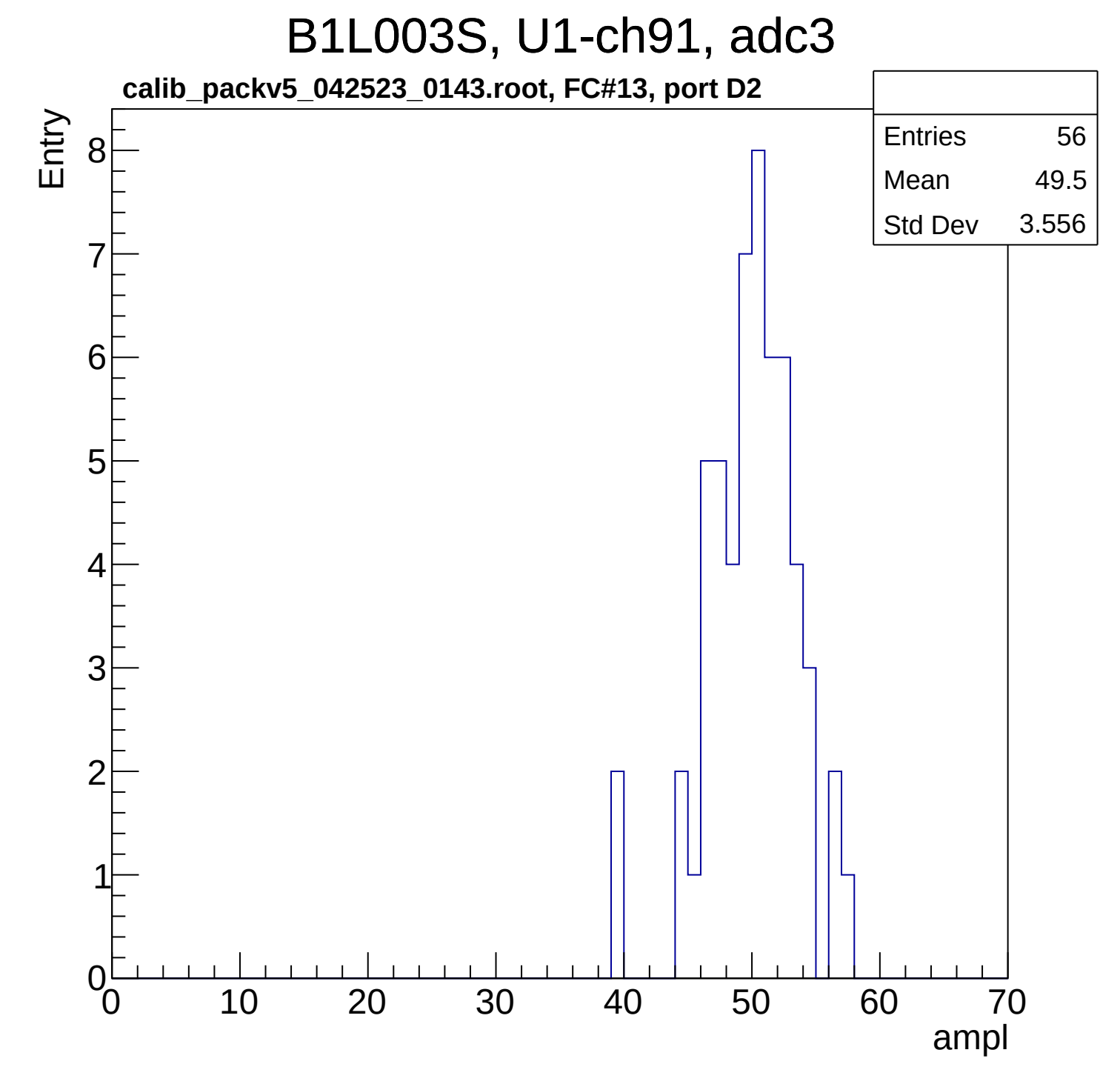

B1L003S, U1-ch90, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 23
Mean 58.74
Std Dev 12.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

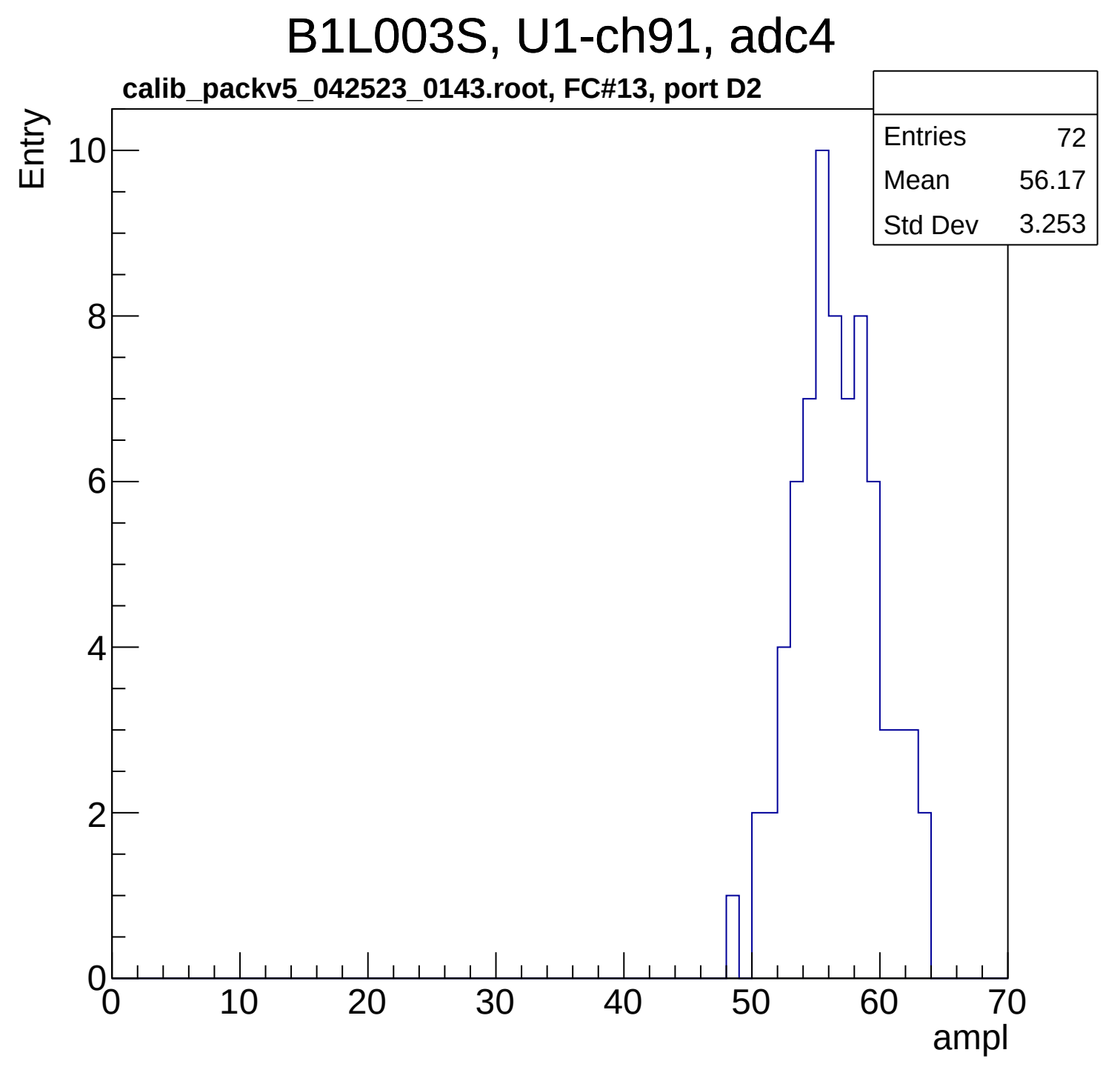

B1L003S, U1-ch90, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

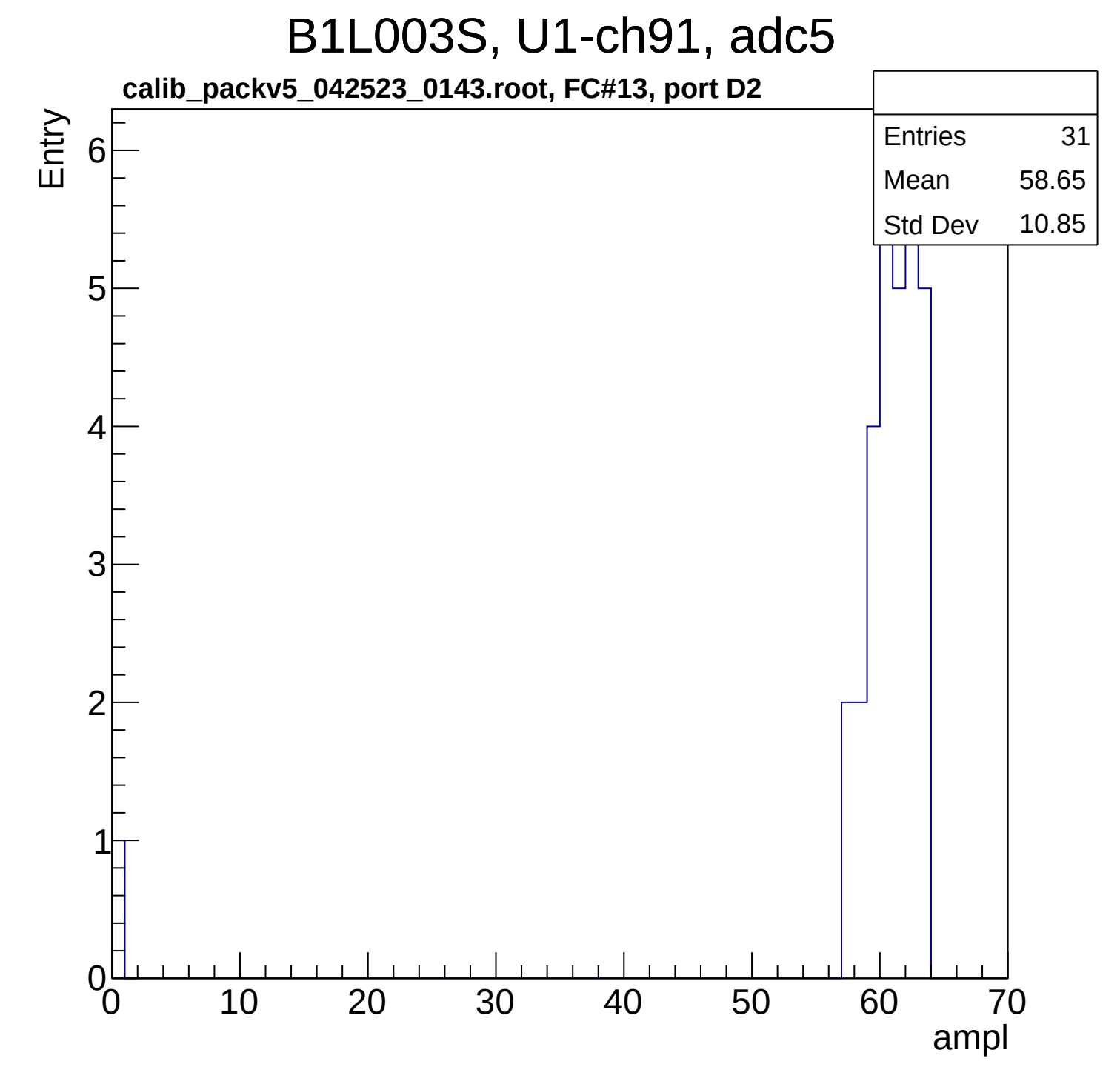

B1L003S, U1-ch91, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 30.59
Std Dev 3.45
Gaus mean : 31.1813
Gaus Width: 3.5864
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

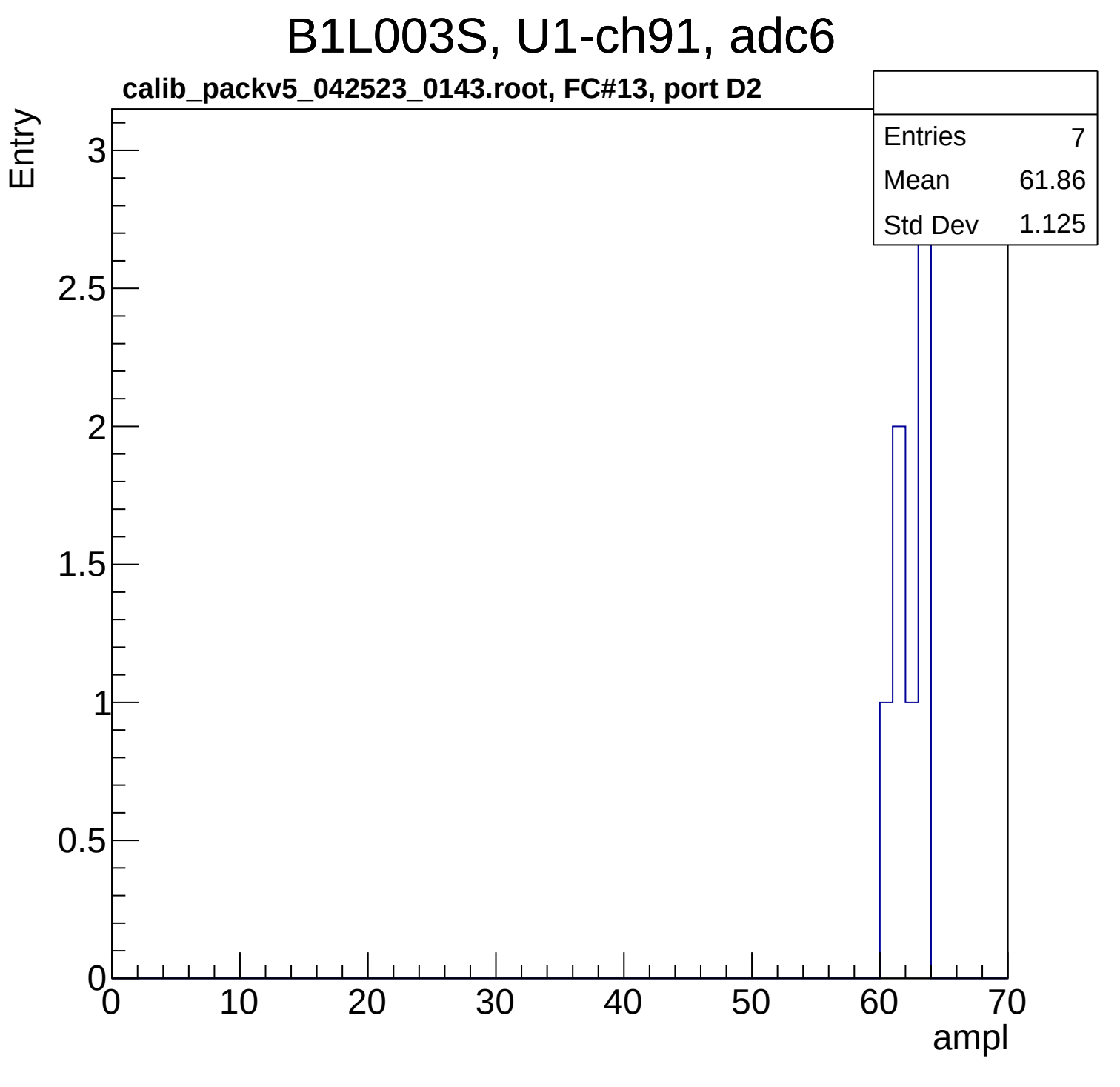

B1L003S, U1-ch91, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.11
Std Dev 3.356
Gaus mean : 37.2917
Gaus Width: 4.2357
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

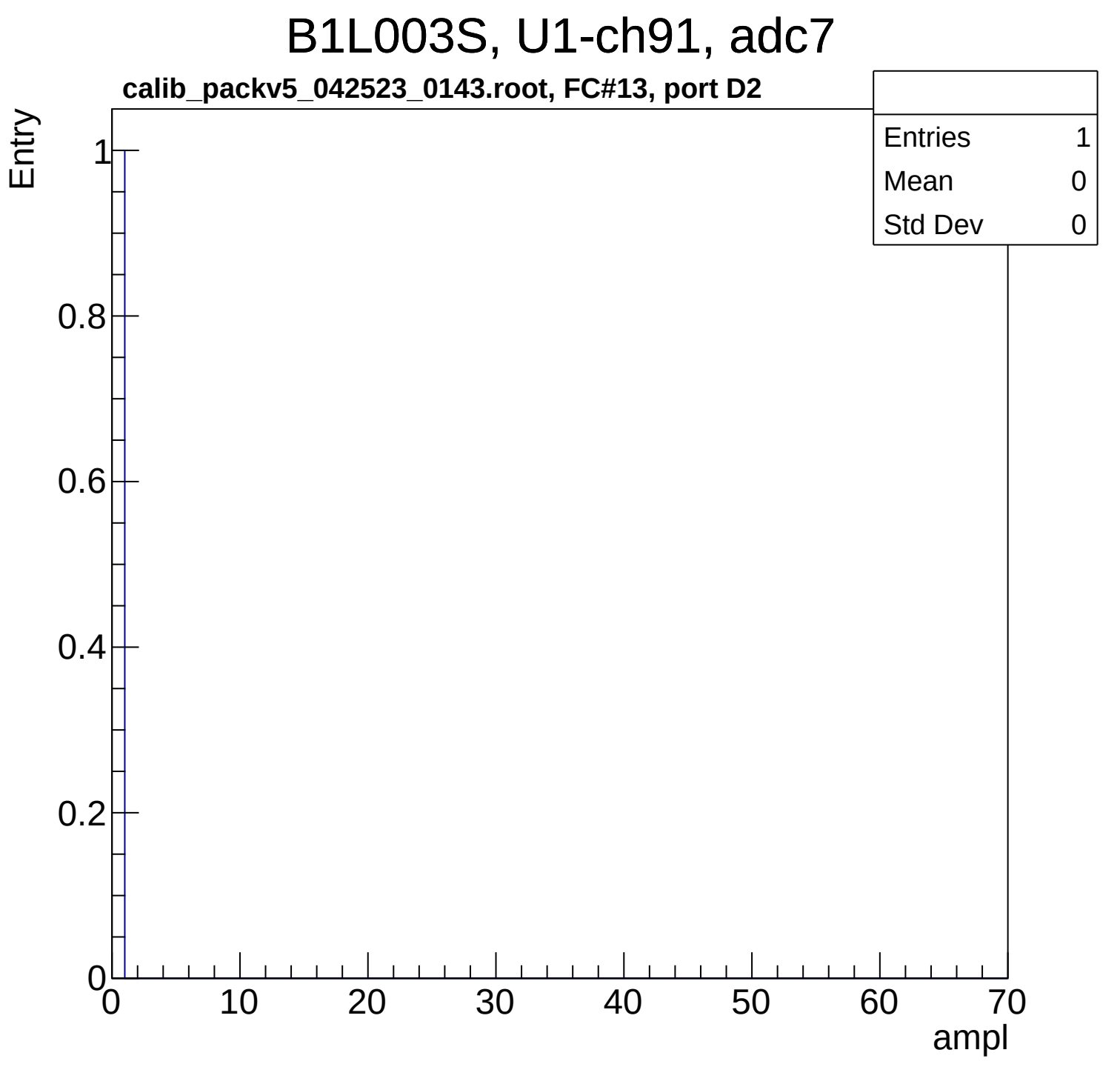

B1L003S, U1-ch91, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 43.51
Std Dev 3.504
Gaus mean : 44.4837
Gaus Width: 4.1304
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

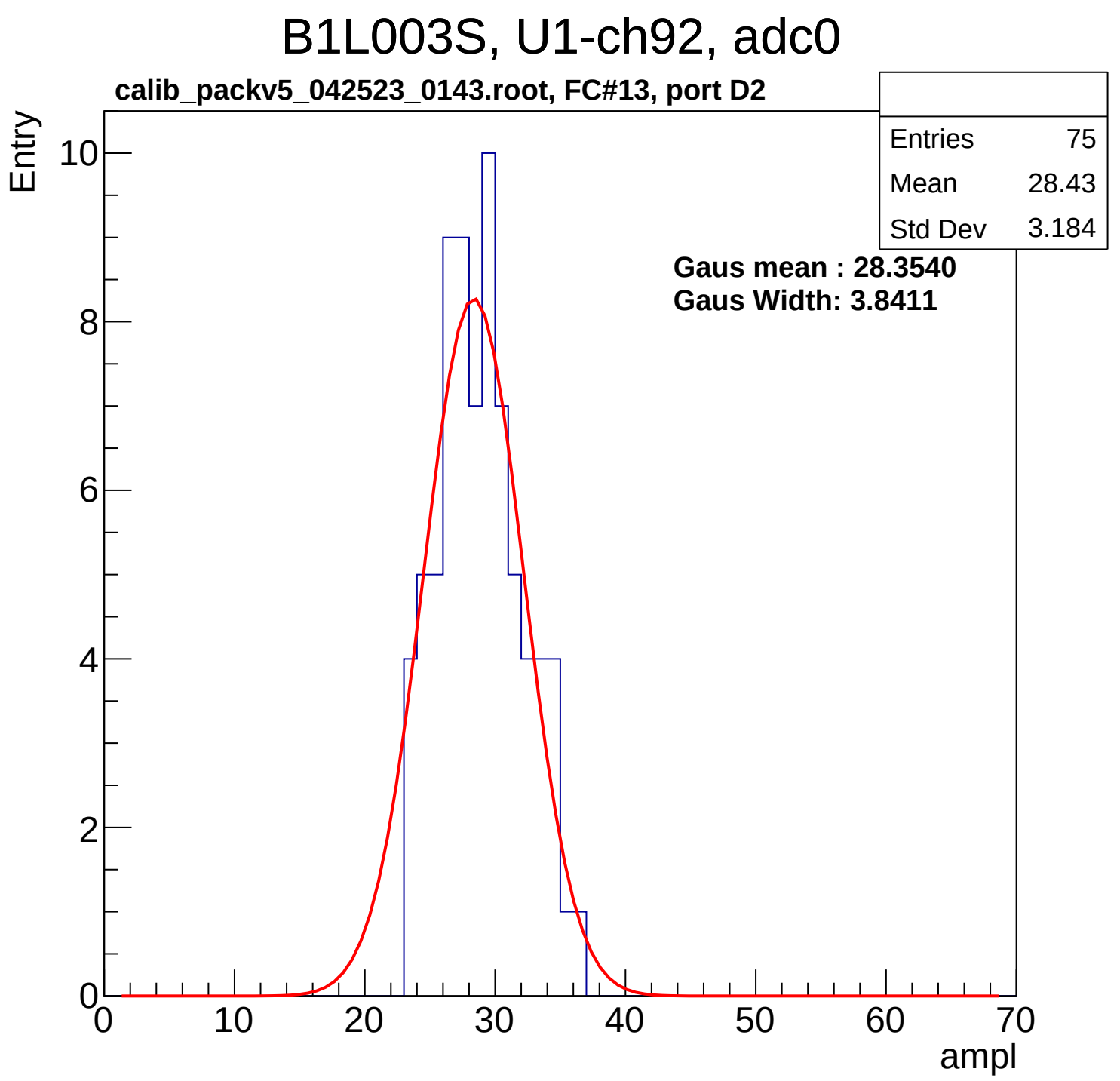

B1L003S, U1-ch91, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 49.5
Std Dev 3.556
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

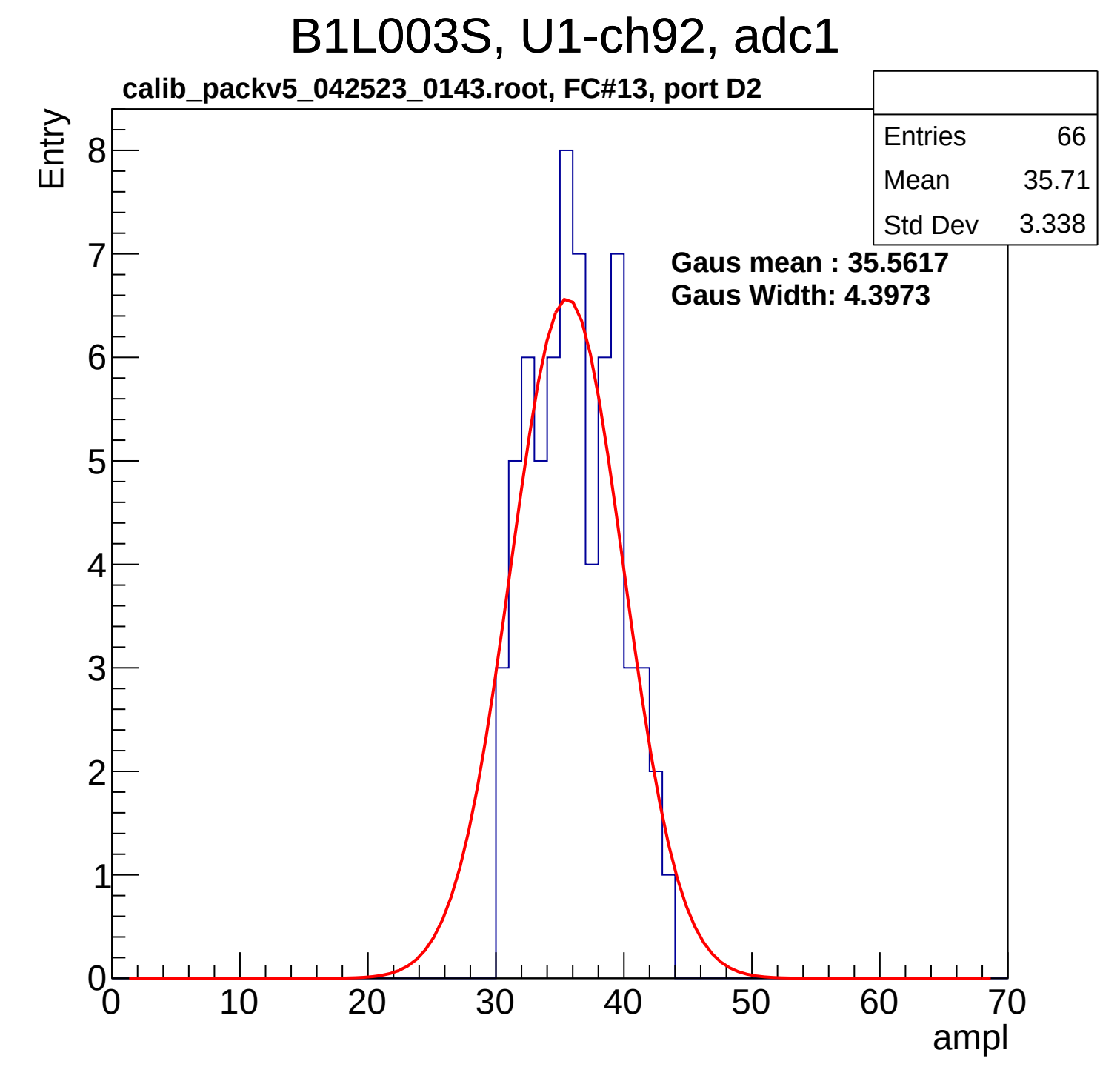

B1L003S, U1-ch91, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 56.17
Std Dev 3.253
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

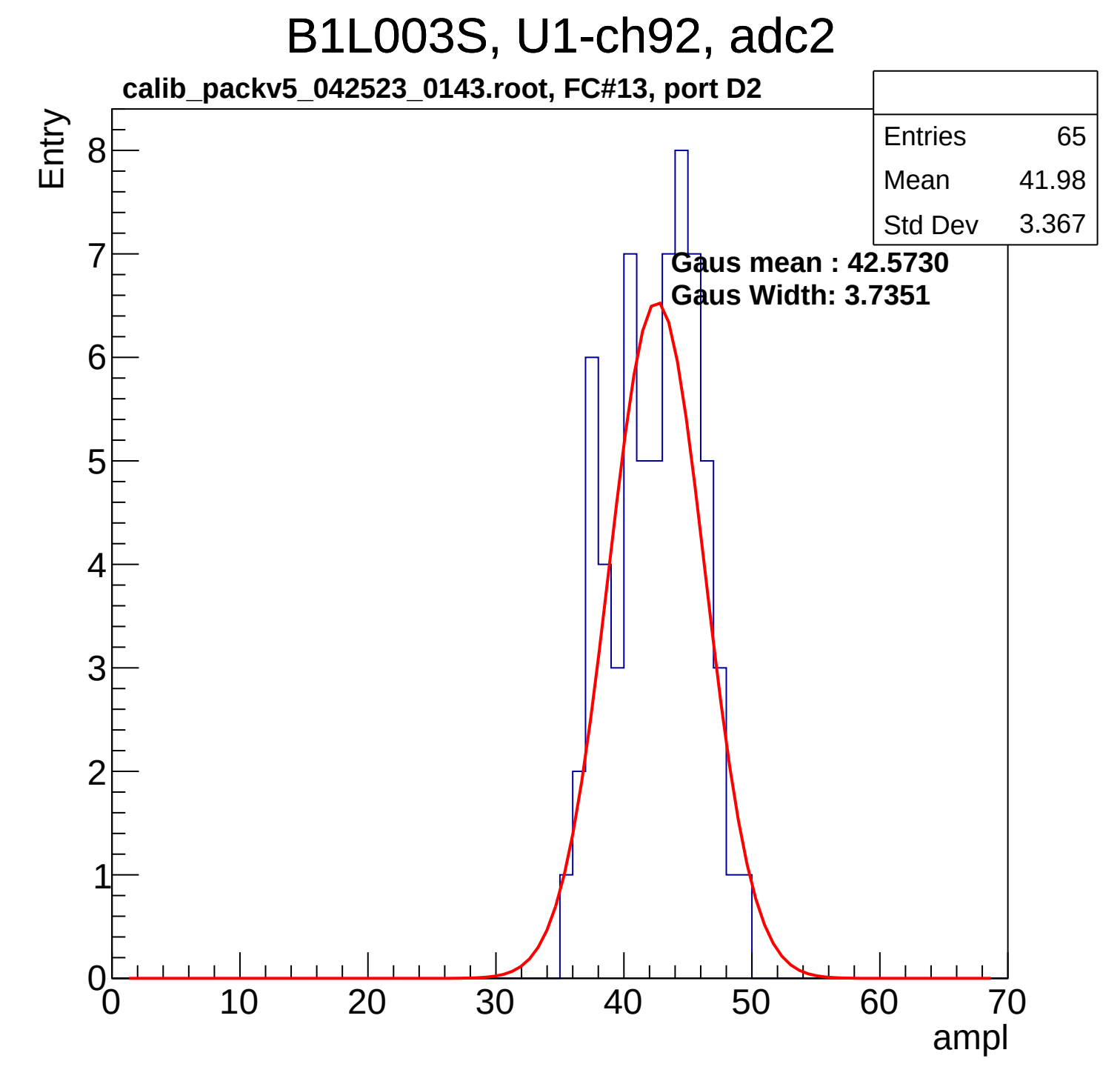

B1L003S, U1-ch91, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 31
Mean 58.65
Std Dev 10.85
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

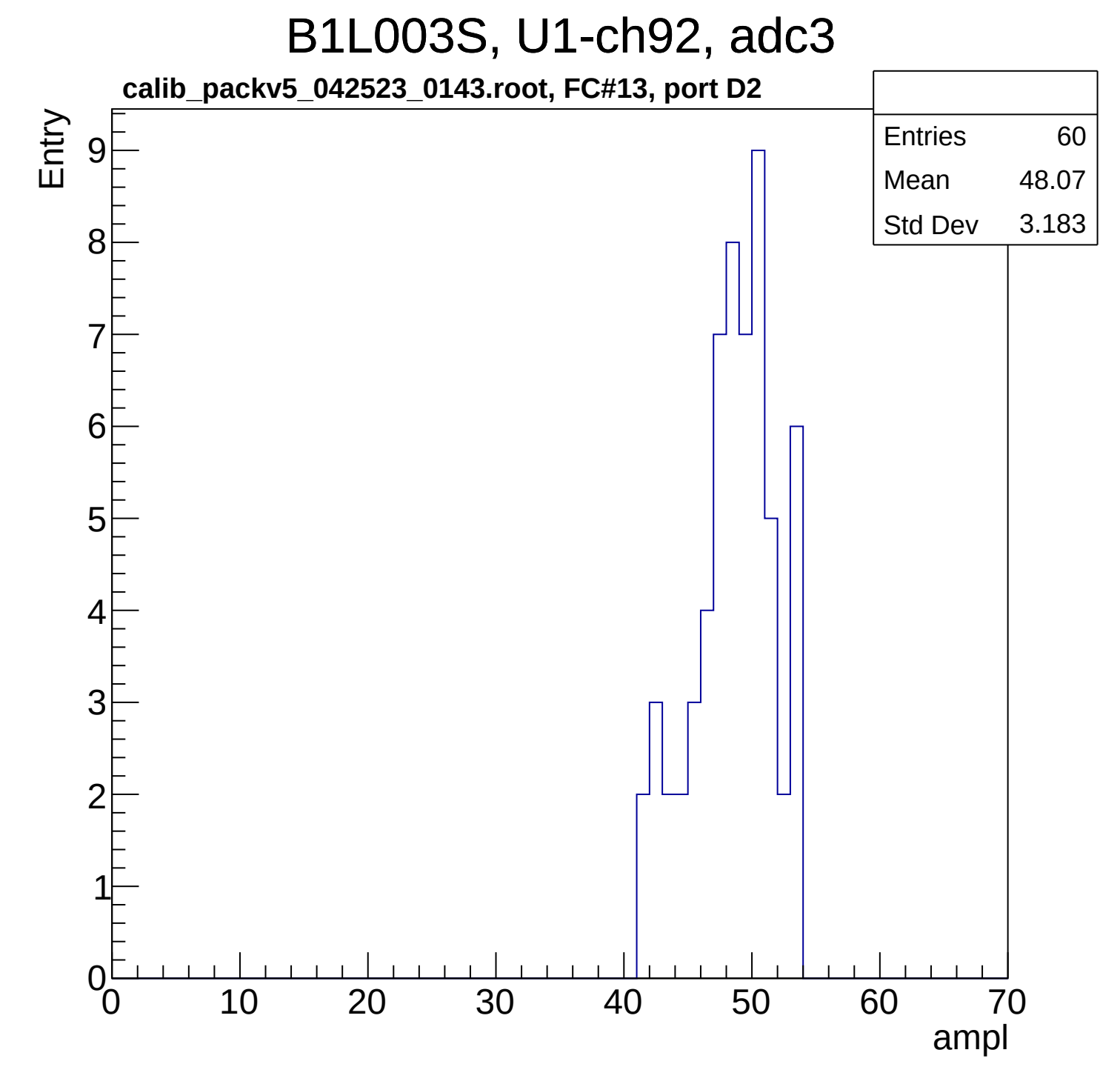

B1L003S, U1-ch91, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 61.86
Std Dev 1.125
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl

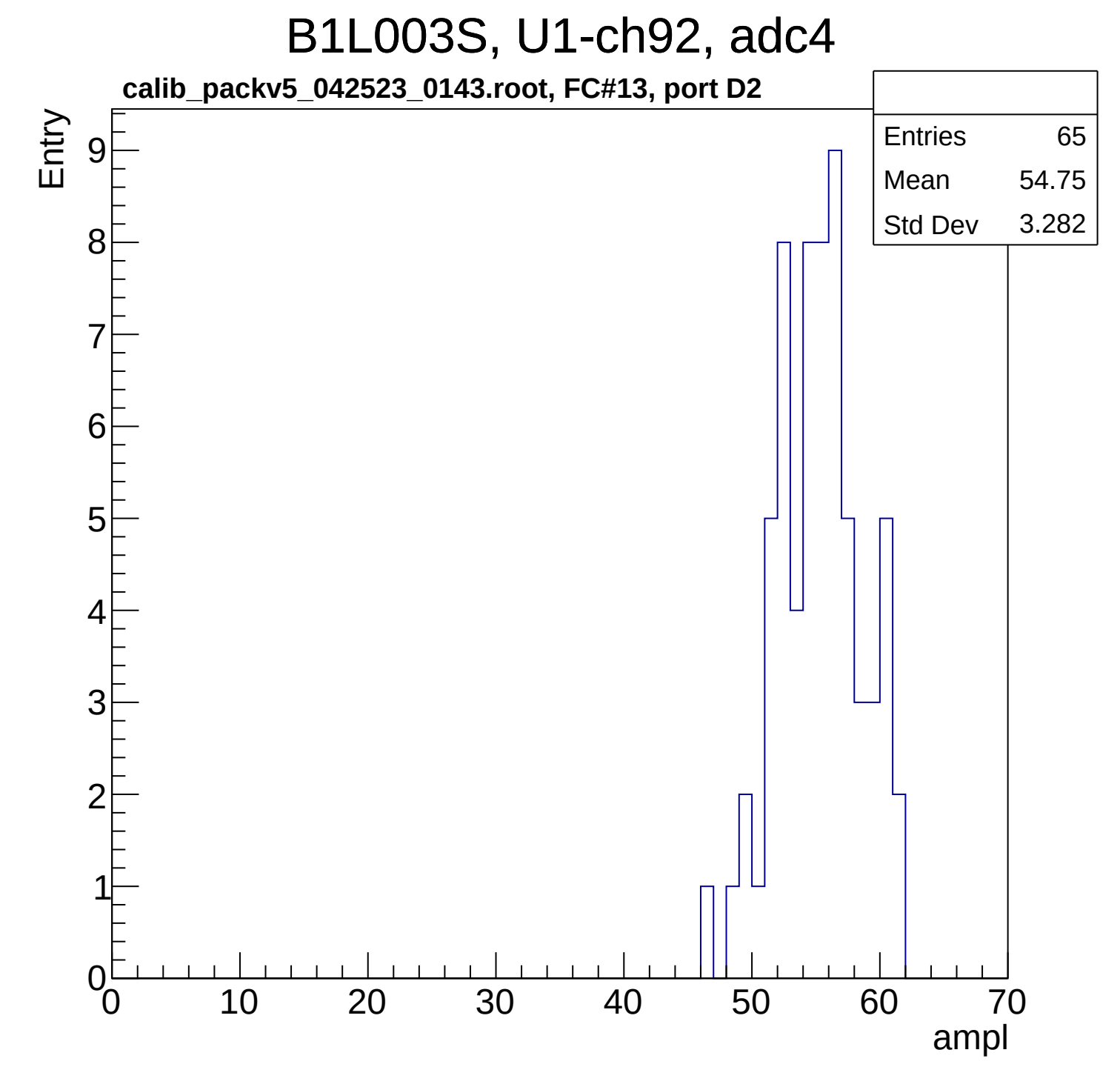

B1L003S, U1-ch91, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

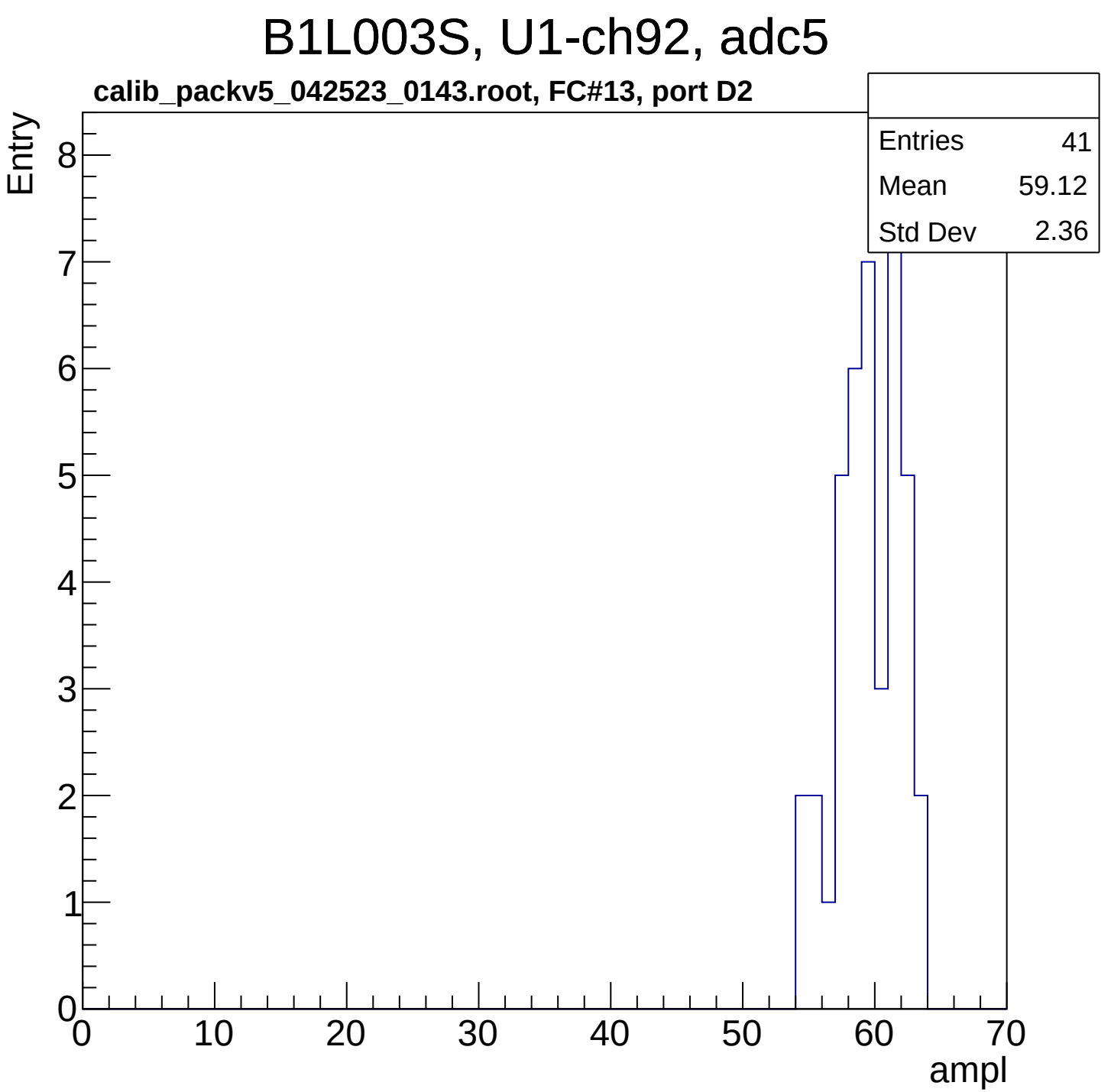

B1L003S, U1-ch92, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 28.43
Std Dev 3.184
Gaus mean : 28.3540
Gaus Width: 3.8411
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

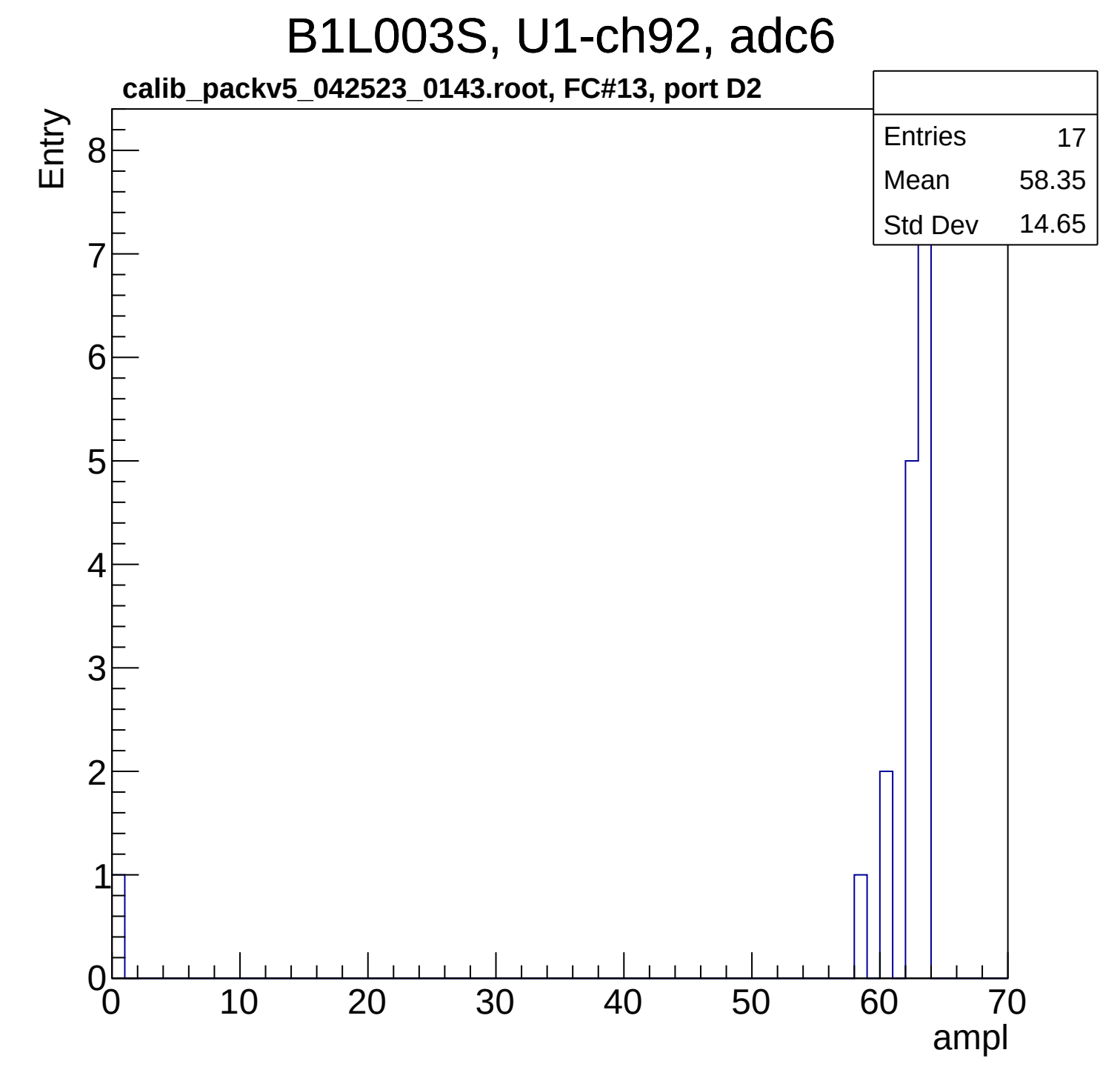

B1L003S, U1-ch92, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 35.71
Std Dev 3.338
Gaus mean : 35.5617
Gaus Width: 4.3973
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

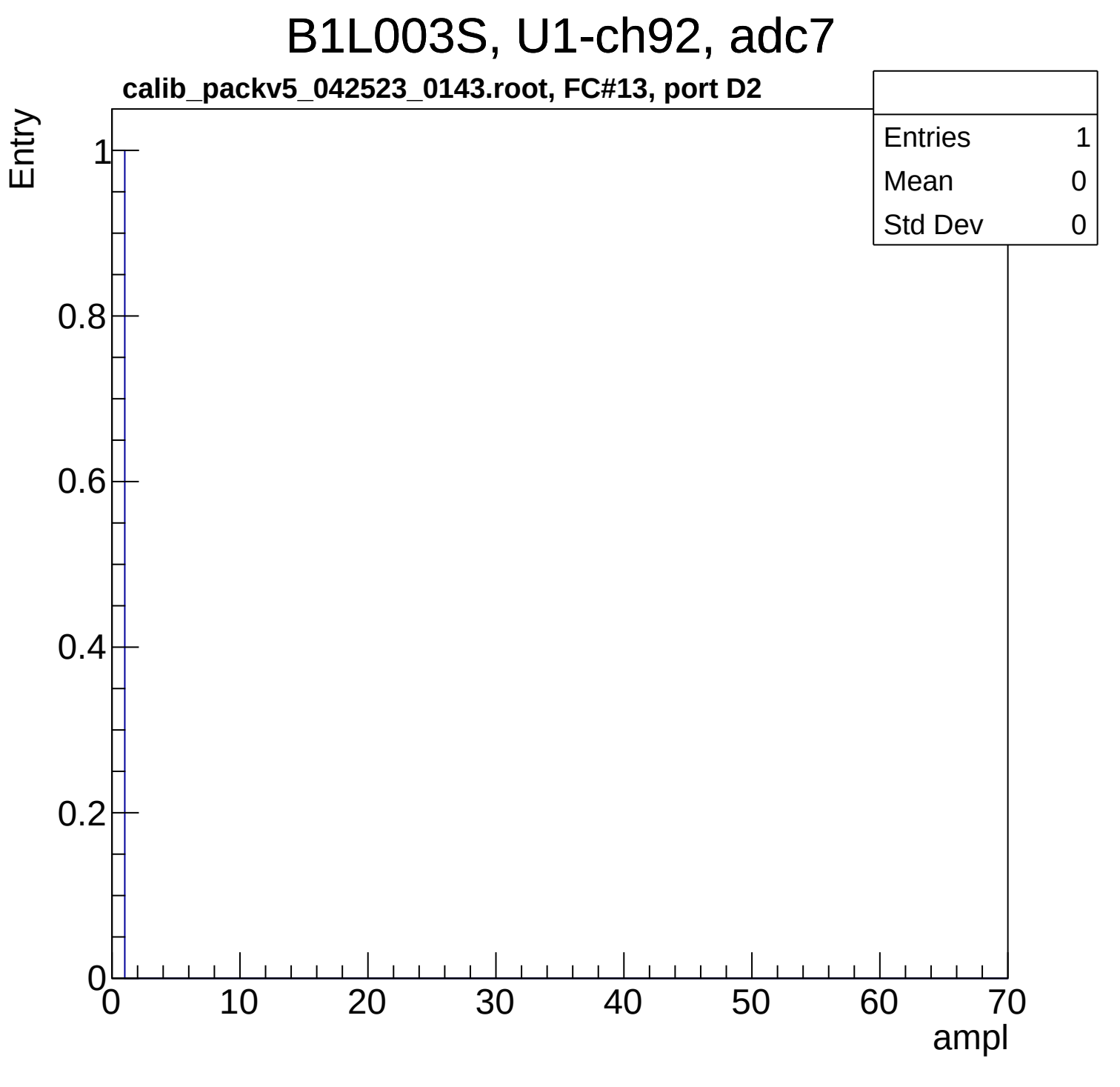

B1L003S, U1-ch92, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 41.98
Std Dev 3.367
Gaus mean : 42.5730
Gaus Width: 3.7351
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

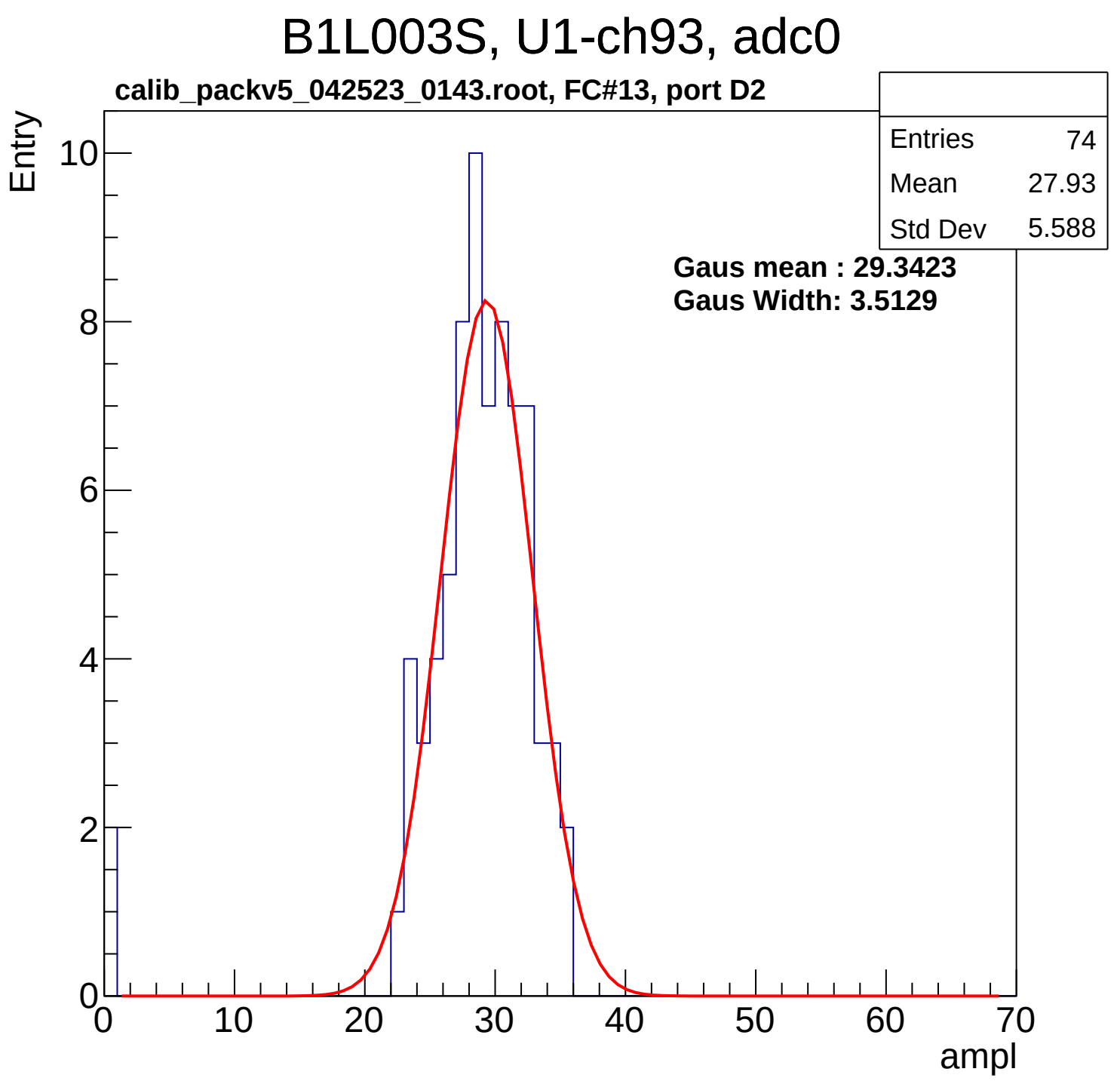

B1L003S, U1-ch92, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 48.07
Std Dev 3.183
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl

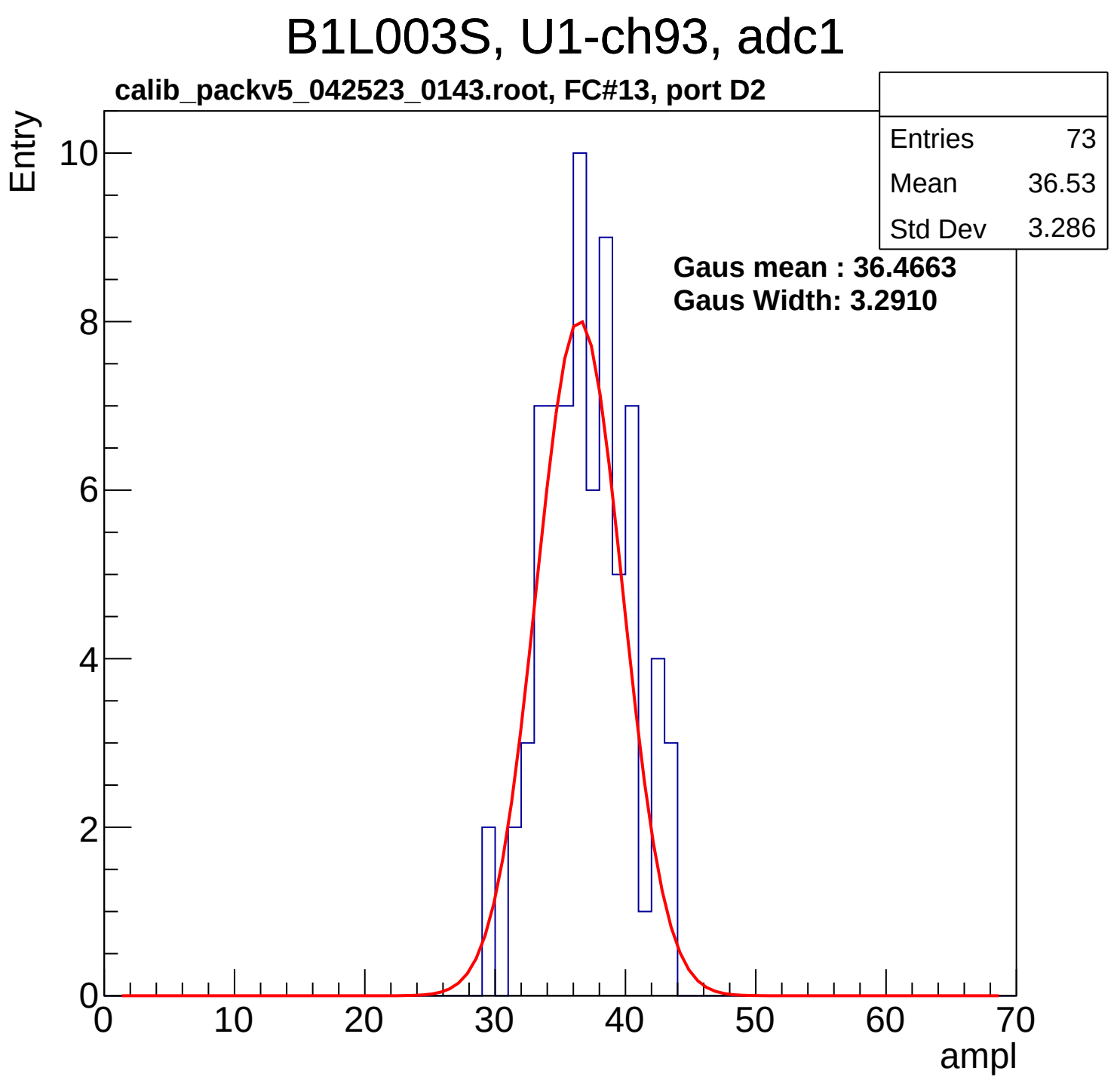

B1L003S, U1-ch92, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 54.75
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

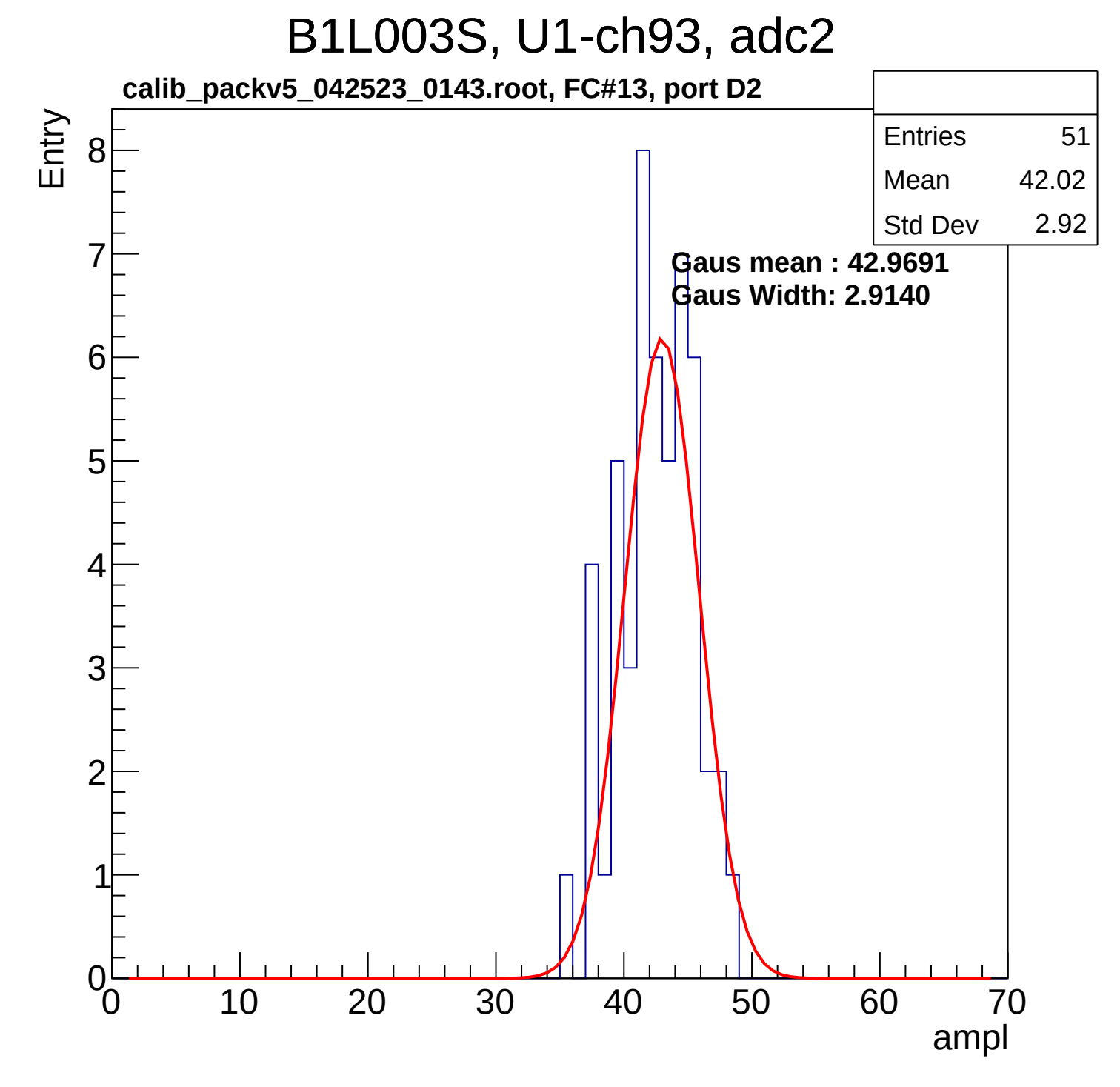

B1L003S, U1-ch92, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 59.12
Std Dev 2.36
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

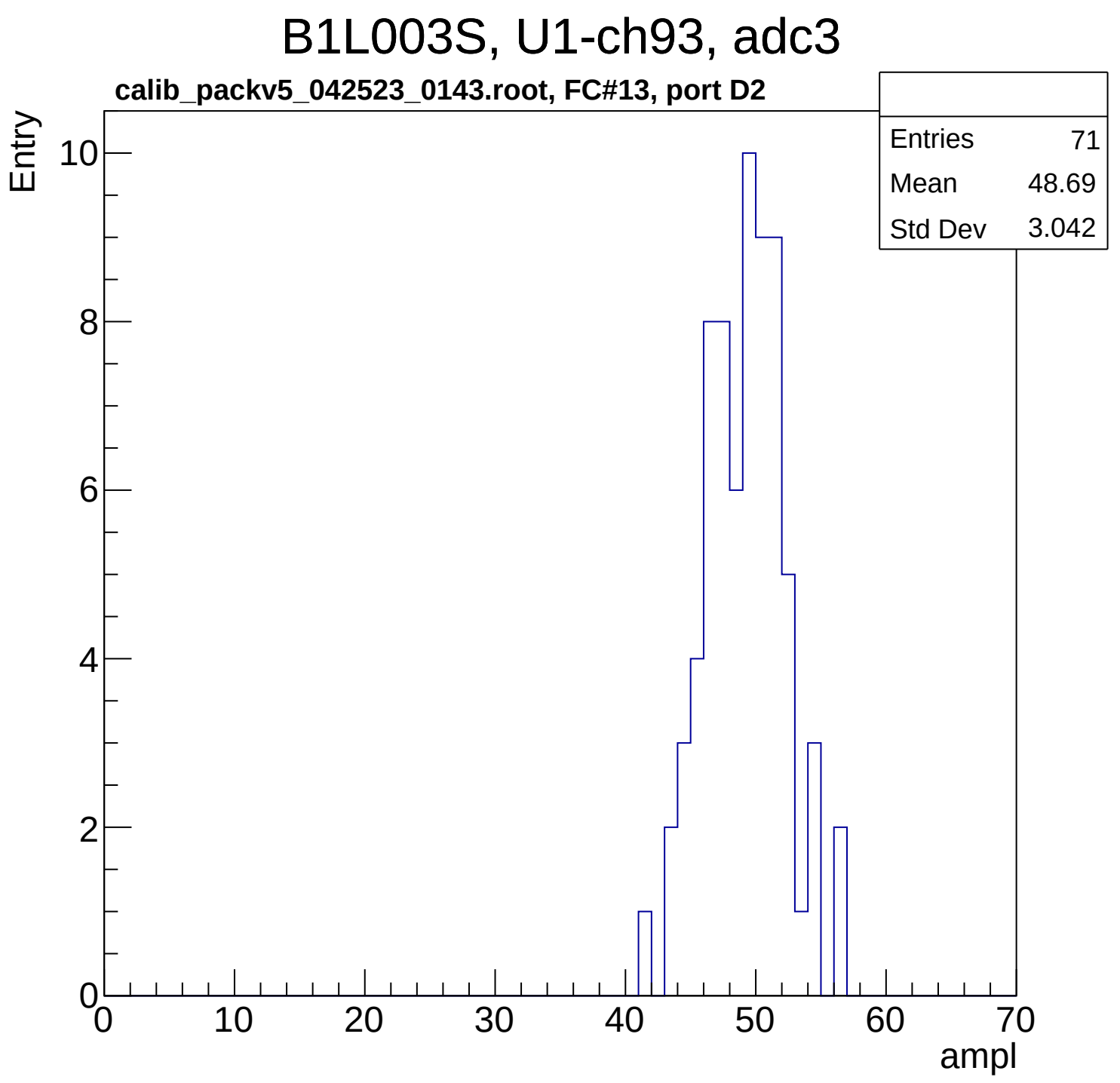

B1L003S, U1-ch92, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 17
Mean 58.35
Std Dev 14.65
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

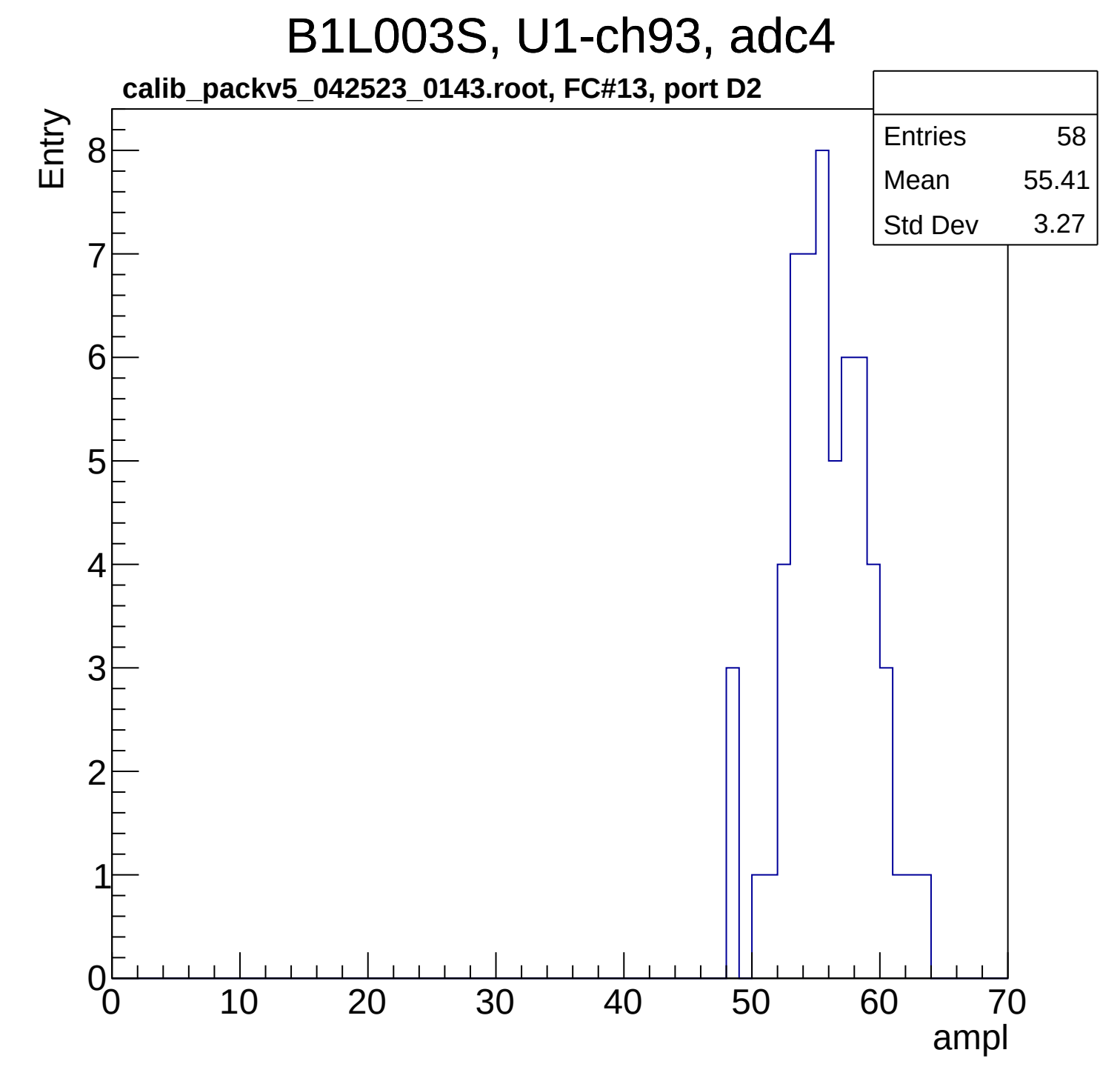

B1L003S, U1-ch92, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

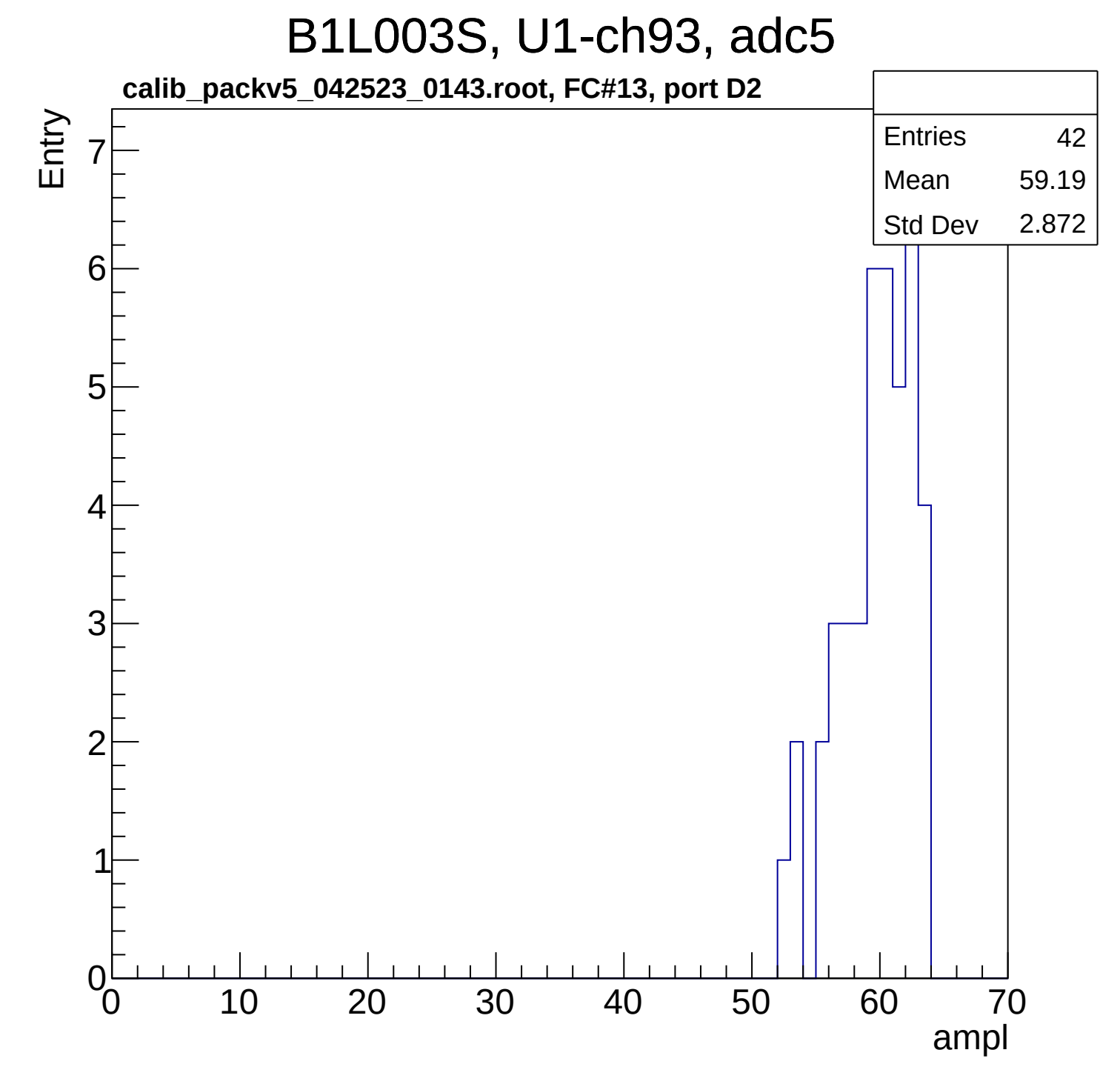

B1L003S, U1-ch93, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.93
Std Dev 5.588
Gaus mean : 29.3423
Gaus Width: 3.5129
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

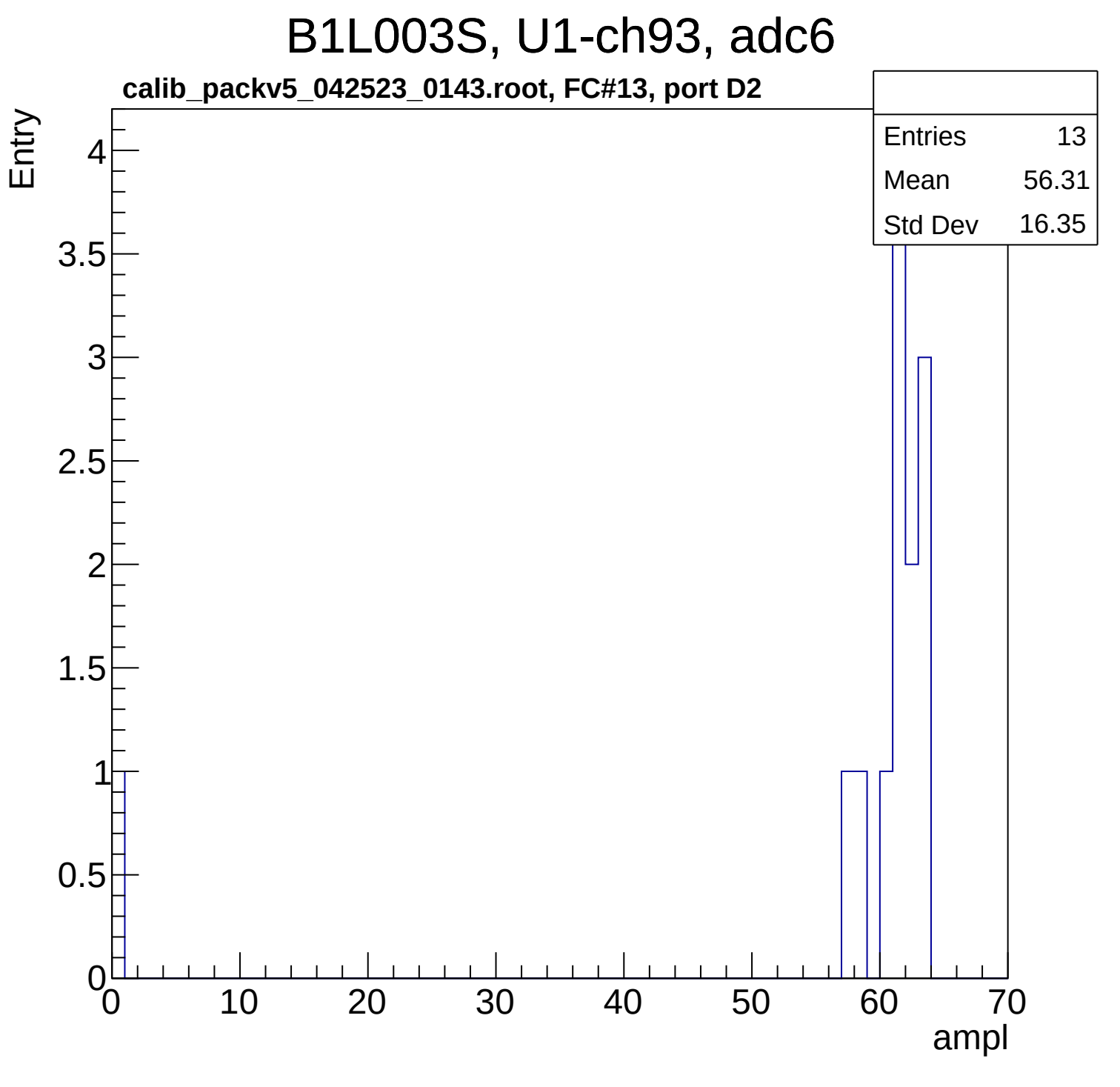

B1L003S, U1-ch93, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 36.53
Std Dev 3.286
Gaus mean : 36.4663
Gaus Width: 3.2910
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch93, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 42.02
Std Dev 2.92
Gaus mean : 42.9691
Gaus Width: 2.9140
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch93, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 48.69
Std Dev 3.042
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch93, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 55.41
Std Dev 3.27
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch93, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 59.19
Std Dev 2.872
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch93, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 56.31
Std Dev 16.35
Entry
ampl
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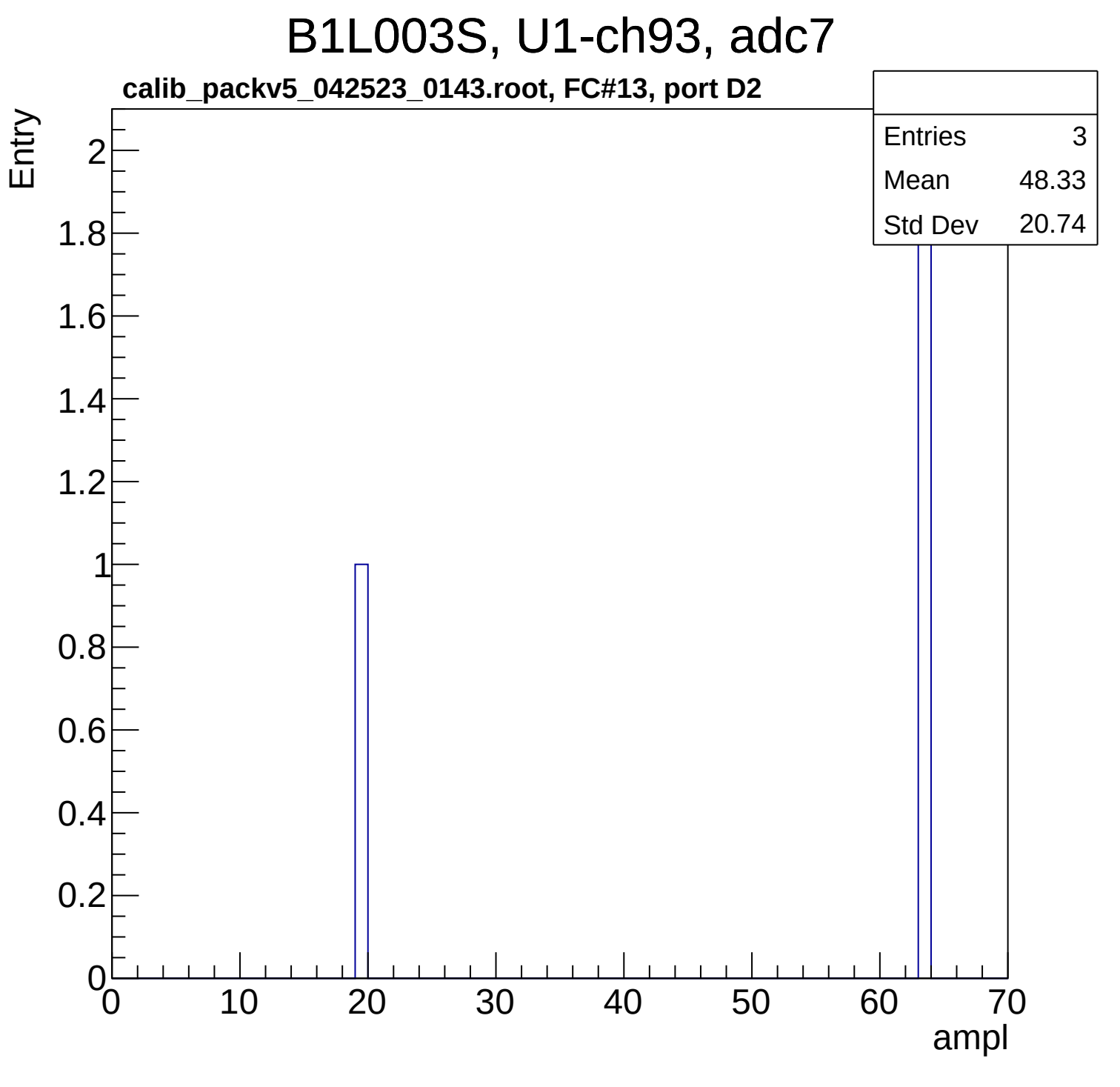
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch93, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 48.33
Std Dev 20.74
Entry
ampl
0
0.2
0.4
0.6
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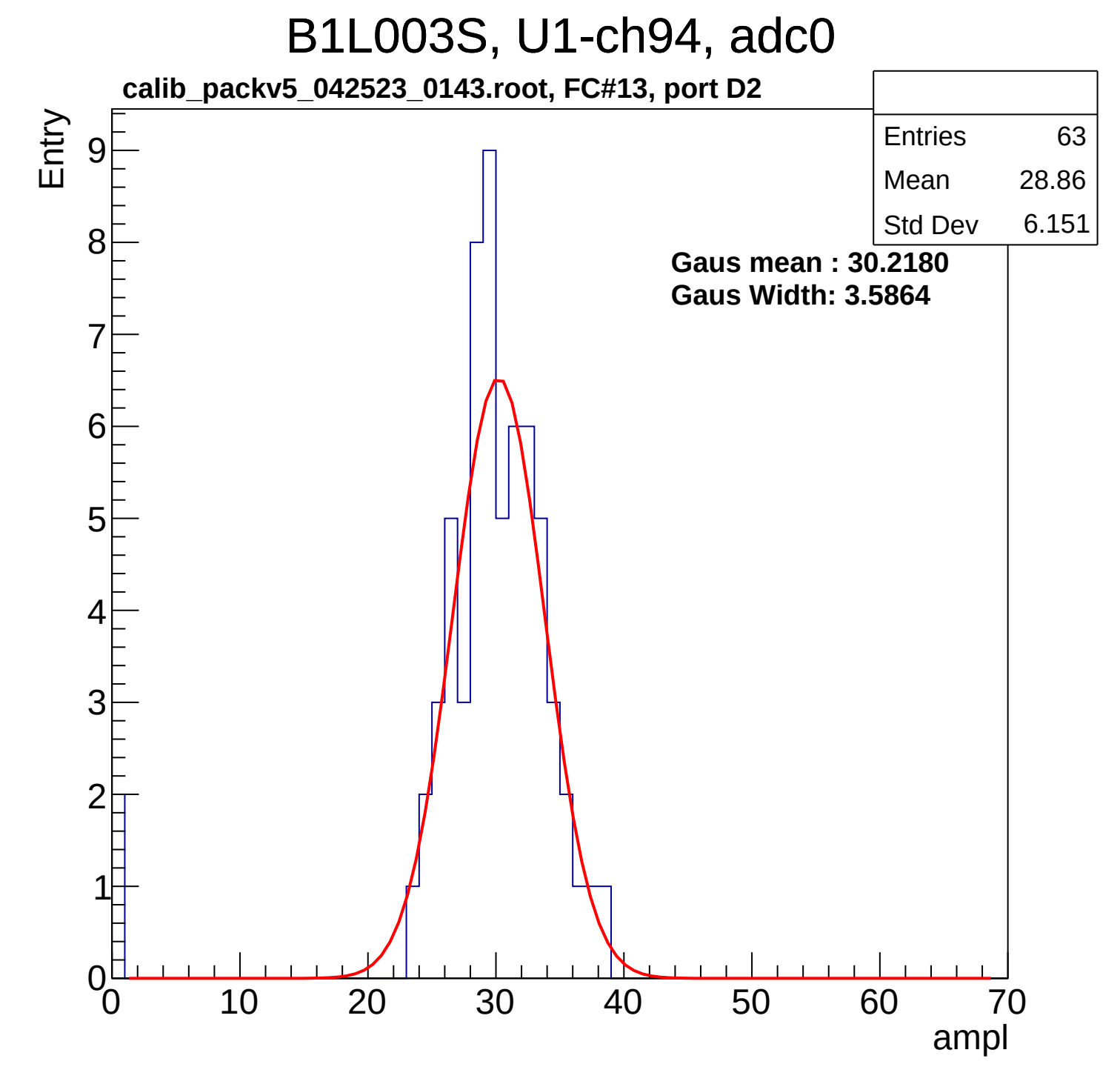
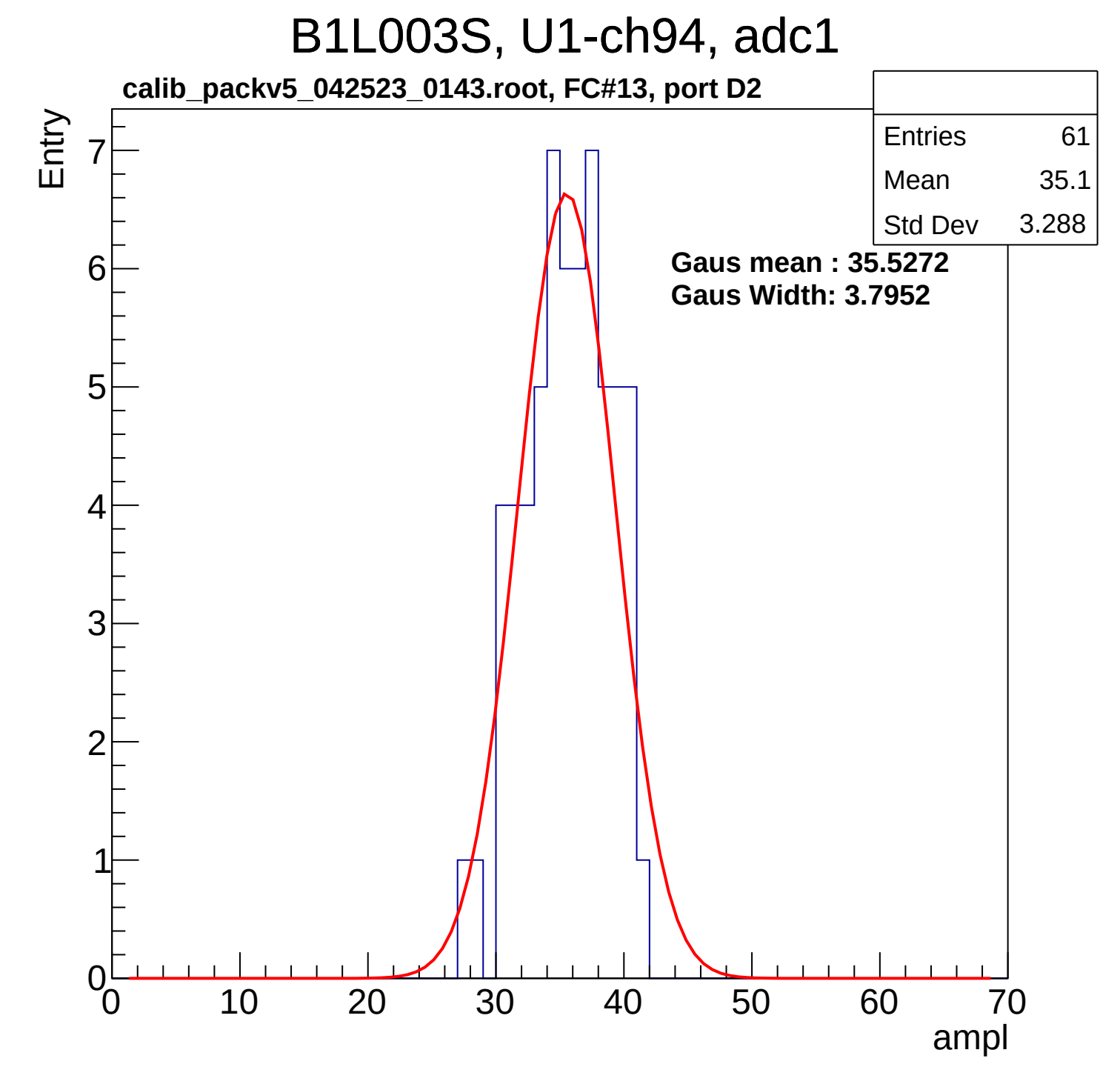
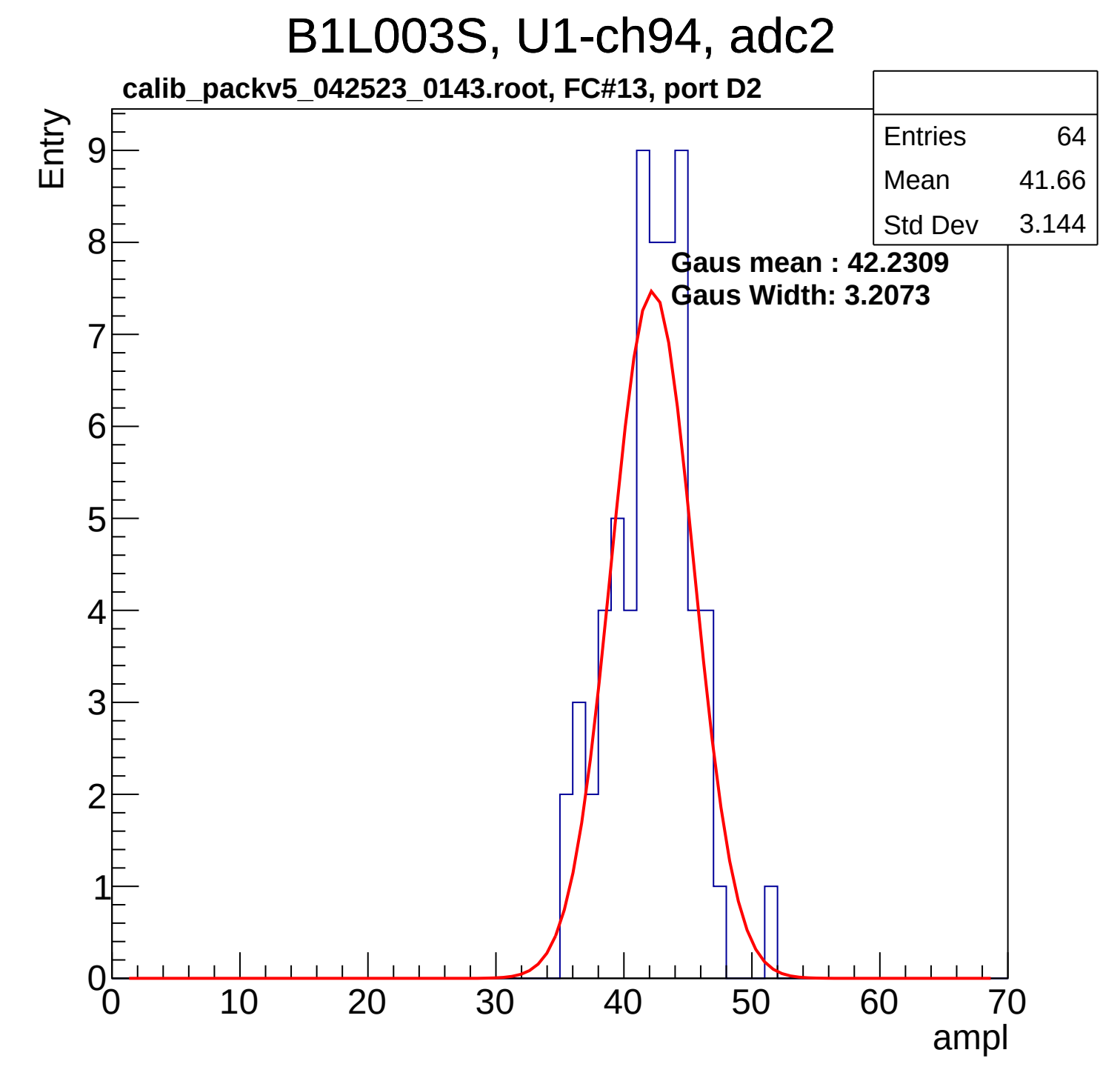
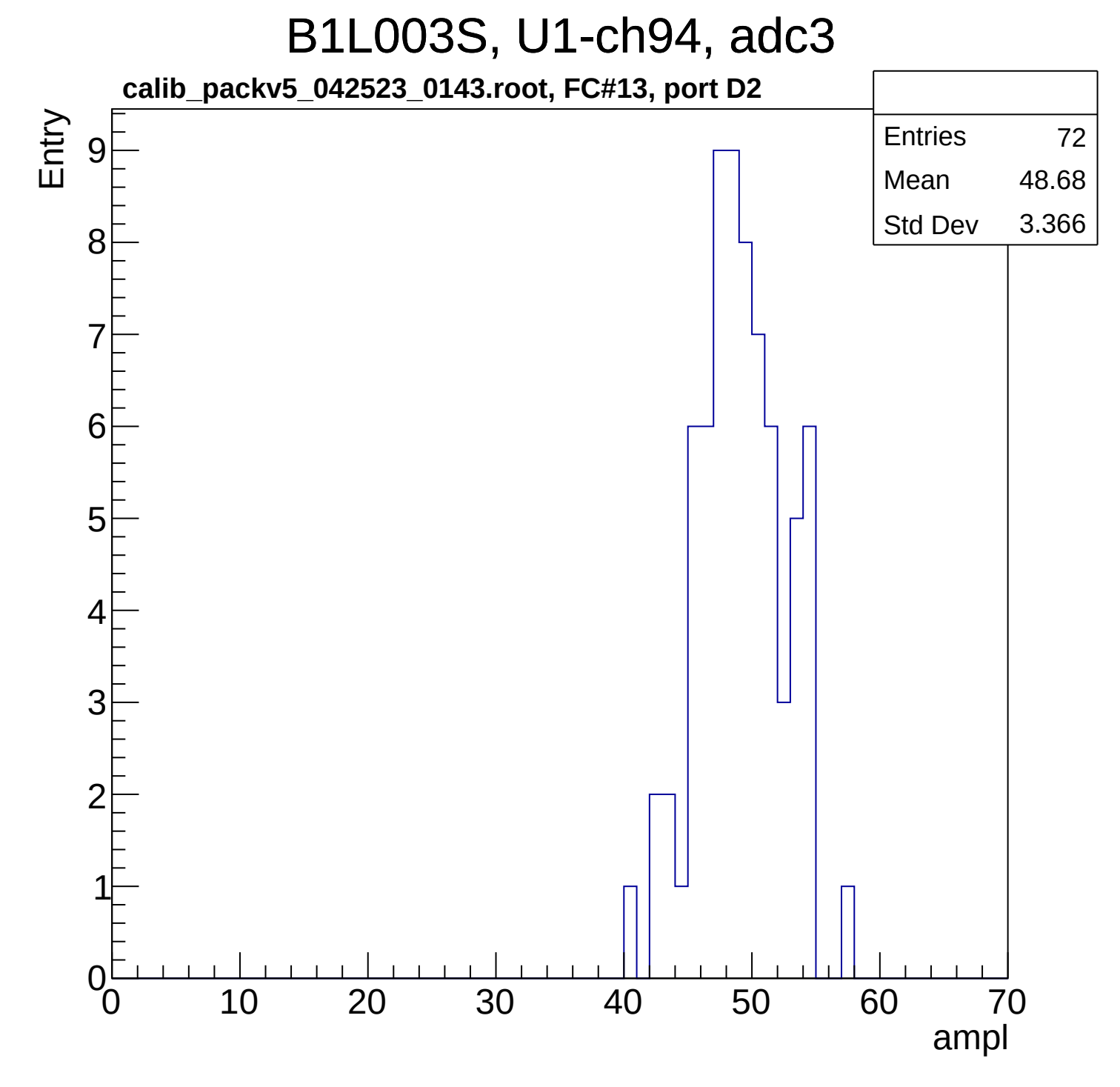
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.86
Std Dev 6.151
Gaus mean : 30.2180
Gaus Width: 3.5864
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 35.1
Std Dev 3.288
Gaus mean : 35.5272
Gaus Width: 3.7952
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 41.66
Std Dev 3.144
Gaus mean : 42.2309
Gaus Width: 3.2073
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 48.68
Std Dev 3.366
Entry
ampl
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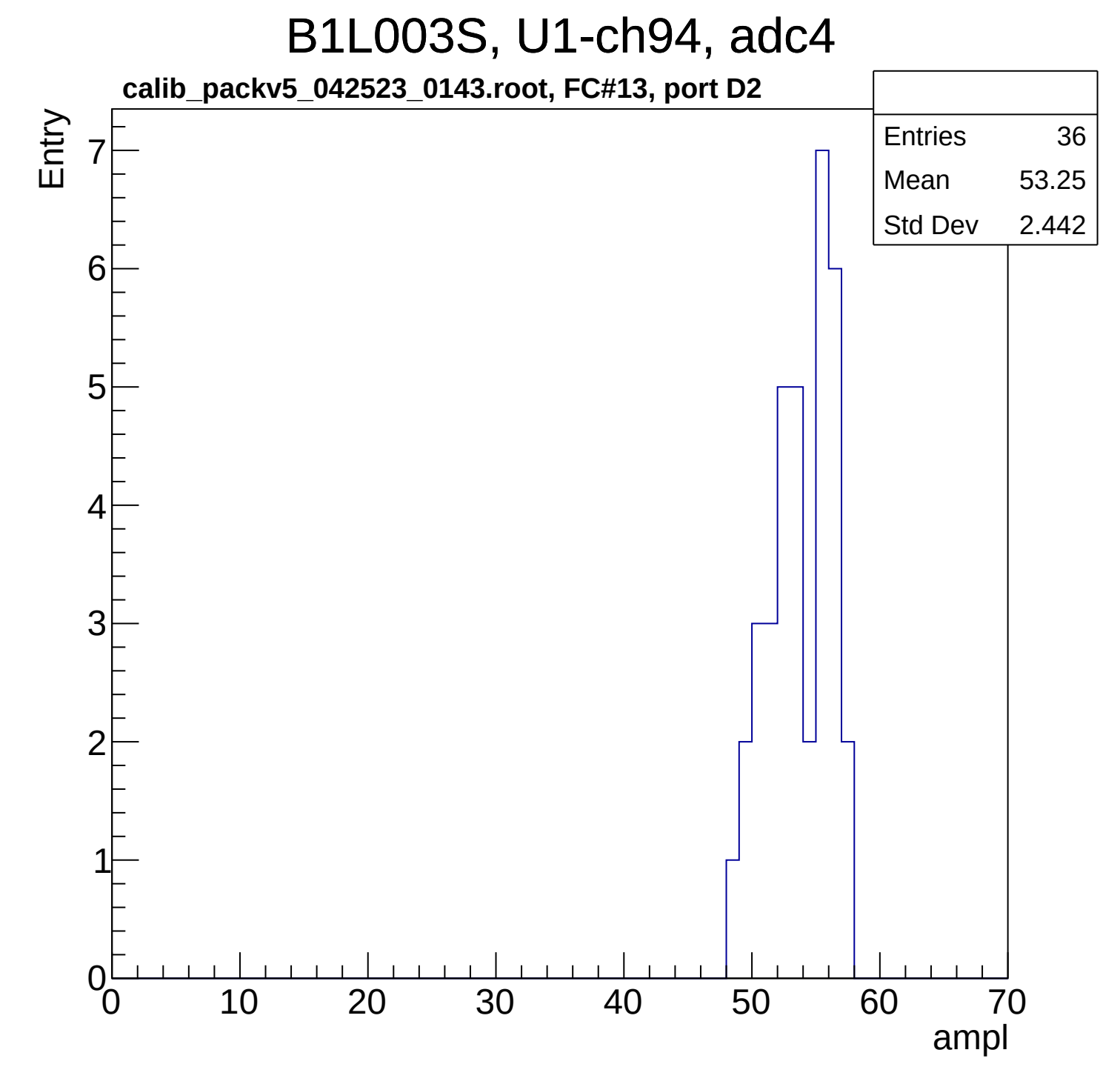
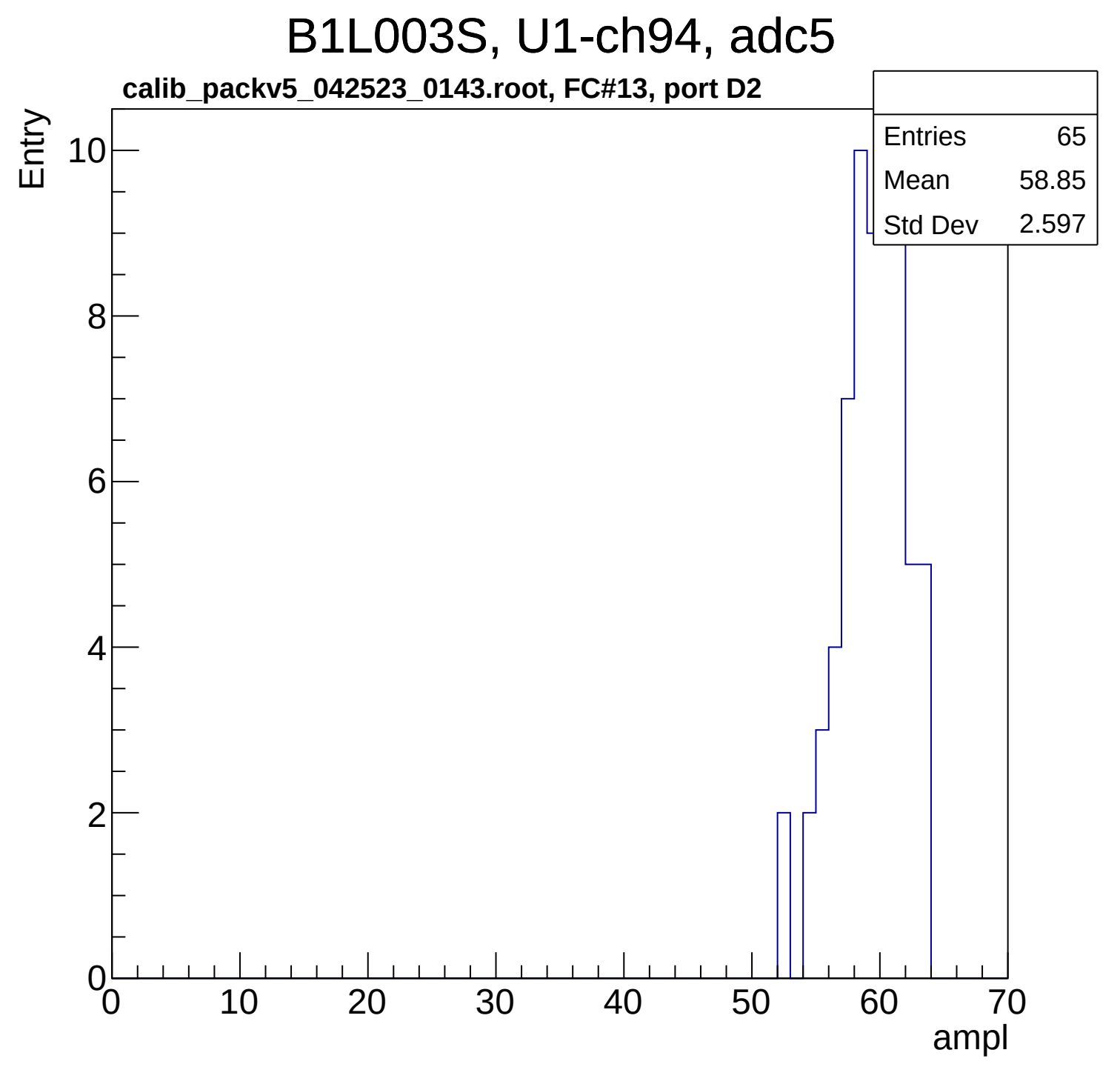
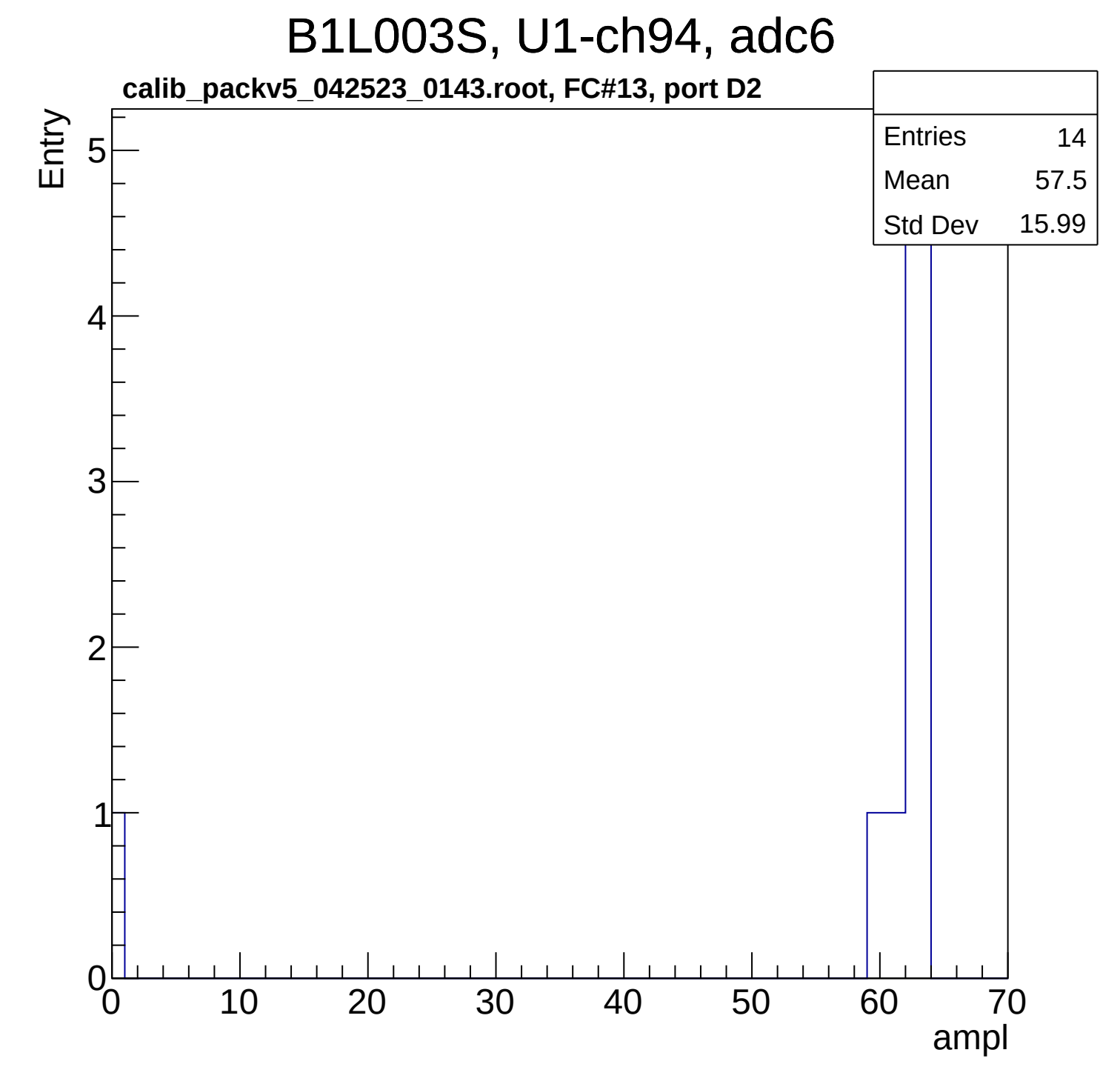
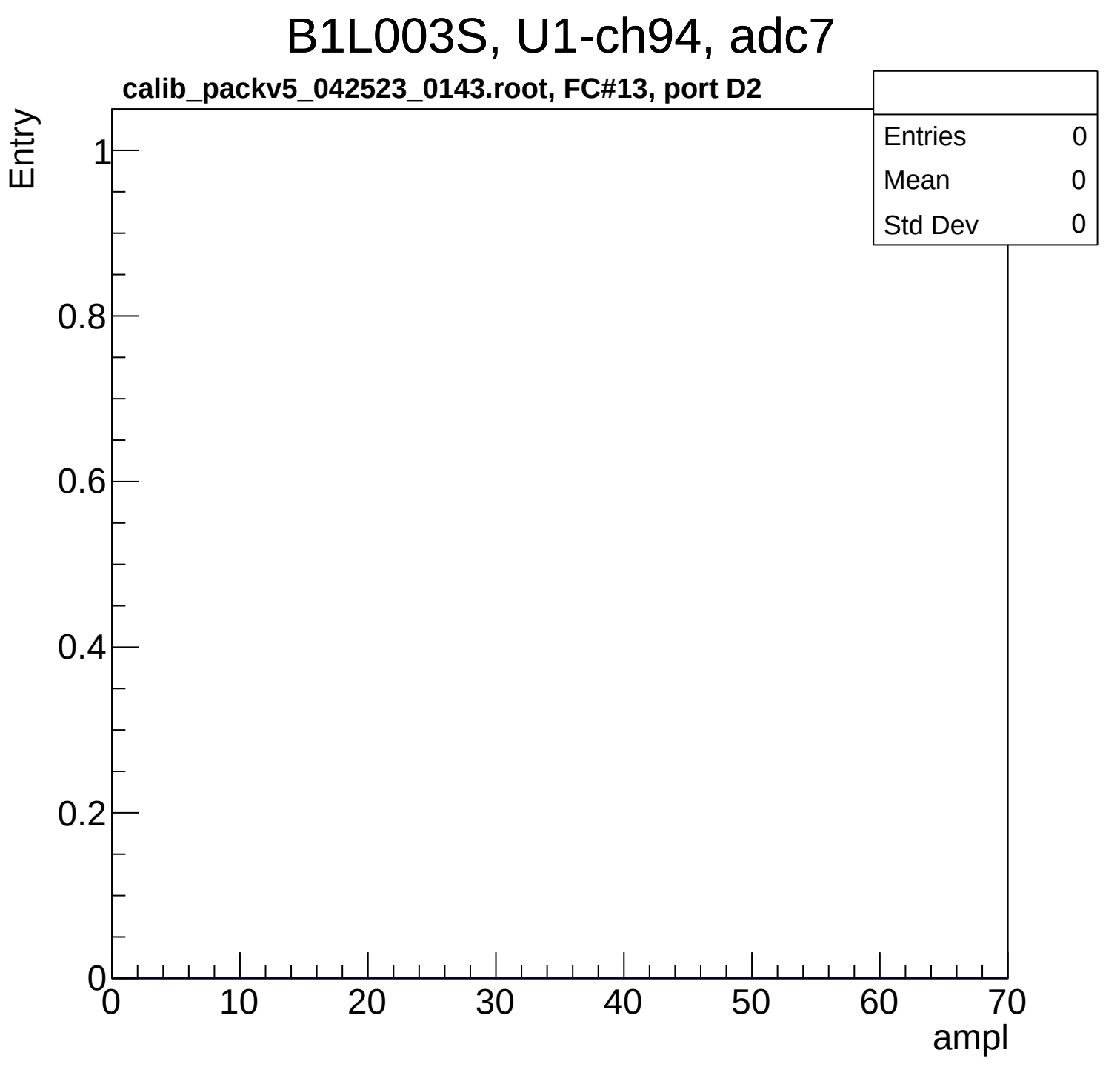
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 36
Mean 53.25
Std Dev 2.442
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 58.85
Std Dev 2.597
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch94, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 14
Mean 57.5
Std Dev 15.99
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch94, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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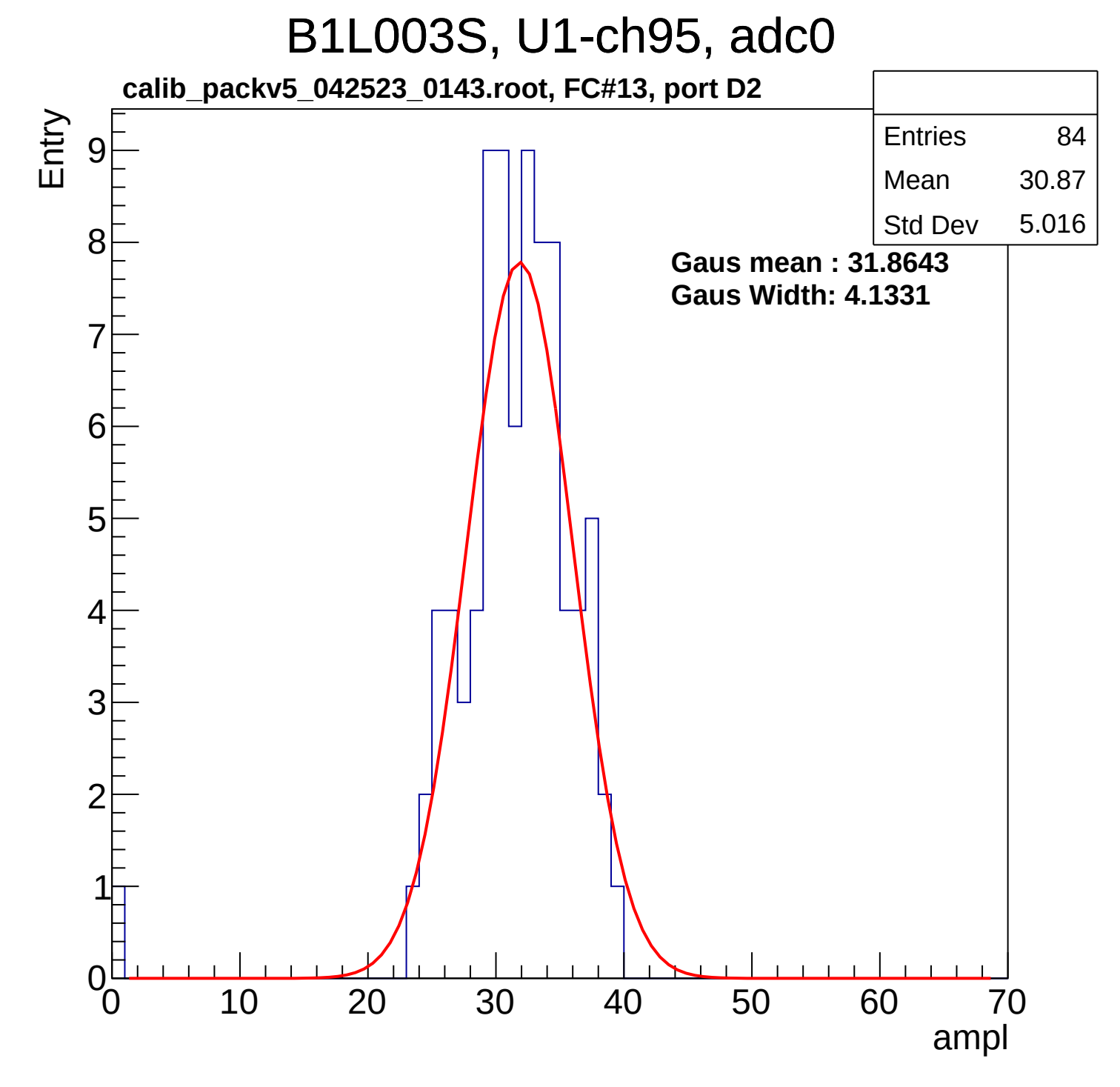
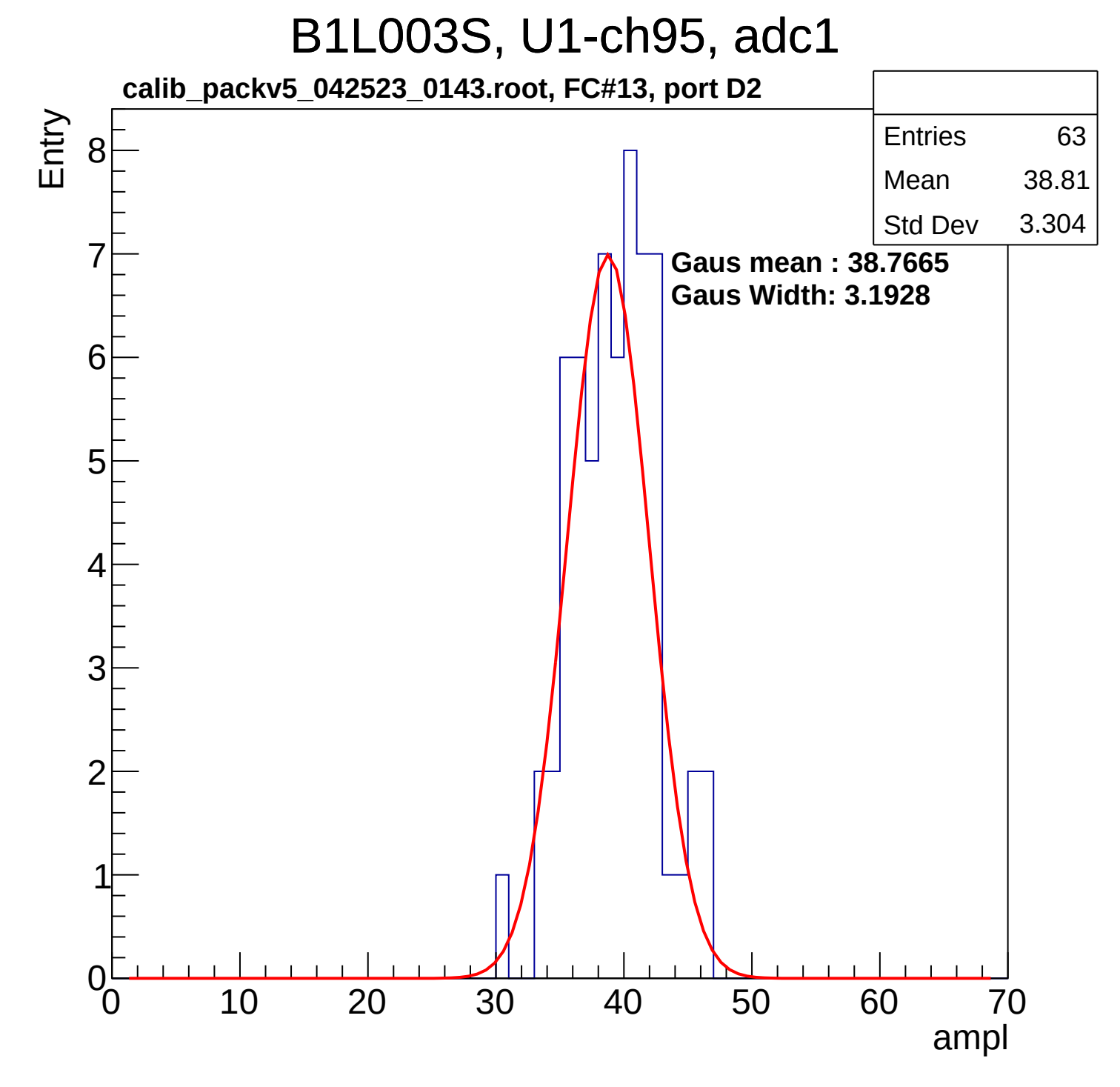
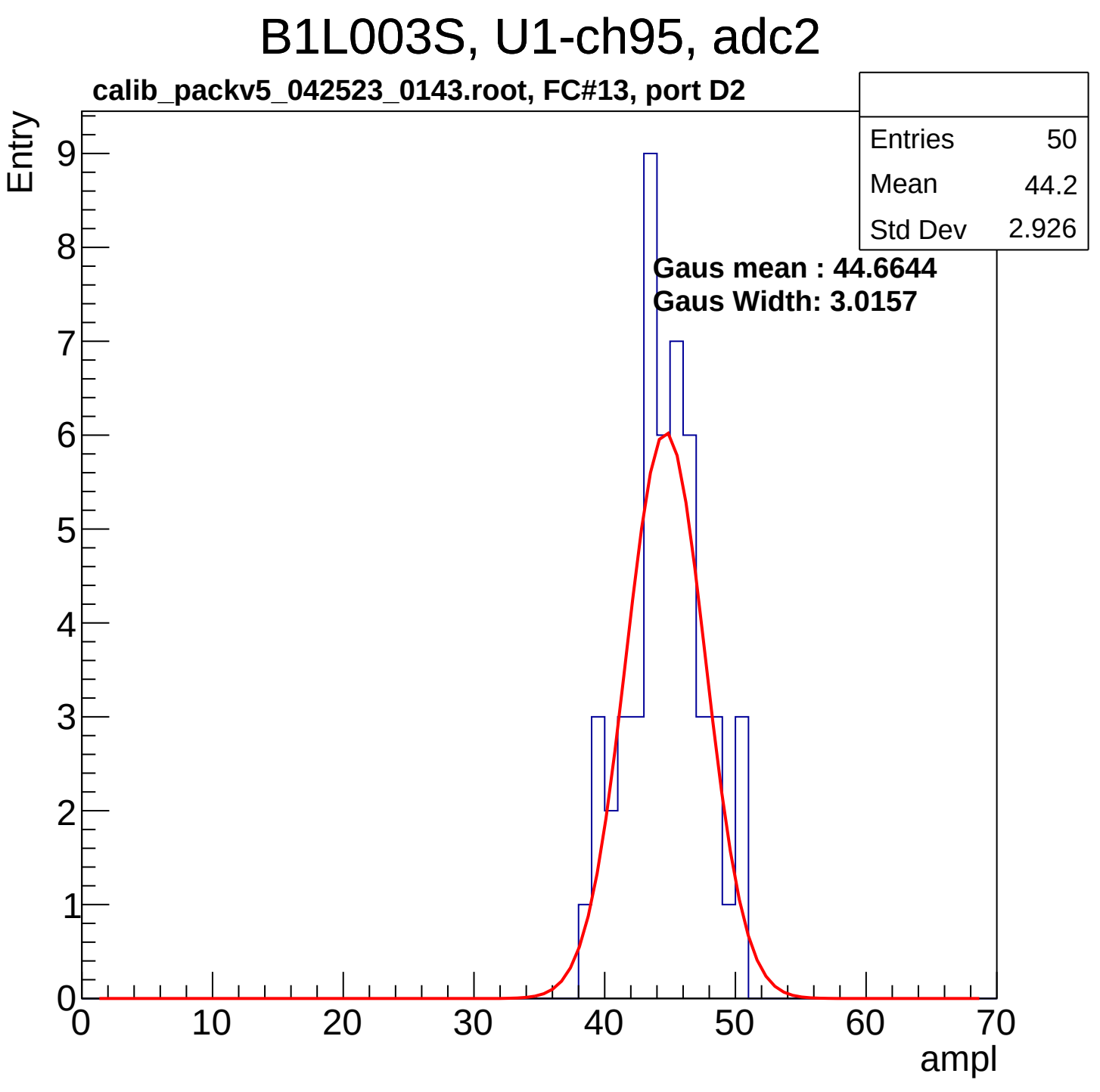
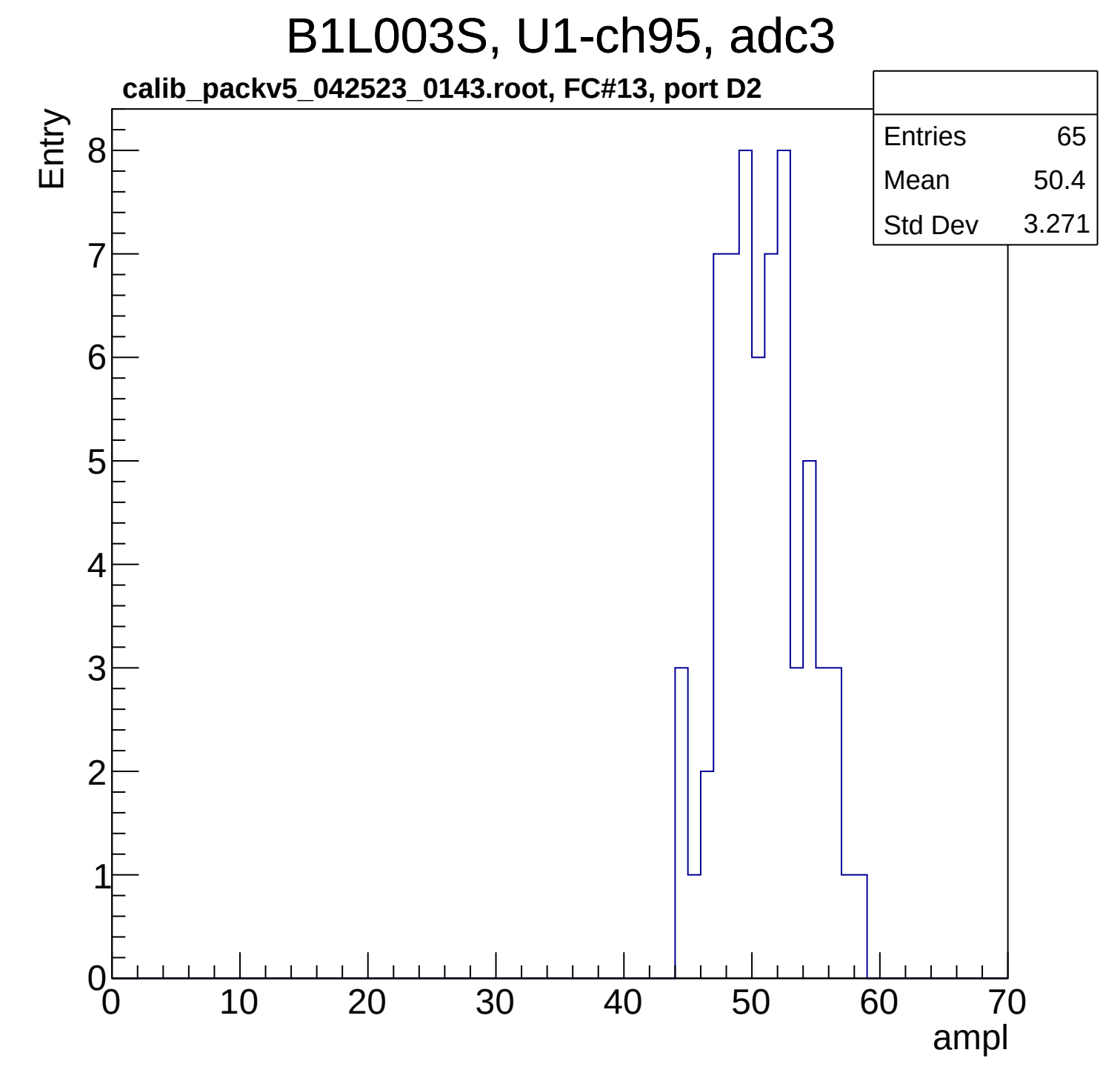
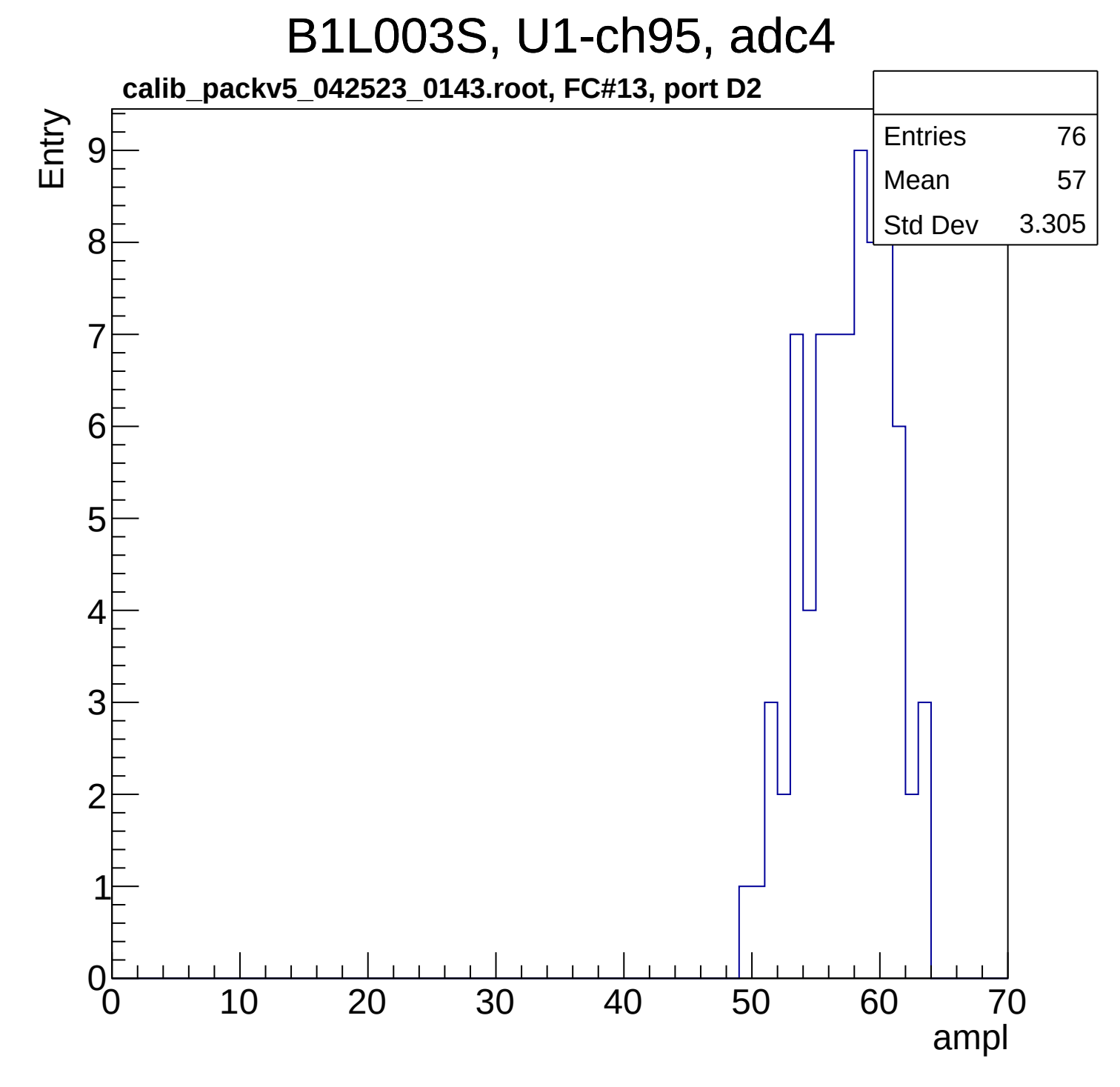
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch95, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 84
Mean 30.87
Std Dev 5.016
Gaus mean : 31.8643
Gaus Width: 4.1331
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch95, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 38.81
Std Dev 3.304
Gaus mean : 38.7665
Gaus Width: 3.1928
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch95, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 44.2
Std Dev 2.926
Gaus mean : 44.6644
Gaus Width: 3.0157
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch95, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 50.4
Std Dev 3.271
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch95, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 57
Std Dev 3.305
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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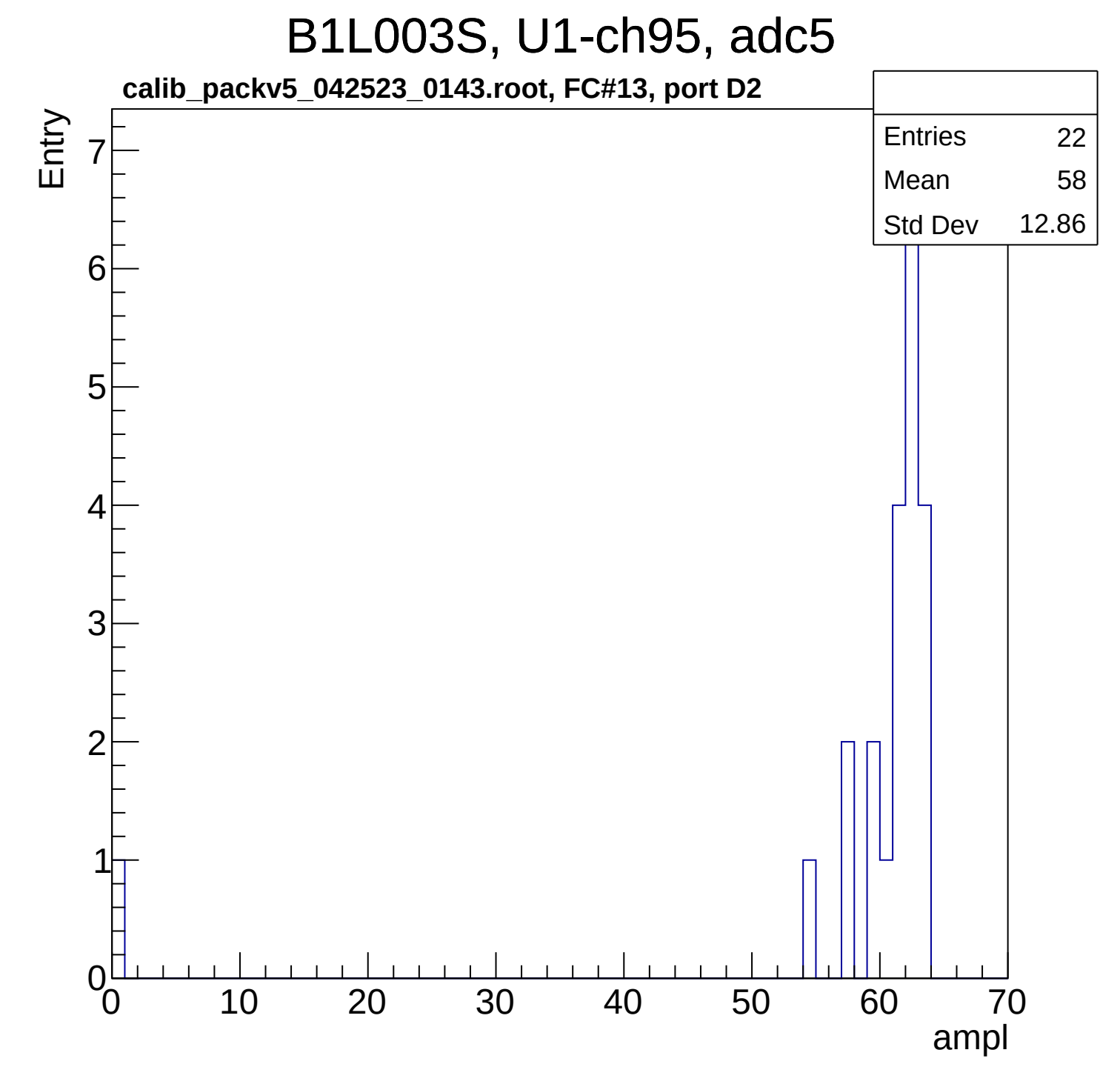
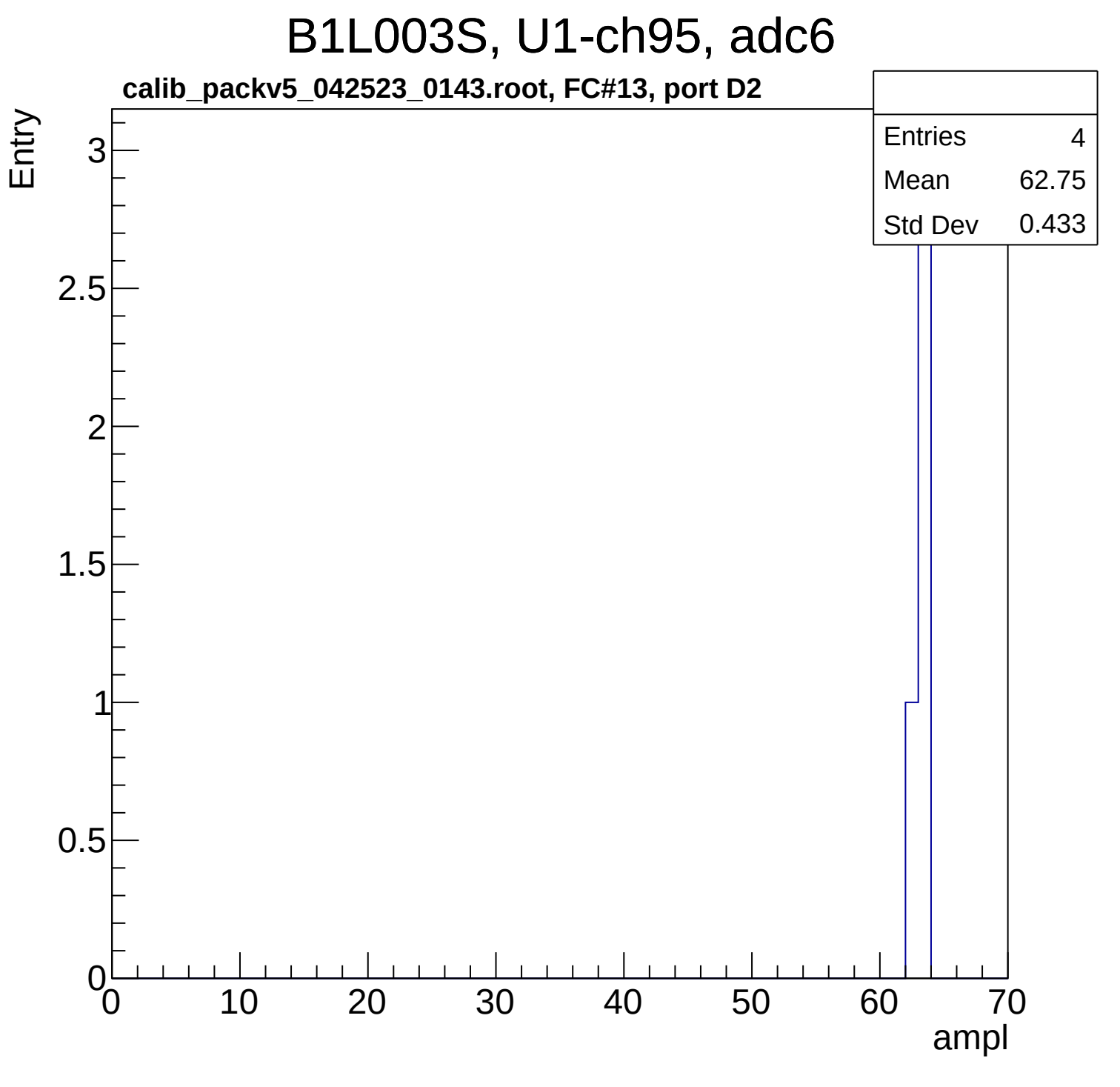
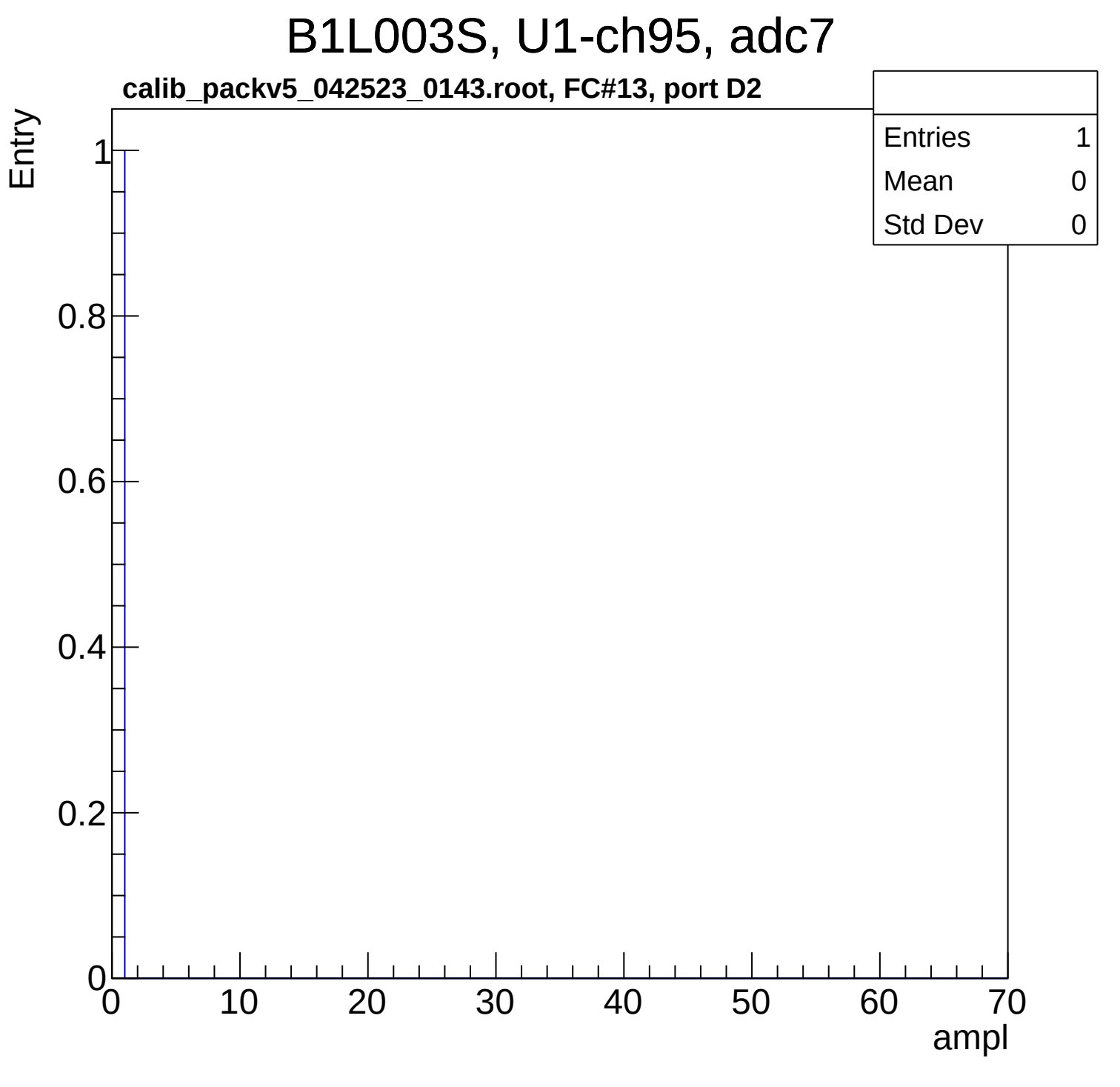
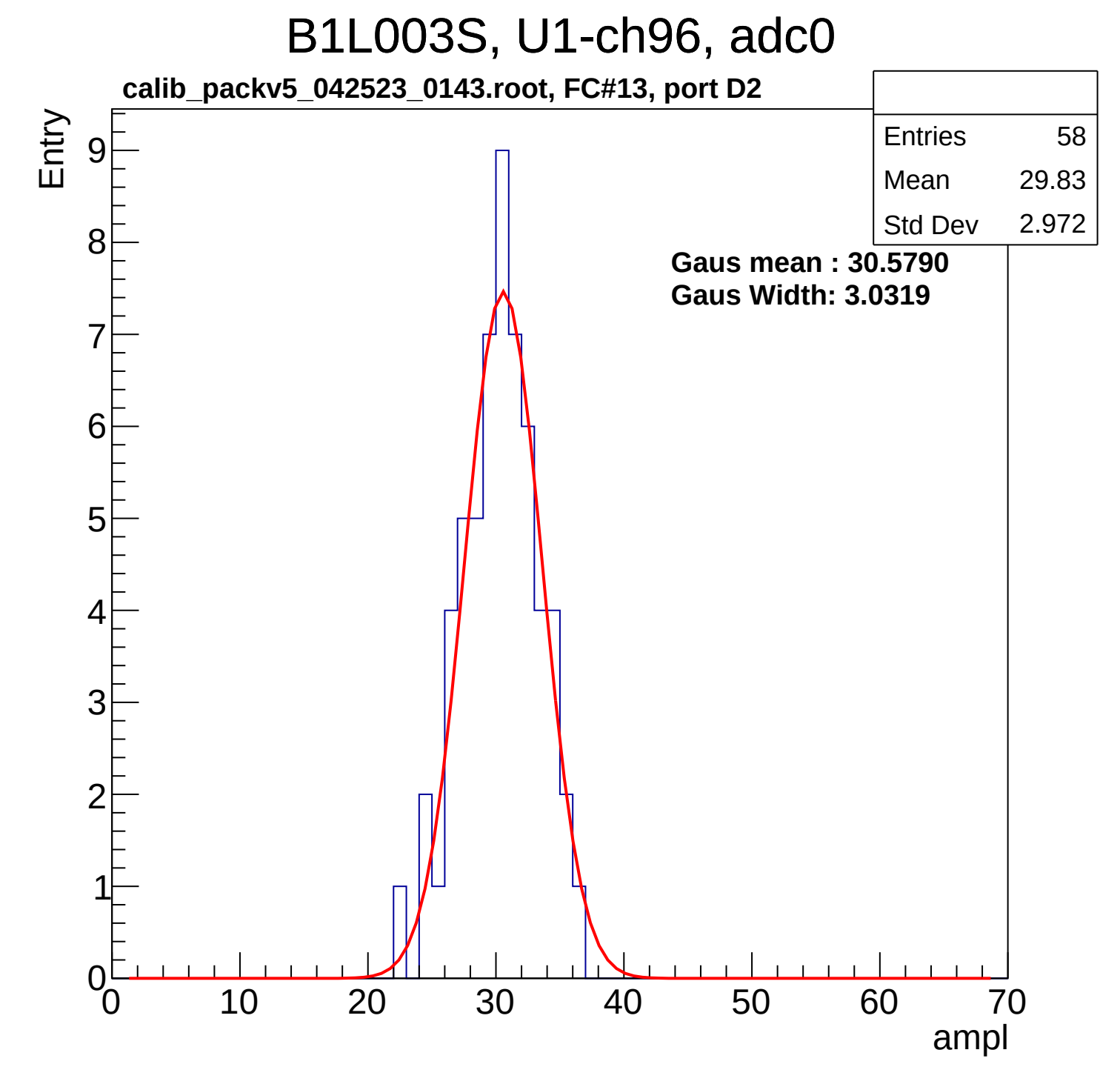
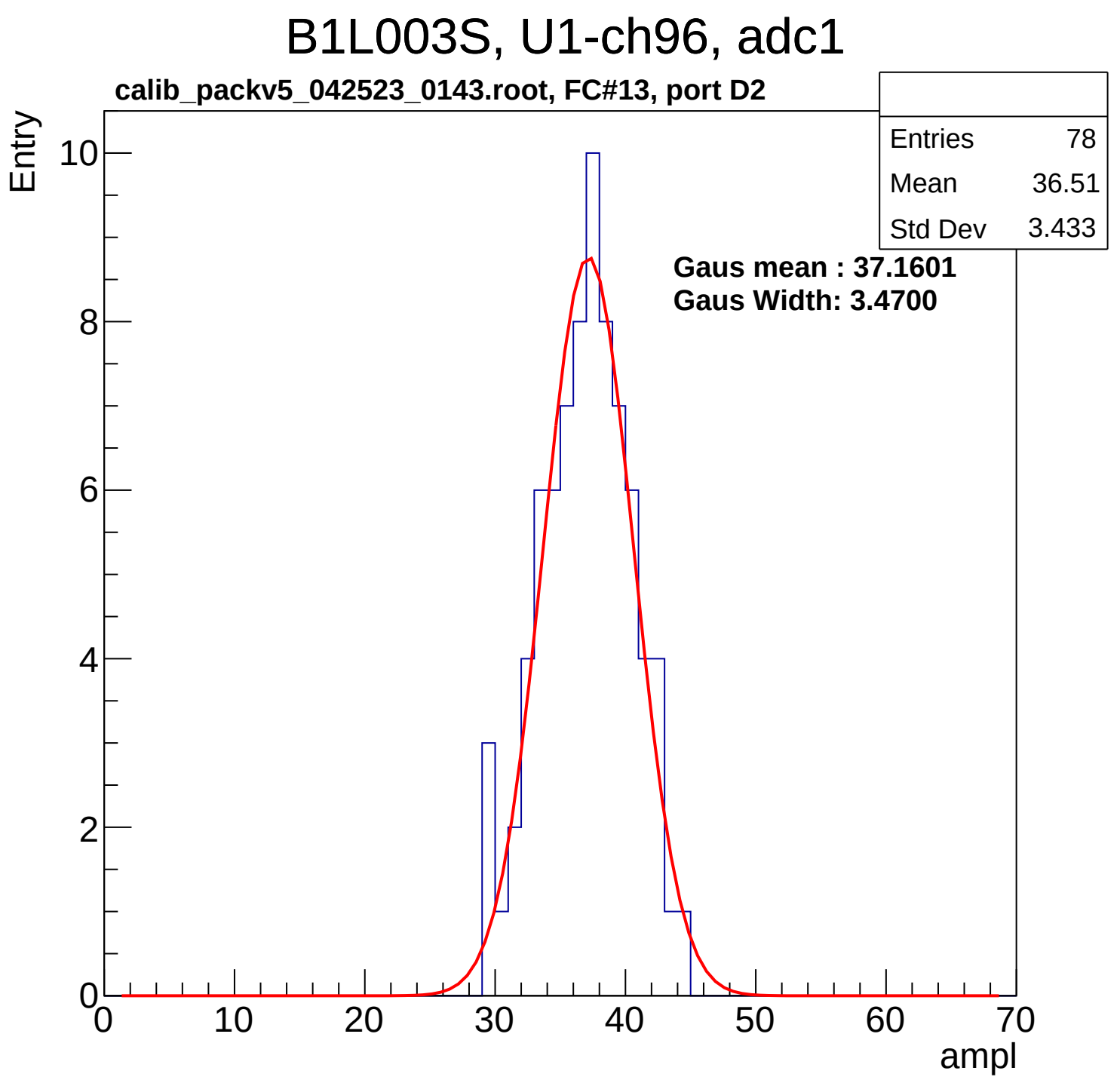
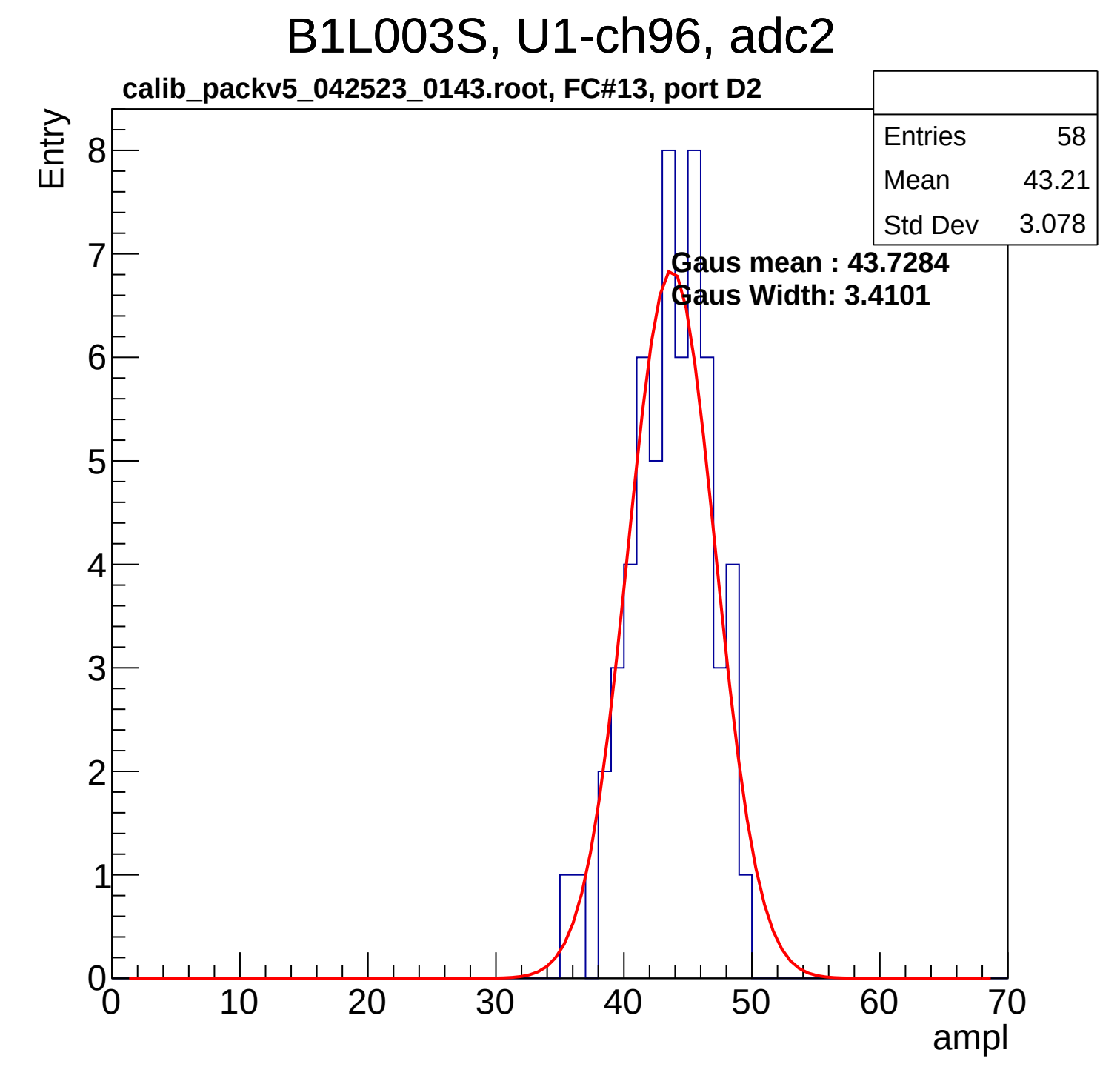

B1L003S, U1-ch95, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58
Std Dev 12.86
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

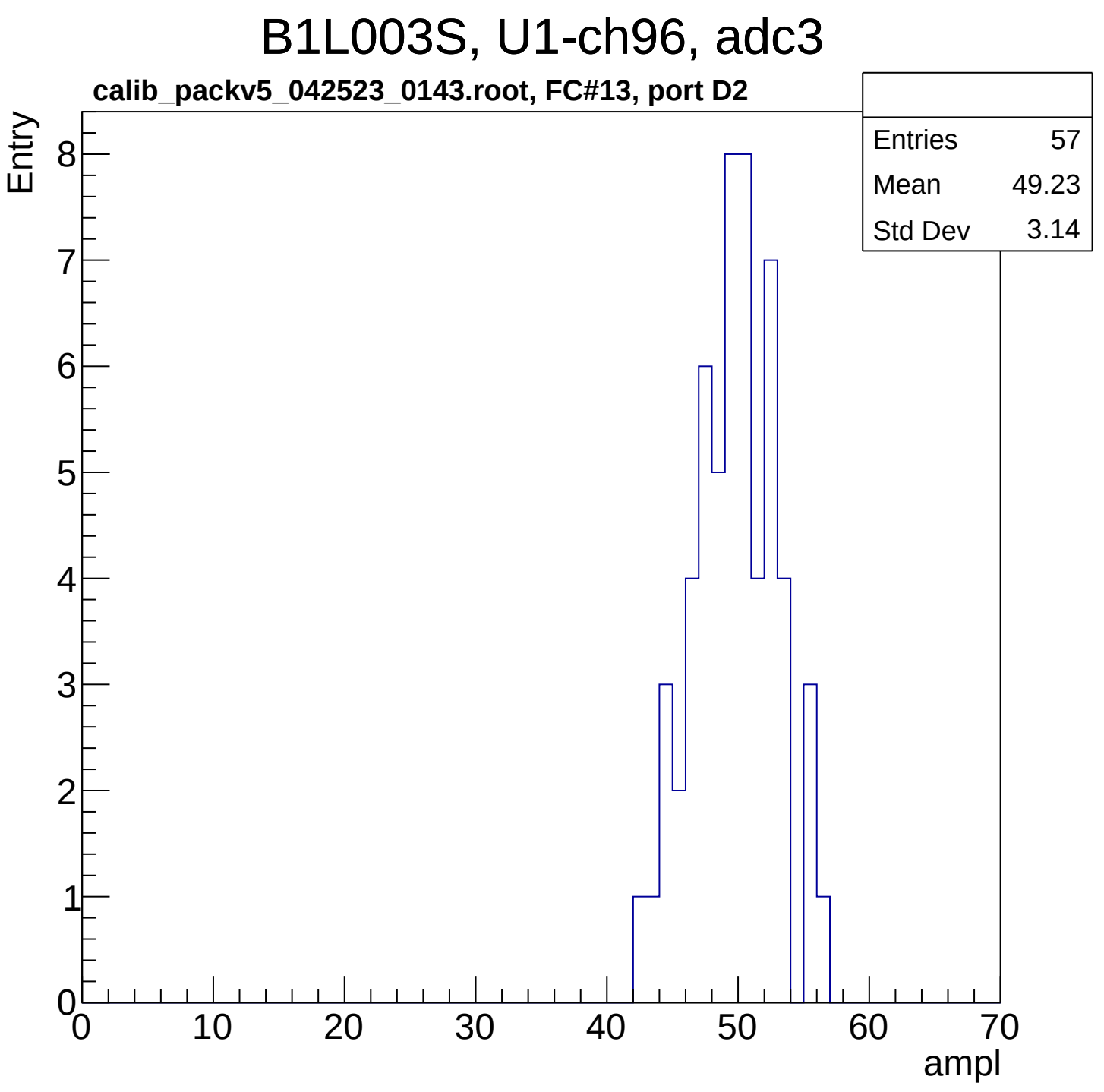

B1L003S, U1-ch95, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

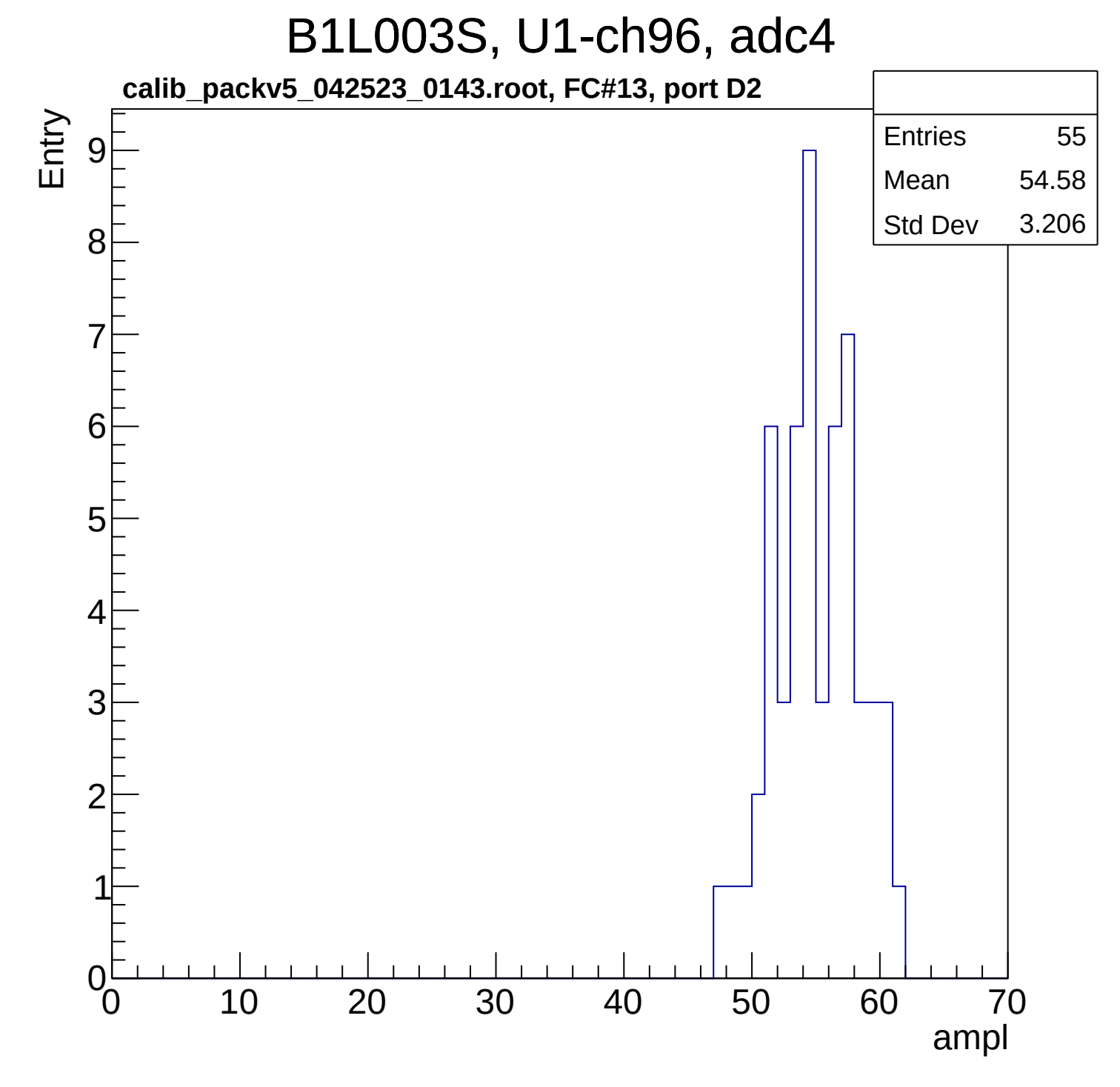

B1L003S, U1-ch95, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch96, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 29.83
Std Dev 2.972
Gaus mean : 30.5790
Gaus Width: 3.0319
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch96, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 36.51
Std Dev 3.433
Gaus mean : 37.1601
Gaus Width: 3.4700
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch96, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 43.21
Std Dev 3.078
Gaus mean : 43.7284
Gaus Width: 3.4101
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch96, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.23
Std Dev 3.14
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch96, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 55
Mean 54.58
Std Dev 3.206
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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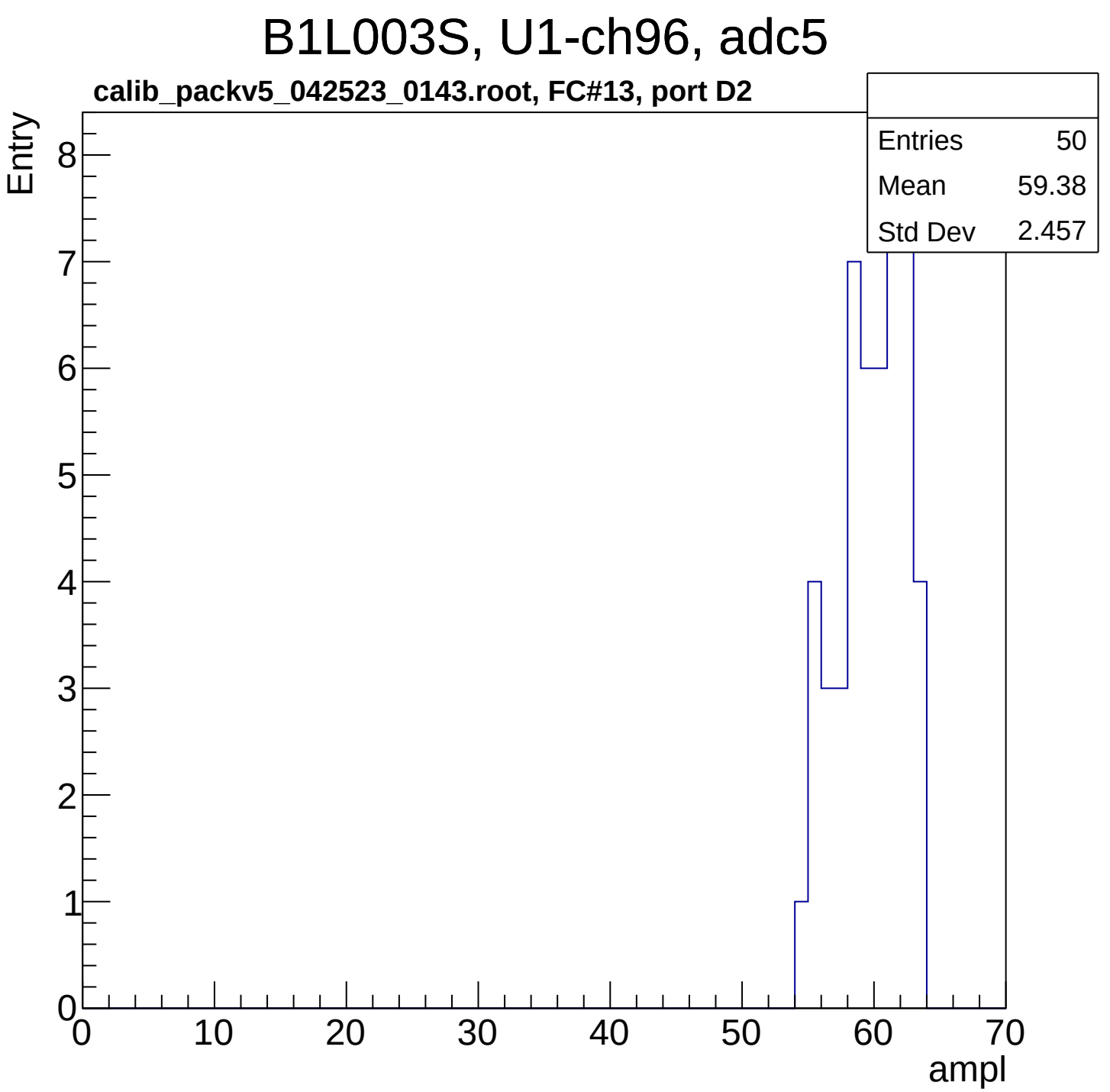
40
50
60
70


B1L003S, U1-ch96, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 59.38
Std Dev 2.457
Entry
ampl
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1
2
3
4
5
6
7
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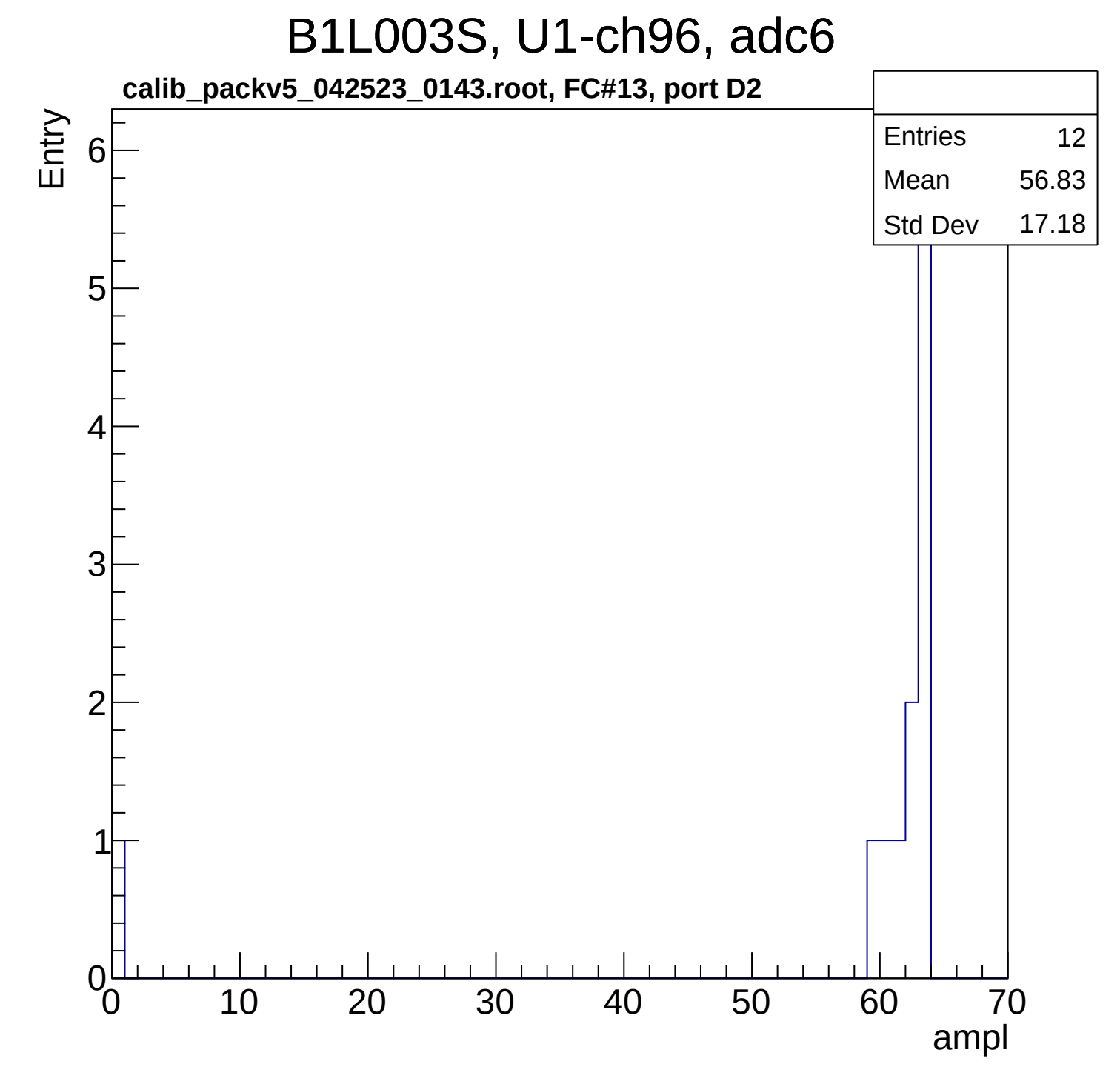
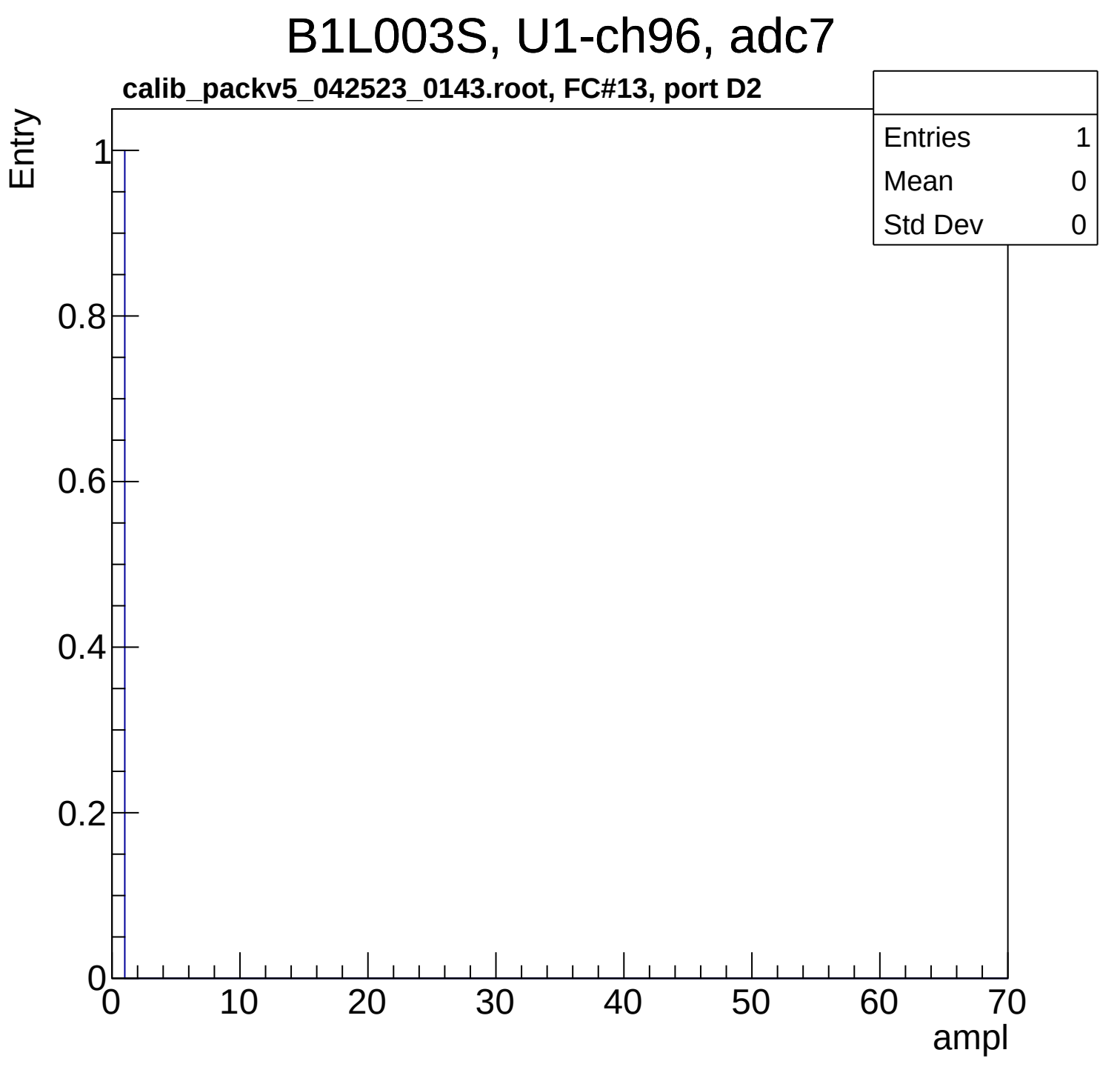
0
10
20
30
40
50
60
70


B1L003S, U1-ch96, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 12
Mean 56.83
Std Dev 17.18
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch96, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
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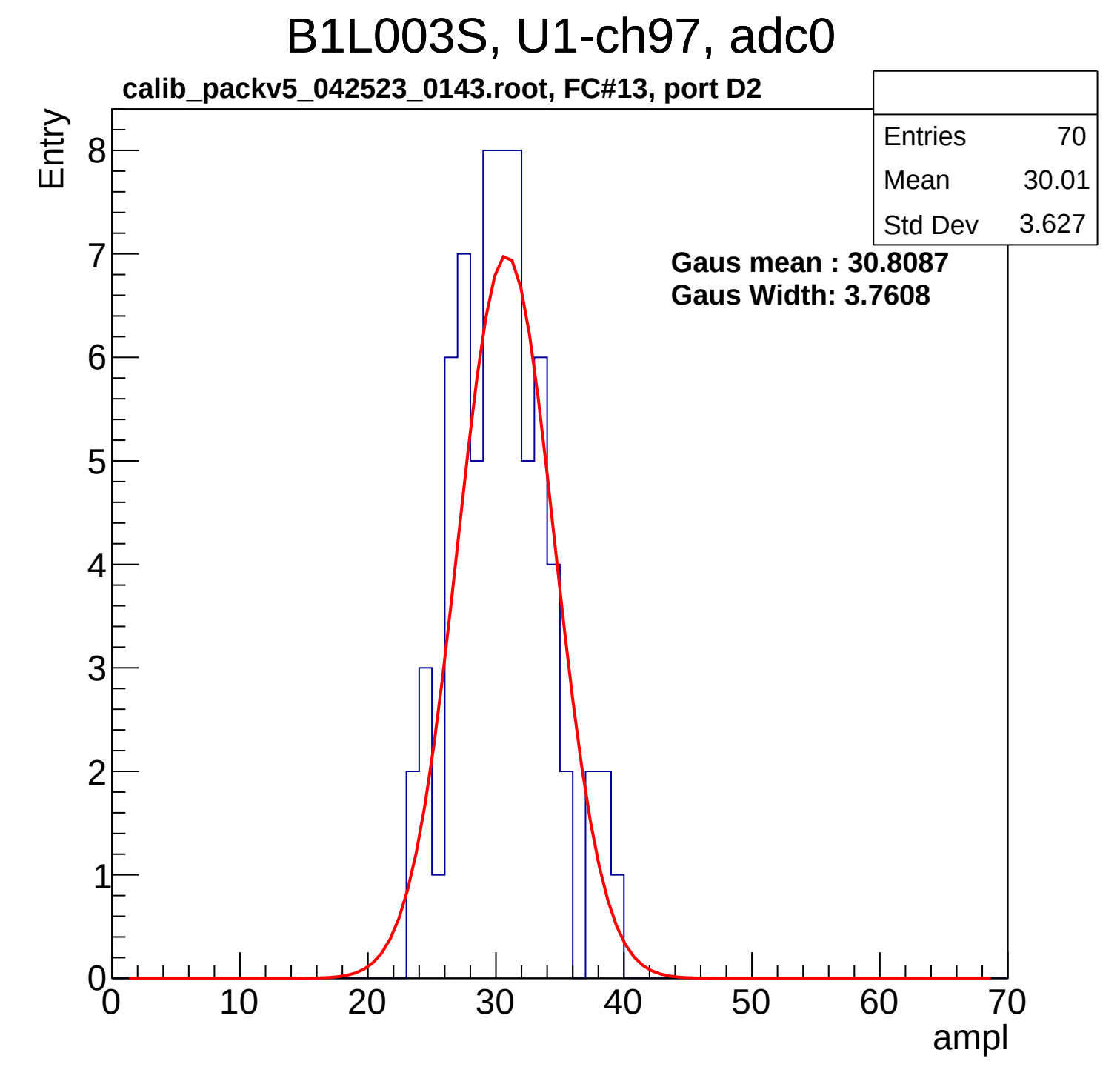
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch97, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 30.01
Std Dev 3.627
Gaus mean : 30.8087
Gaus Width: 3.7608
Entry
ampl
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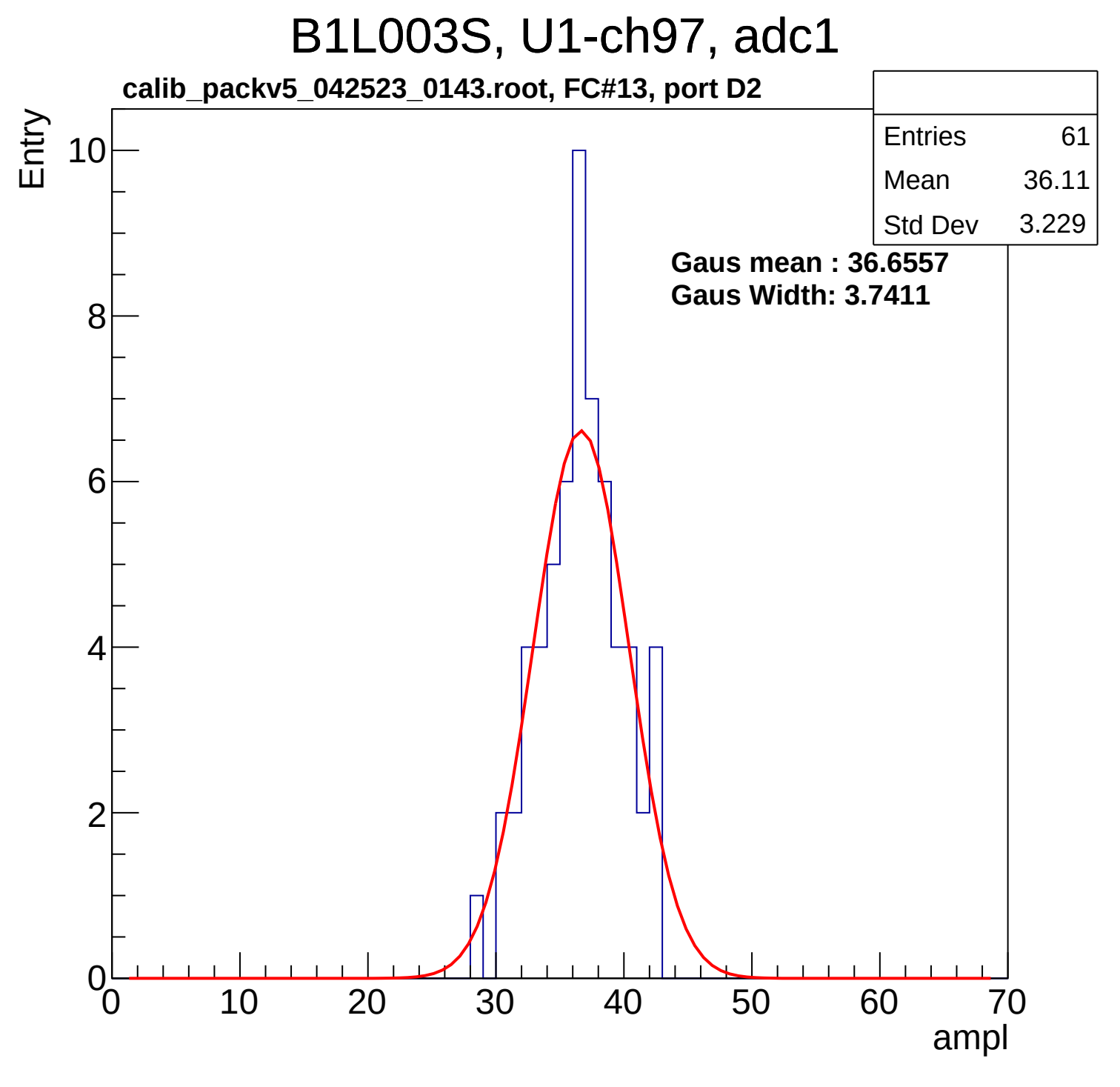
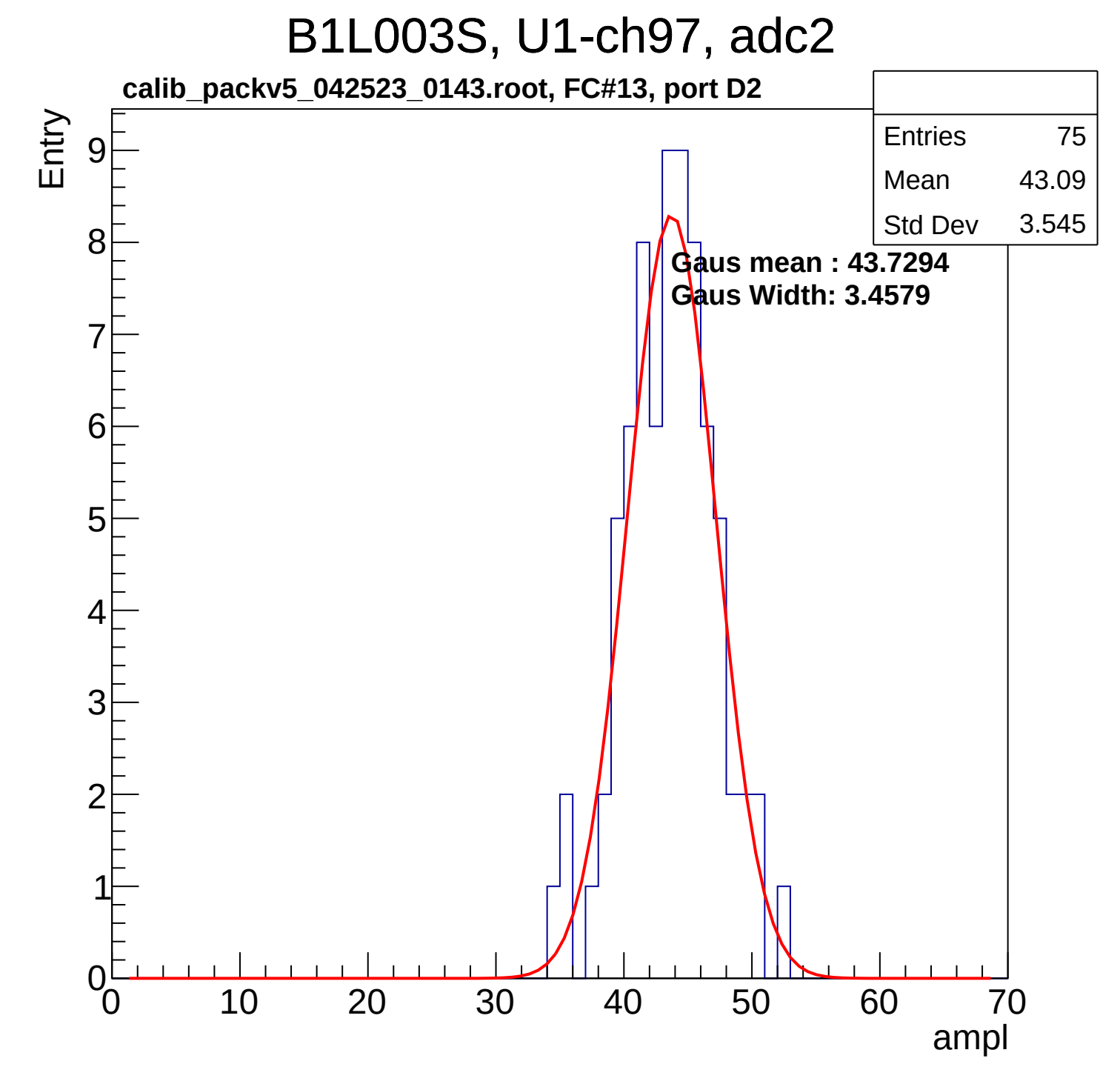
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch97, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 36.11
Std Dev 3.229
Gaus mean : 36.6557
Gaus Width: 3.7411
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch97, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 43.09
Std Dev 3.545
Gaus mean : 43.7294
Gaus Width: 3.4579
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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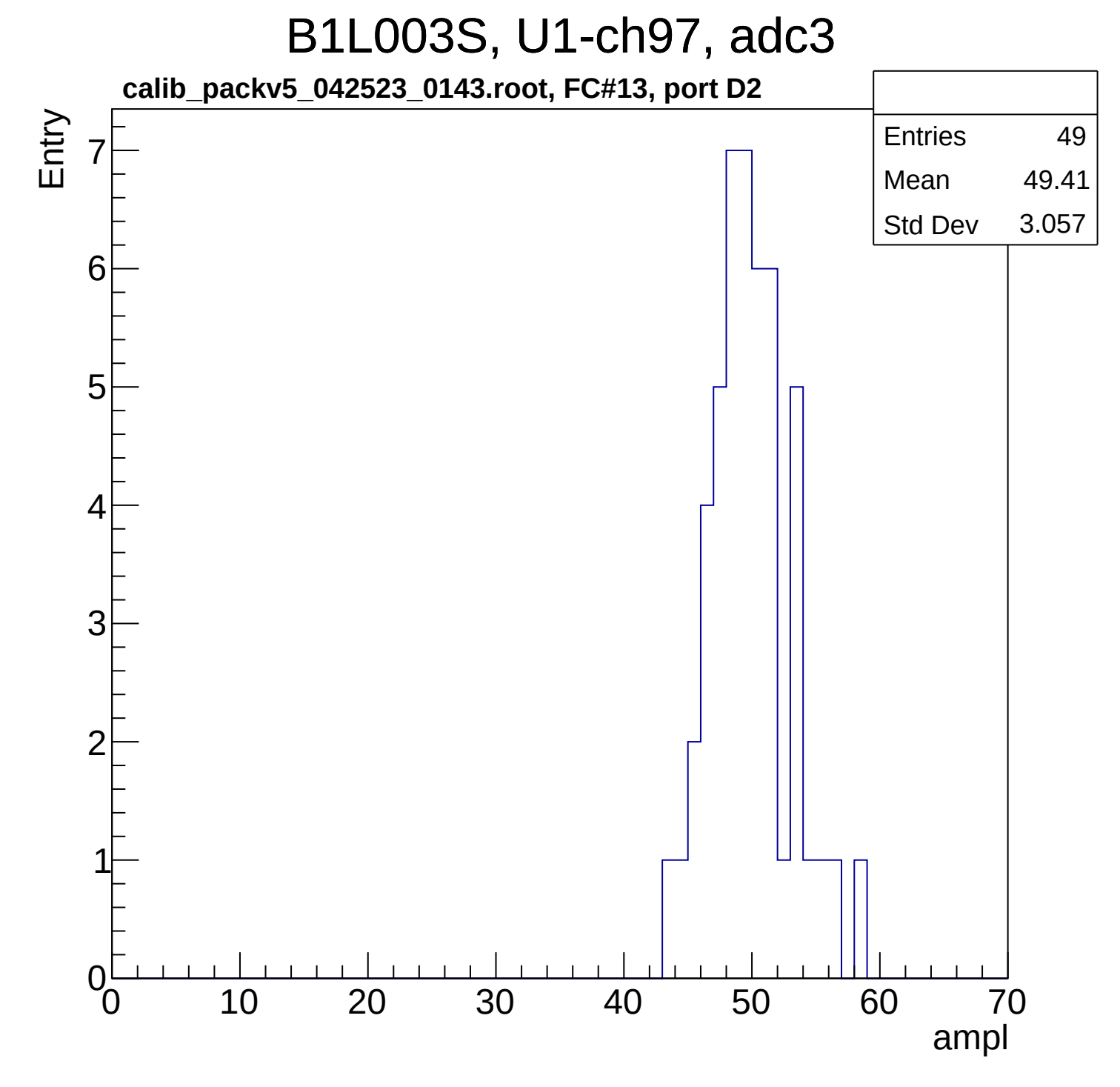
B1L003S, U1-ch97, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 49.41
Std Dev 3.057
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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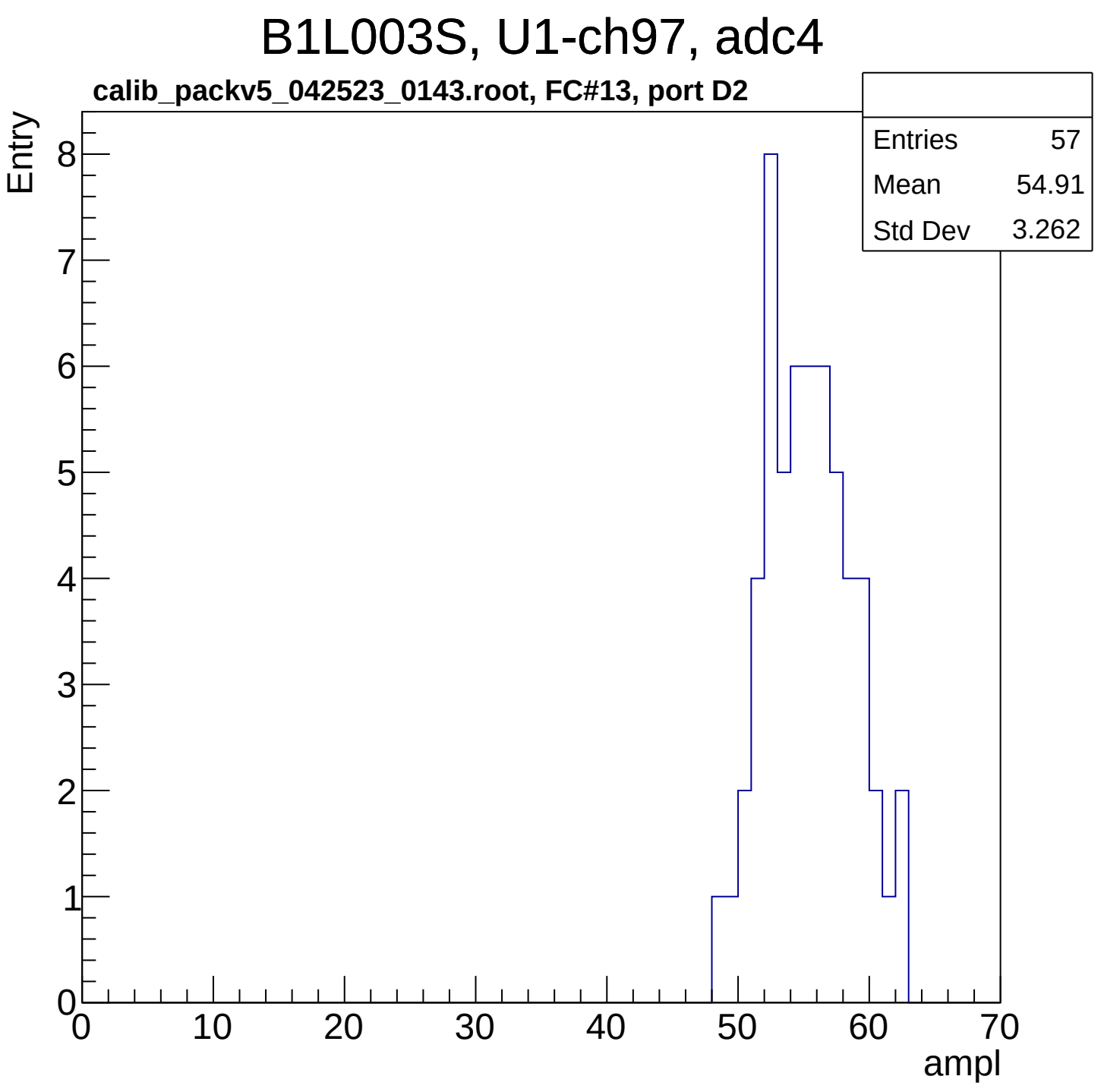
40
50
60
70


B1L003S, U1-ch97, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.91
Std Dev 3.262
Entry
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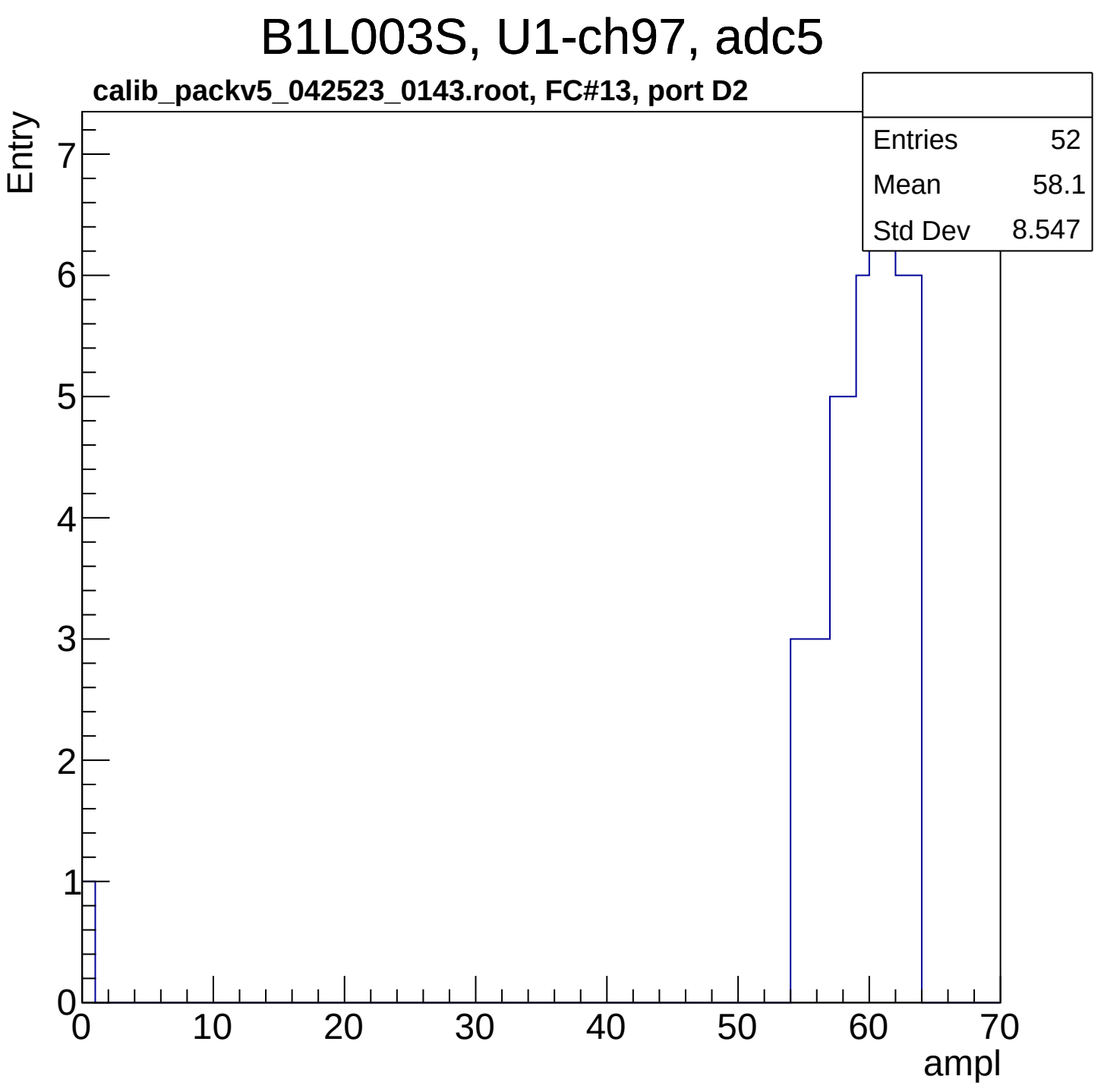
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch97, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.1
Std Dev 8.547
Entry
ampl
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1
2
3
4
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0
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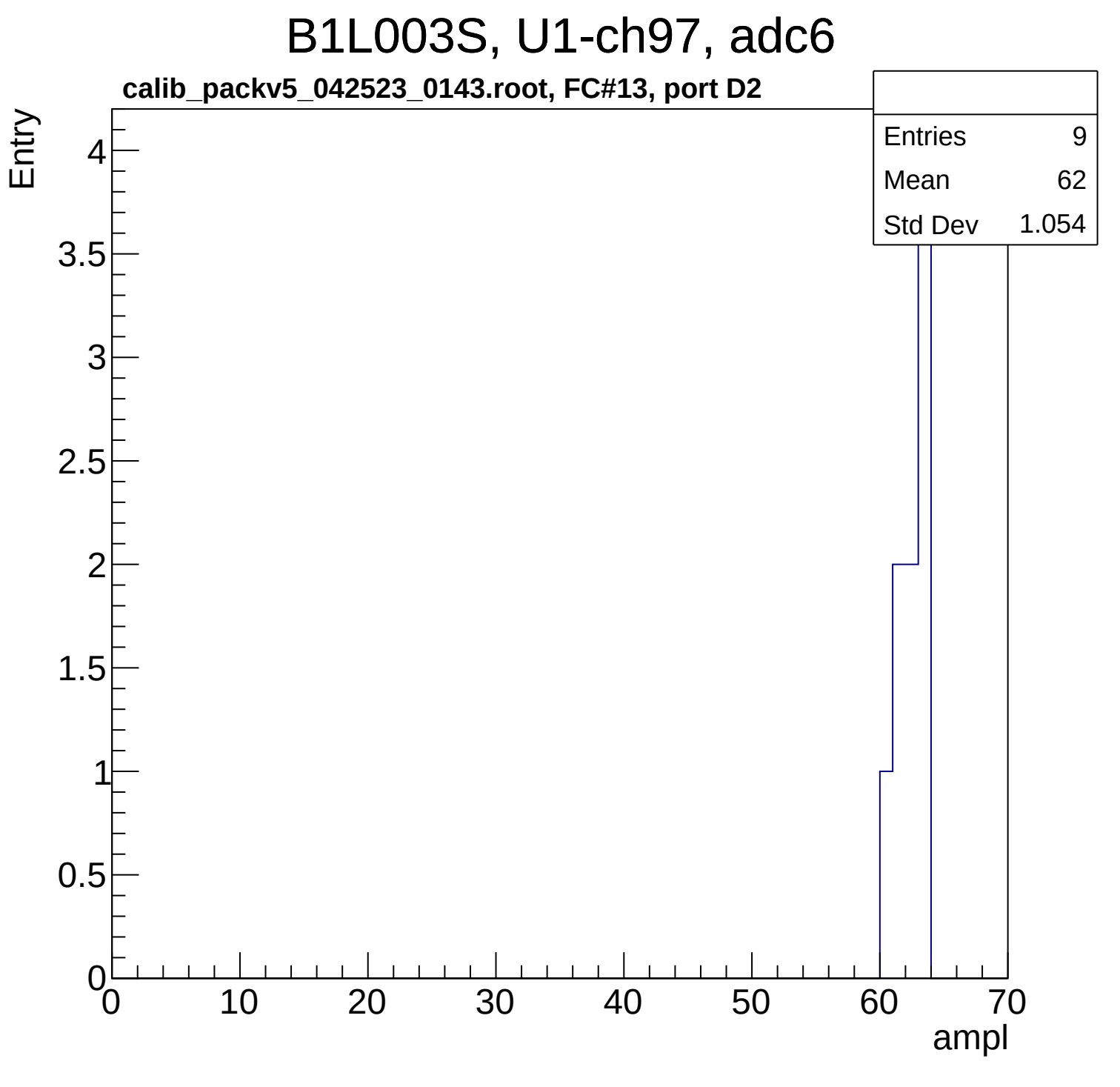
B1L003S, U1-ch97, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62
Std Dev 1.054
Entry
ampl
0
0.5
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1.5
2
2.5
3
3.5
4
0
10
20
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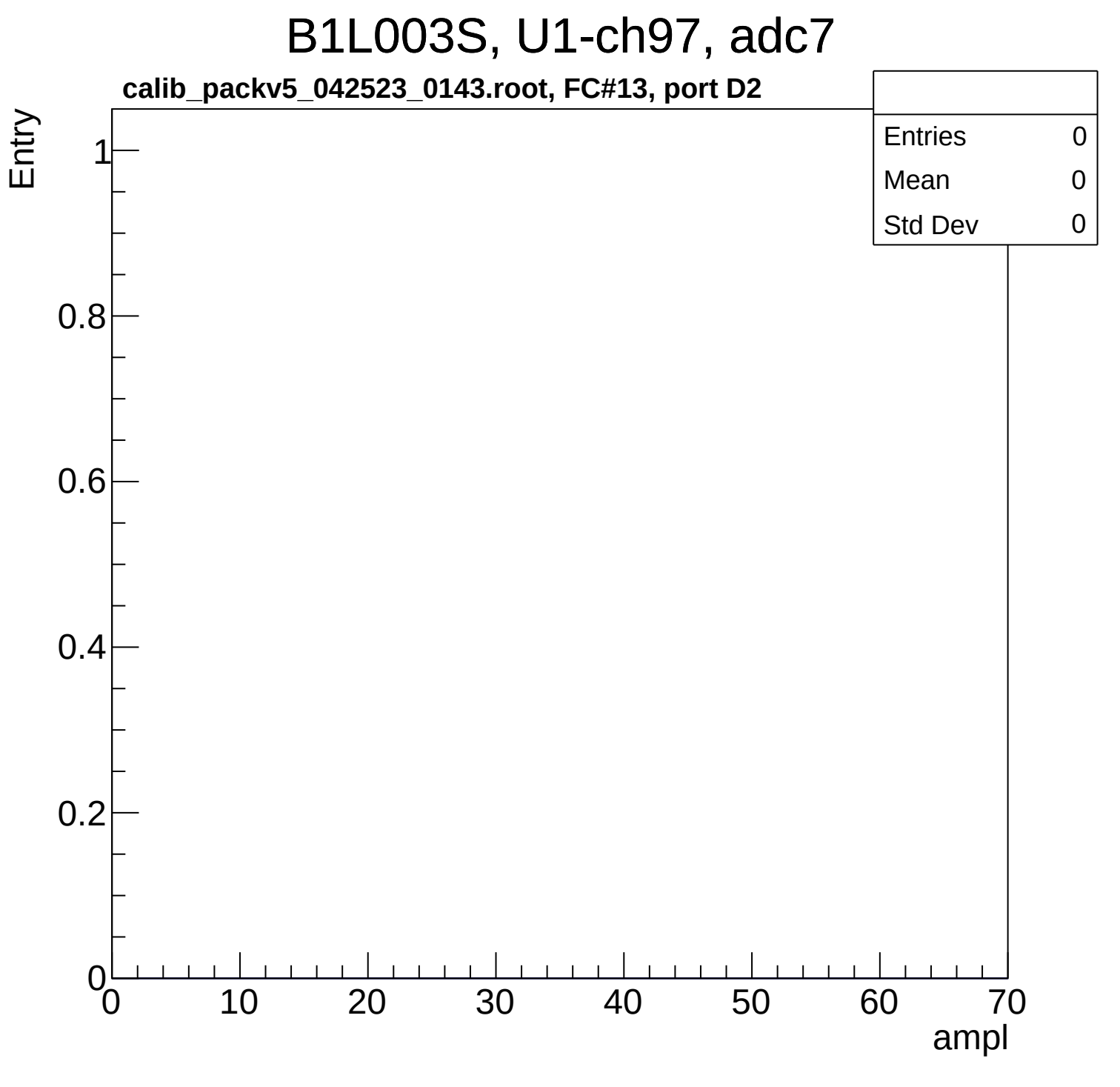
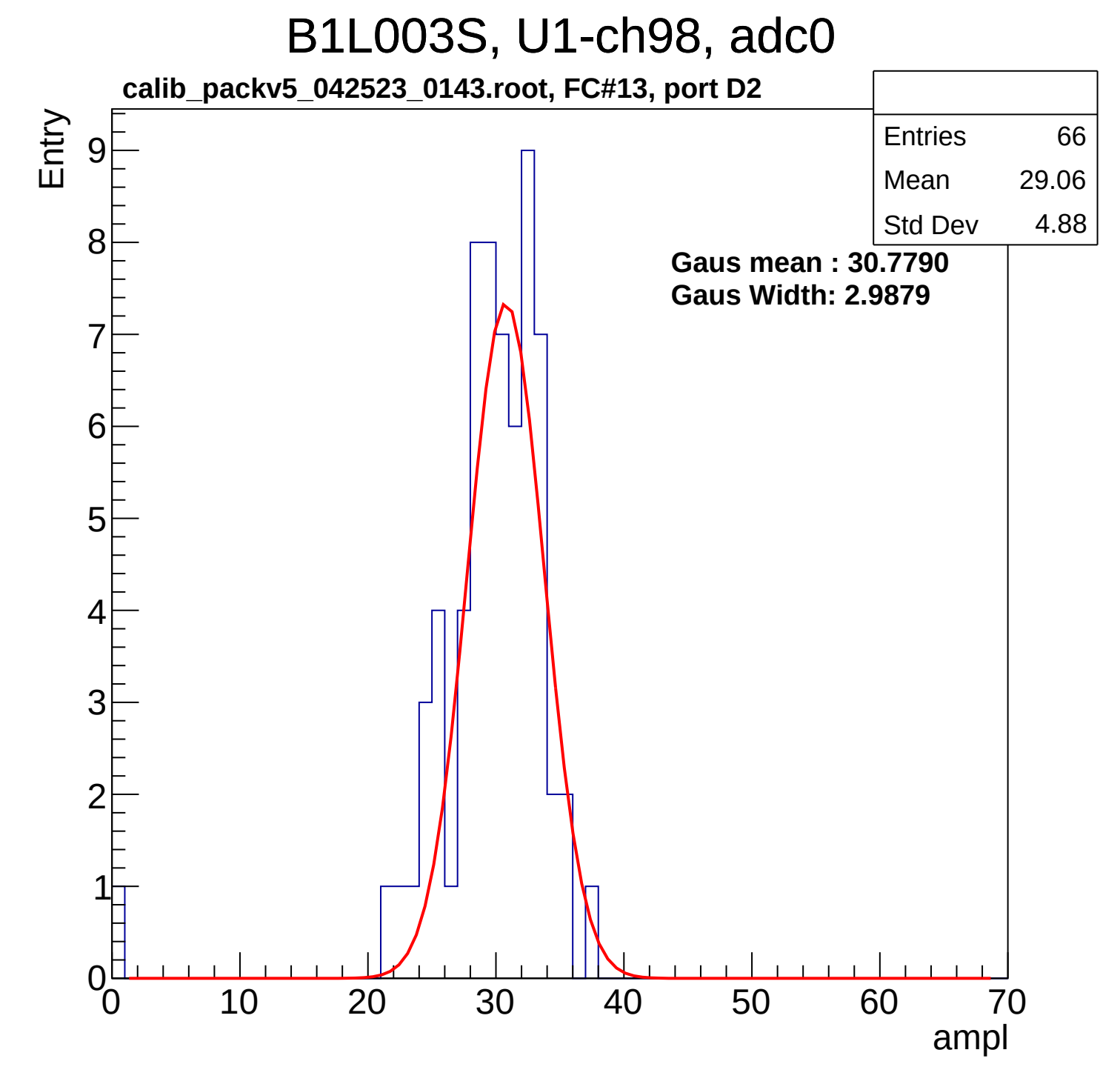
40
50
60
70


B1L003S, U1-ch97, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch98, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 29.06
Std Dev 4.88
Gaus mean : 30.7790
Gaus Width: 2.9879
Entry
ampl
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2
3
4
5
6
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0
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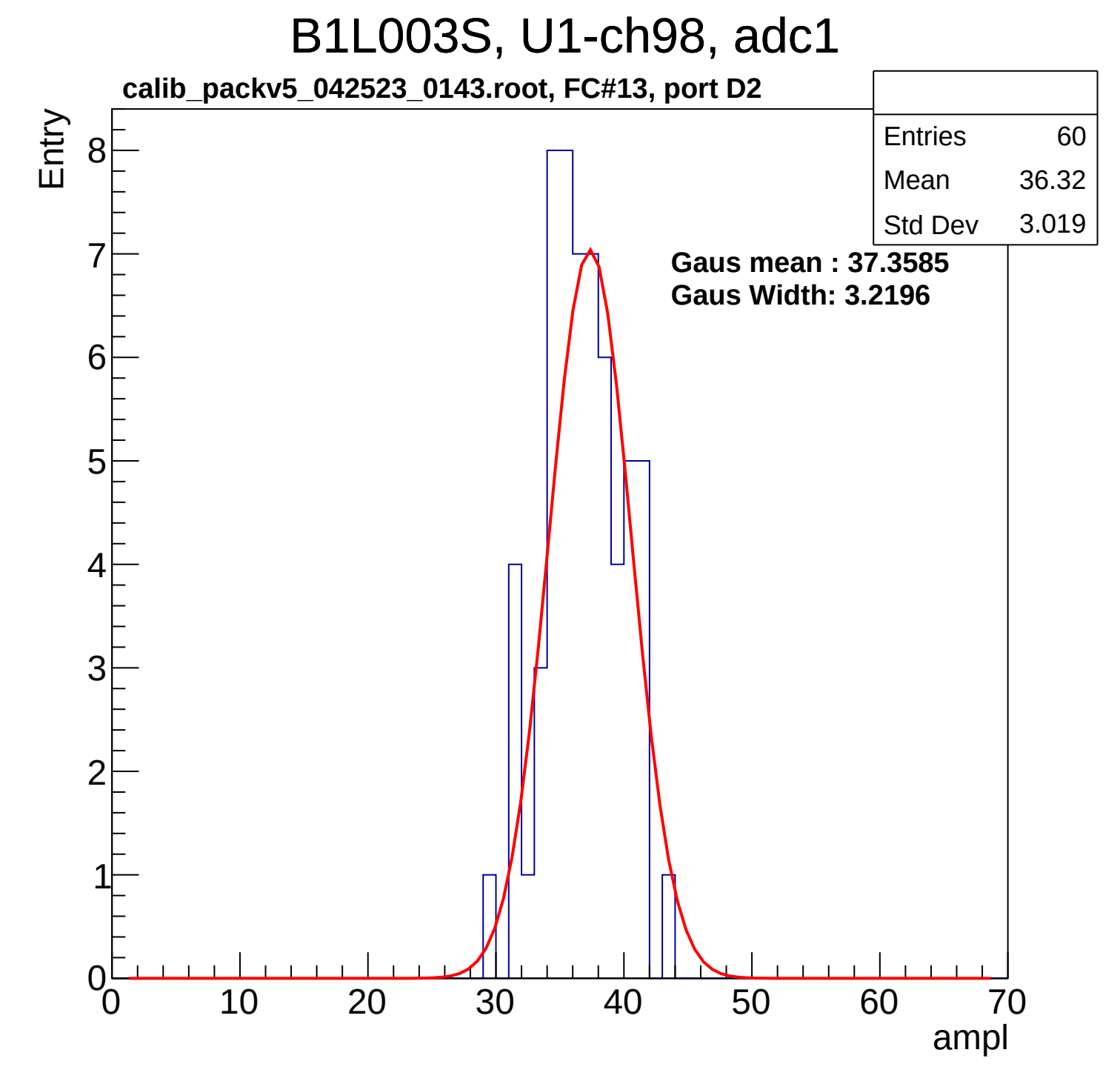
B1L003S, U1-ch98, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.32
Std Dev 3.019
Gaus mean : 37.3585
Gaus Width: 3.2196
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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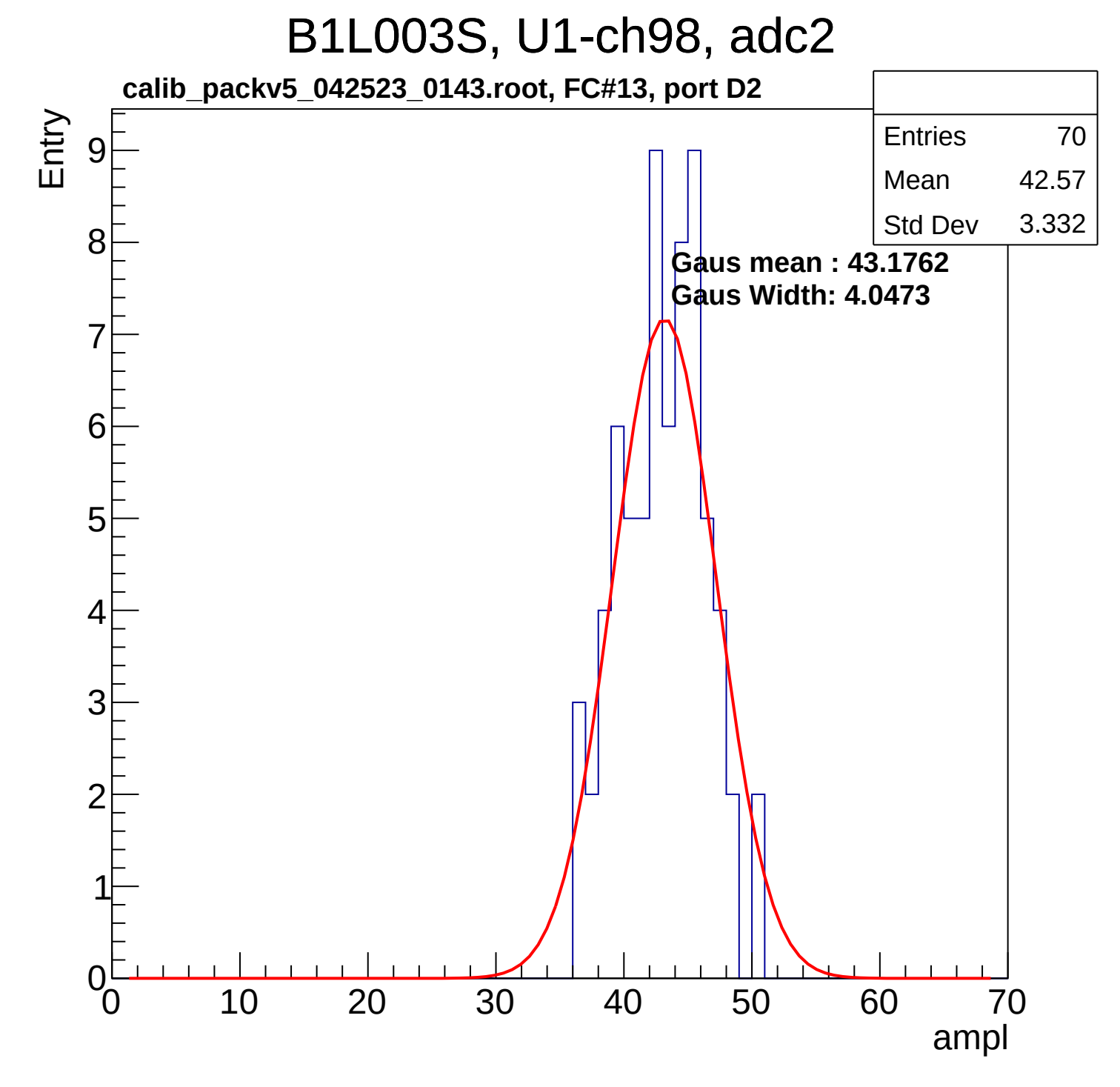
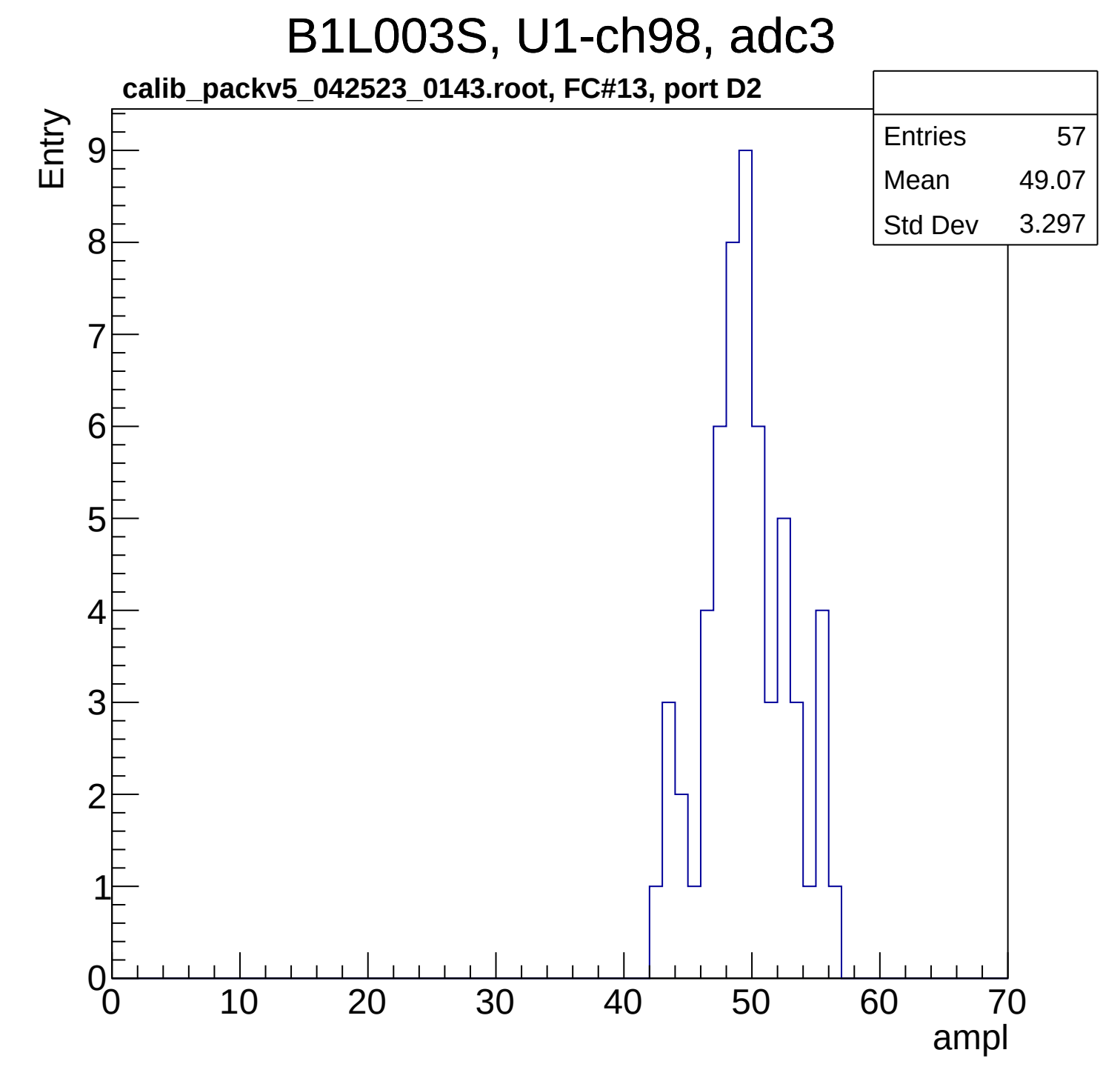

B1L003S, U1-ch98, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 42.57
Std Dev 3.332
Gaus mean : 43.1762
Gaus Width: 4.0473
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch98, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 49.07
Std Dev 3.297
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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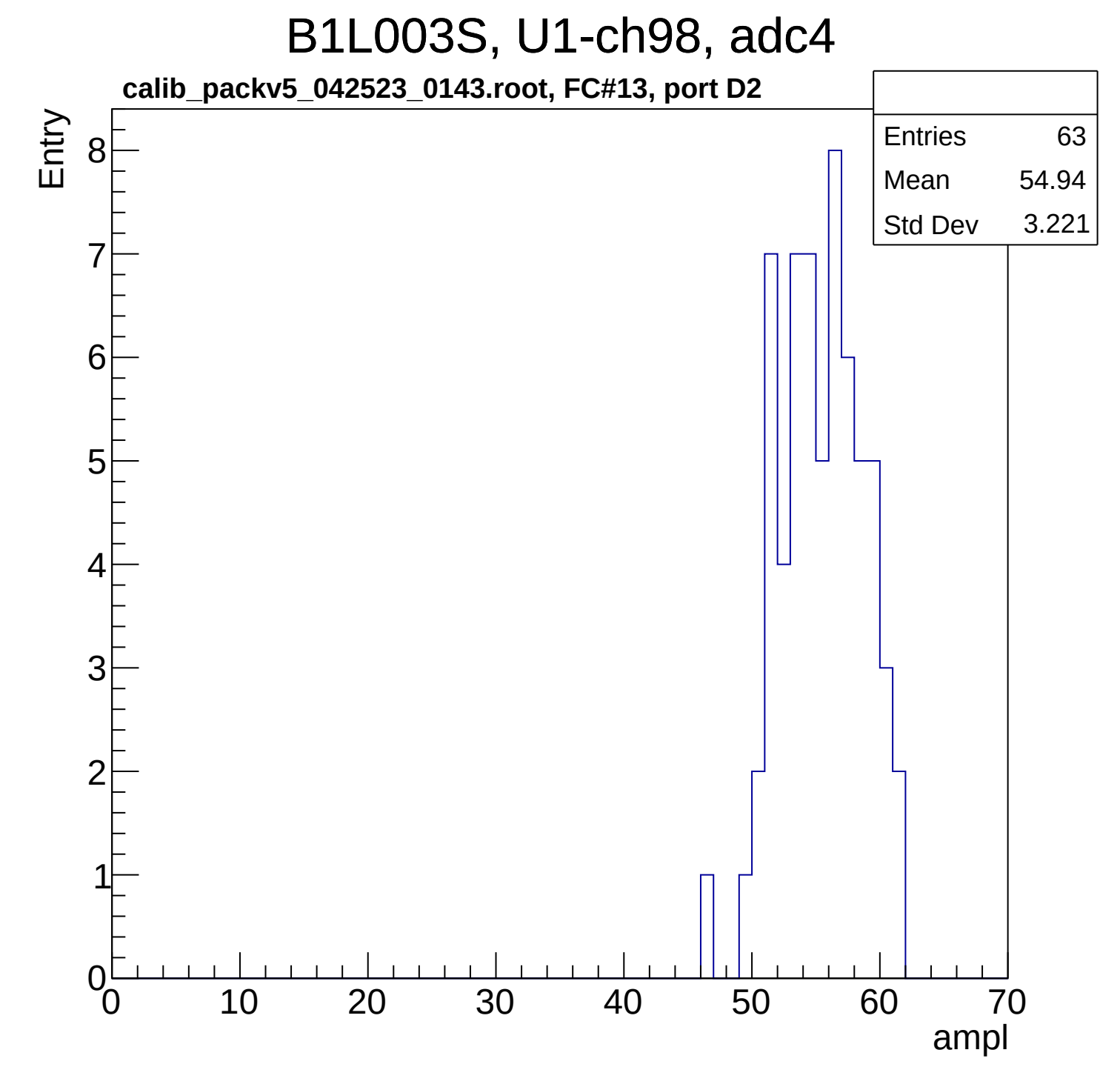
B1L003S, U1-ch98, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 54.94
Std Dev 3.221
Entry
ampl
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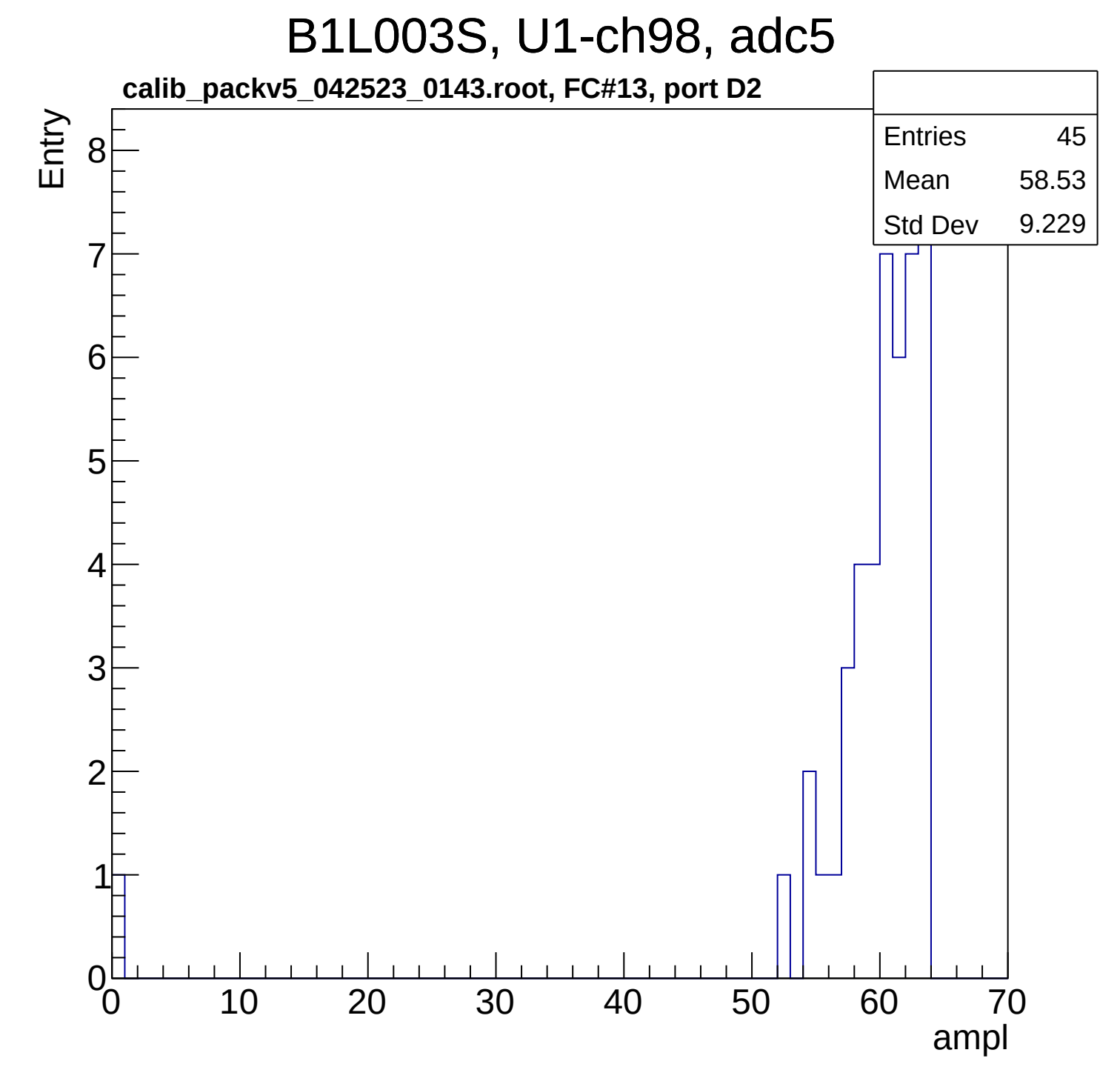
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch98, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 58.53
Std Dev 9.229
Entry
ampl
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2
3
4
5
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7
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0
10
20
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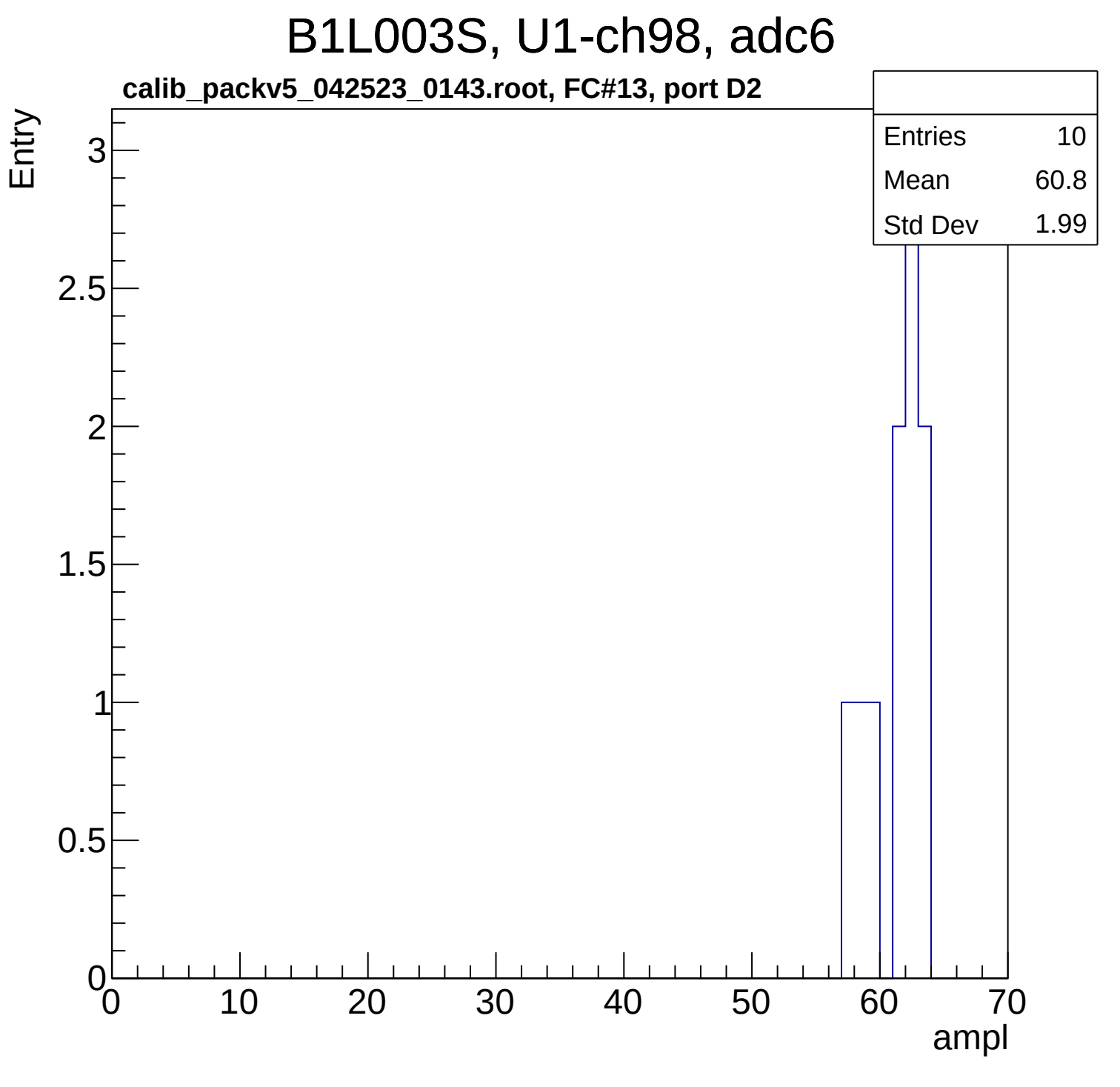
40
50
60
70


B1L003S, U1-ch98, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 60.8
Std Dev 1.99
Entry
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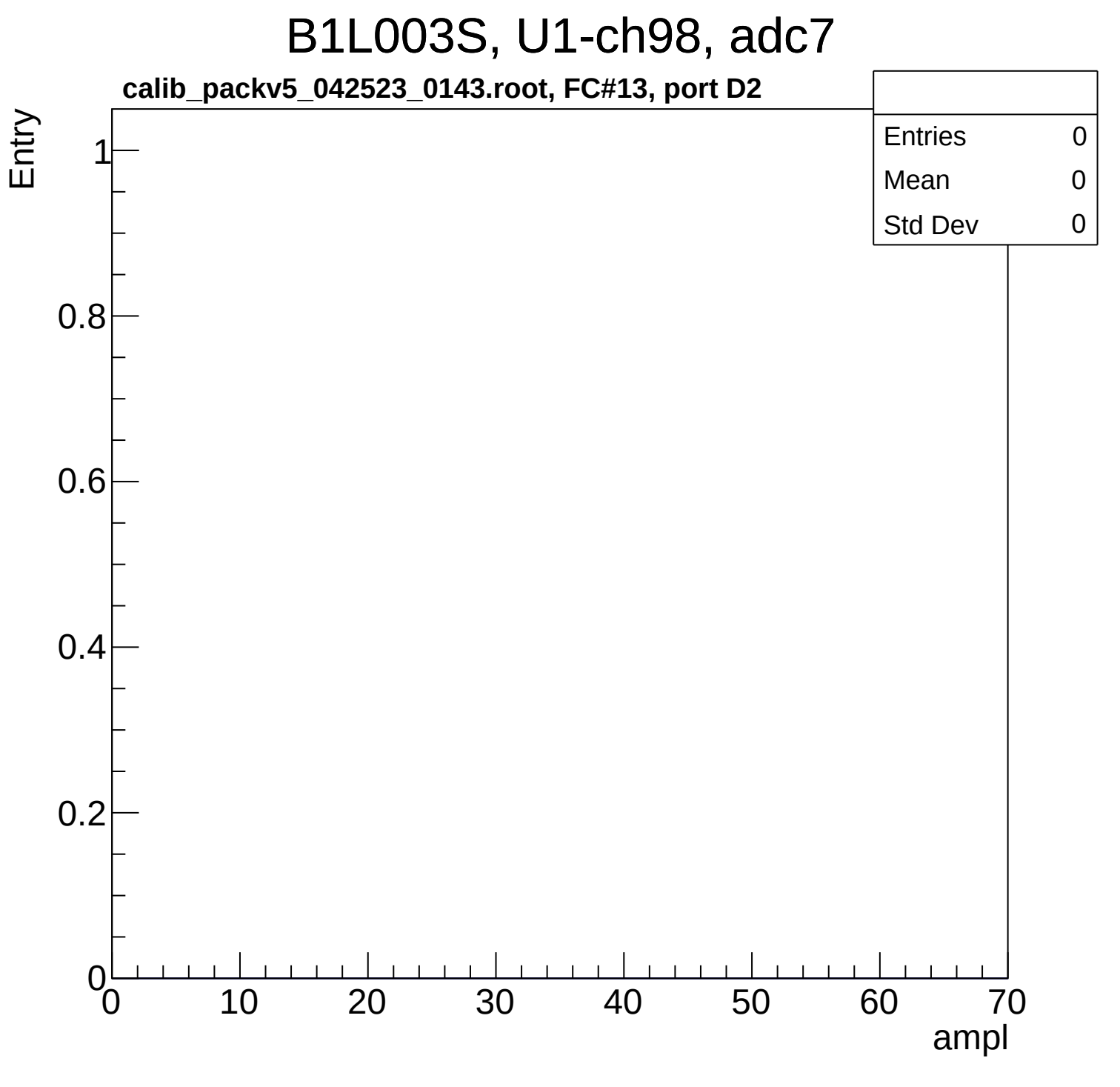
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch98, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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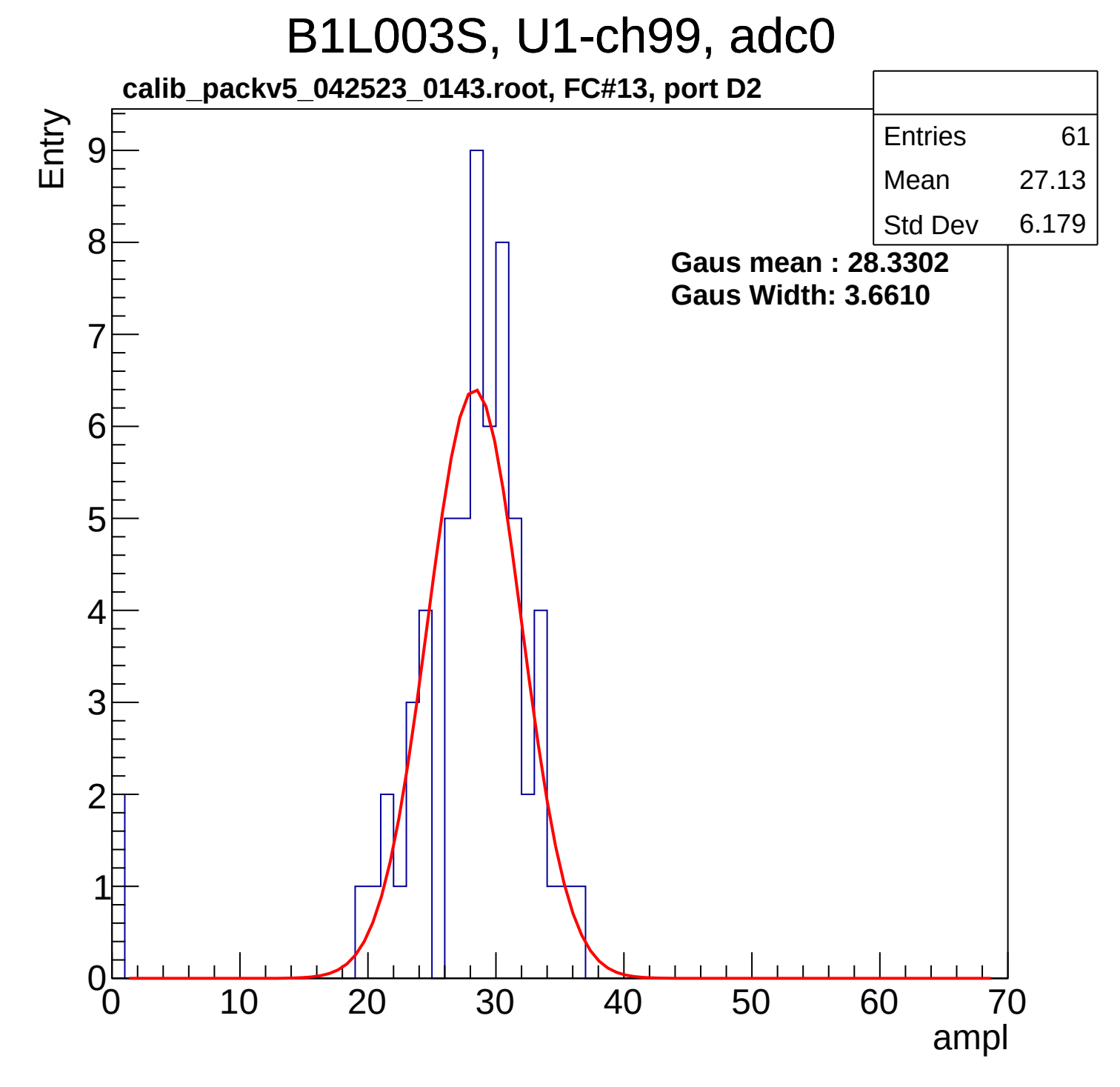
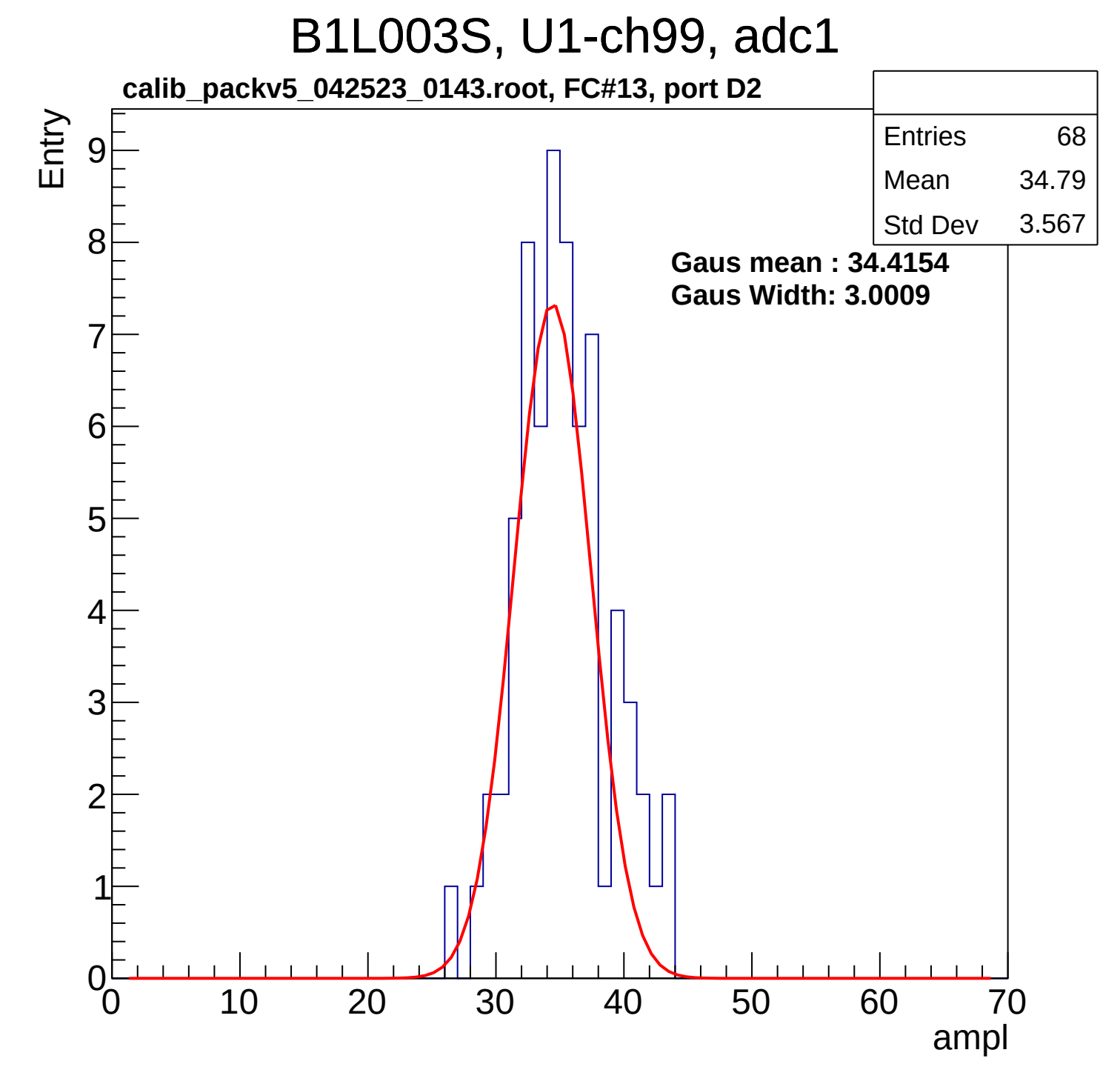
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch99, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.13
Std Dev 6.179
Gaus mean : 28.3302
Gaus Width: 3.6610
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch99, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 34.79
Std Dev 3.567
Gaus mean : 34.4154
Gaus Width: 3.0009
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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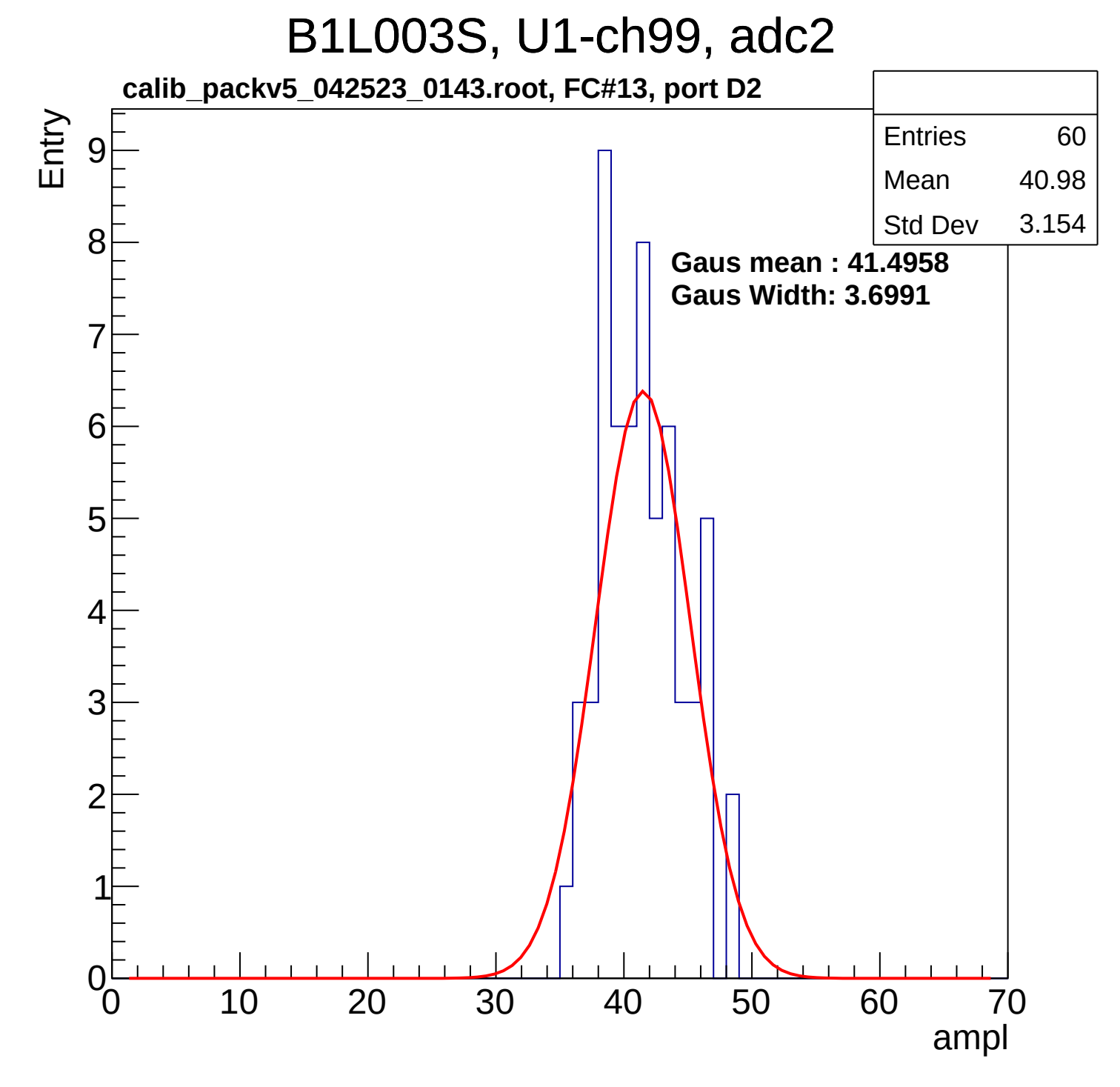
B1L003S, U1-ch99, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 40.98
Std Dev 3.154
Gaus mean : 41.4958
Gaus Width: 3.6991
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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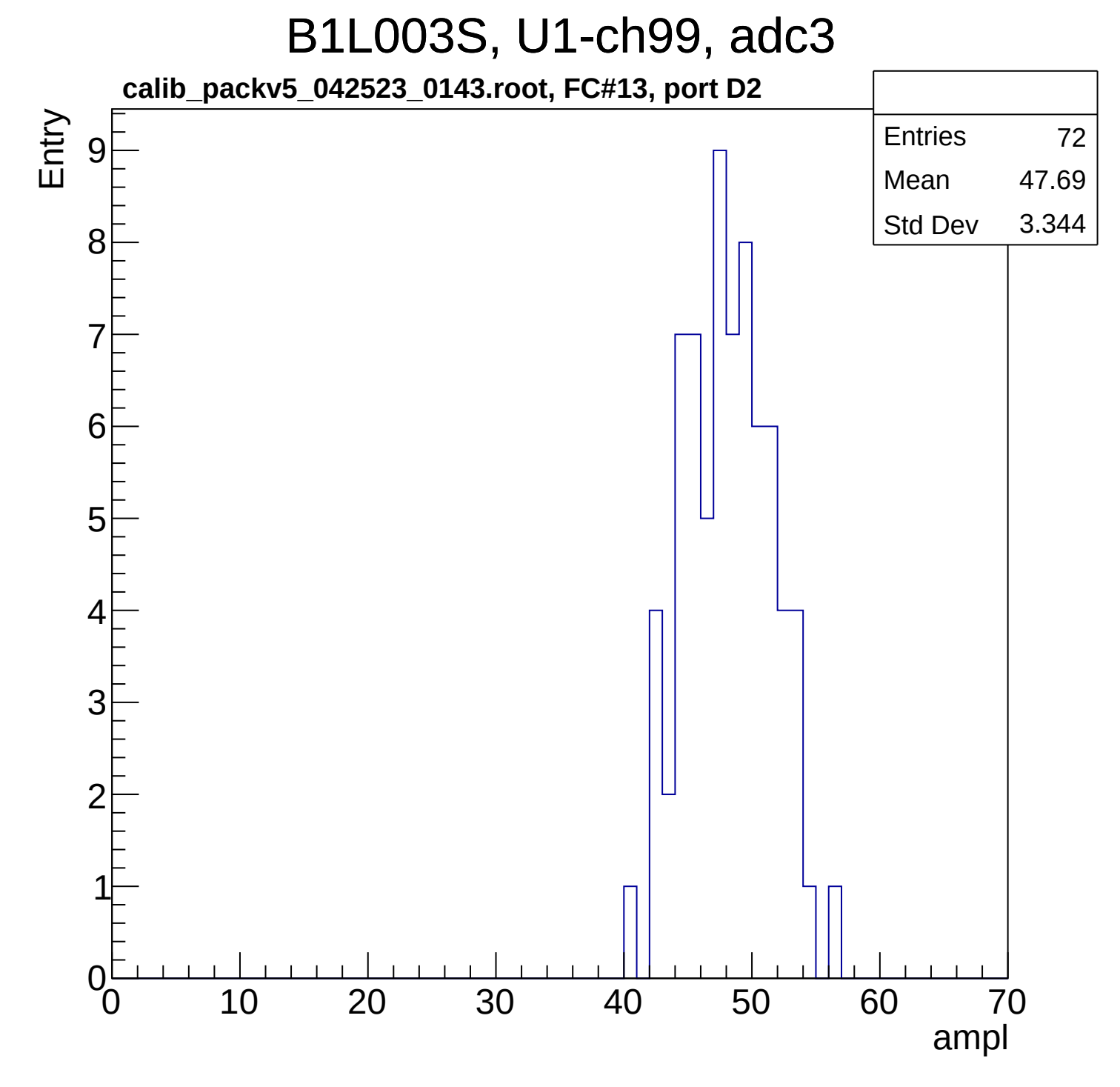
B1L003S, U1-ch99, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 47.69
Std Dev 3.344
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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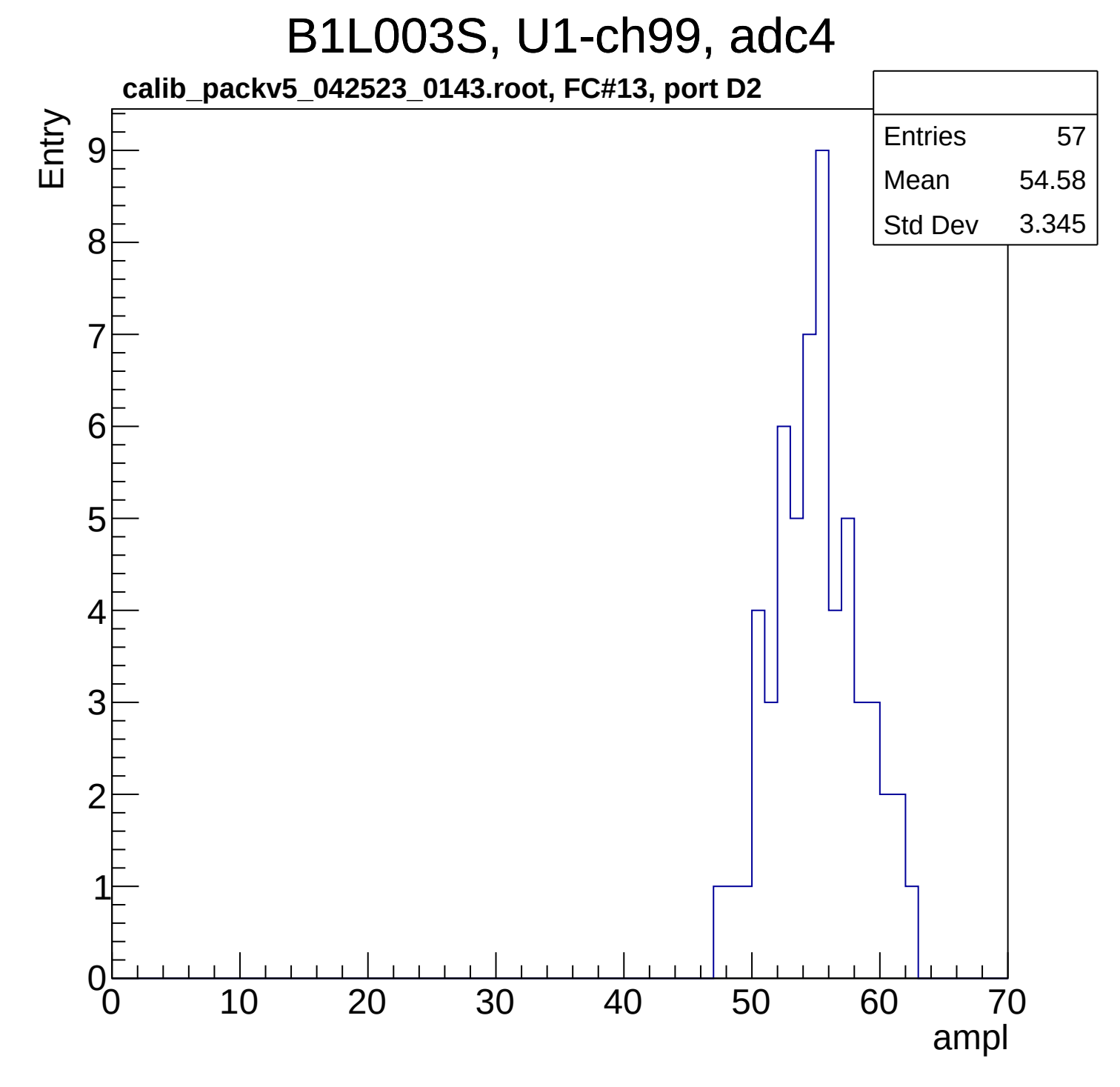
B1L003S, U1-ch99, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.58
Std Dev 3.345
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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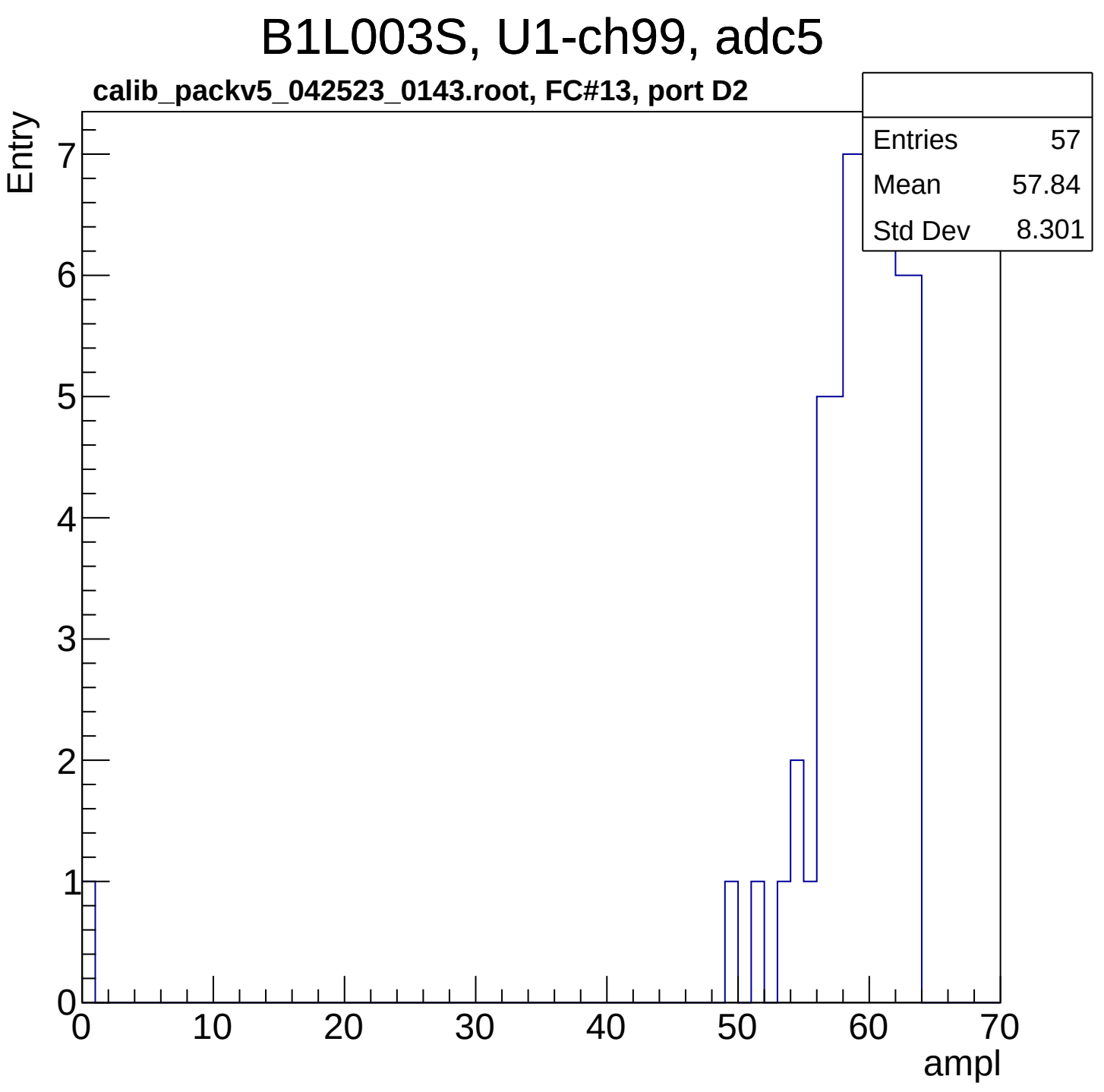
B1L003S, U1-ch99, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.84
Std Dev 8.301
Entry
ampl
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1
2
3
4
5
6
7
0
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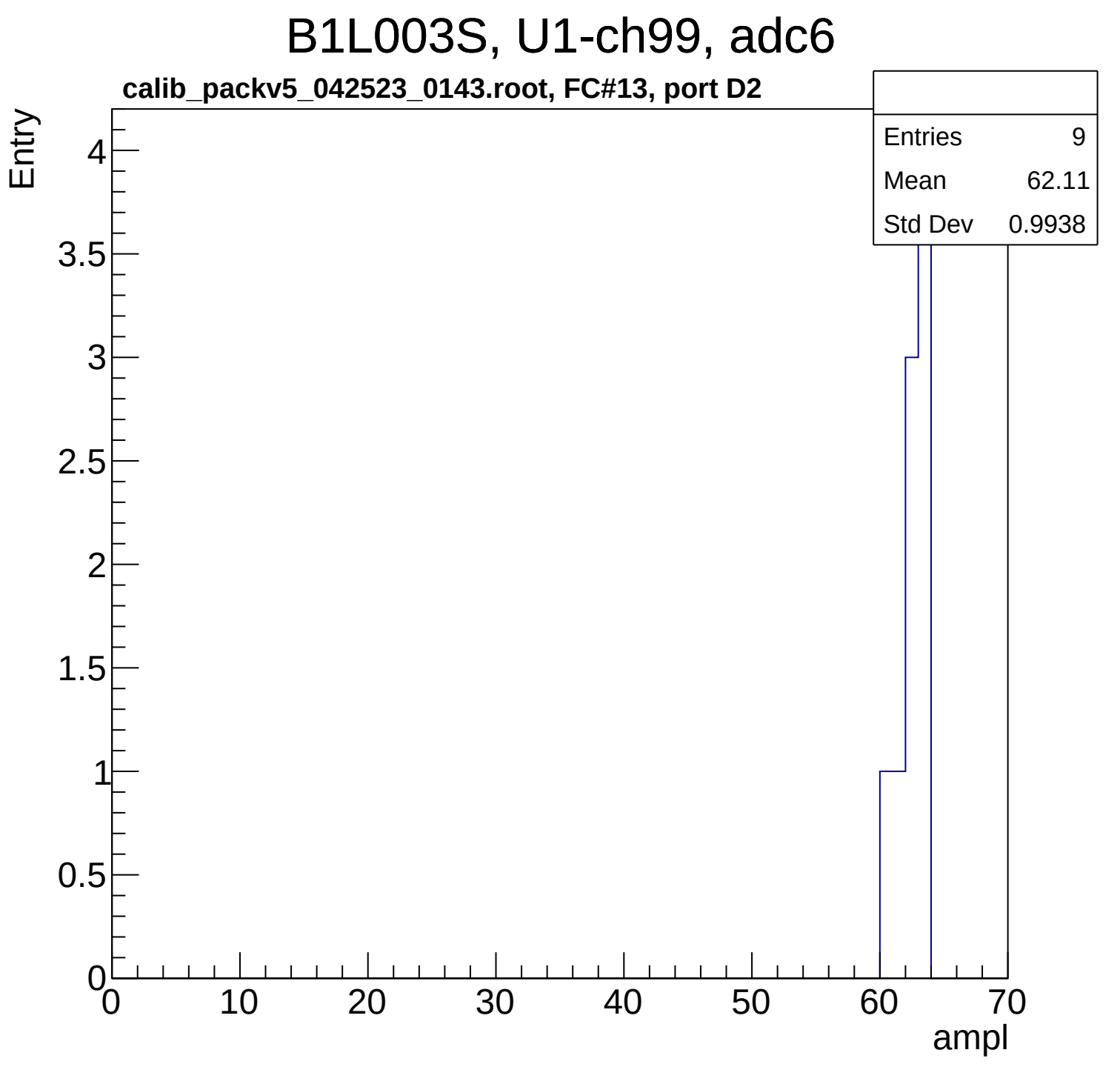
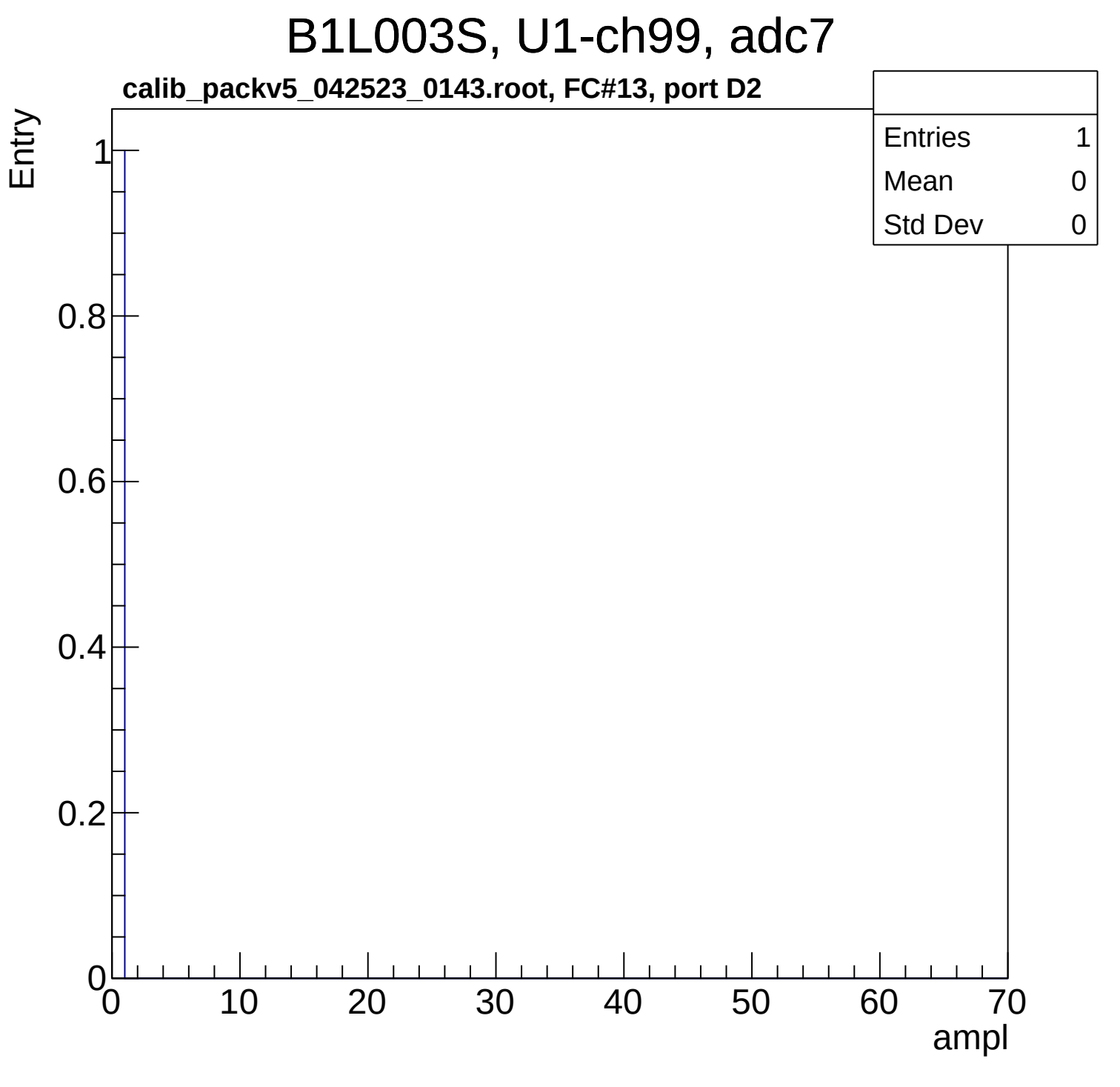
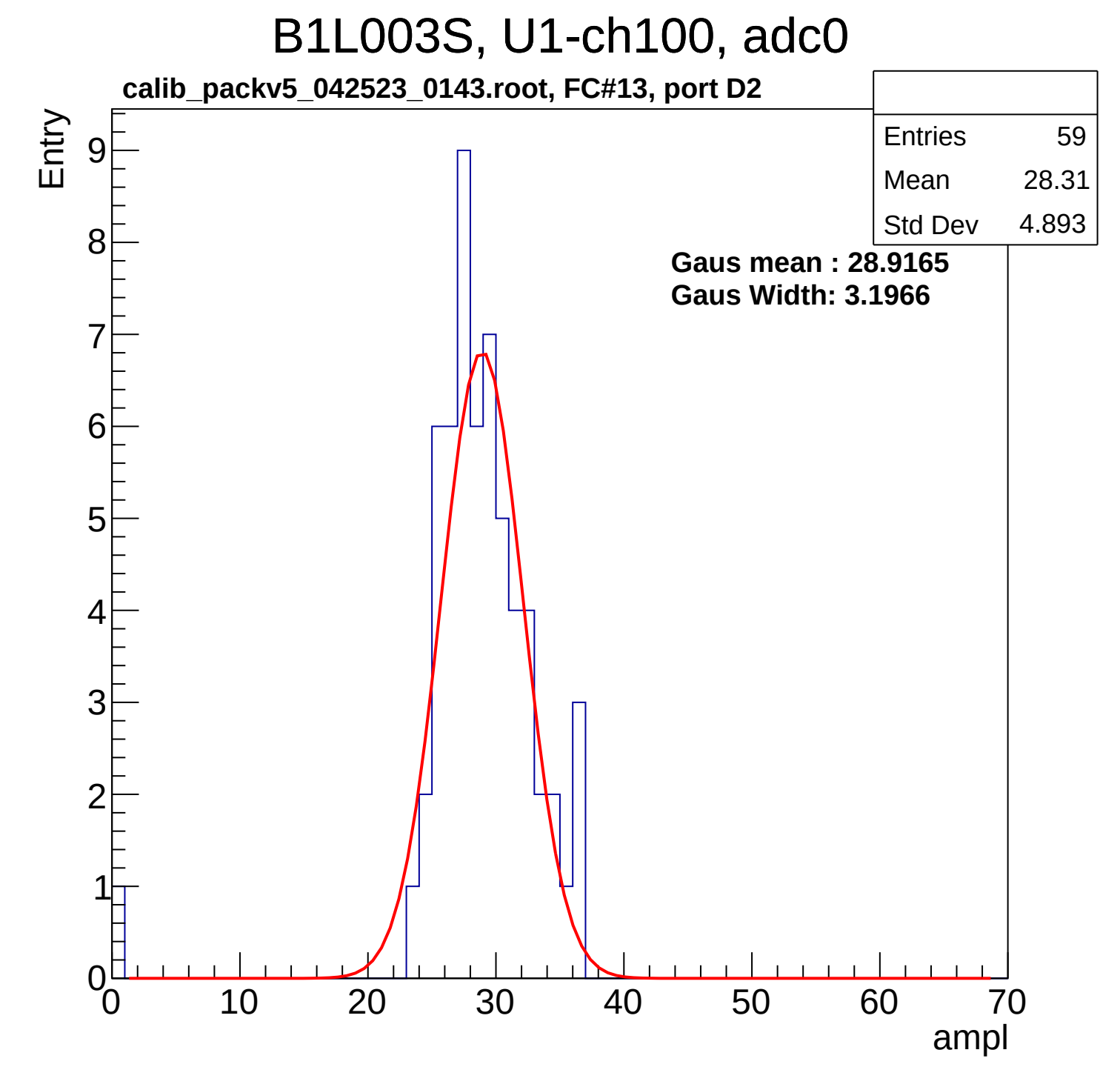
40
50
60
70


B1L003S, U1-ch99, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch99, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch100, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.31
Std Dev 4.893
Gaus mean : 28.9165
Gaus Width: 3.1966
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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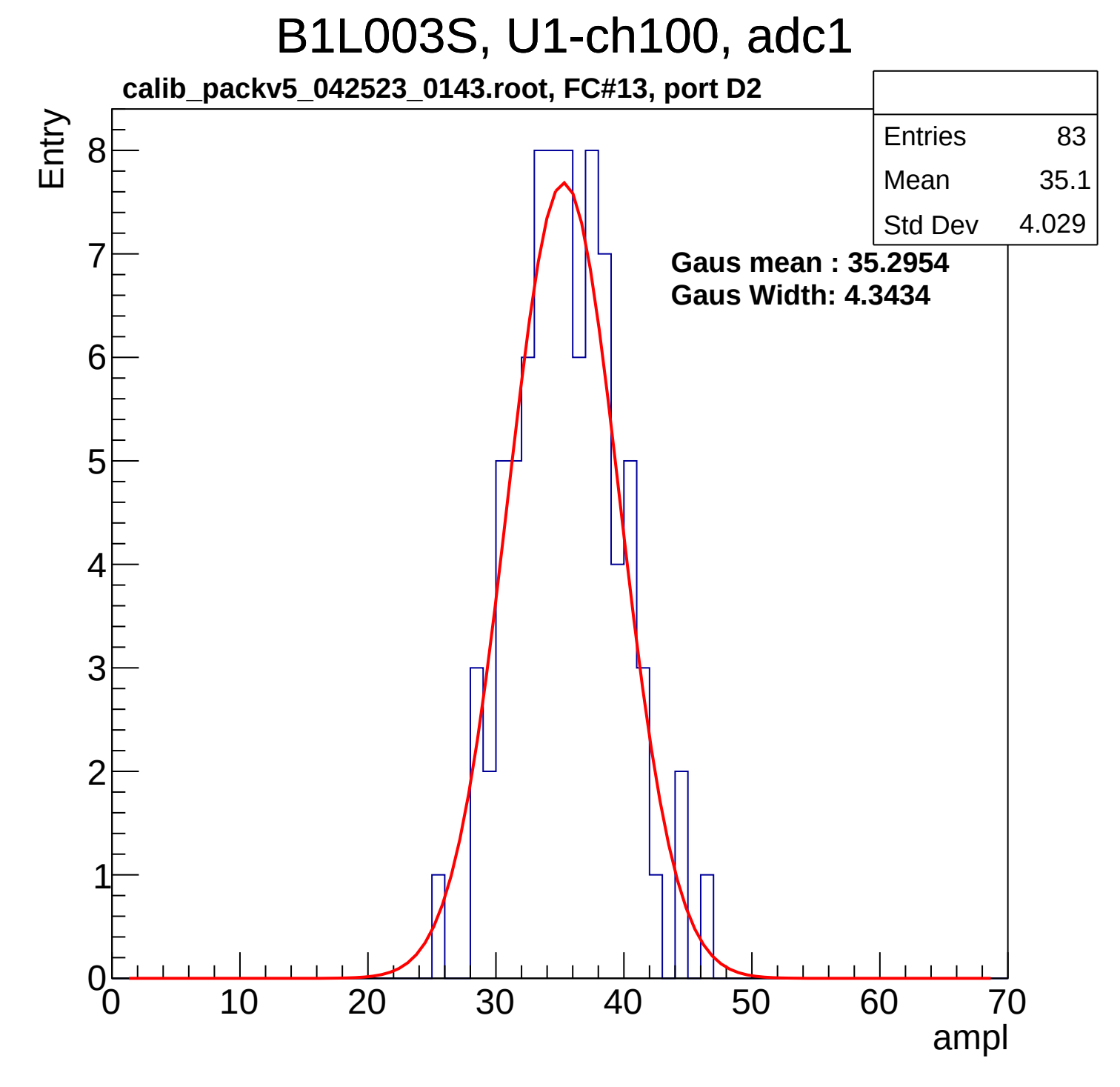
40
50
60
70


B1L003S, U1-ch100, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 83
Mean 35.1
Std Dev 4.029
Gaus mean : 35.2954
Gaus Width: 4.3434
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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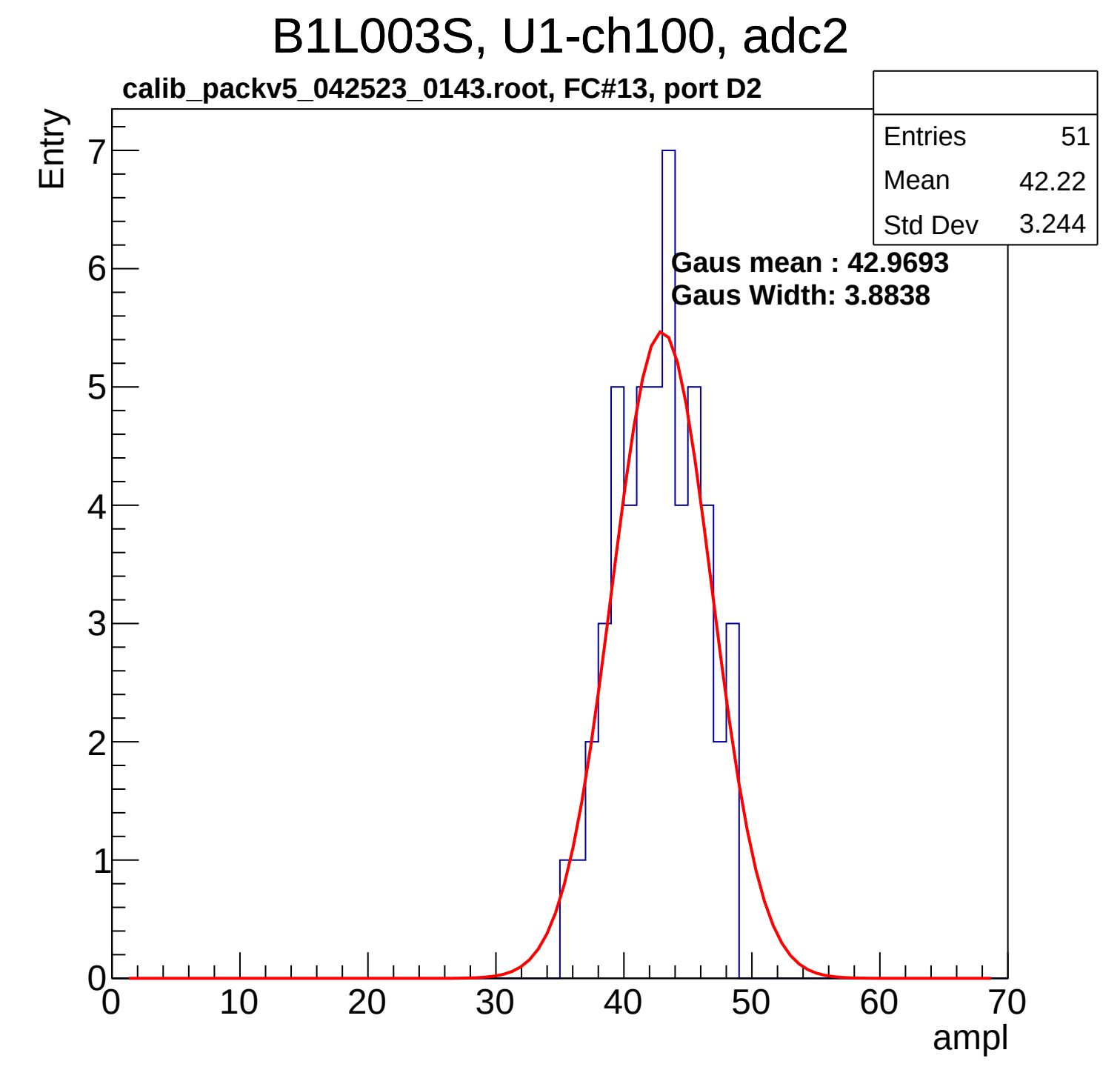
40
50
60
70


B1L003S, U1-ch100, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 42.22
Std Dev 3.244
Gaus mean : 42.9693
Gaus Width: 3.8838
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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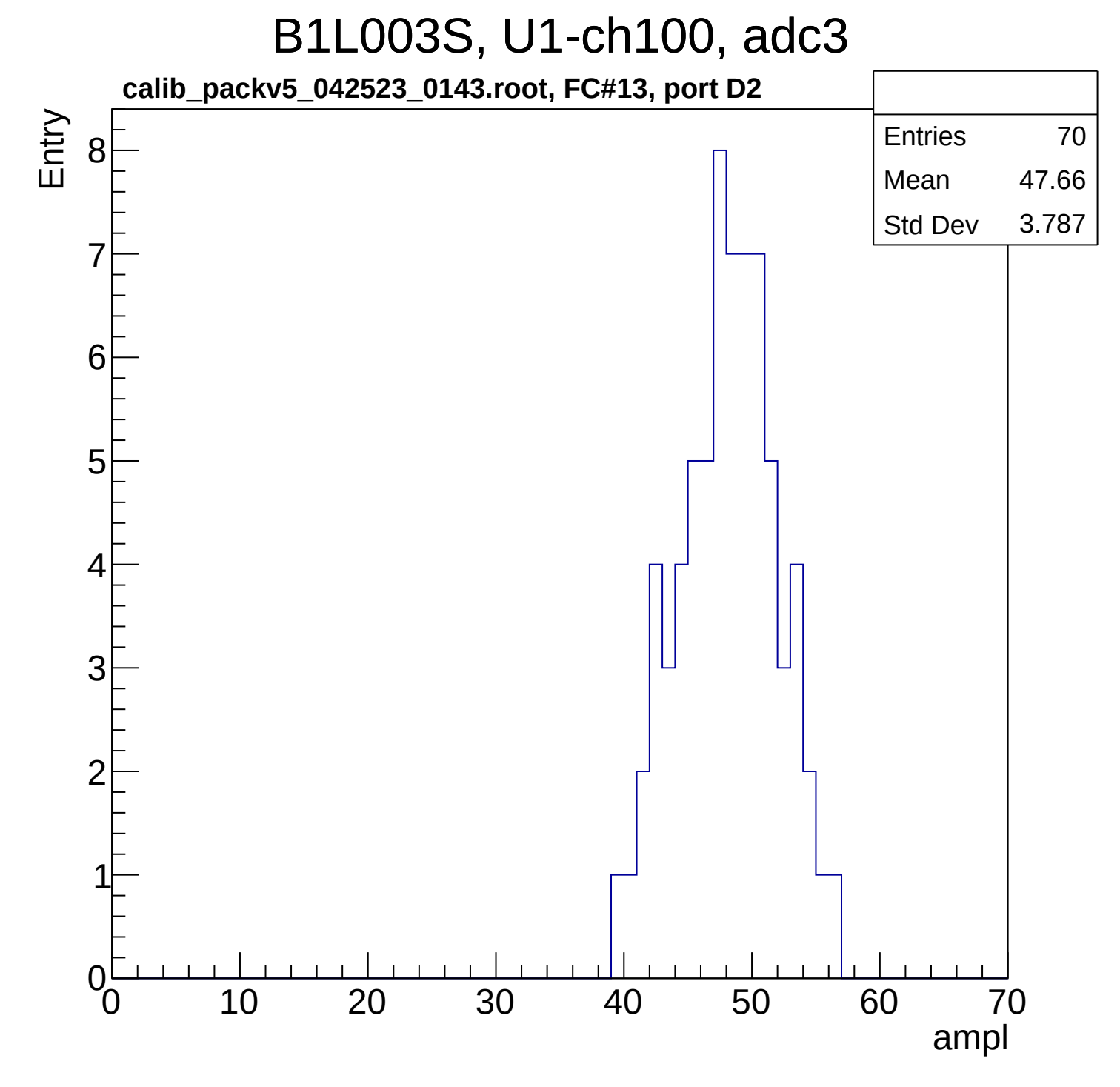
B1L003S, U1-ch100, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 47.66
Std Dev 3.787
Entry
ampl
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1
2
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5
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0
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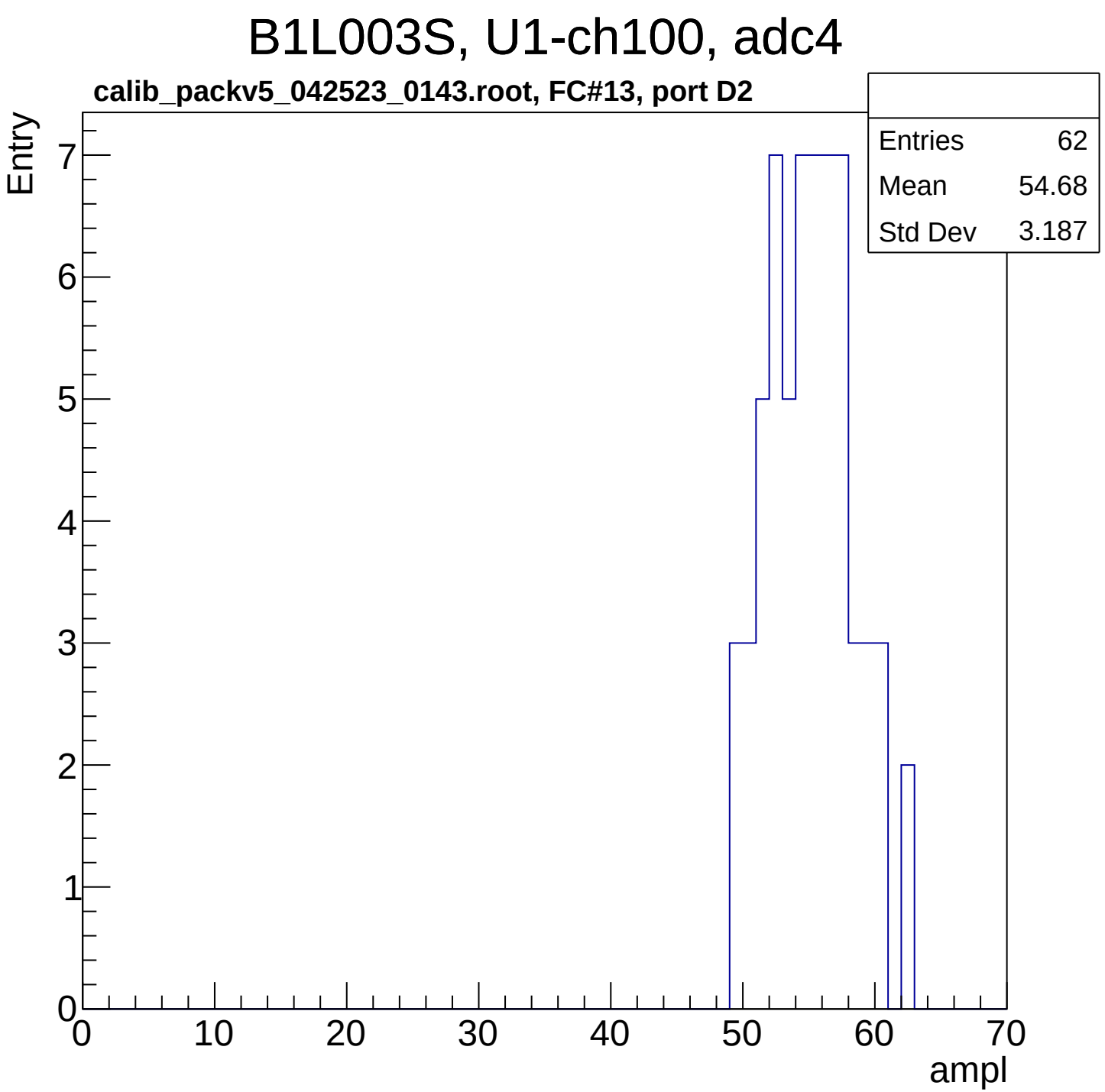
B1L003S, U1-ch100, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 54.68
Std Dev 3.187
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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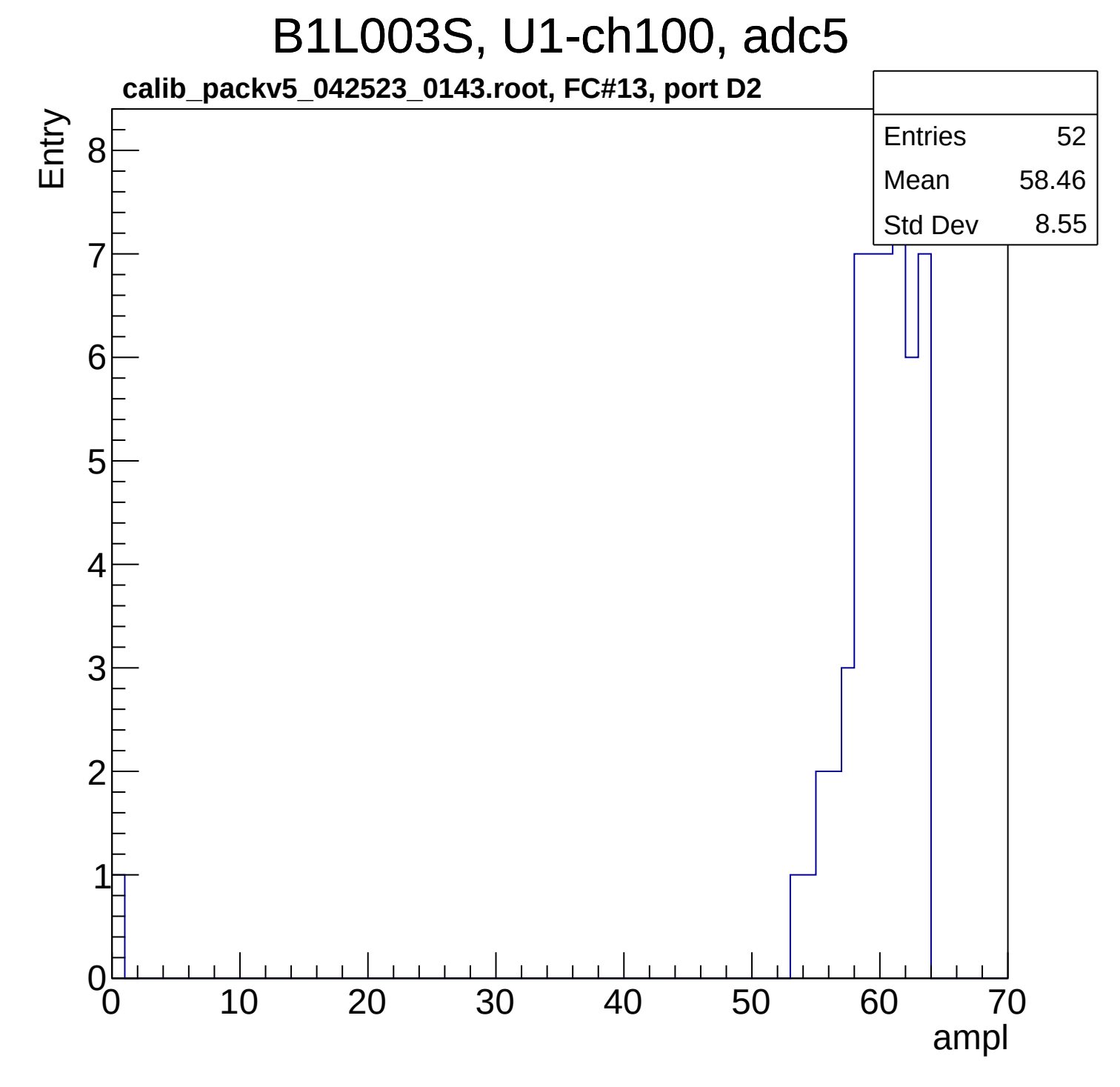
B1L003S, U1-ch100, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.46
Std Dev 8.55
Entry
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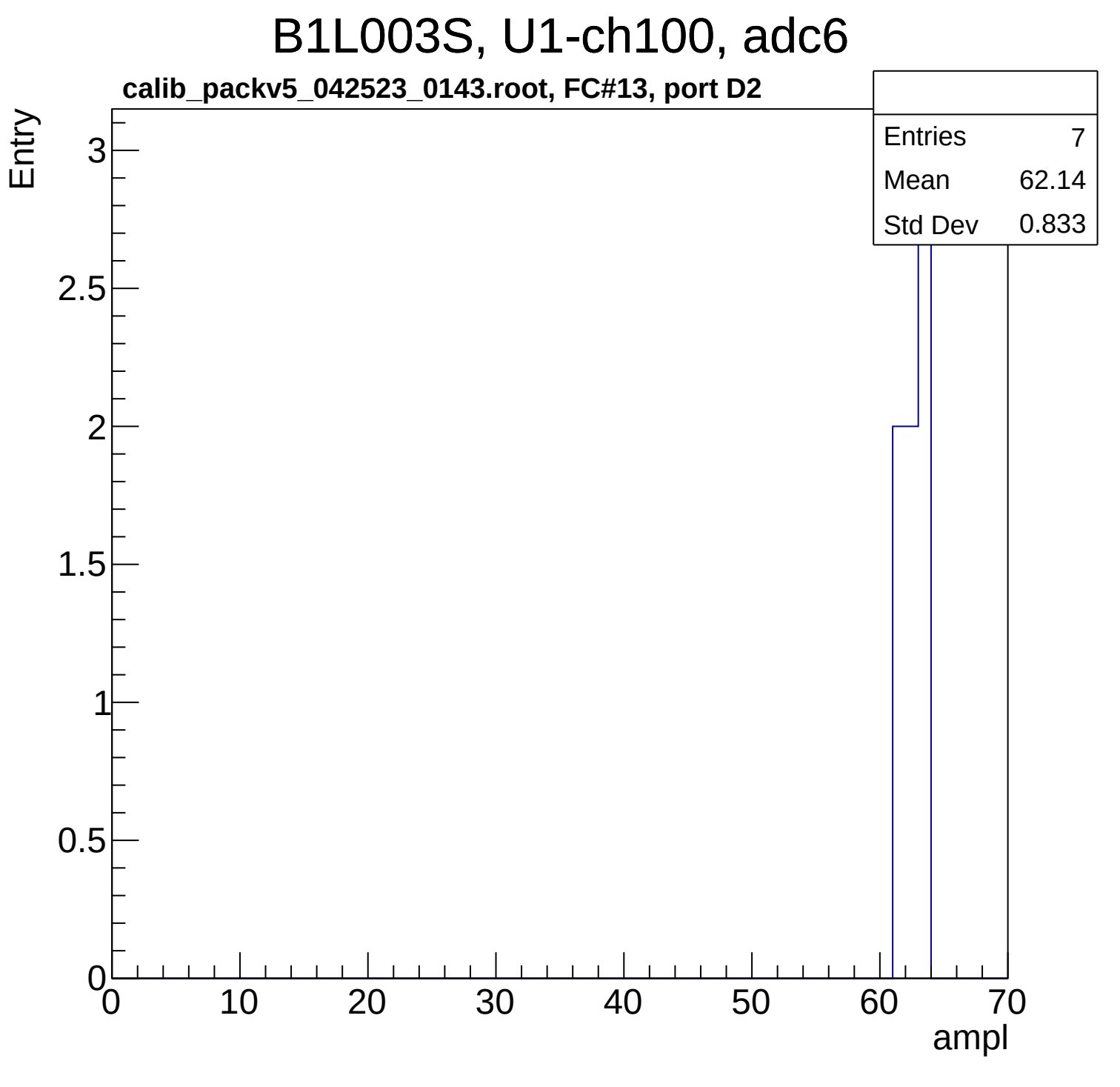
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch100, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 7
Mean 62.14
Std Dev 0.833
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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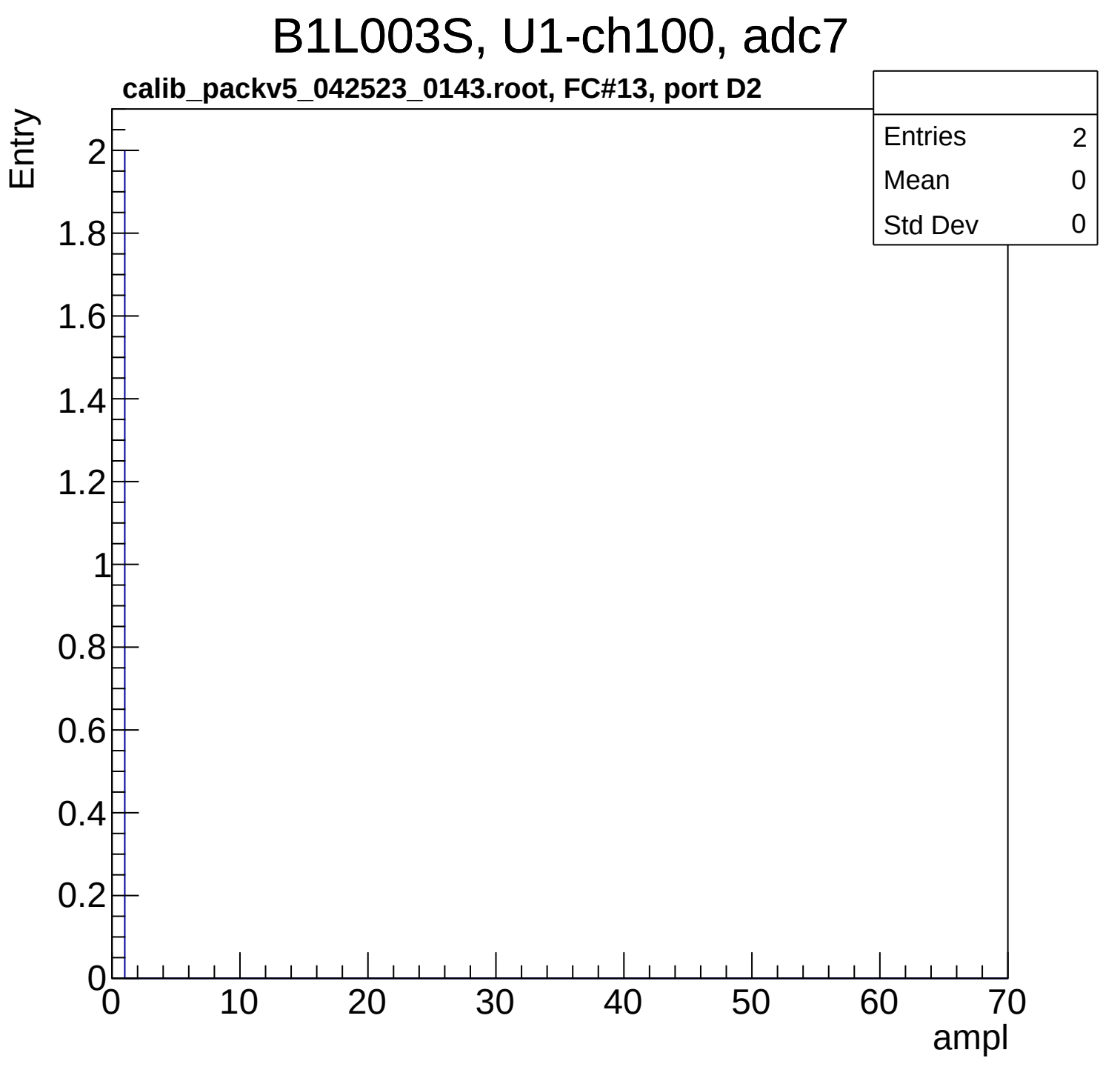
B1L003S, U1-ch100, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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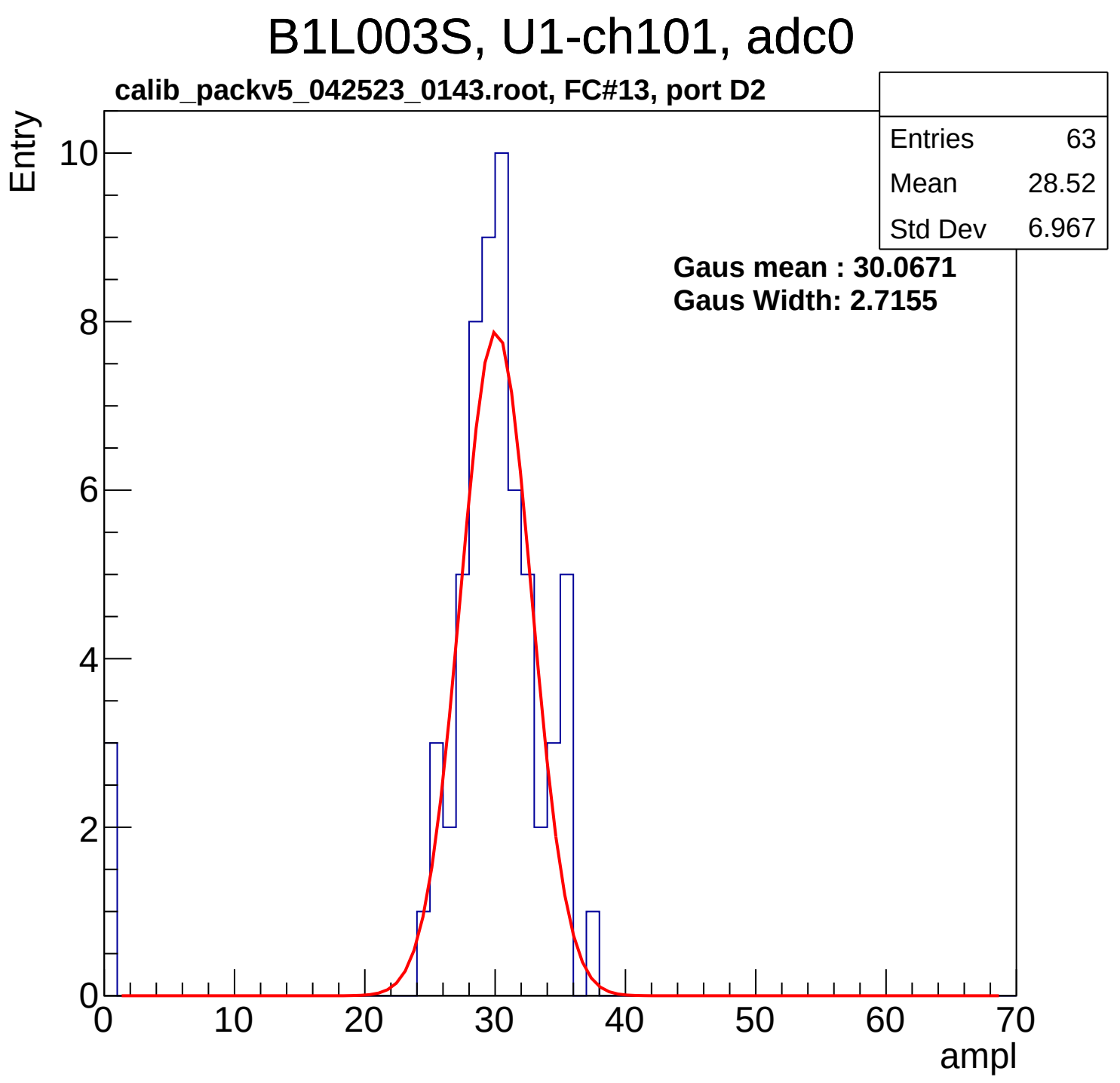
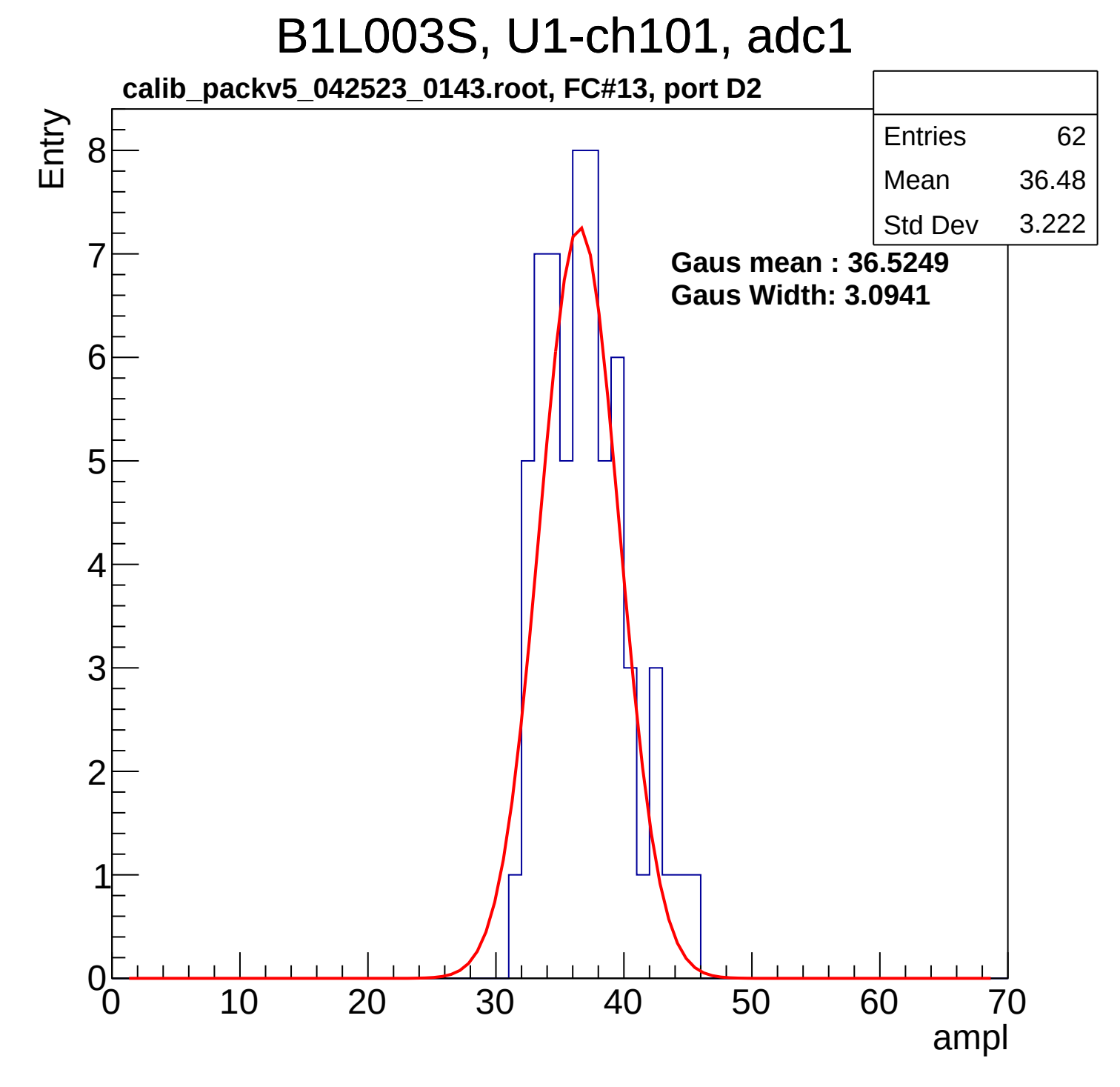

B1L003S, U1-ch101, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 28.52
Std Dev 6.967
Gaus mean : 30.0671
Gaus Width: 2.7155
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch101, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 36.48
Std Dev 3.222
Gaus mean : 36.5249
Gaus Width: 3.0941
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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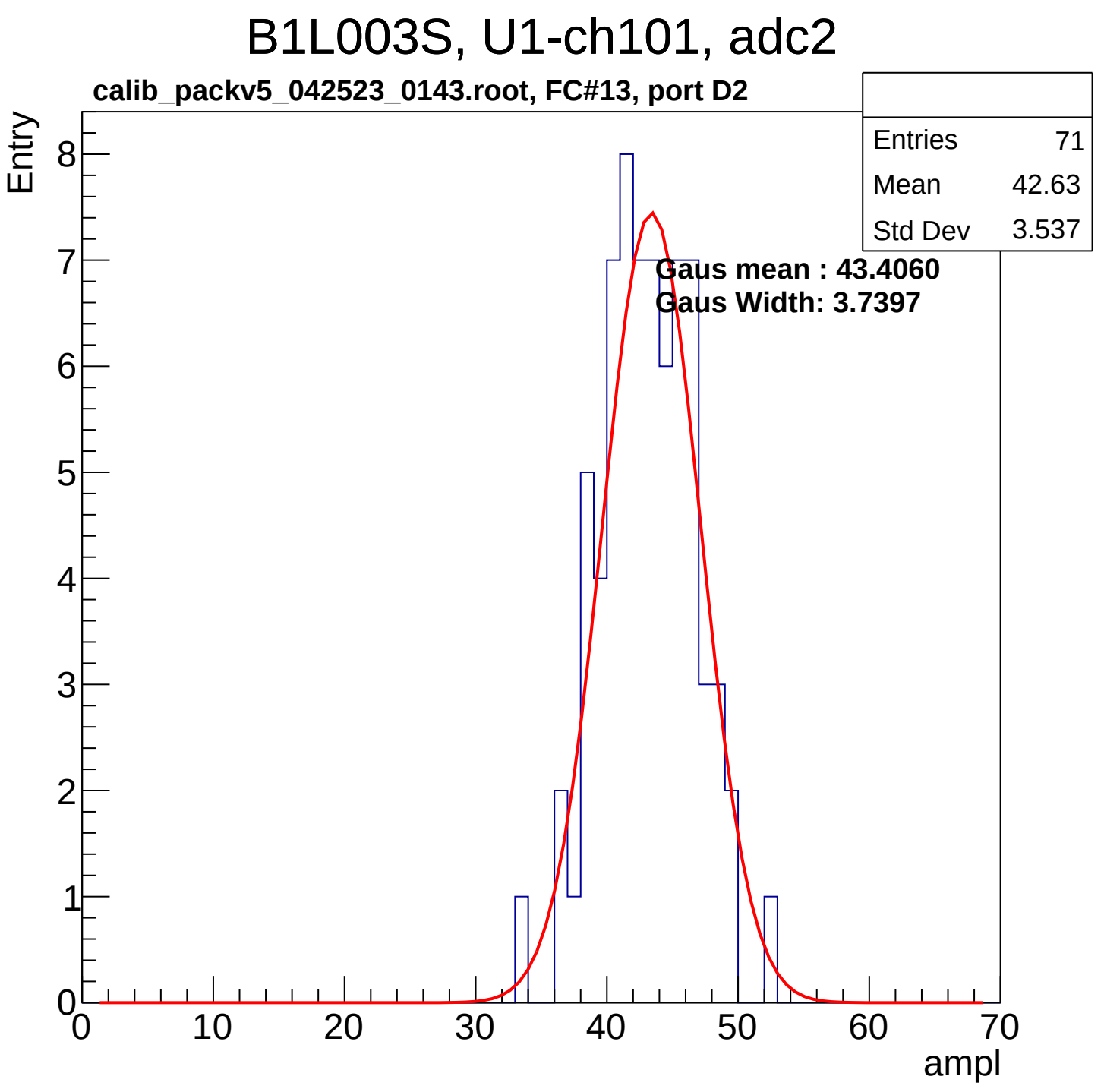
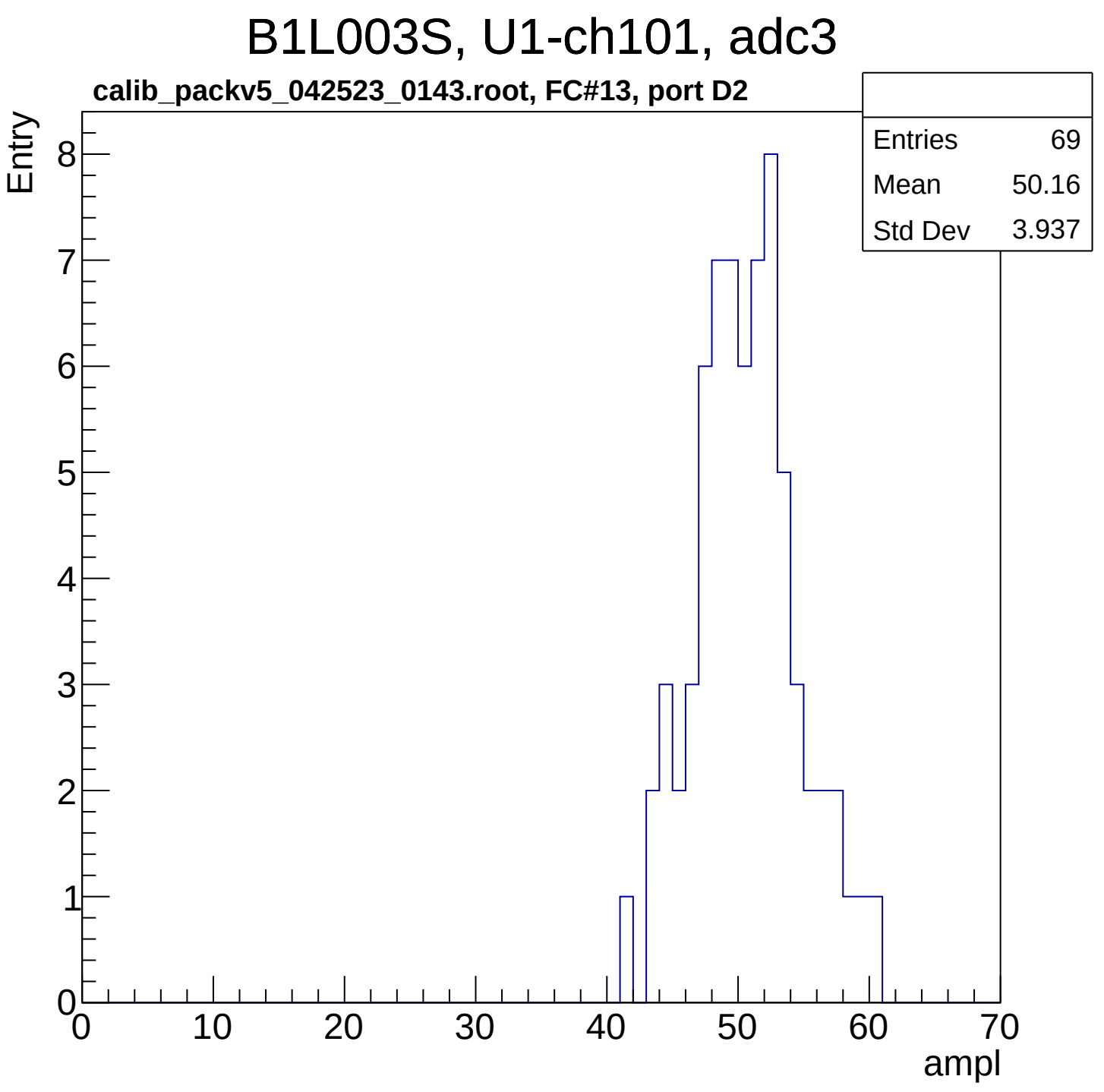

B1L003S, U1-ch101, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 42.63
Std Dev 3.537
Gaus mean : 43.4060
Gaus Width: 3.7397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch101, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 50.16
Std Dev 3.937
Entry
ampl
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2
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4
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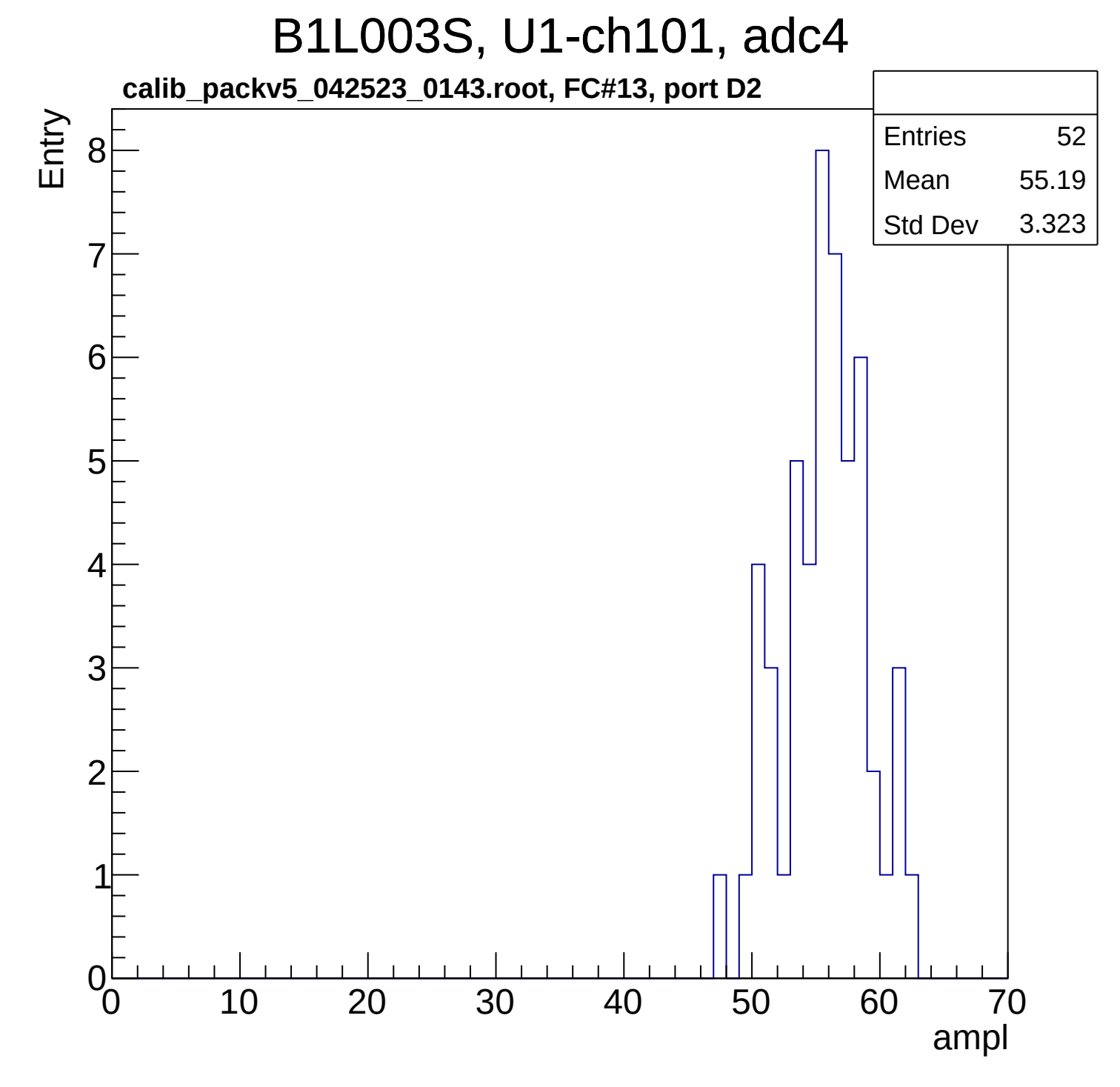
B1L003S, U1-ch101, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 55.19
Std Dev 3.323
Entry
ampl
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1
2
3
4
5
6
7
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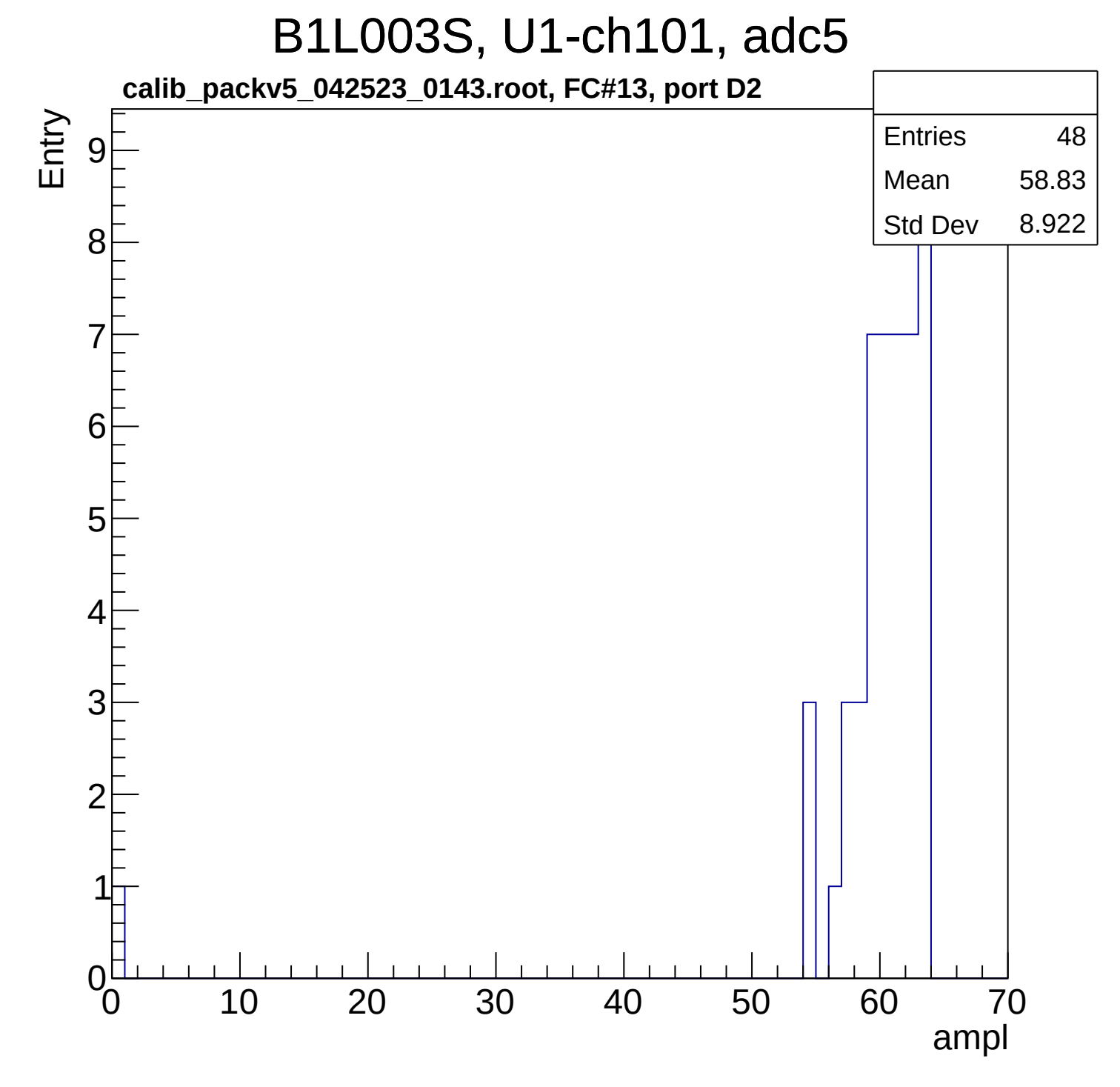
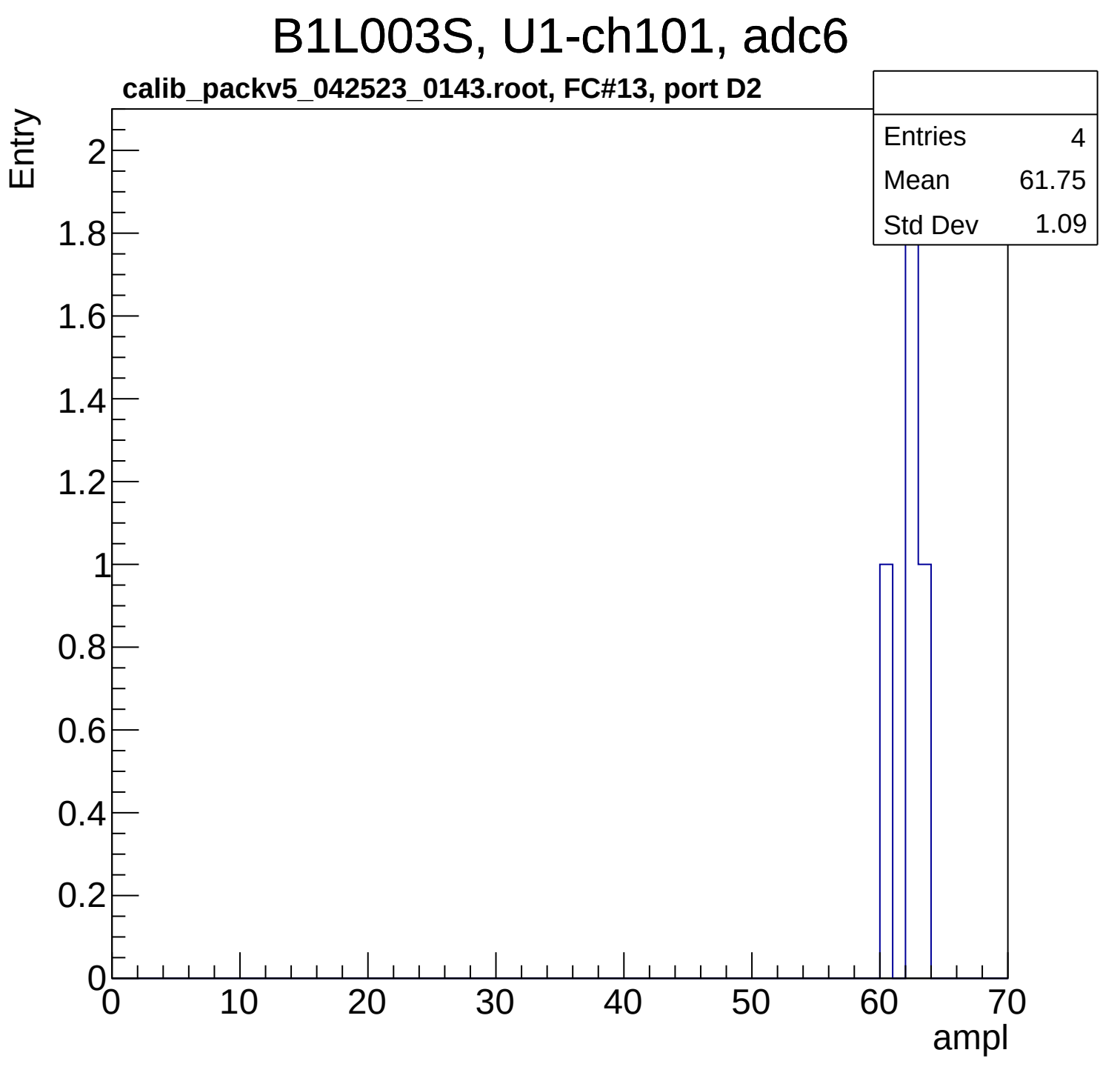
0
10
20
30
40
50
60
70


B1L003S, U1-ch101, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 48
Mean 58.83
Std Dev 8.922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch101, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.75
Std Dev 1.09
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
0
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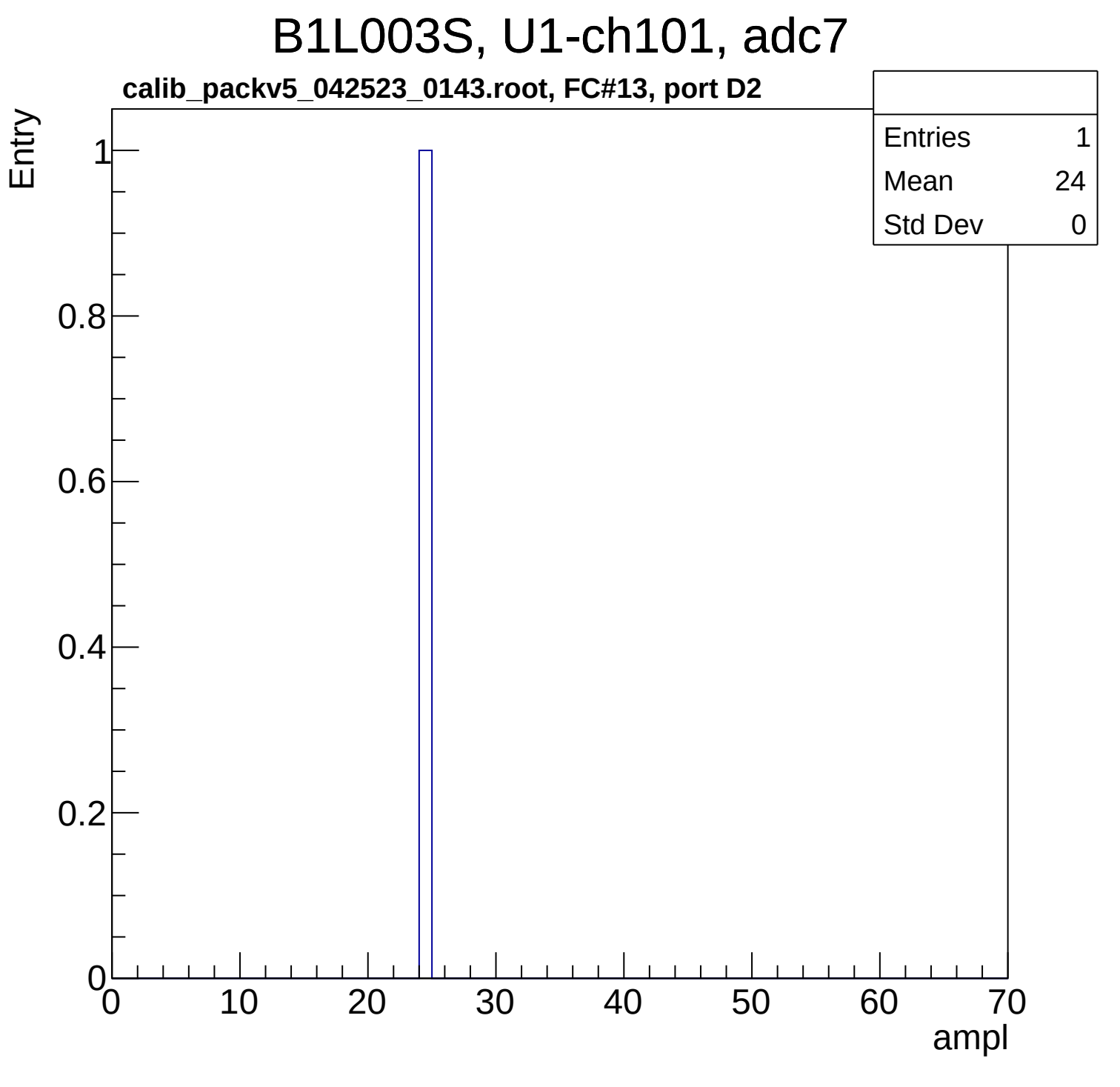
B1L003S, U1-ch101, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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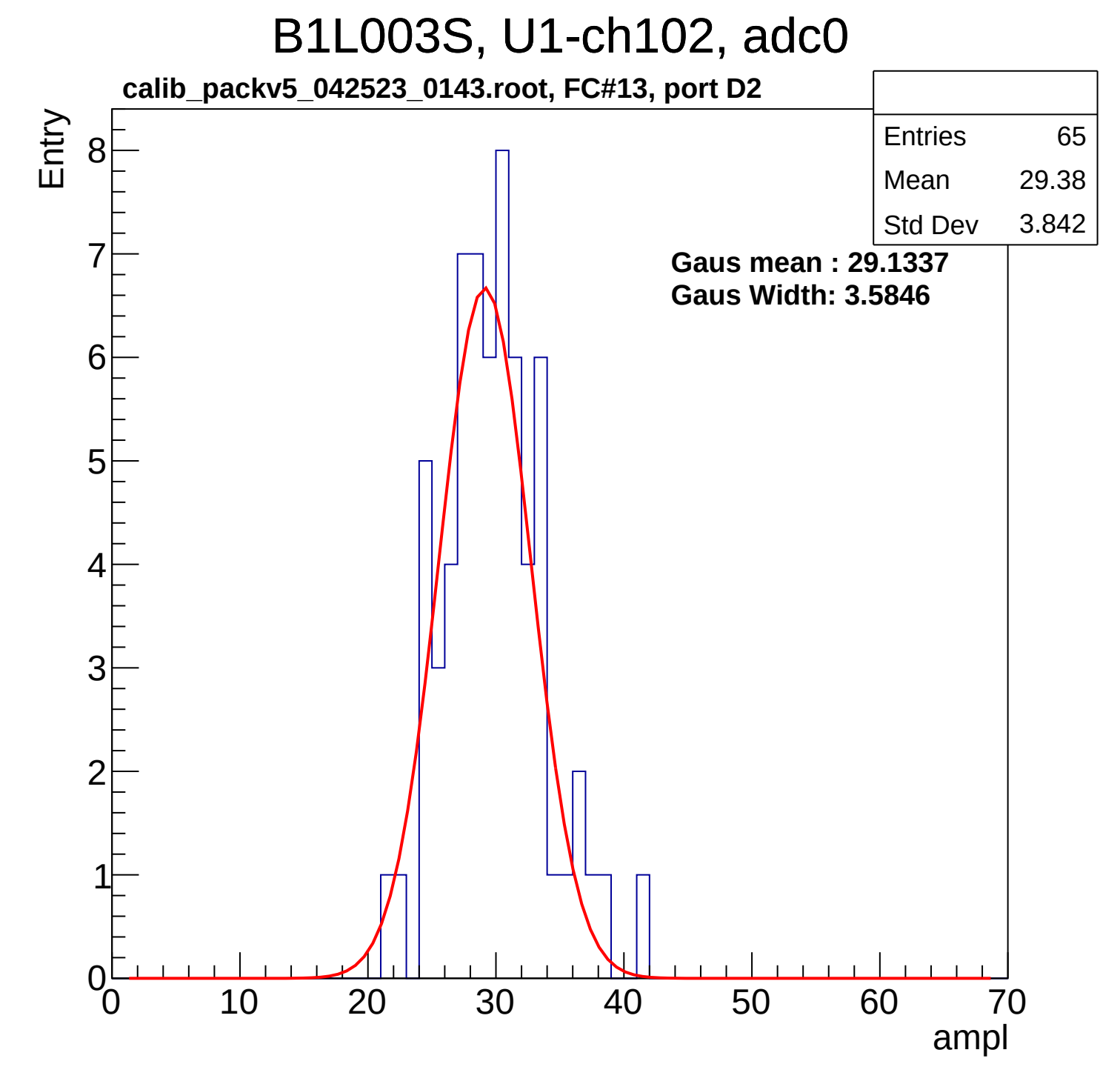
40
50
60
70
ampl


B1L003S, U1-ch102, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 29.38
Std Dev 3.842
Gaus mean : 29.1337
Gaus Width: 3.5846
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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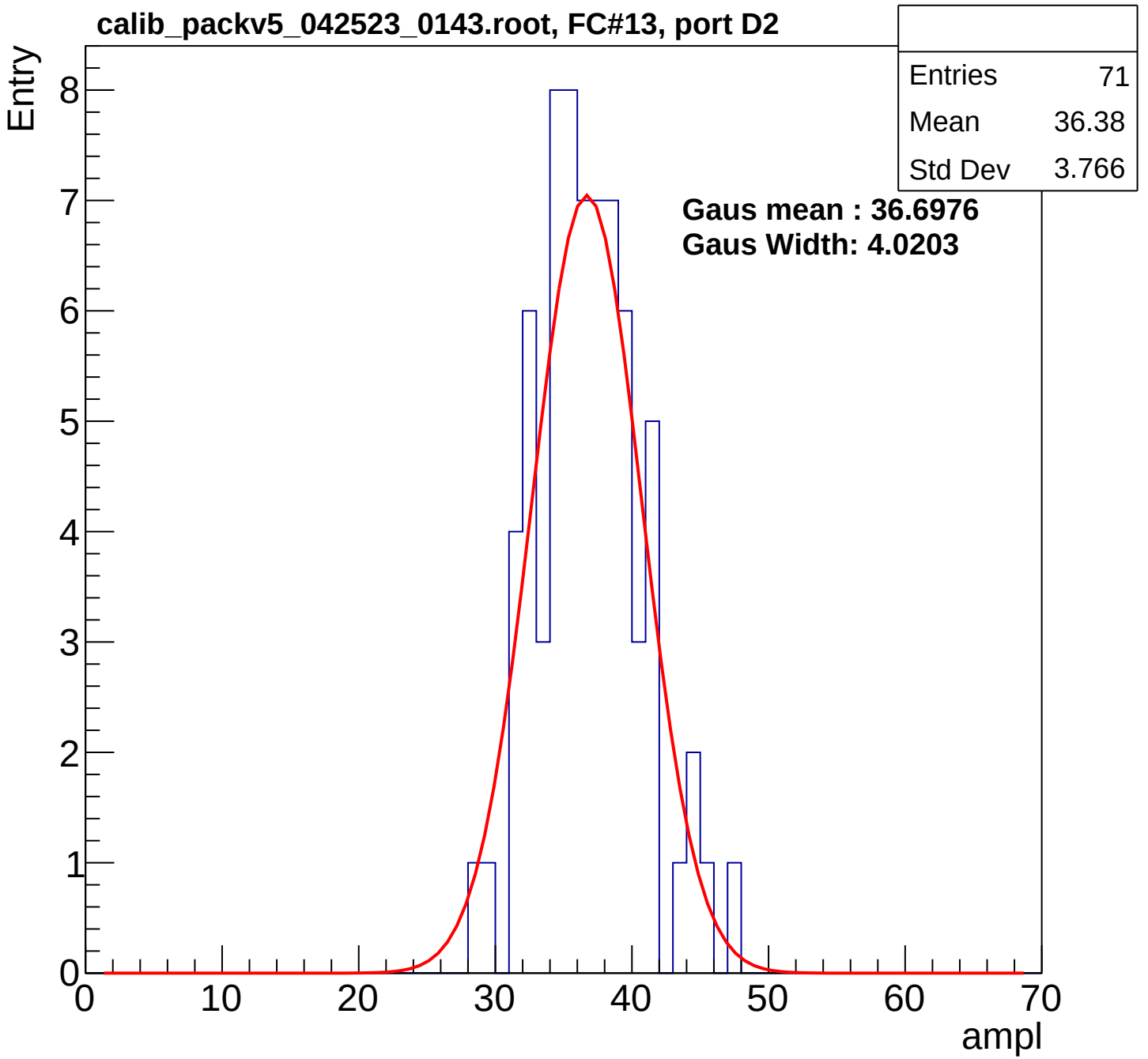
B1L003S, U1-ch102, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 36.38
Std Dev 3.766
Gaus mean : 36.6976
Gaus Width: 4.0203
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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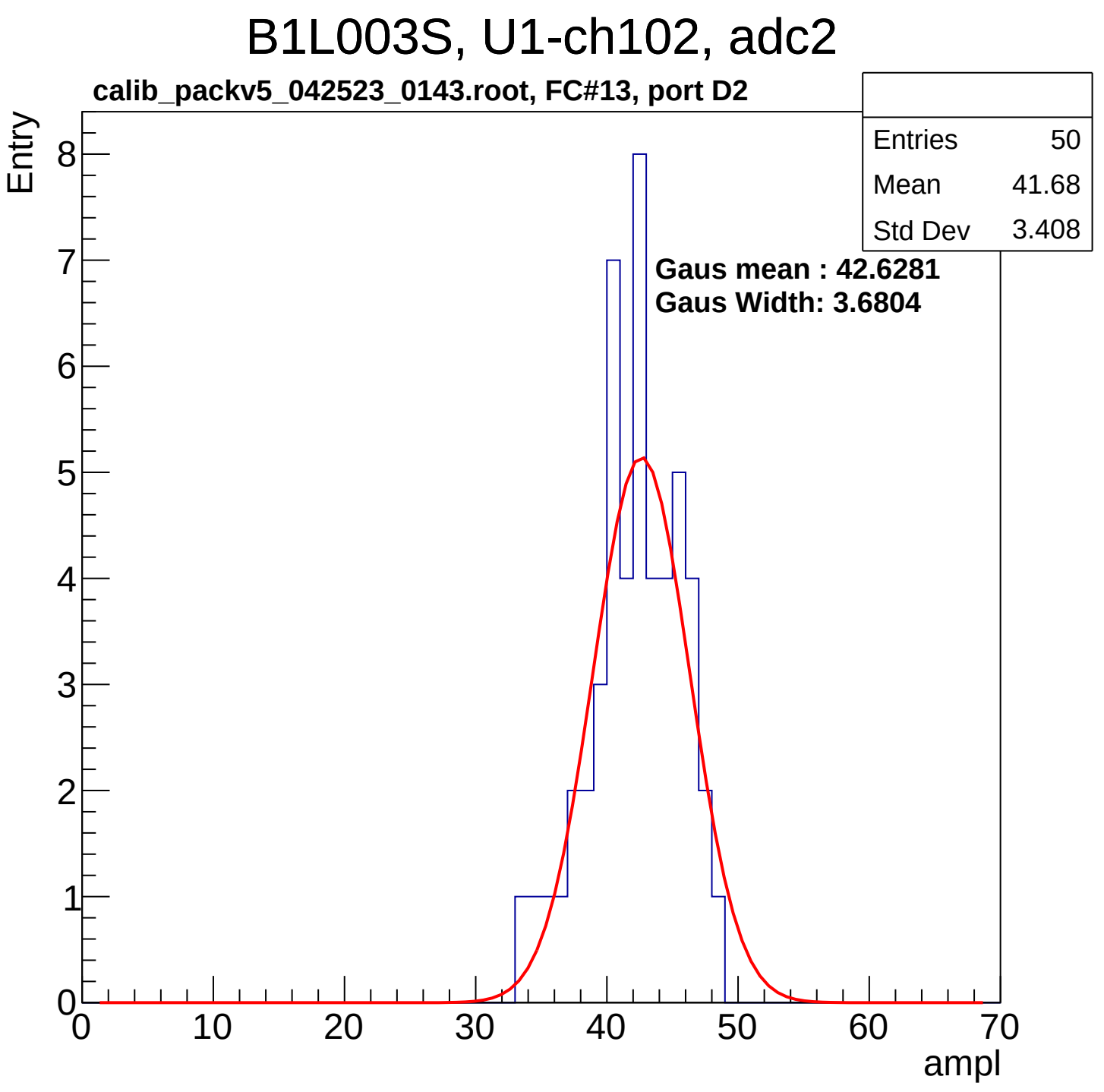
B1L003S, U1-ch102, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 41.68
Std Dev 3.408
Gaus mean : 42.6281
Gaus Width: 3.6804
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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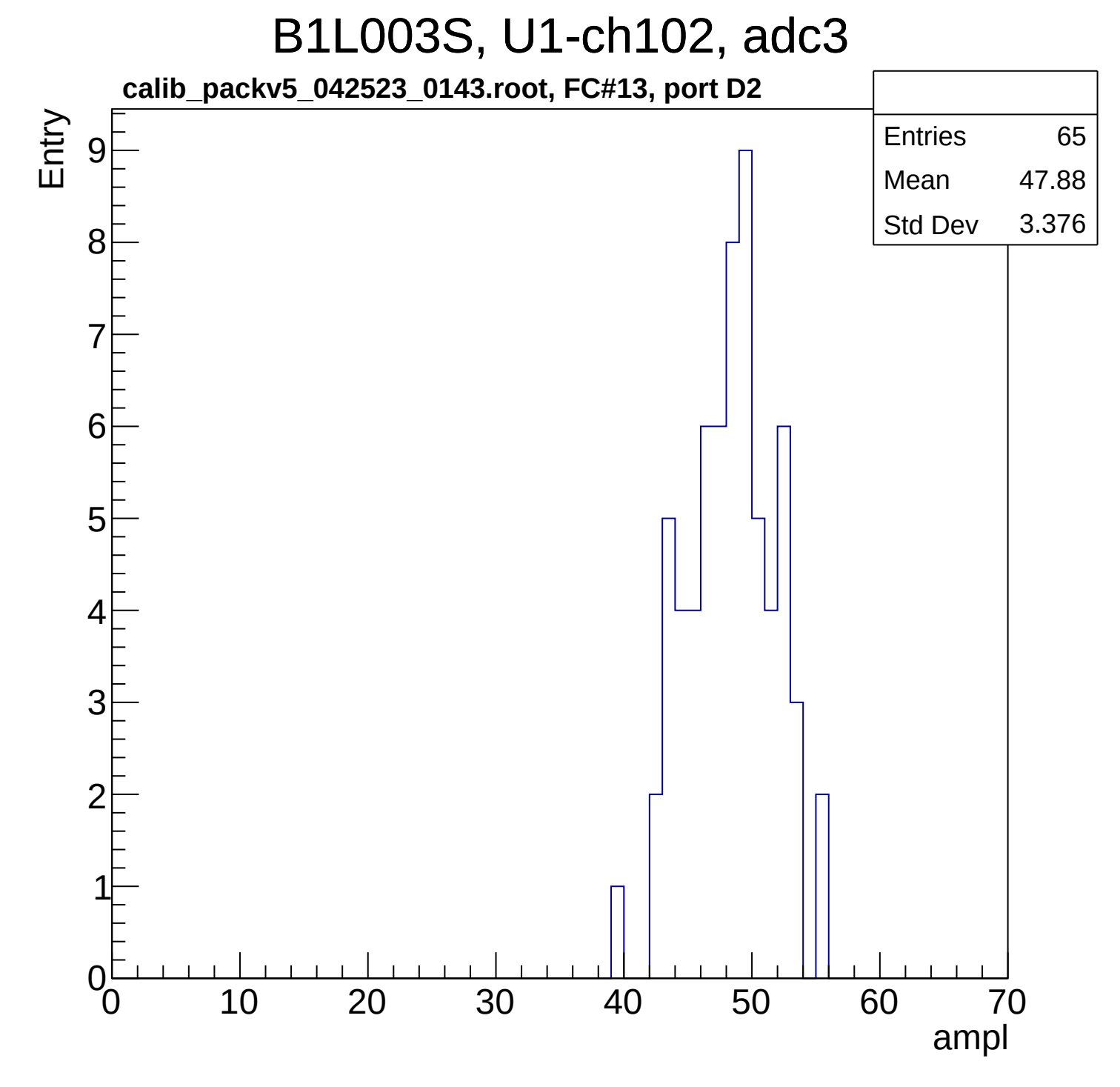
B1L003S, U1-ch102, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 47.88
Std Dev 3.376
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
50
60
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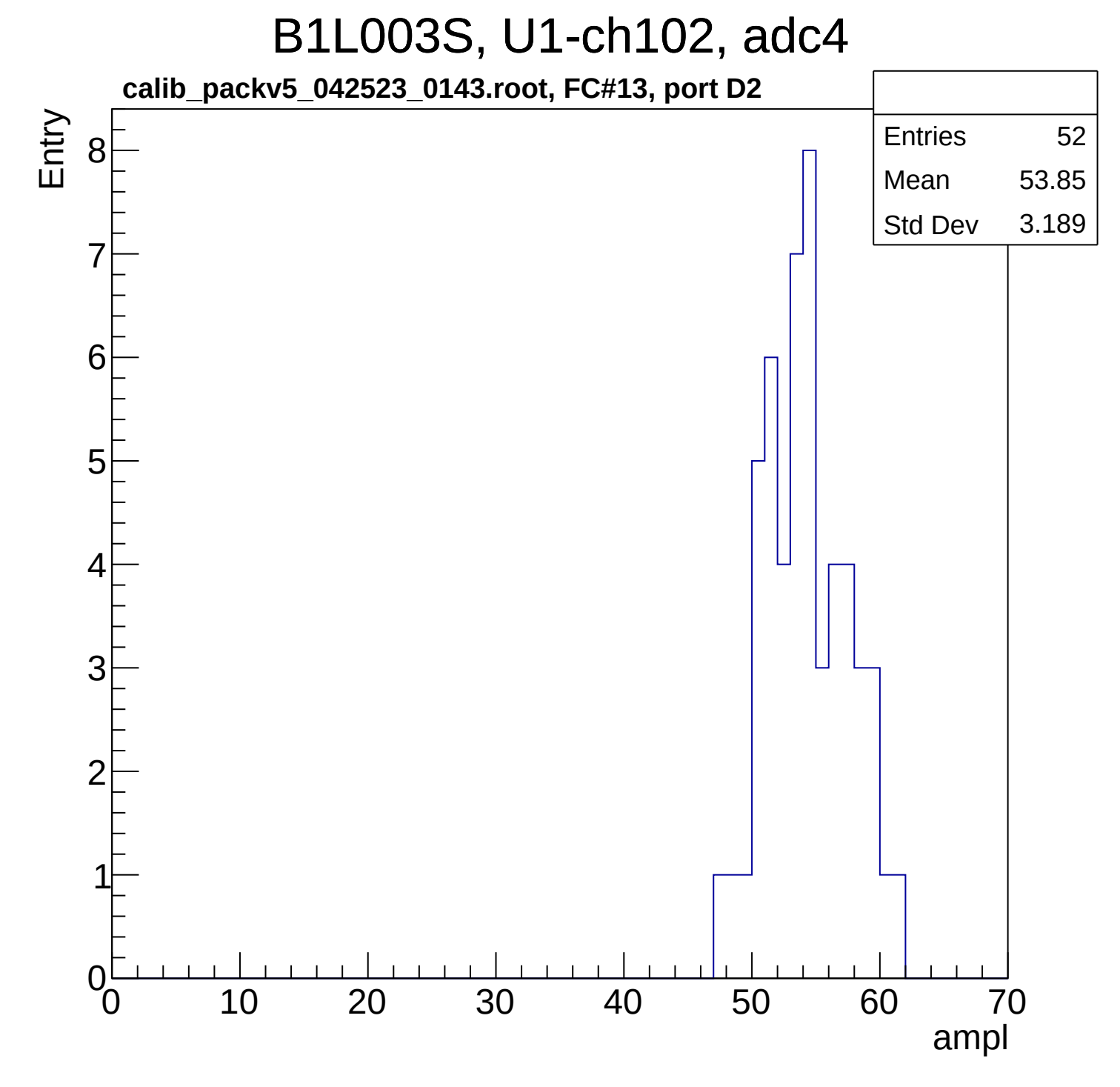
B1L003S, U1-ch102, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 53.85
Std Dev 3.189
Entry
ampl
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2
3
4
5
6
7
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0
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60
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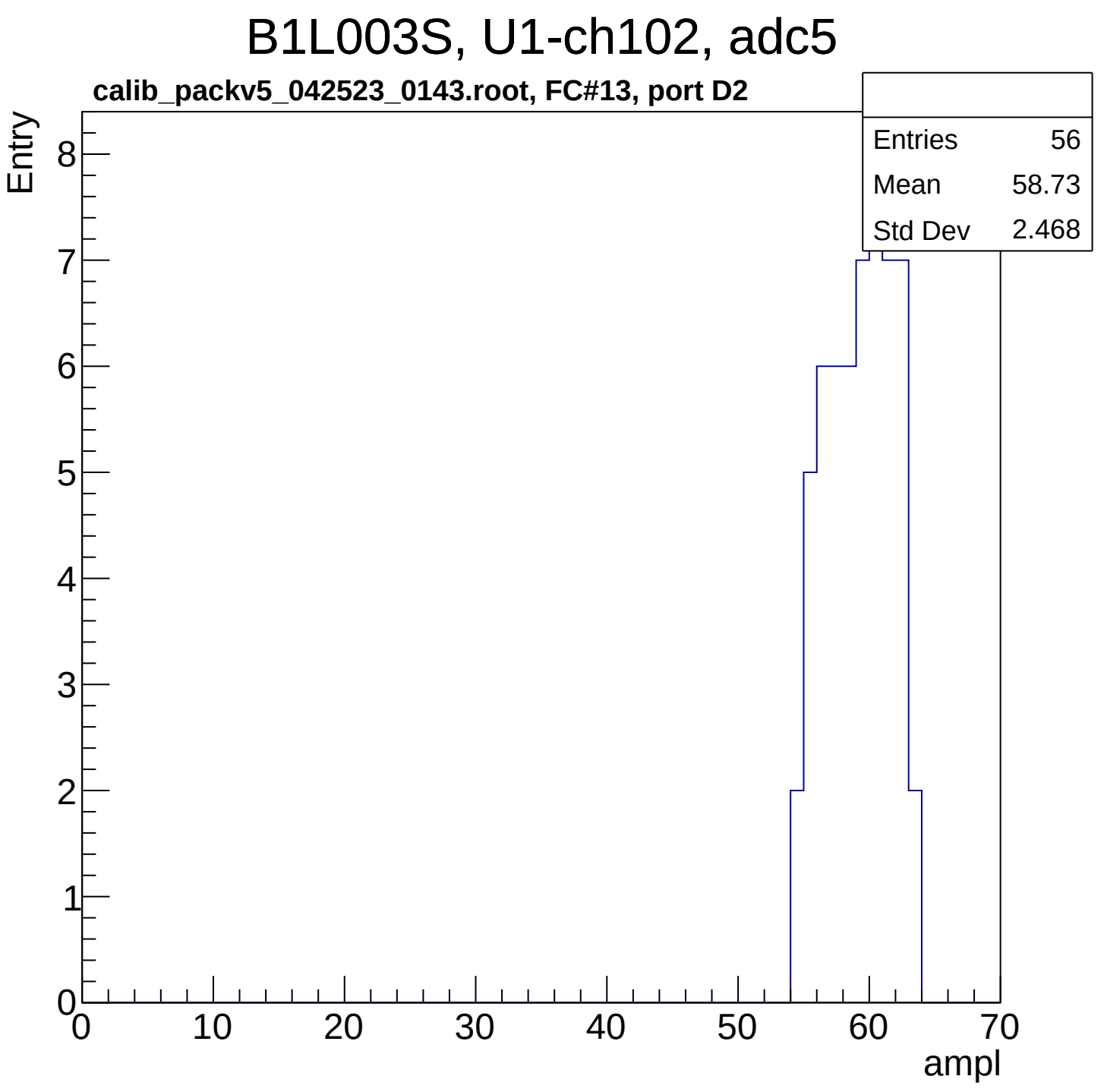
B1L003S, U1-ch102, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 58.73
Std Dev 2.468
Entry
0
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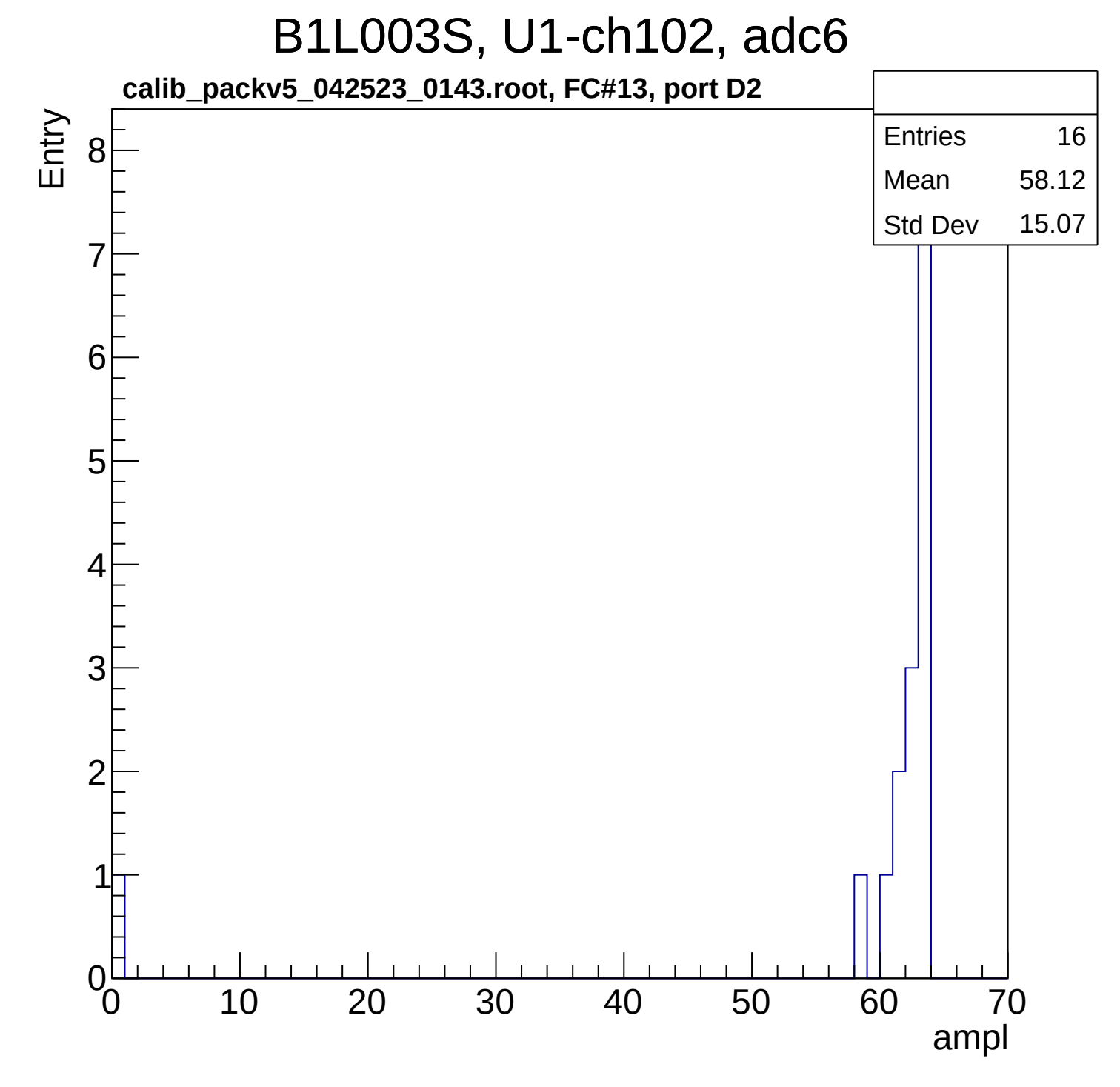
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch102, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 58.12
Std Dev 15.07
Entry
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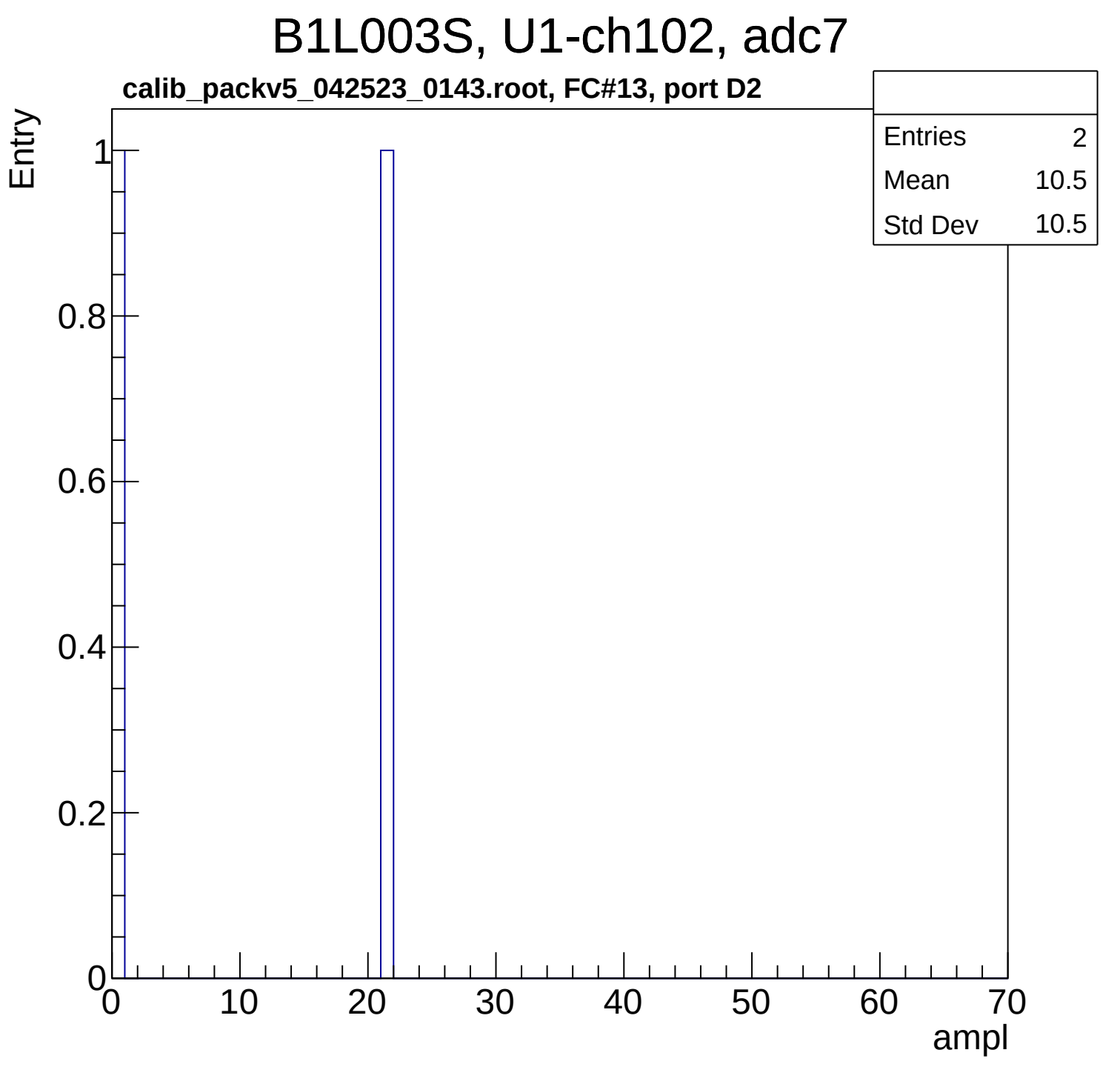
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch102, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
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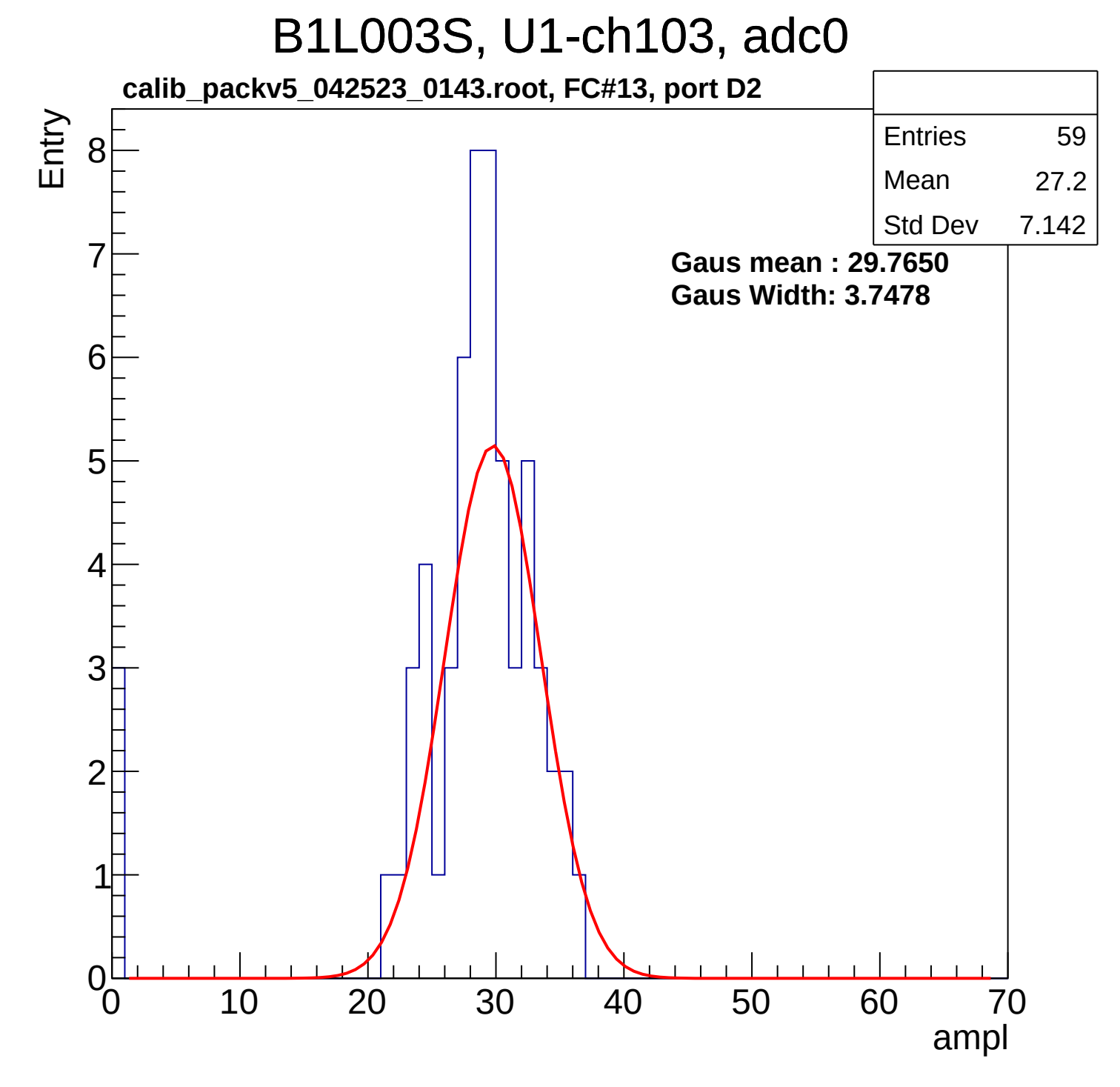
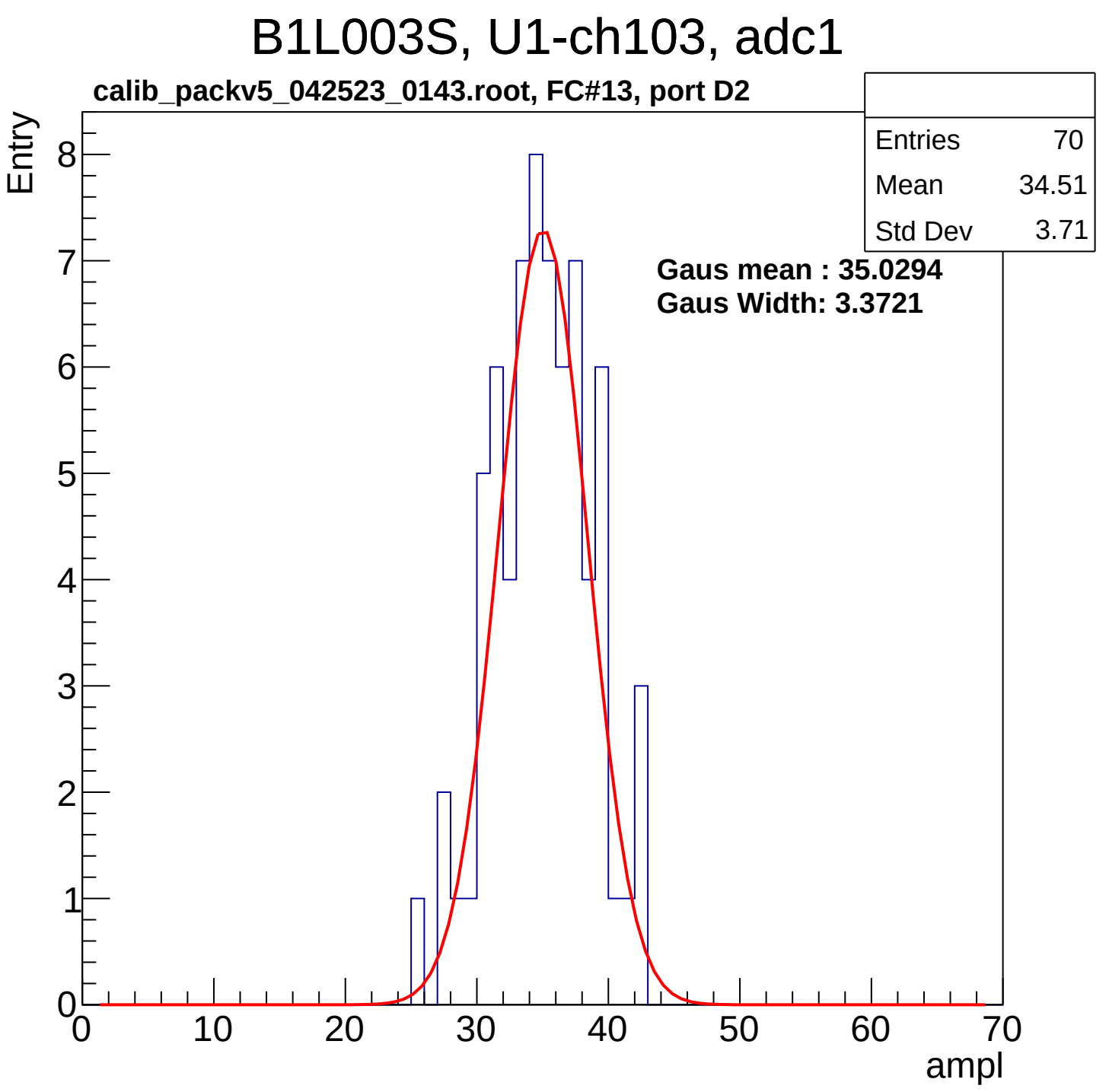
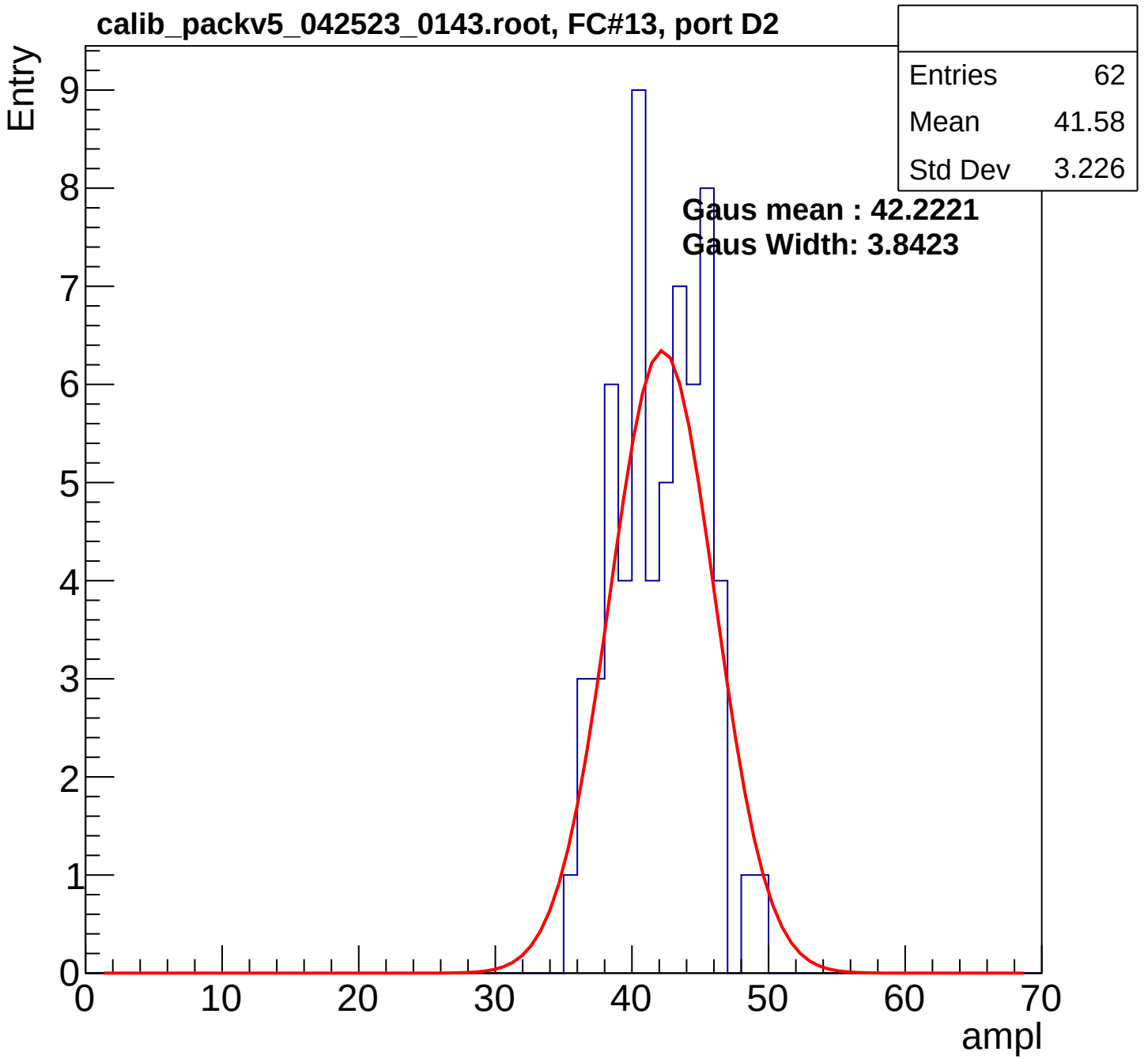
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch103, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 27.2
Std Dev 7.142
Gaus mean : 29.7650
Gaus Width: 3.7478
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch103, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 34.51
Std Dev 3.71
Gaus mean : 35.0294
Gaus Width: 3.3721
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch103, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 41.58
Std Dev 3.226
Gaus mean : 42.2221
Gaus Width: 3.8423
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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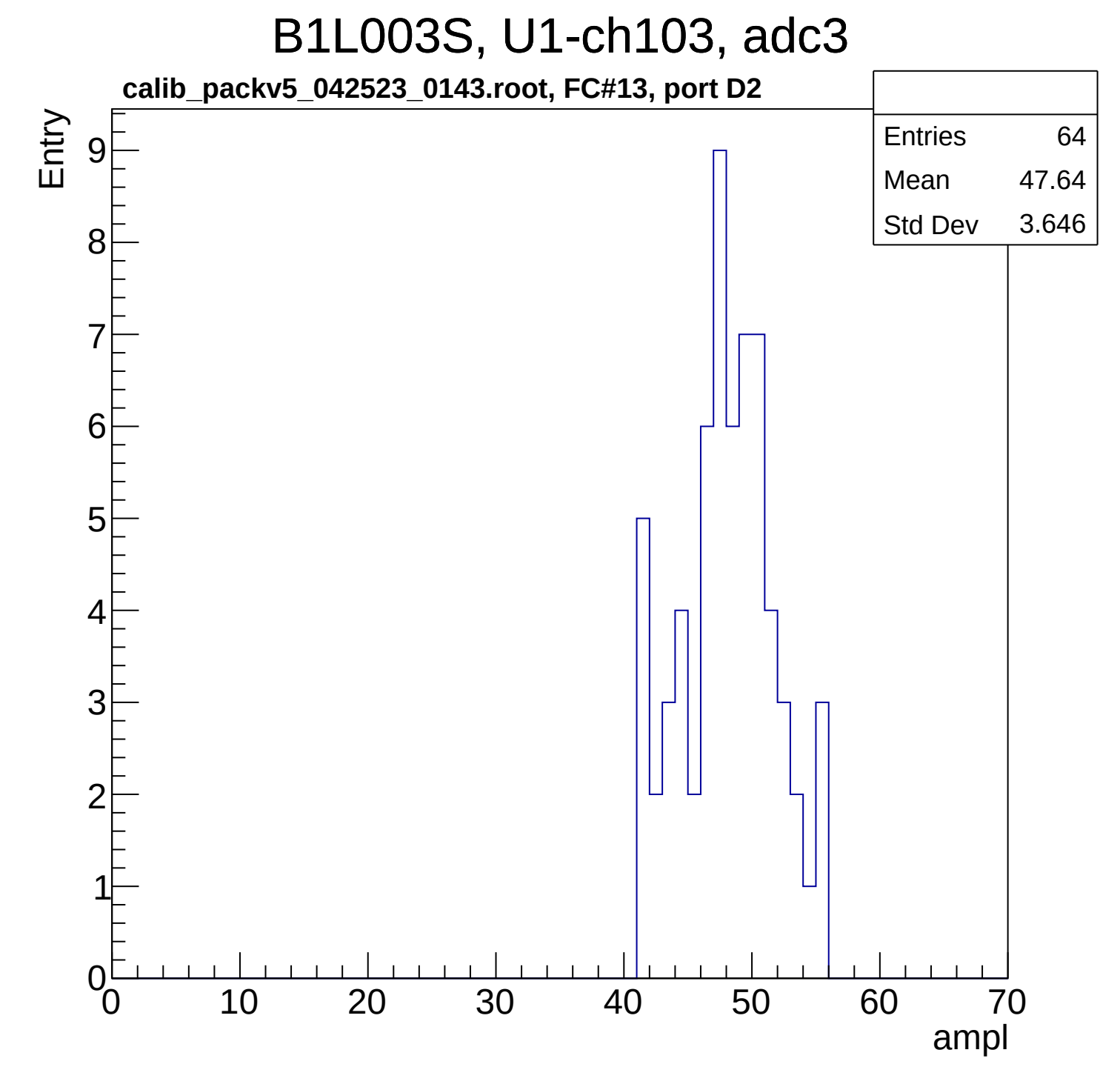
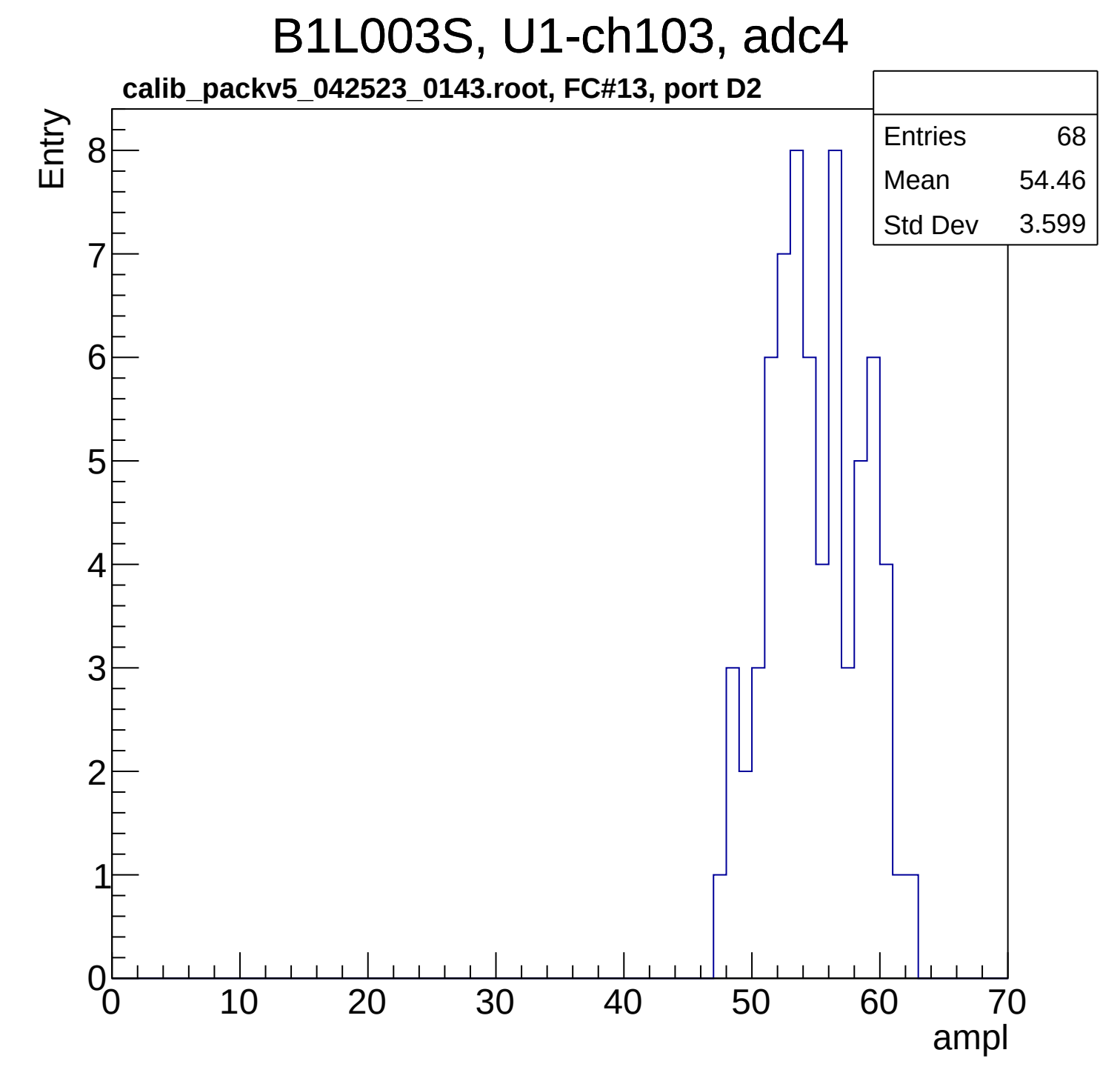

B1L003S, U1-ch103, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 47.64
Std Dev 3.646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch103, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 54.46
Std Dev 3.599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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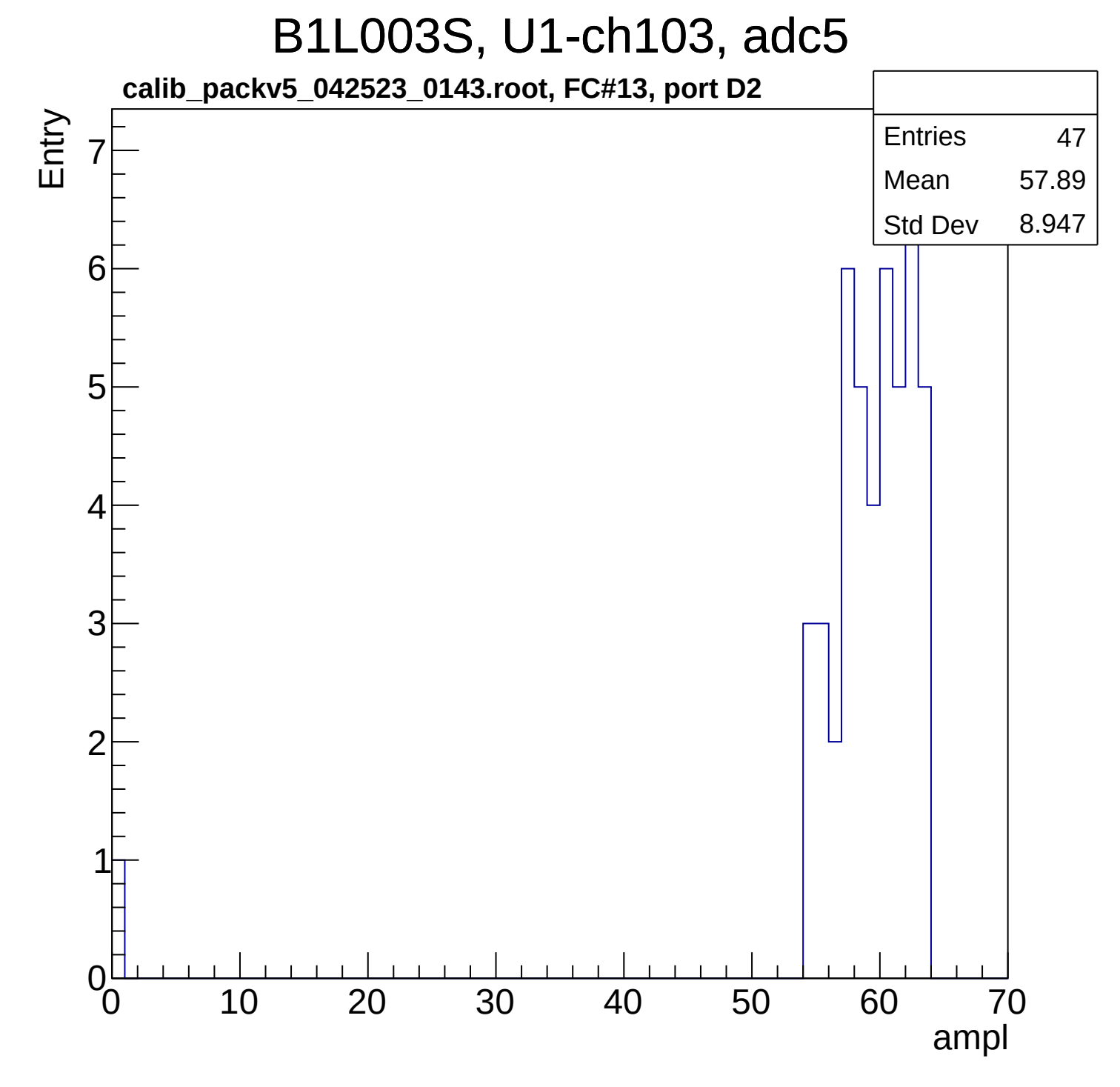
B1L003S, U1-ch103, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 57.89
Std Dev 8.947
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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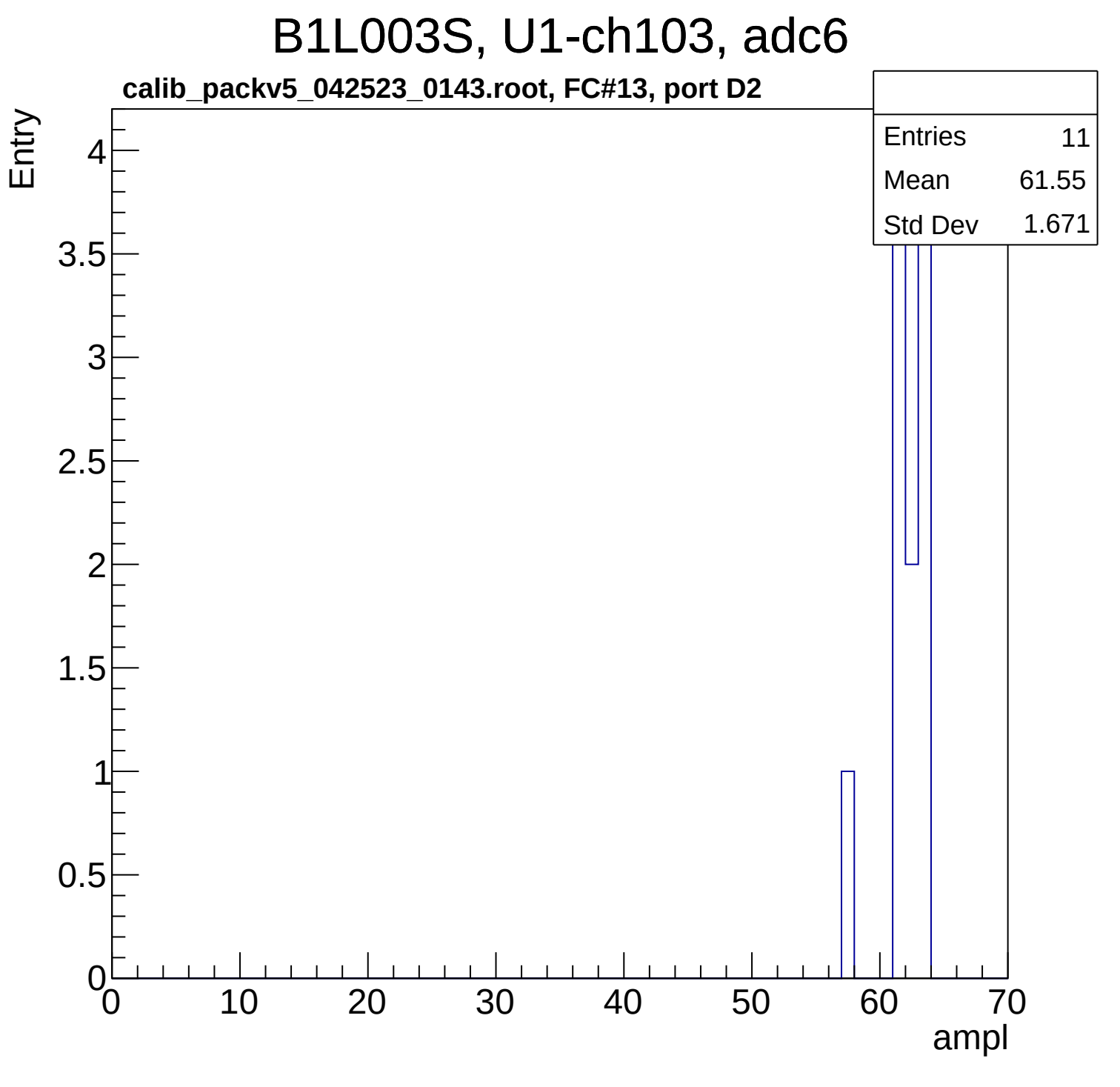
B1L003S, U1-ch103, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.55
Std Dev 1.671
Entry
ampl
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0.5
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1.5
2
2.5
3
3.5
4
0
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40
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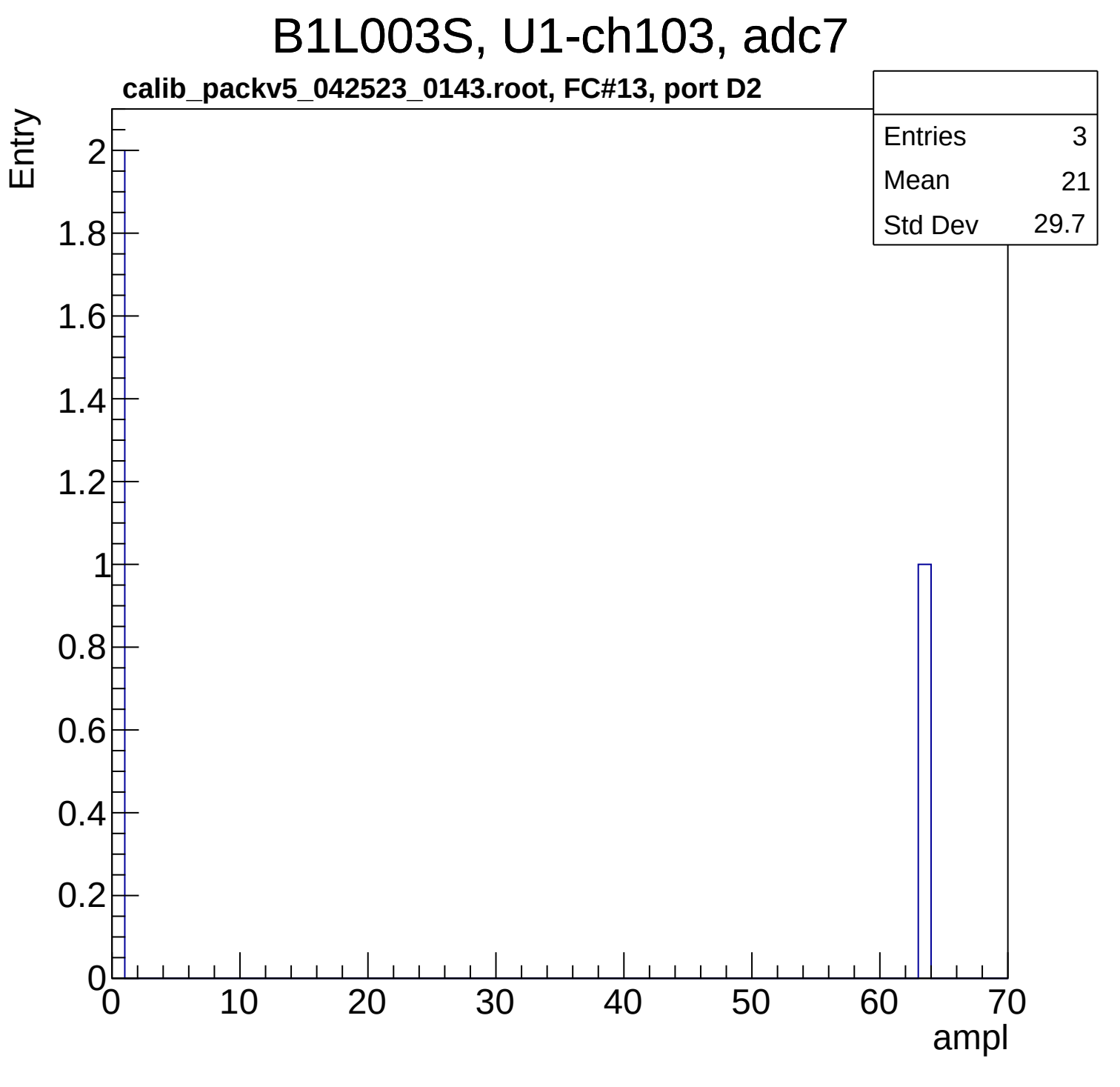
60
70


B1L003S, U1-ch103, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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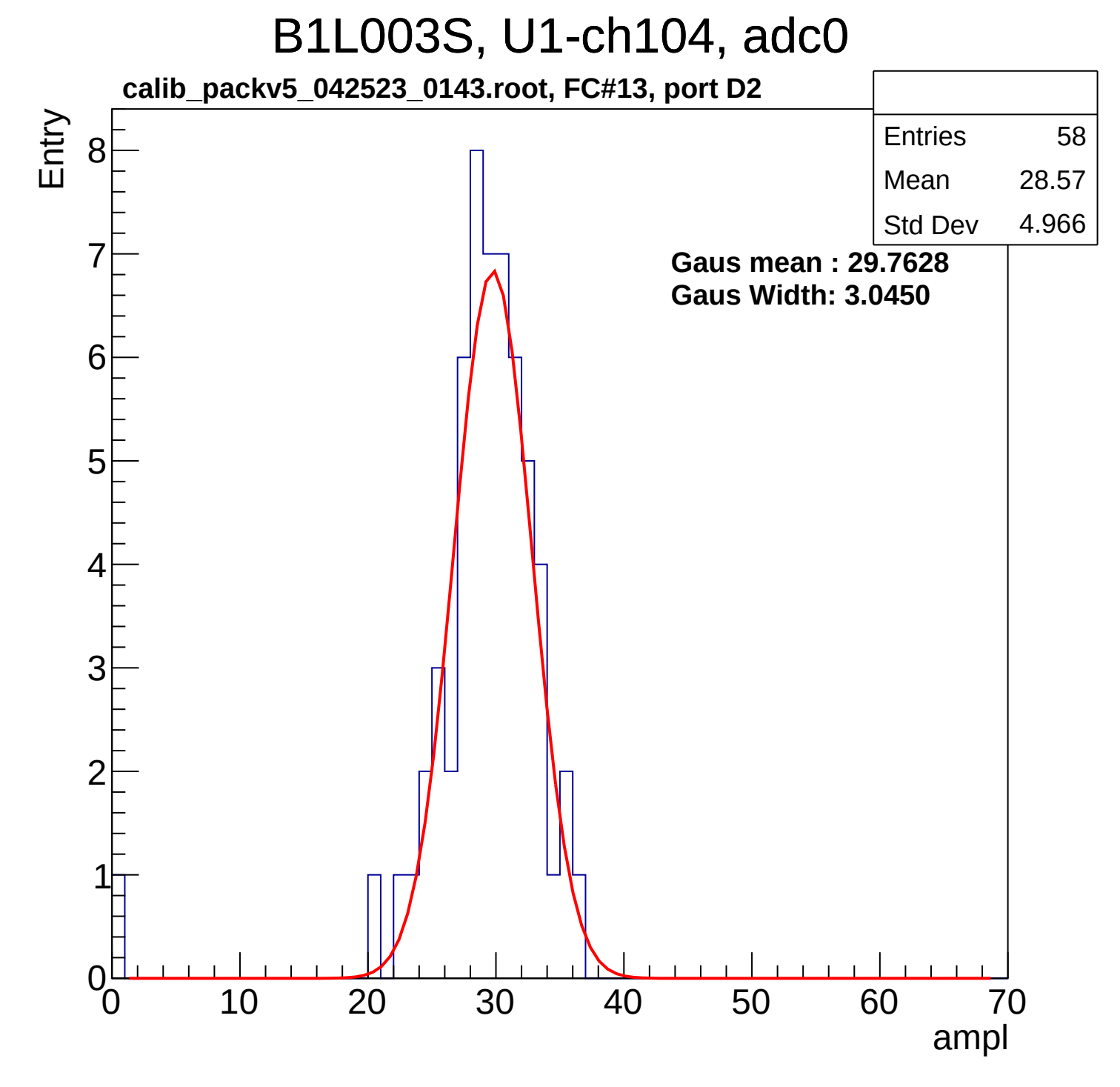
B1L003S, U1-ch104, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 28.57
Std Dev 4.966
Gaus mean : 29.7628
Gaus Width: 3.0450
Entry
ampl
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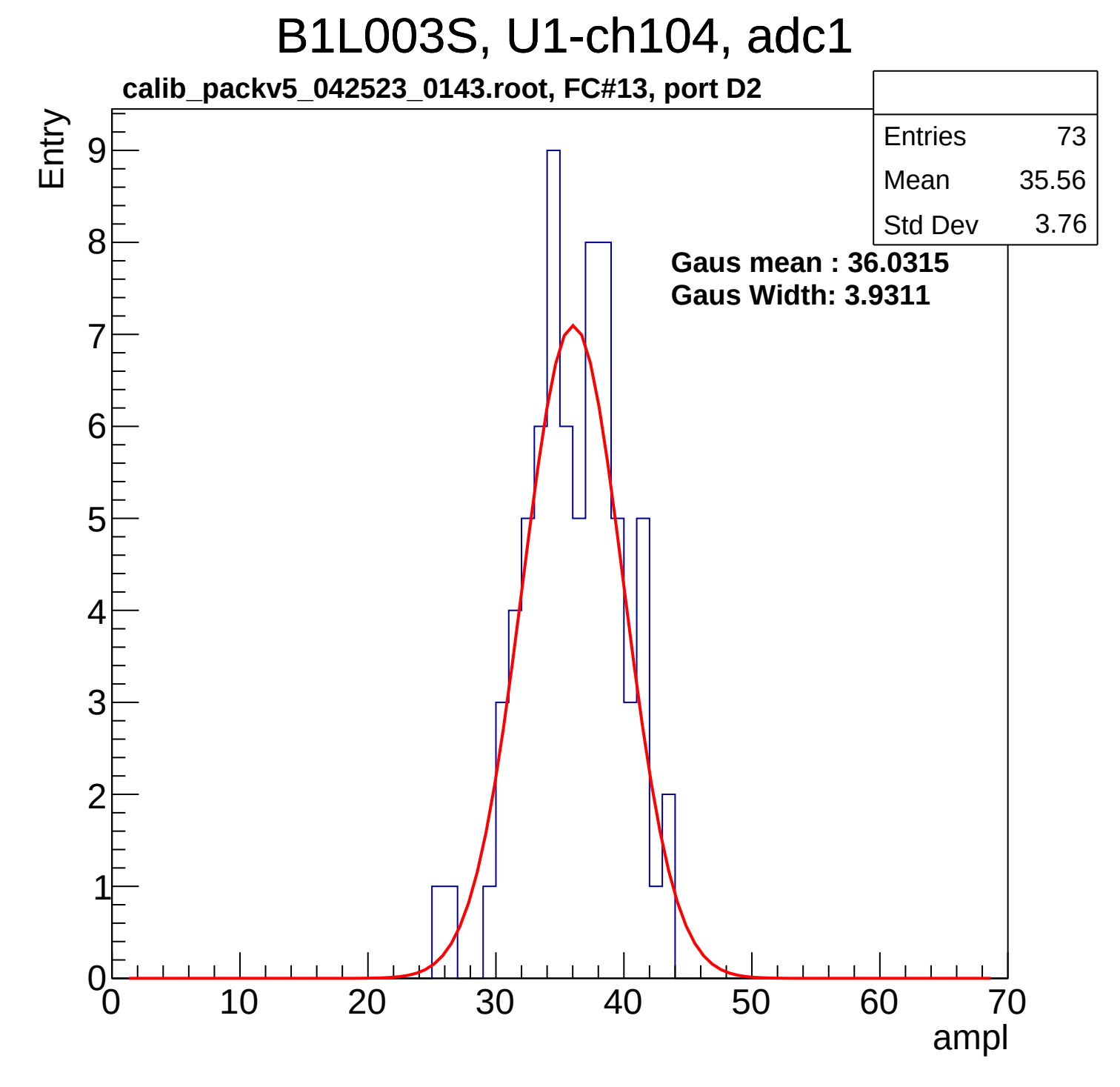
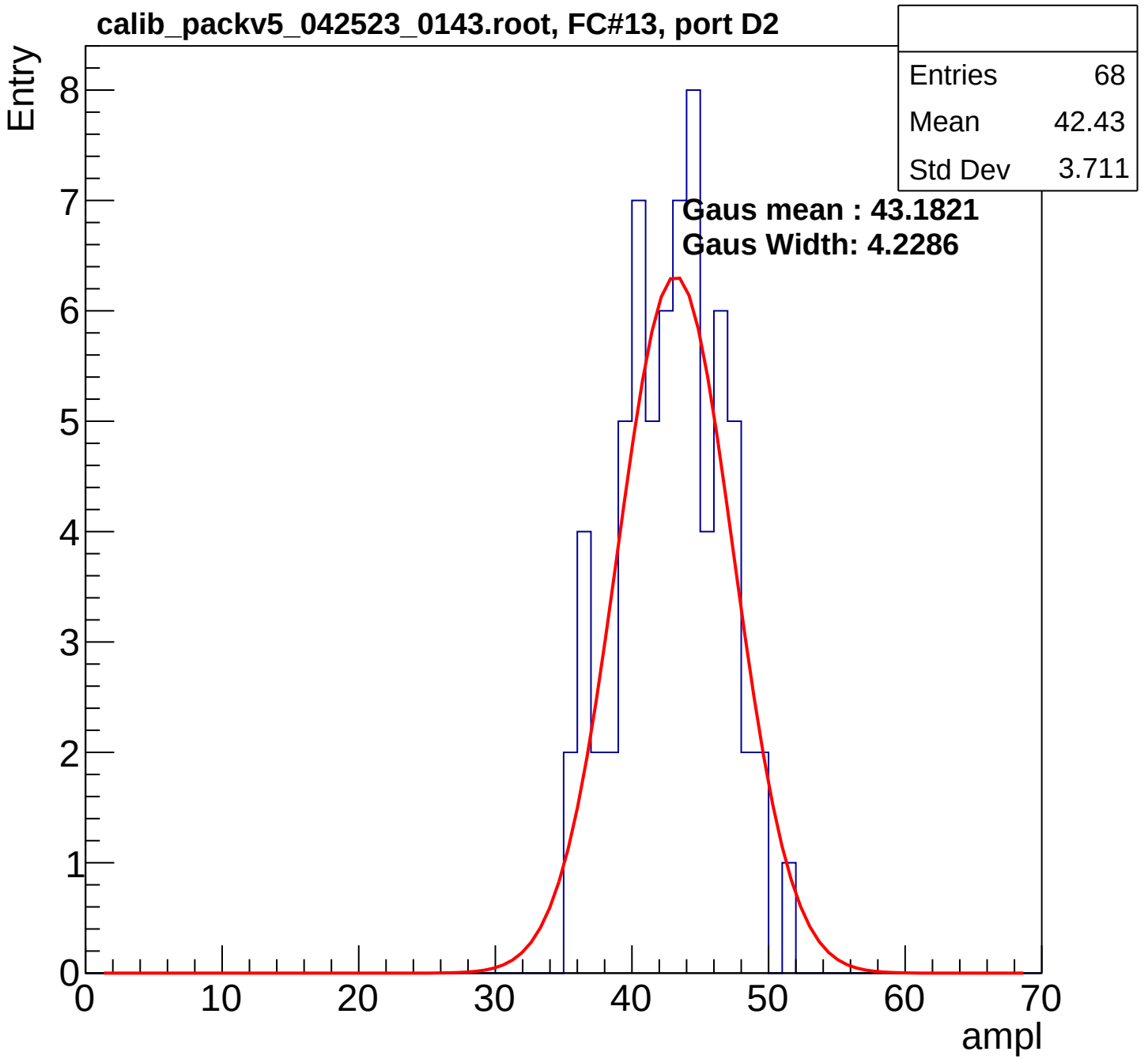
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch104, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 35.56
Std Dev 3.76
Gaus mean : 36.0315
Gaus Width: 3.9311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch104, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 42.43
Std Dev 3.711
Gaus mean : 43.1821
Gaus Width: 4.2286
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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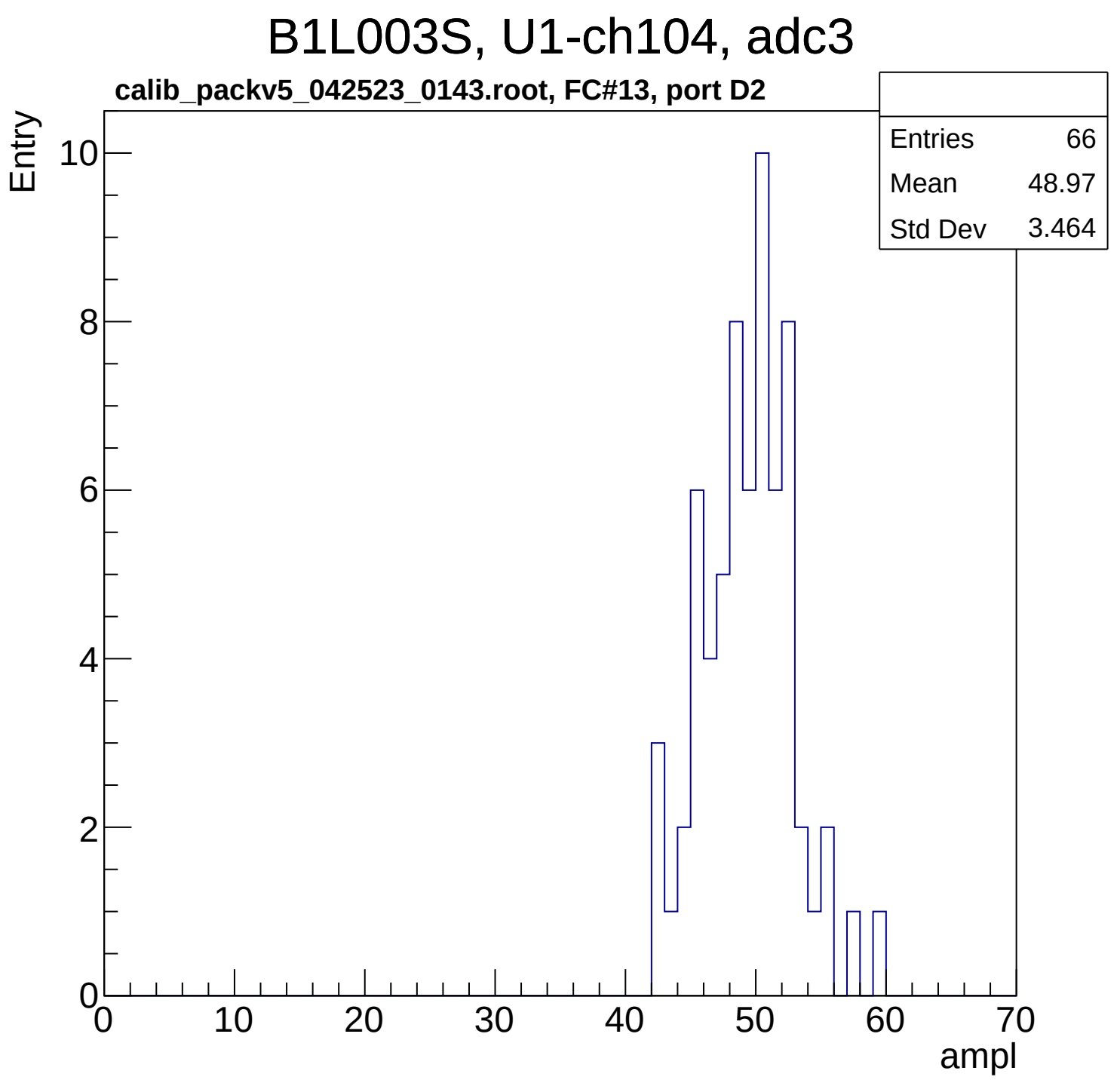
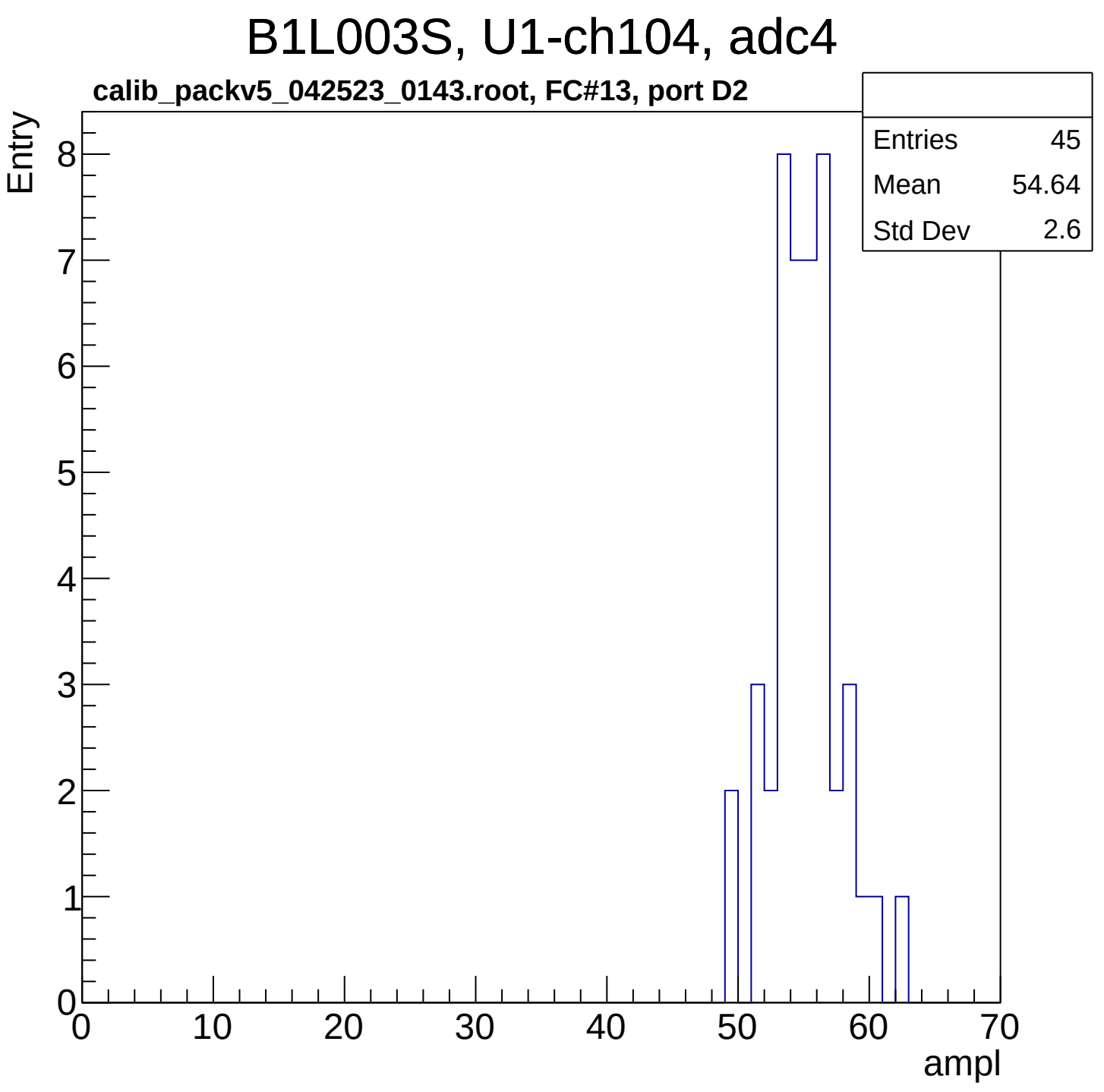
60
70


B1L003S, U1-ch104, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 48.97
Std Dev 3.464
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch104, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 54.64
Std Dev 2.6
Entry
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2
3
4
5
6
7
8
0
10
20
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50
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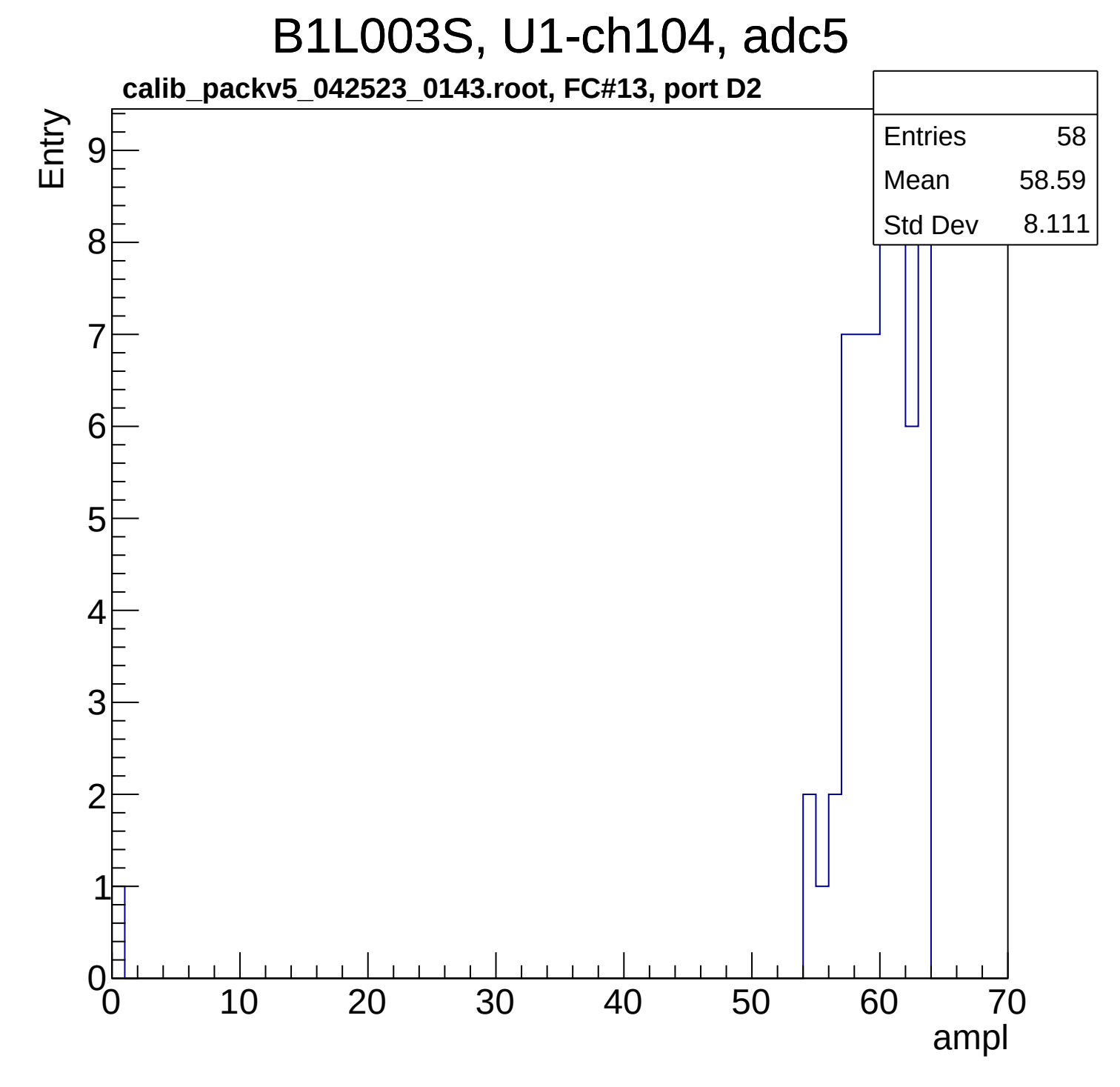
70
ampl


B1L003S, U1-ch104, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 58.59
Std Dev 8.111
Entry
ampl
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4
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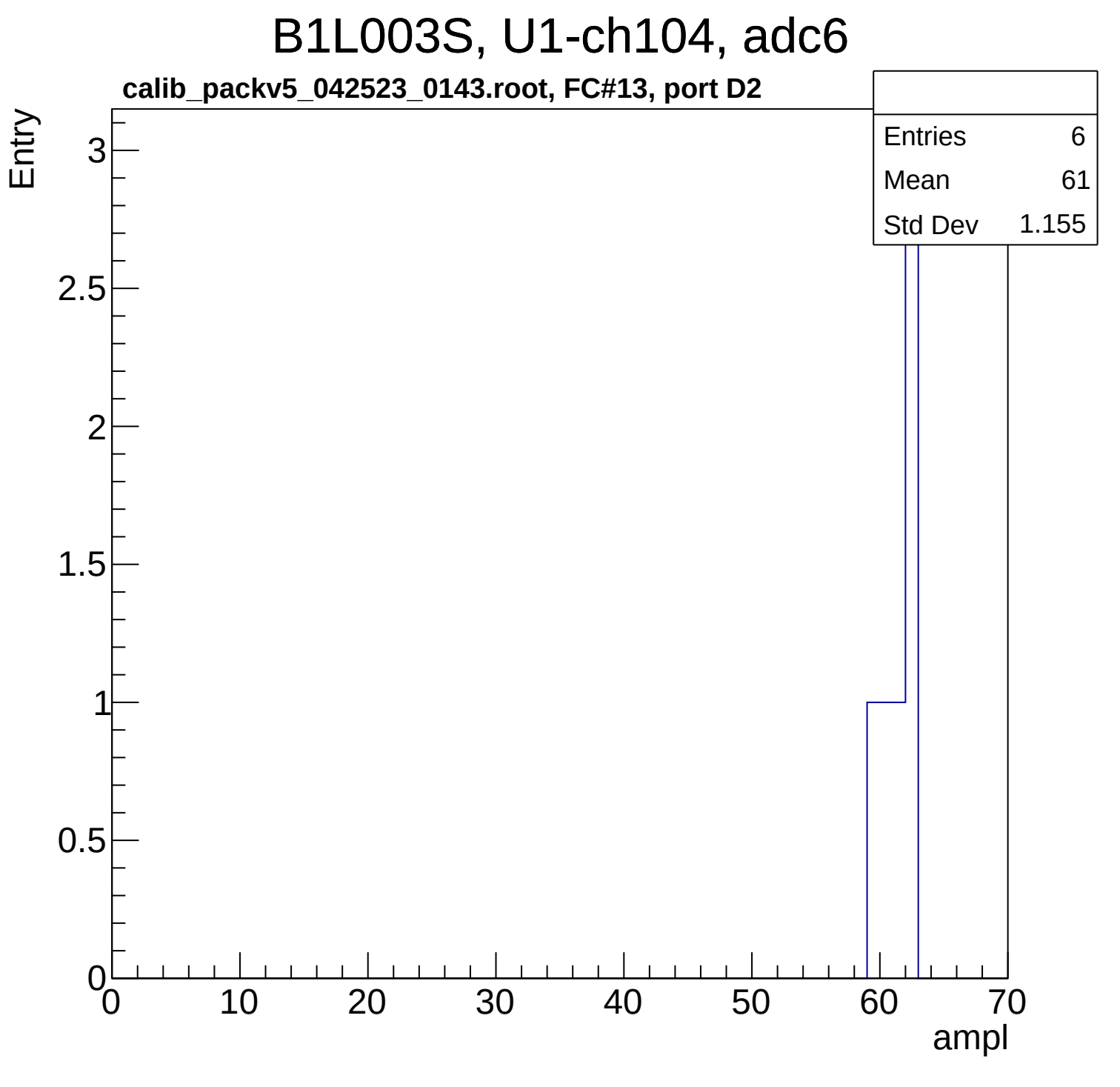
B1L003S, U1-ch104, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 6
Mean 61
Std Dev 1.155
Entry
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1
1.5
2
2.5
3
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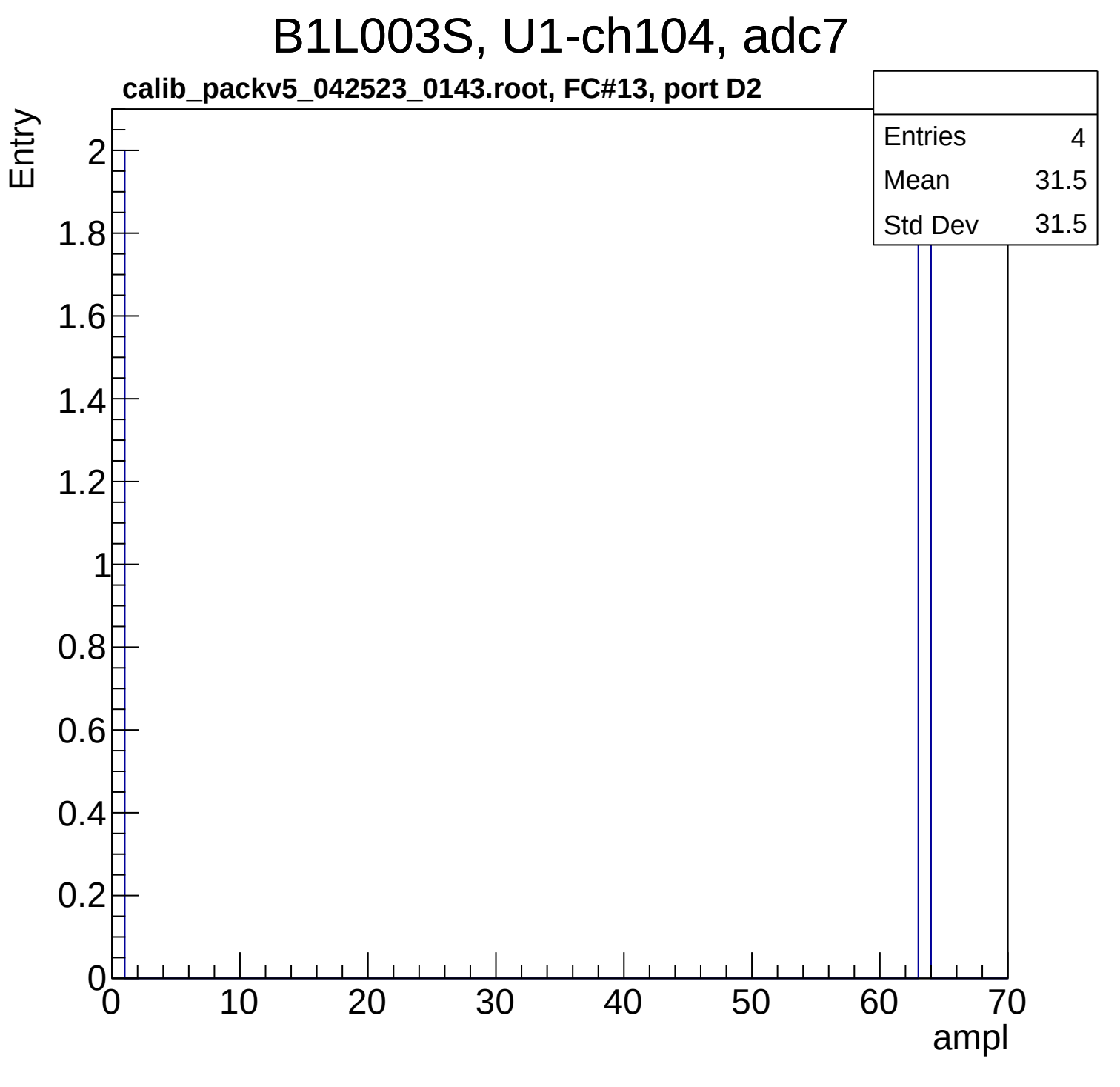
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch104, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 31.5
Std Dev 31.5
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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60
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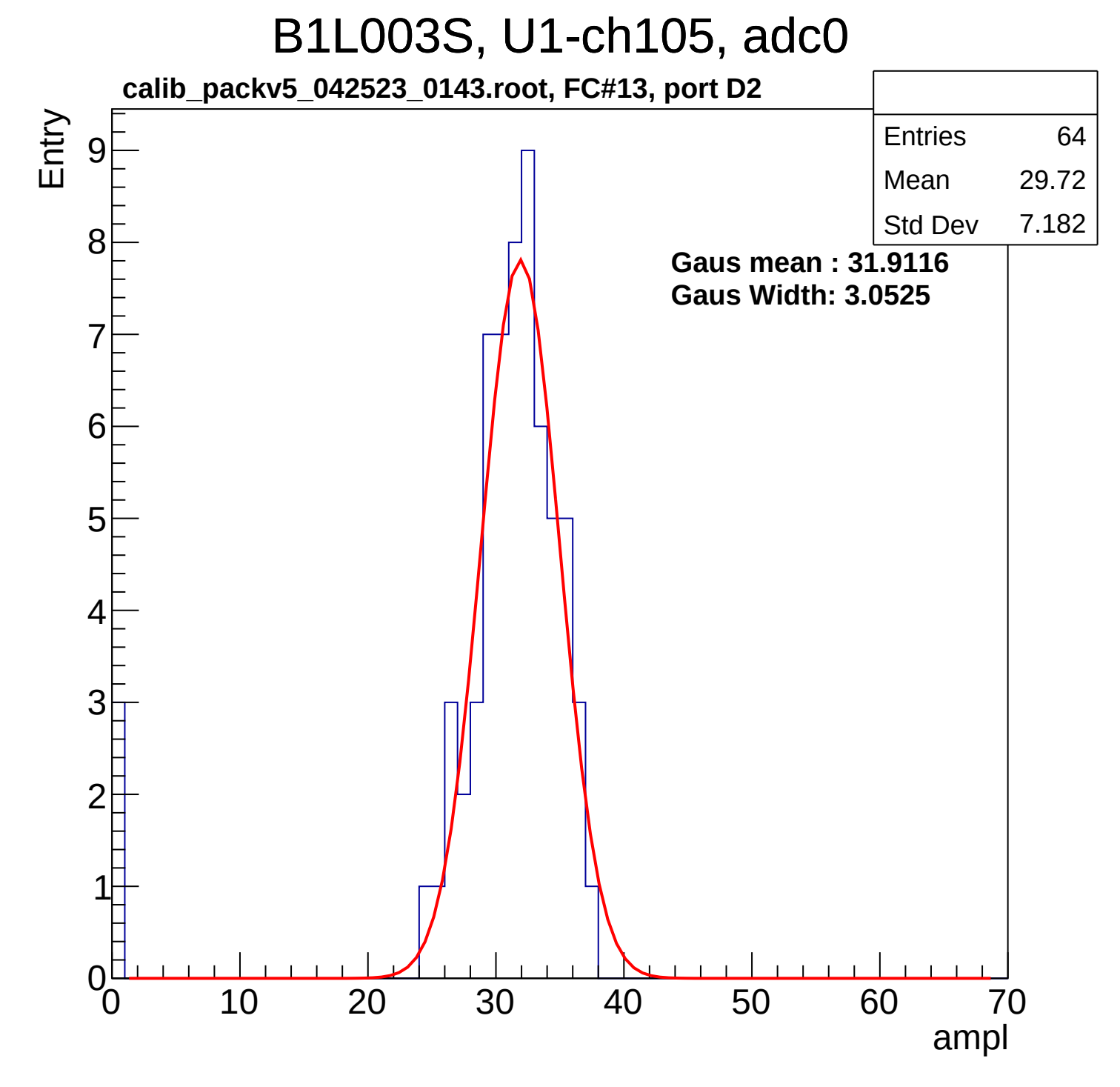
B1L003S, U1-ch105, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.72
Std Dev 7.182
Gaus mean : 31.9116
Gaus Width: 3.0525
Entry
ampl
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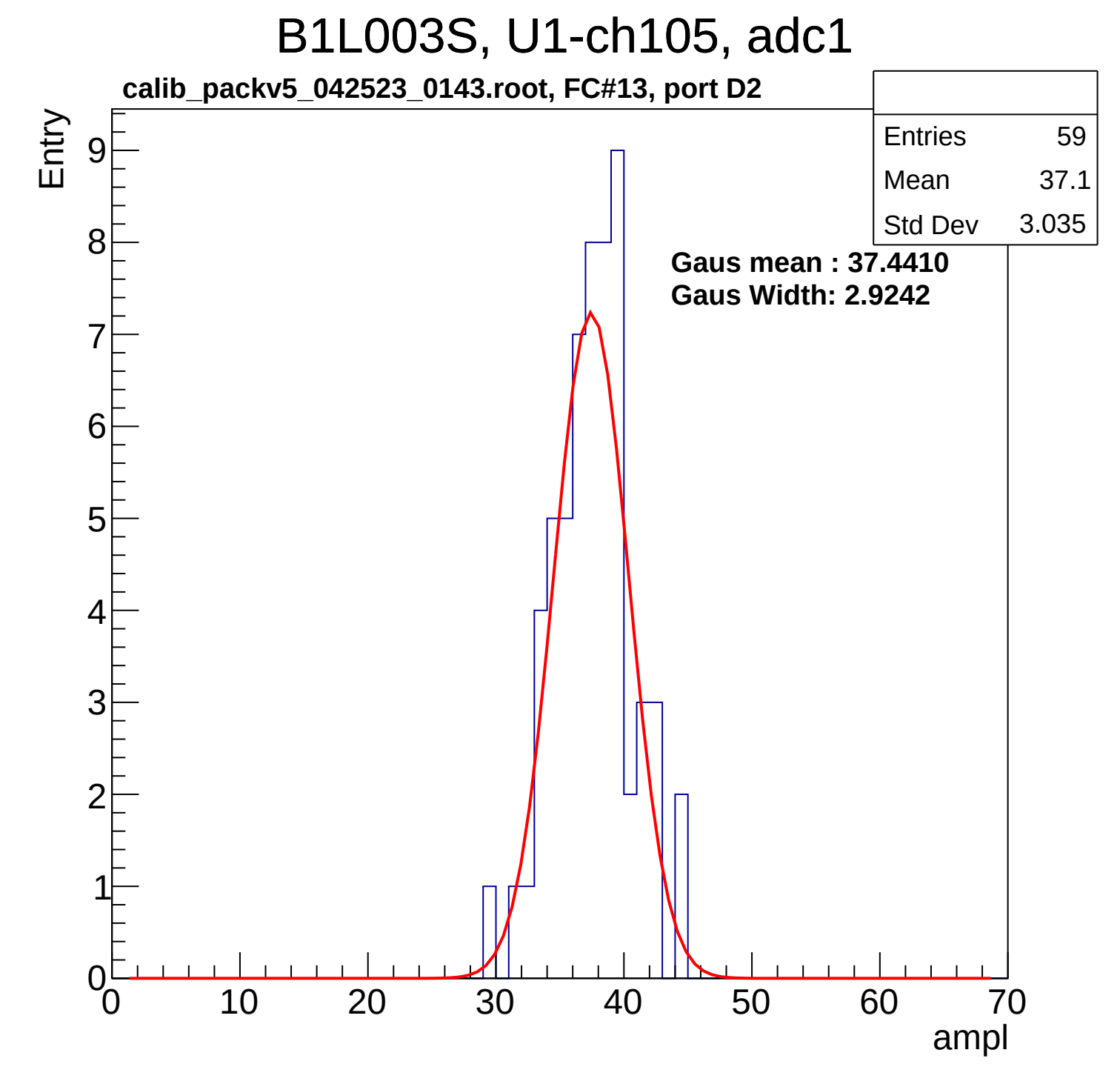
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch105, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 37.1
Std Dev 3.035
Gaus mean : 37.4410
Gaus Width: 2.9242
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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30
40
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60
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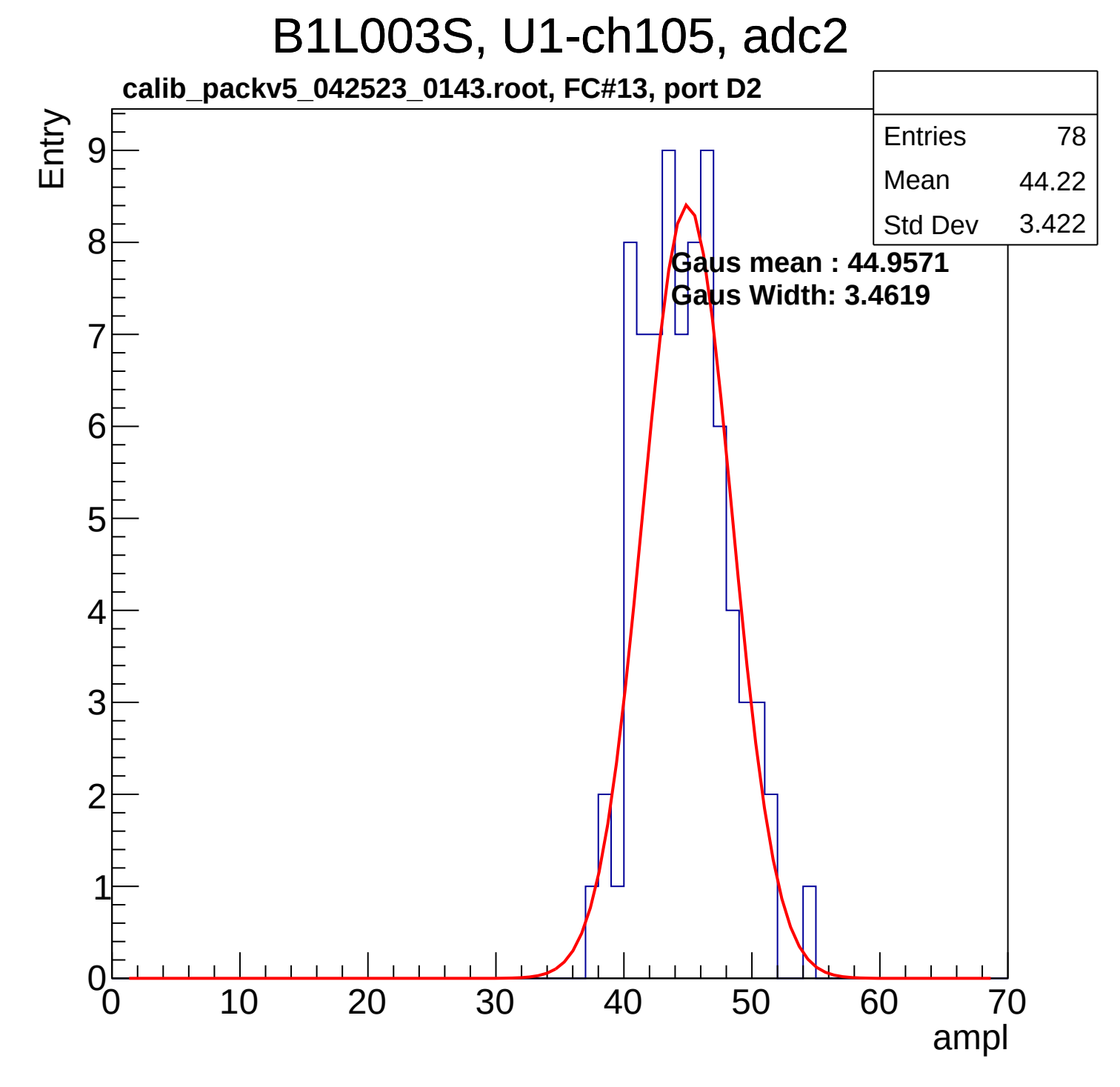
B1L003S, U1-ch105, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 78
Mean 44.22
Std Dev 3.422
Gaus mean : 44.9571
Gaus Width: 3.4619
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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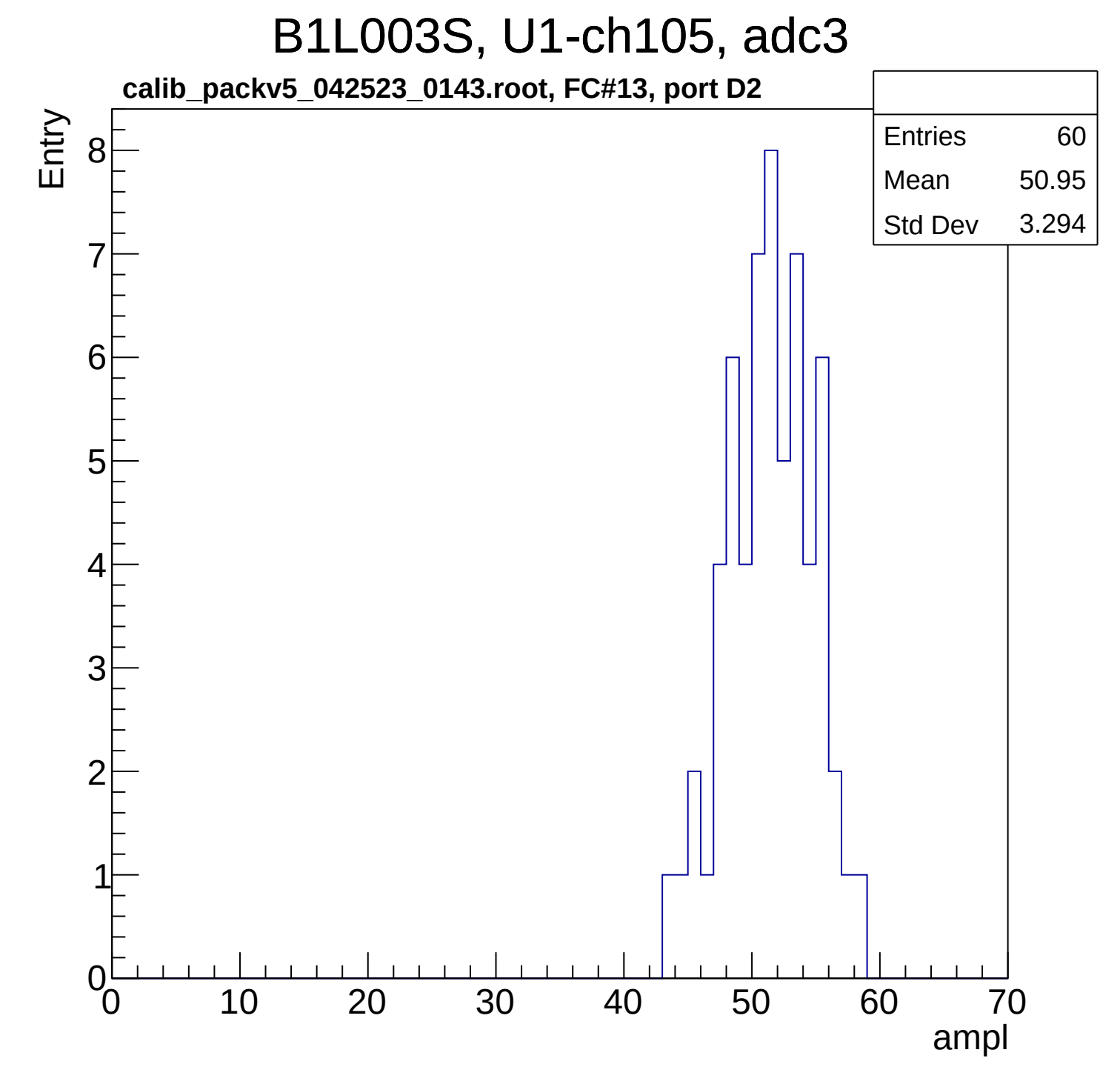
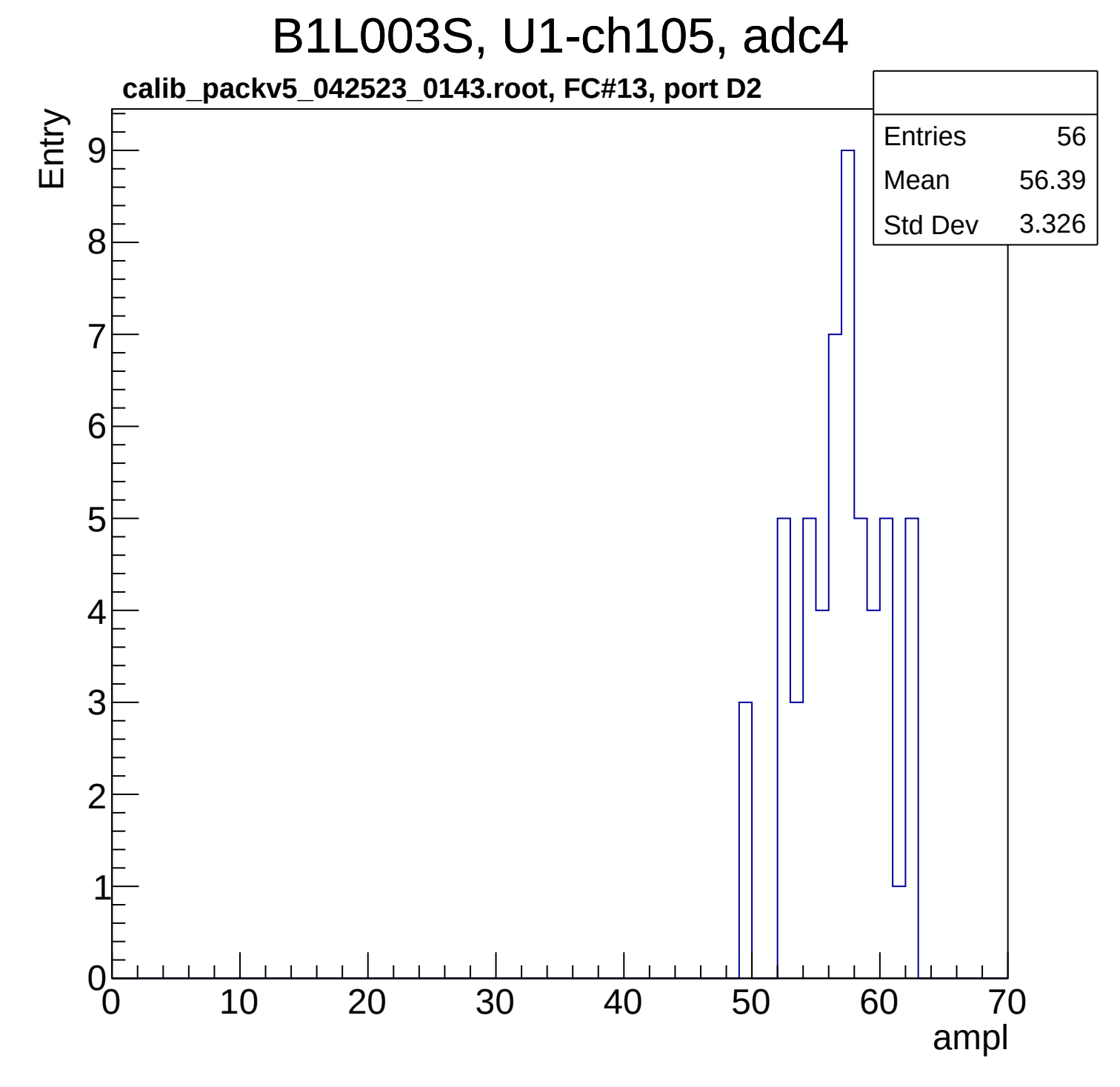
40
50
60
70


B1L003S, U1-ch105, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 50.95
Std Dev 3.294
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch105, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 56.39
Std Dev 3.326
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
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60
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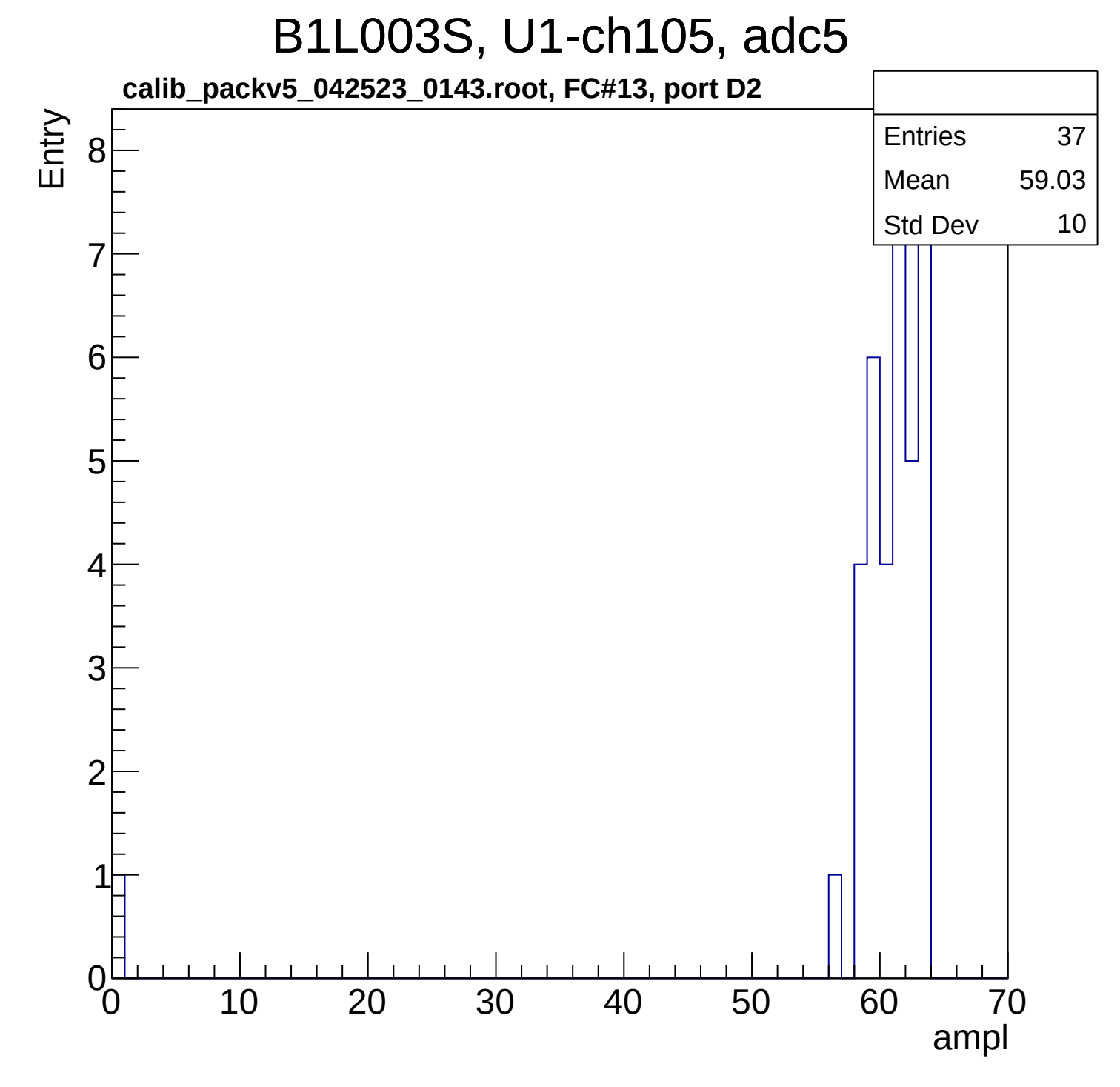
B1L003S, U1-ch105, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 37
Mean 59.03
Std Dev 10
Entry
ampl
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3
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5
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0
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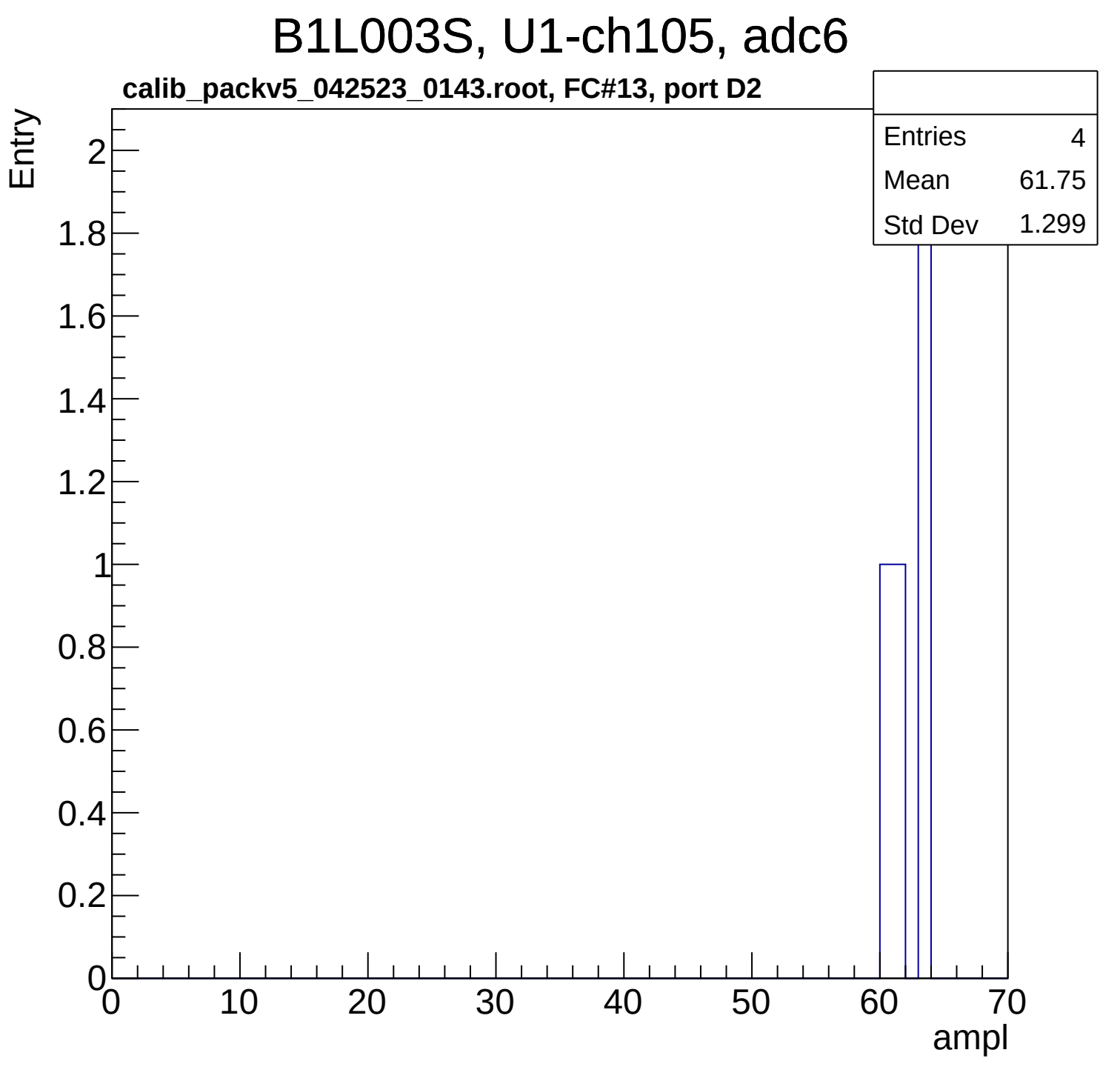
B1L003S, U1-ch105, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 61.75
Std Dev 1.299
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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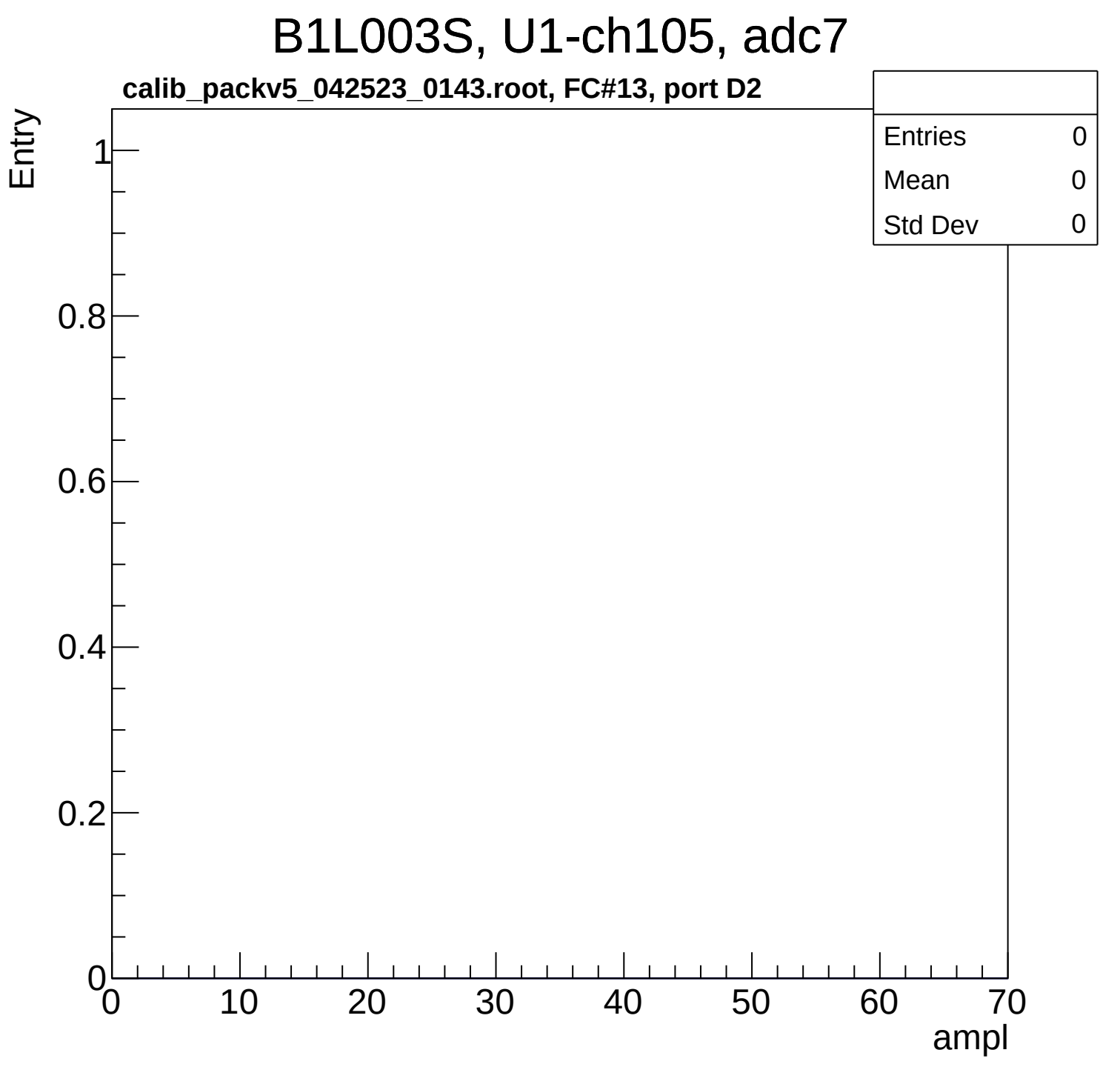
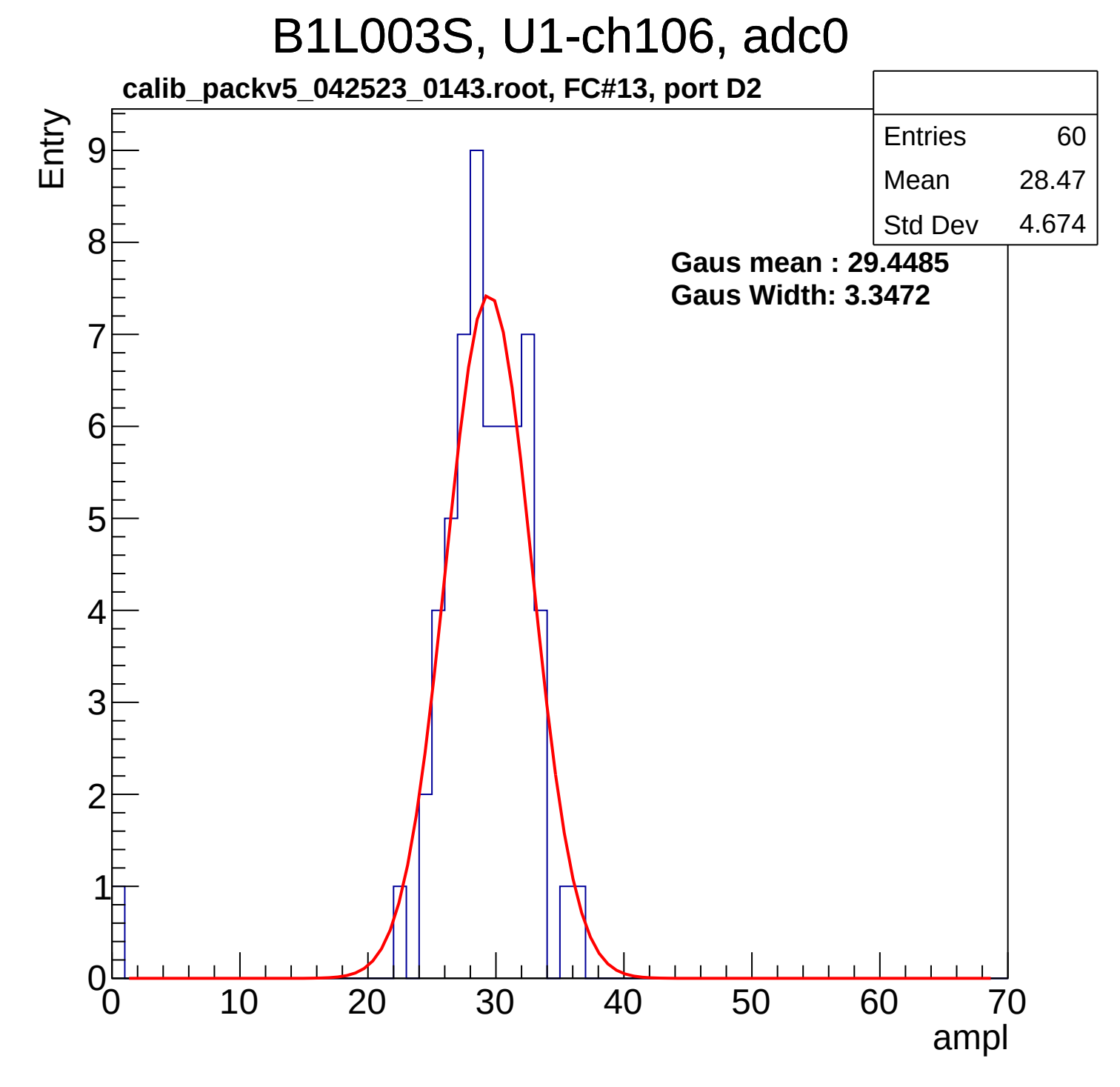

B1L003S, U1-ch105, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch106, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 28.47
Std Dev 4.674
Gaus mean : 29.4485
Gaus Width: 3.3472
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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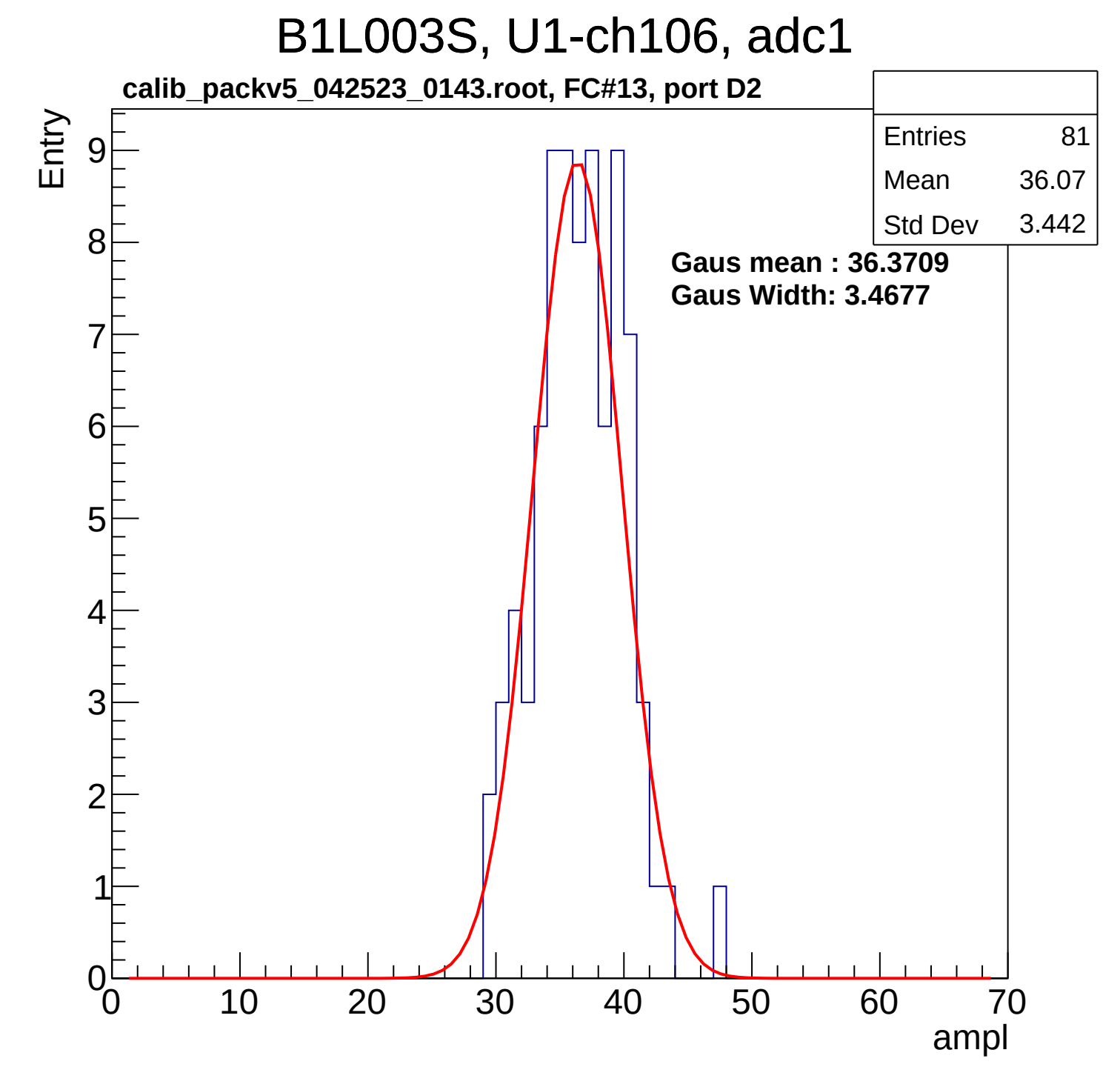
B1L003S, U1-ch106, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 36.07
Std Dev 3.442
Gaus mean : 36.3709
Gaus Width: 3.4677
Entry
ampl
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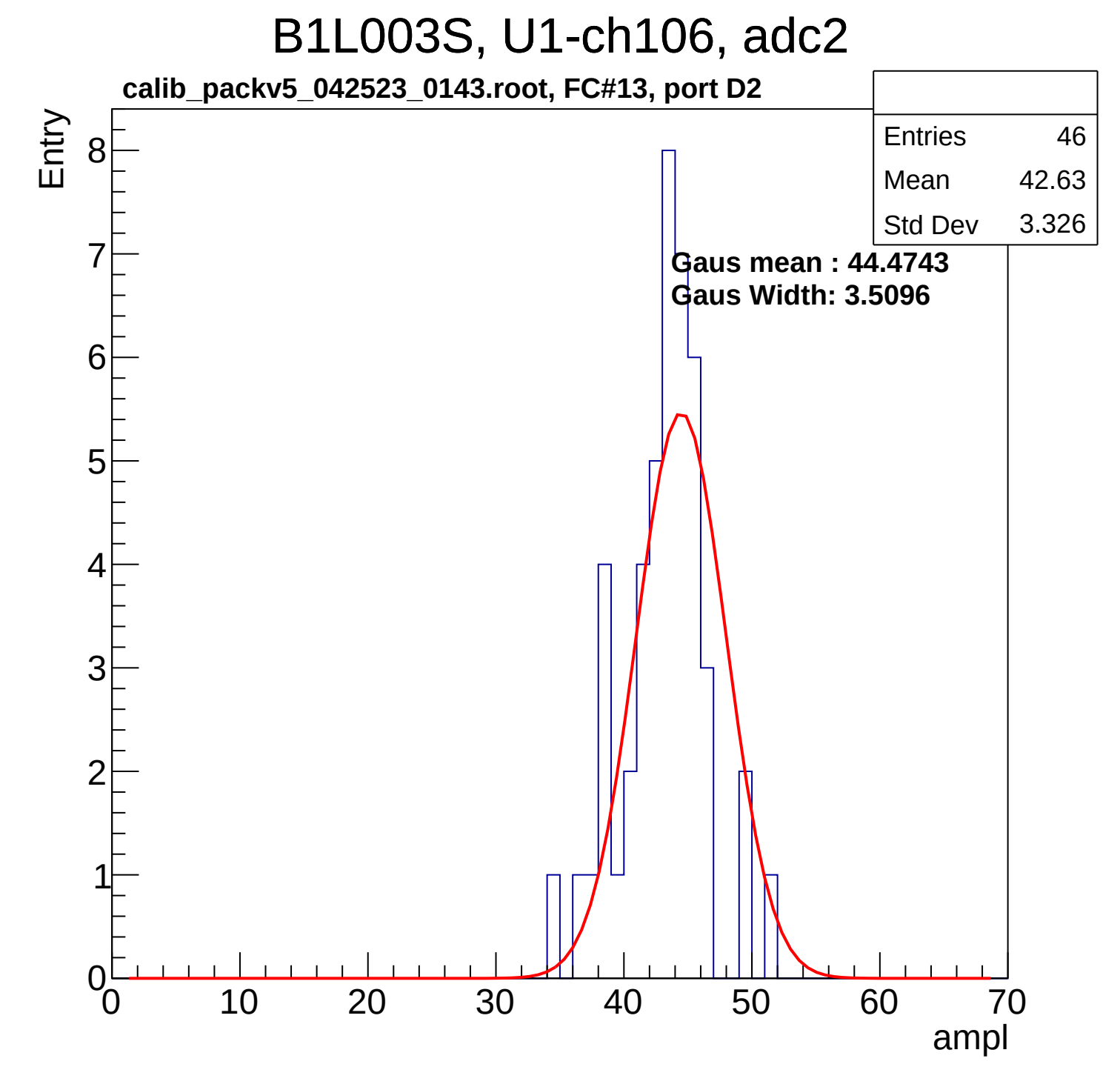
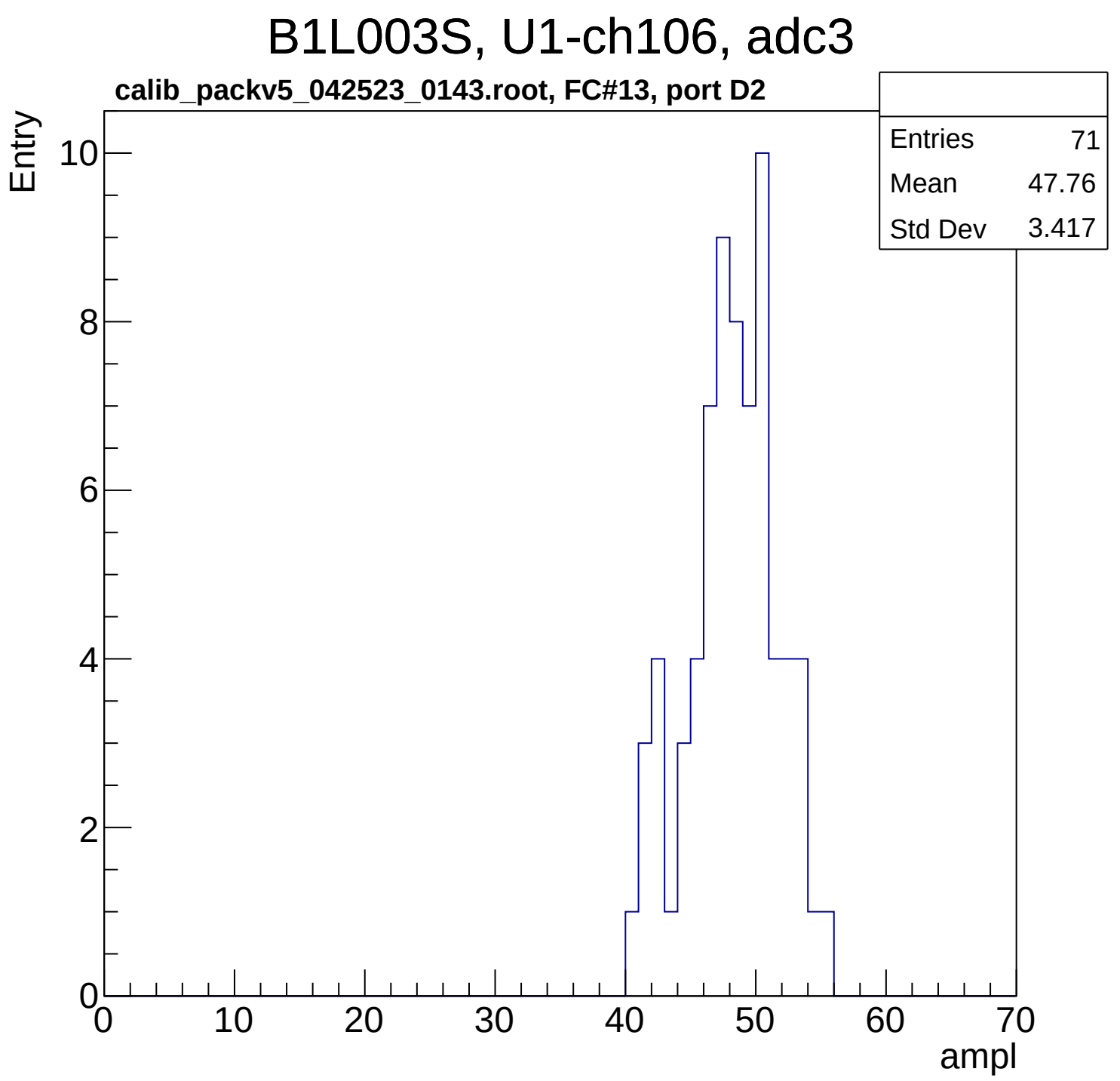
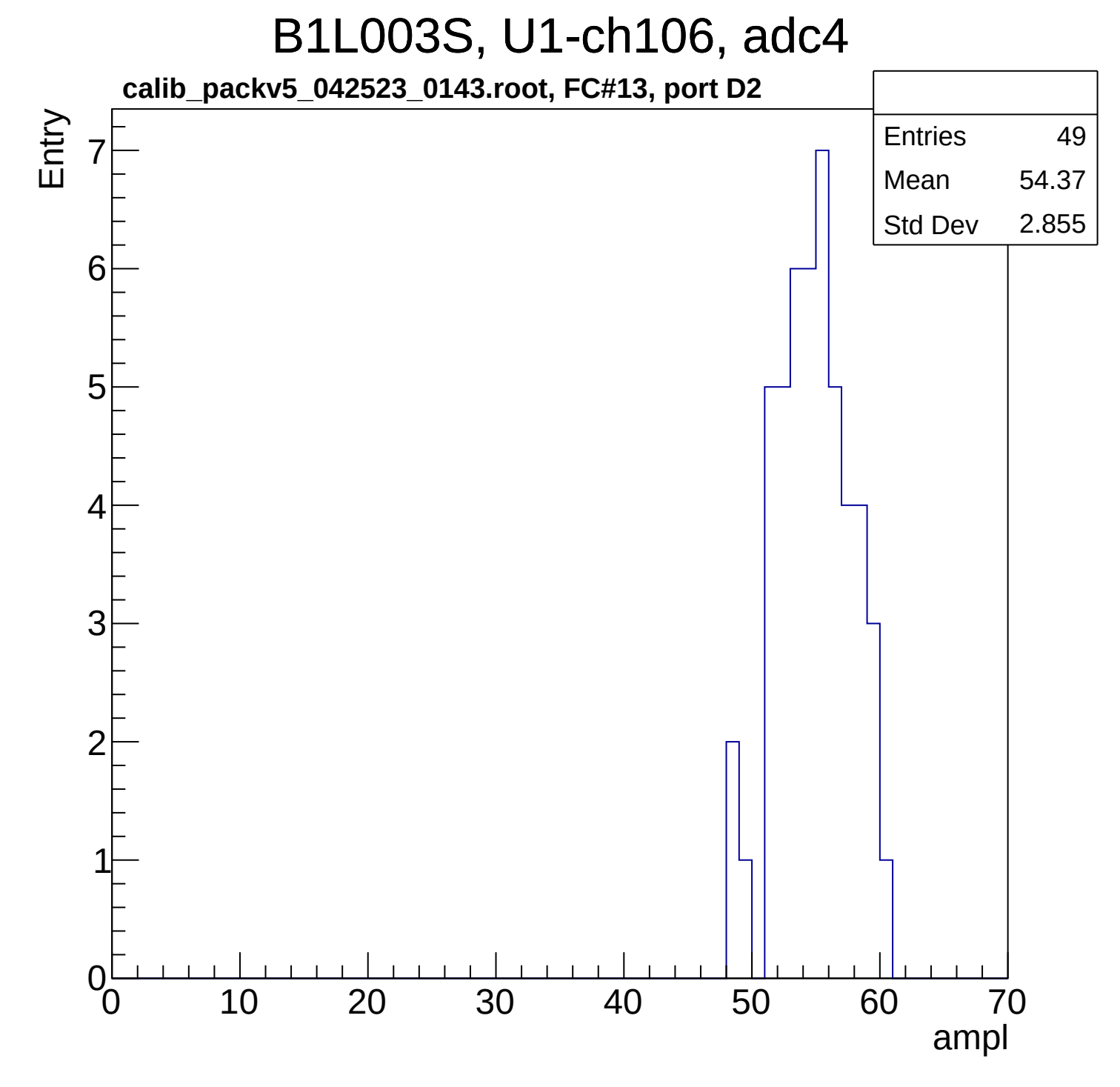
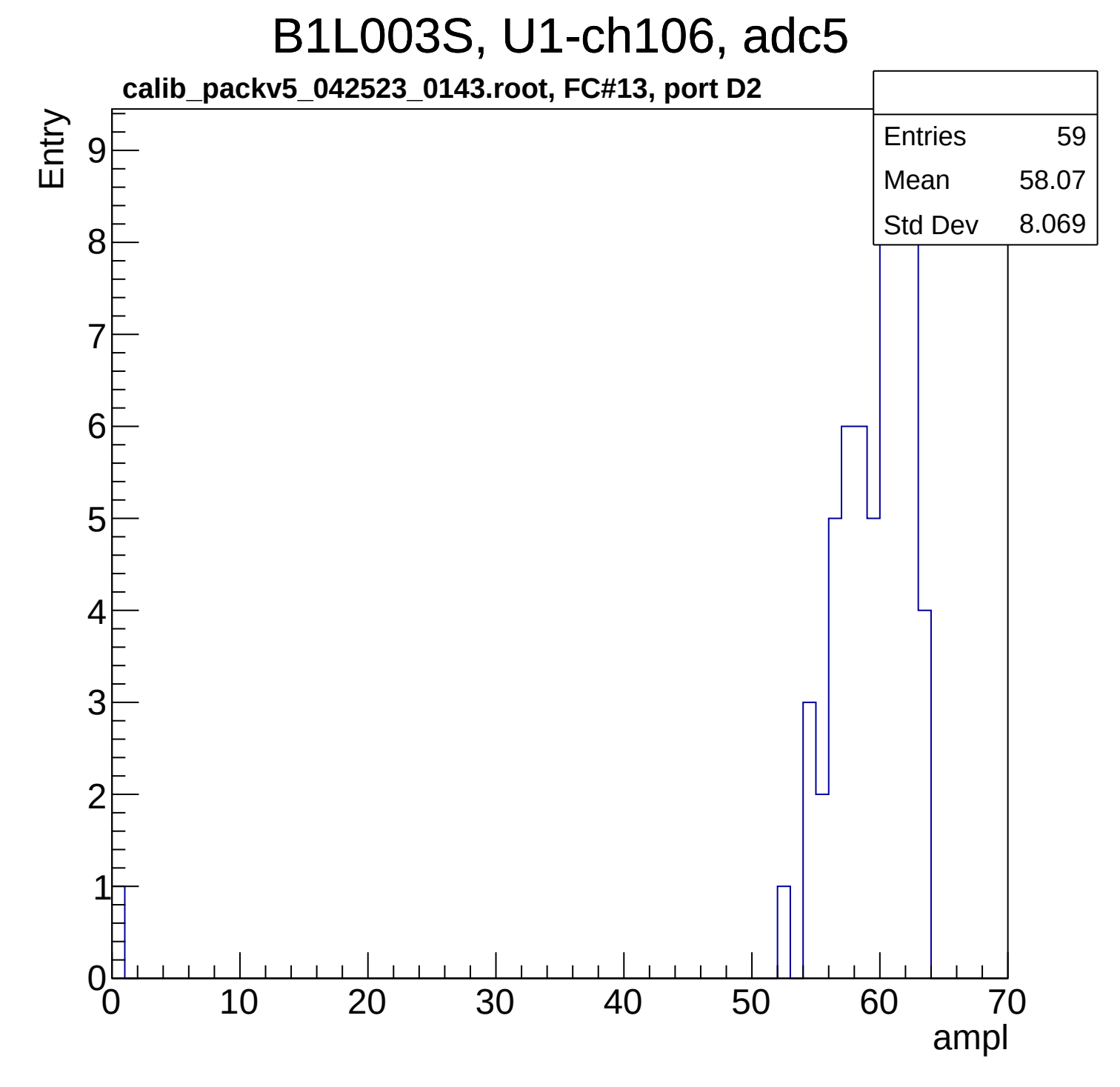
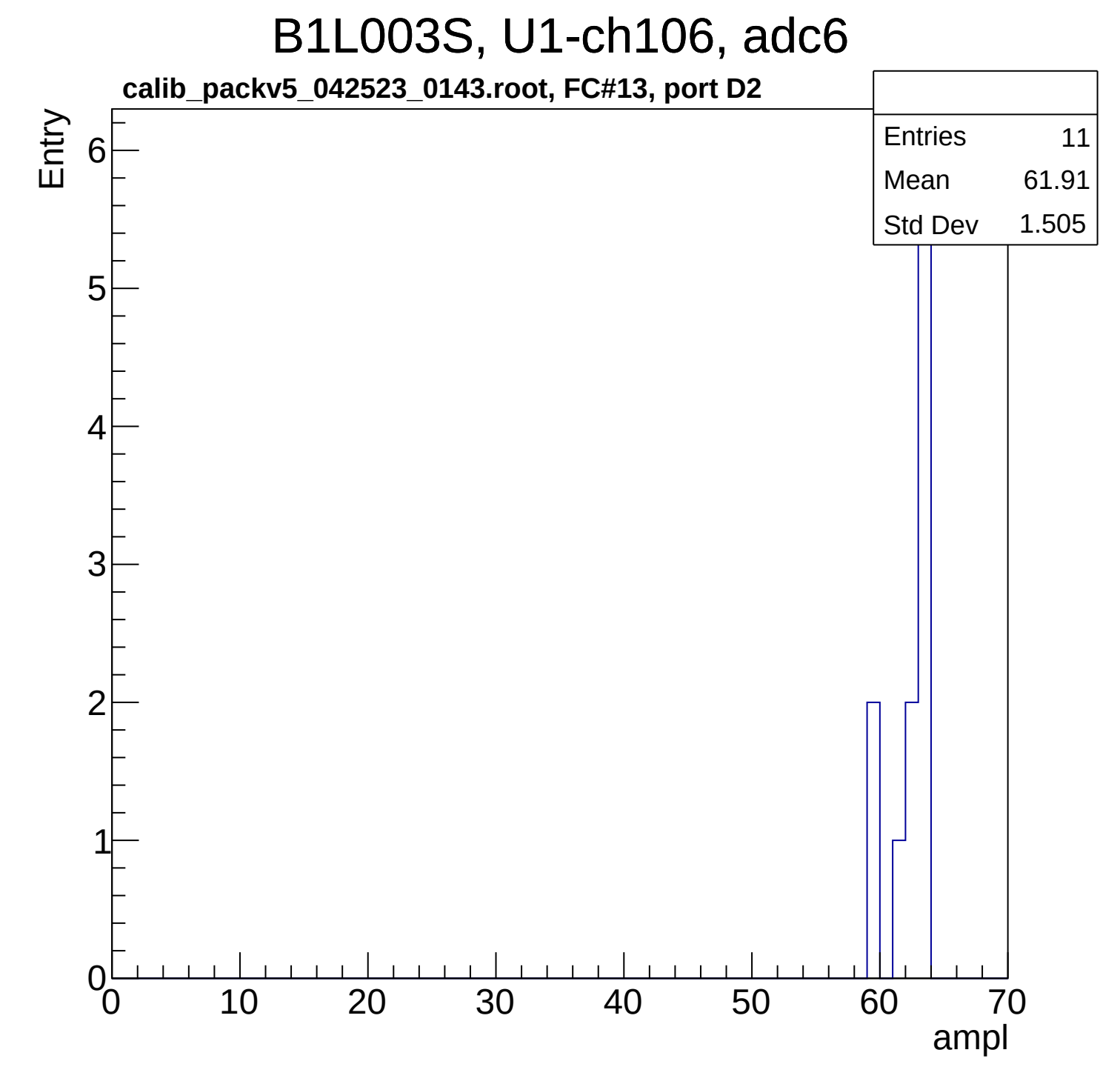
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

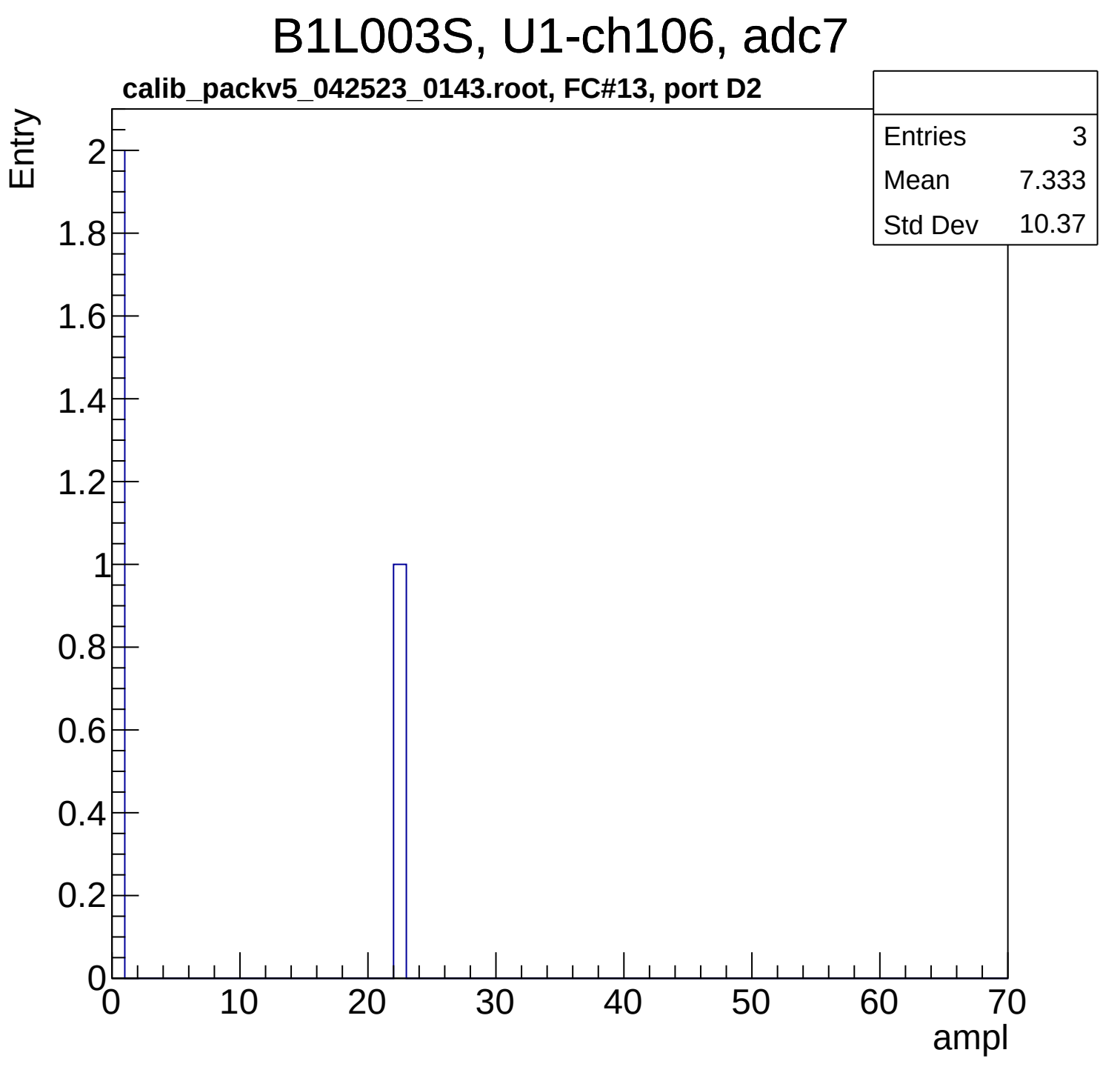

B1L003S, U1-ch106, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 42.63
Std Dev 3.326
Gaus mean : 44.4743
Gaus Width: 3.5096
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

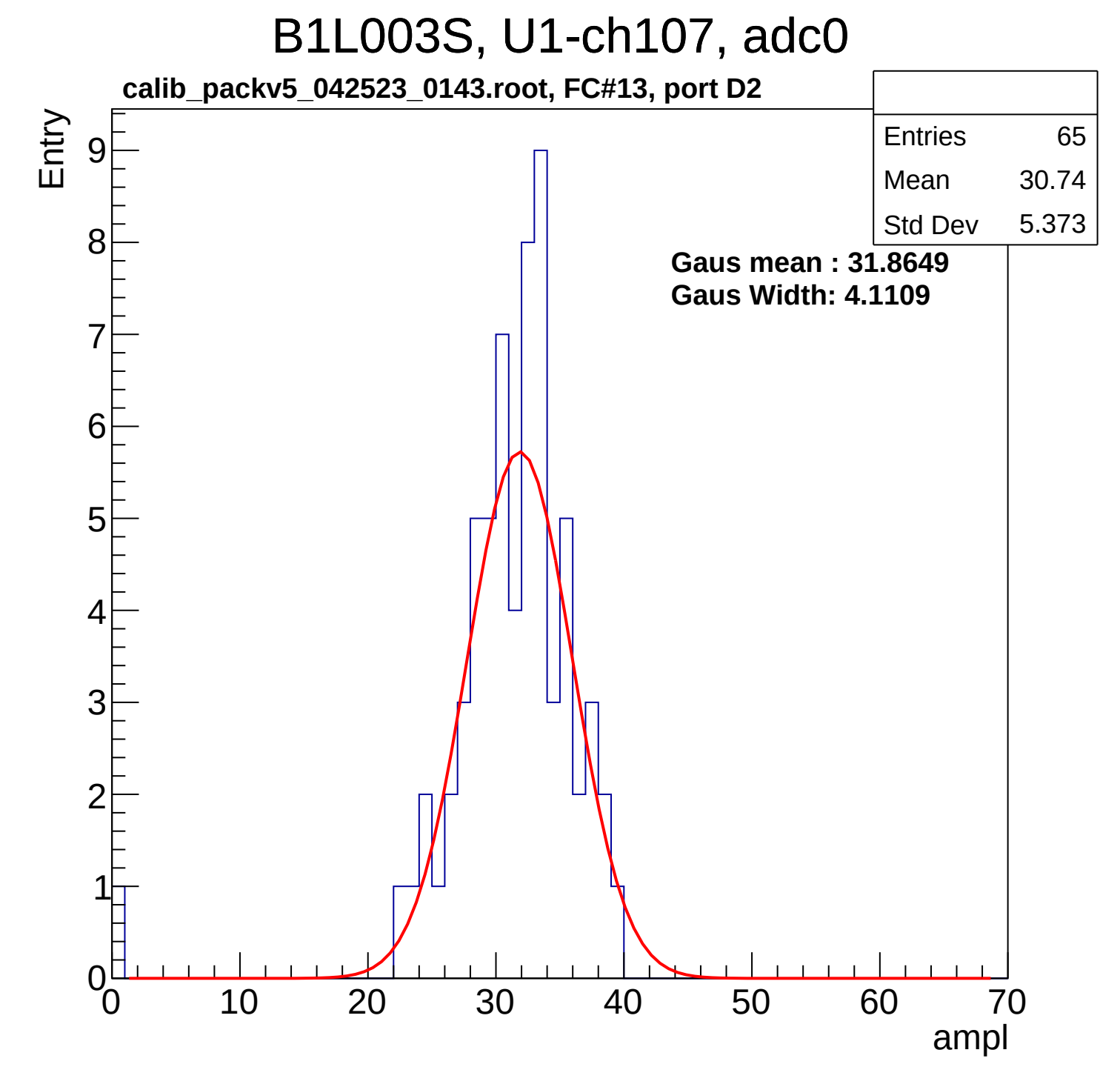

B1L003S, U1-ch106, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.76
Std Dev 3.417
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

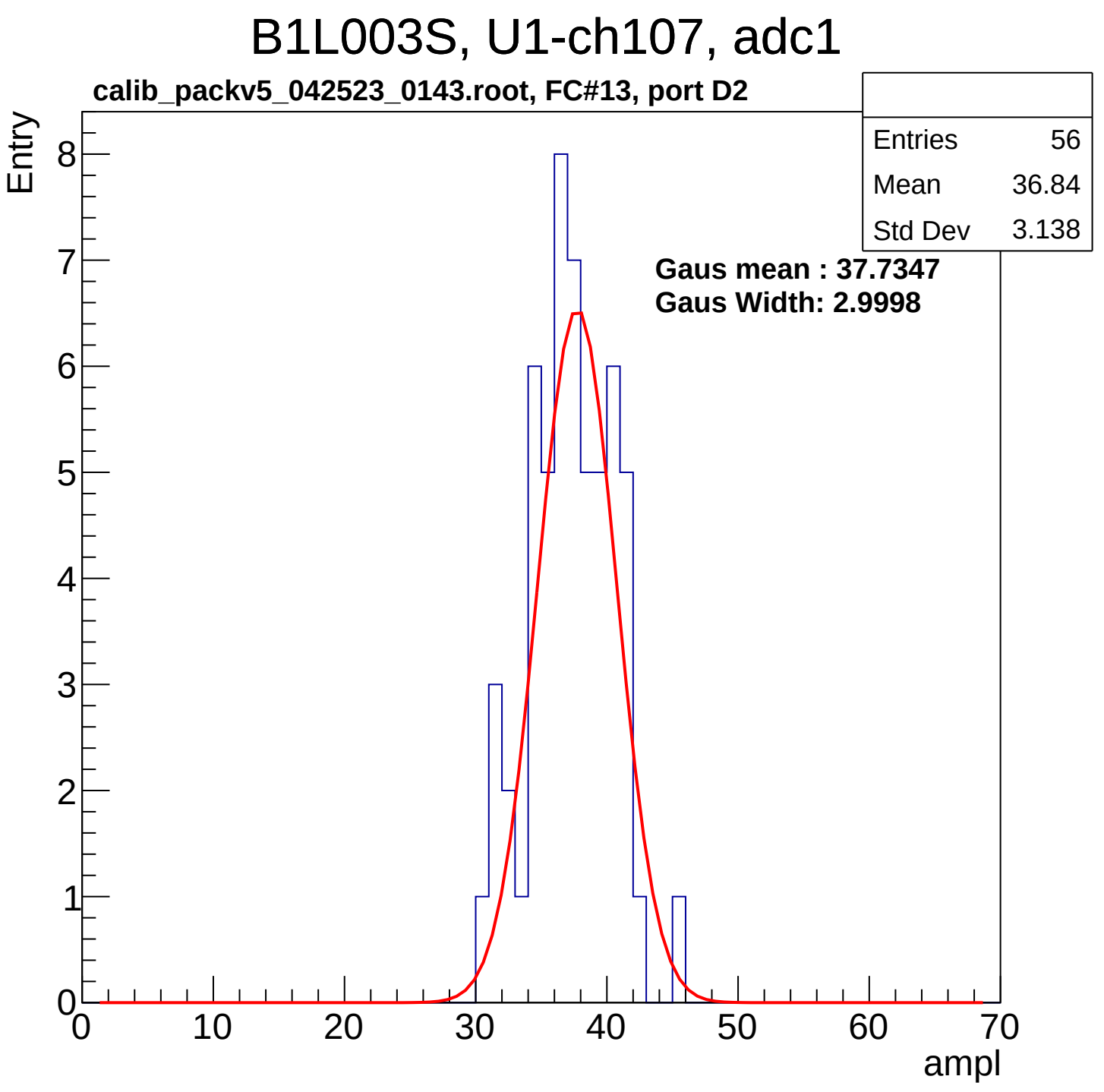

B1L003S, U1-ch106, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 54.37
Std Dev 2.855
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

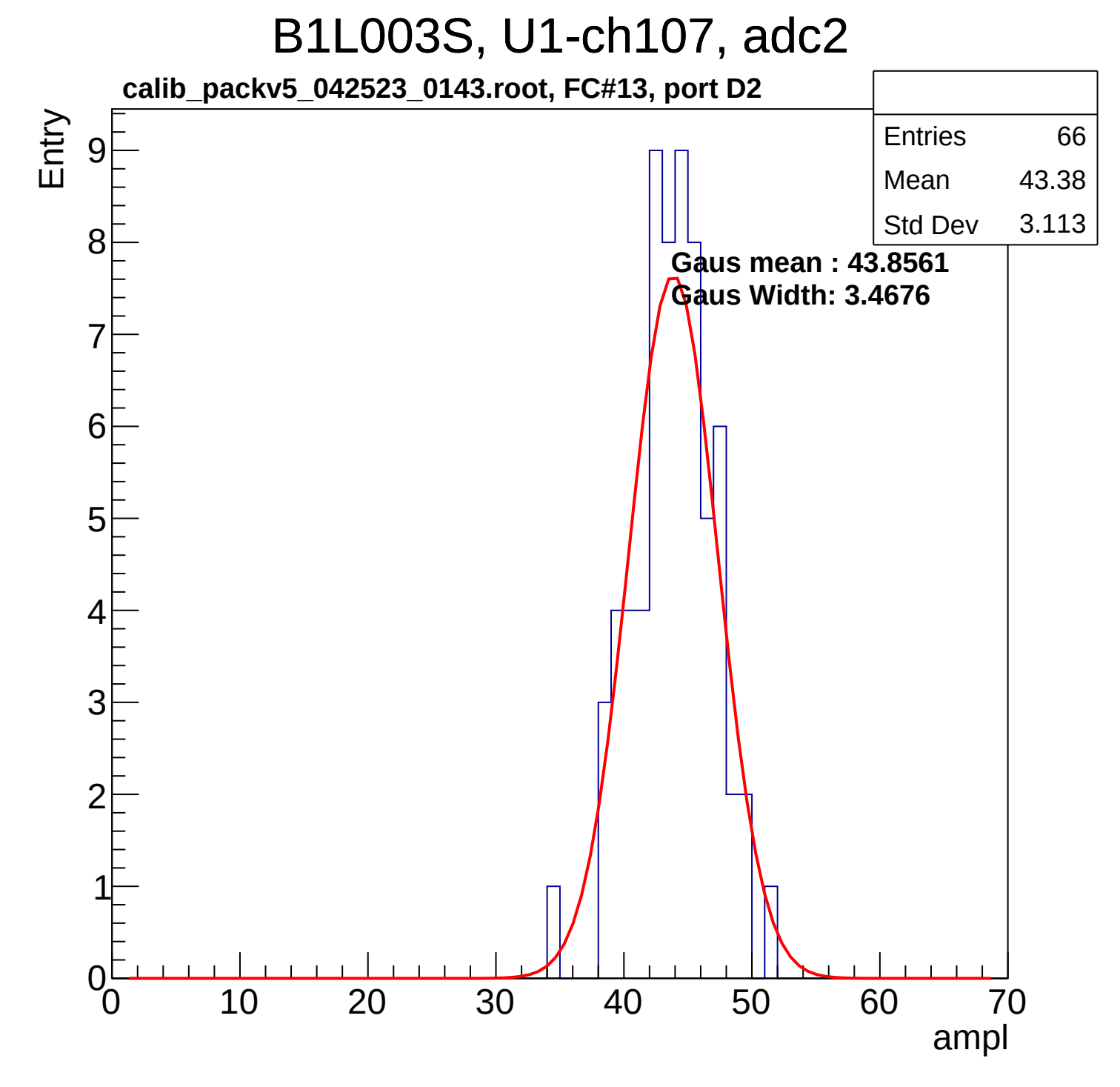

B1L003S, U1-ch106, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 58.07
Std Dev 8.069
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch106, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.91
Std Dev 1.505
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch106, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch107, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 30.74
Std Dev 5.373
Gaus mean : 31.8649
Gaus Width: 4.1109
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch107, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.84
Std Dev 3.138
Gaus mean : 37.7347
Gaus Width: 2.9998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch107, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 43.38
Std Dev 3.113
Gaus mean : 43.8561
Gaus Width: 3.4676
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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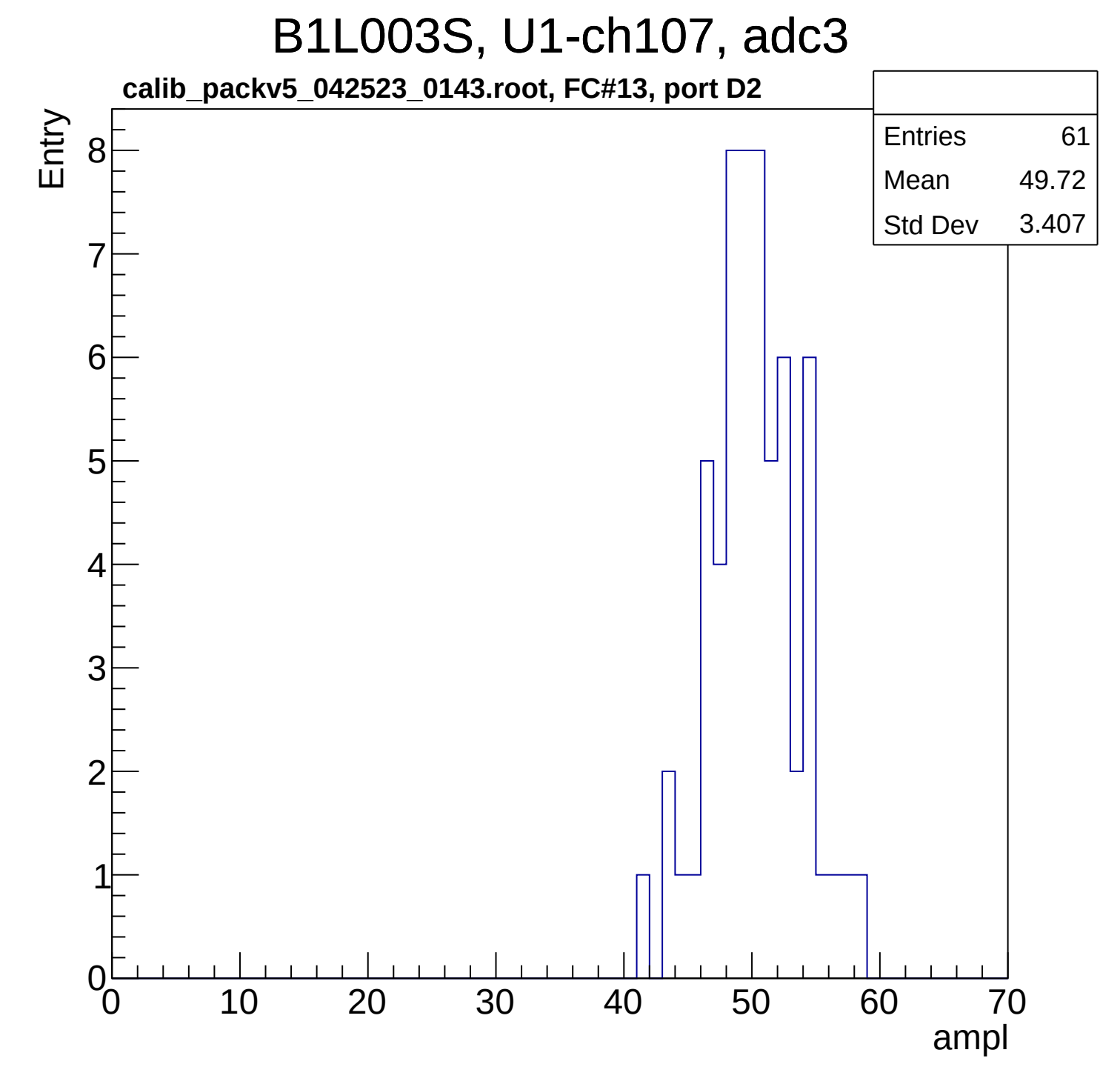
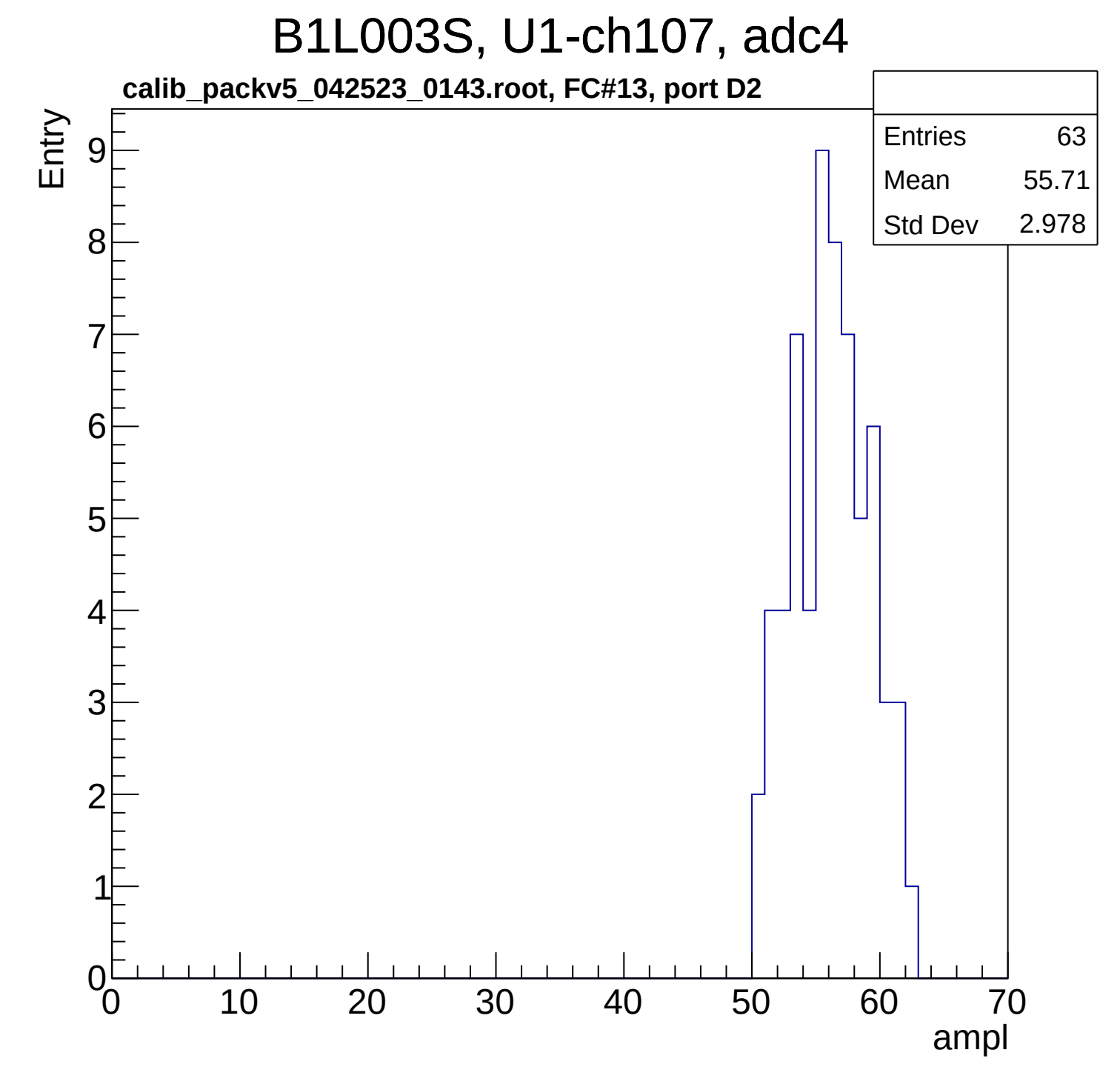
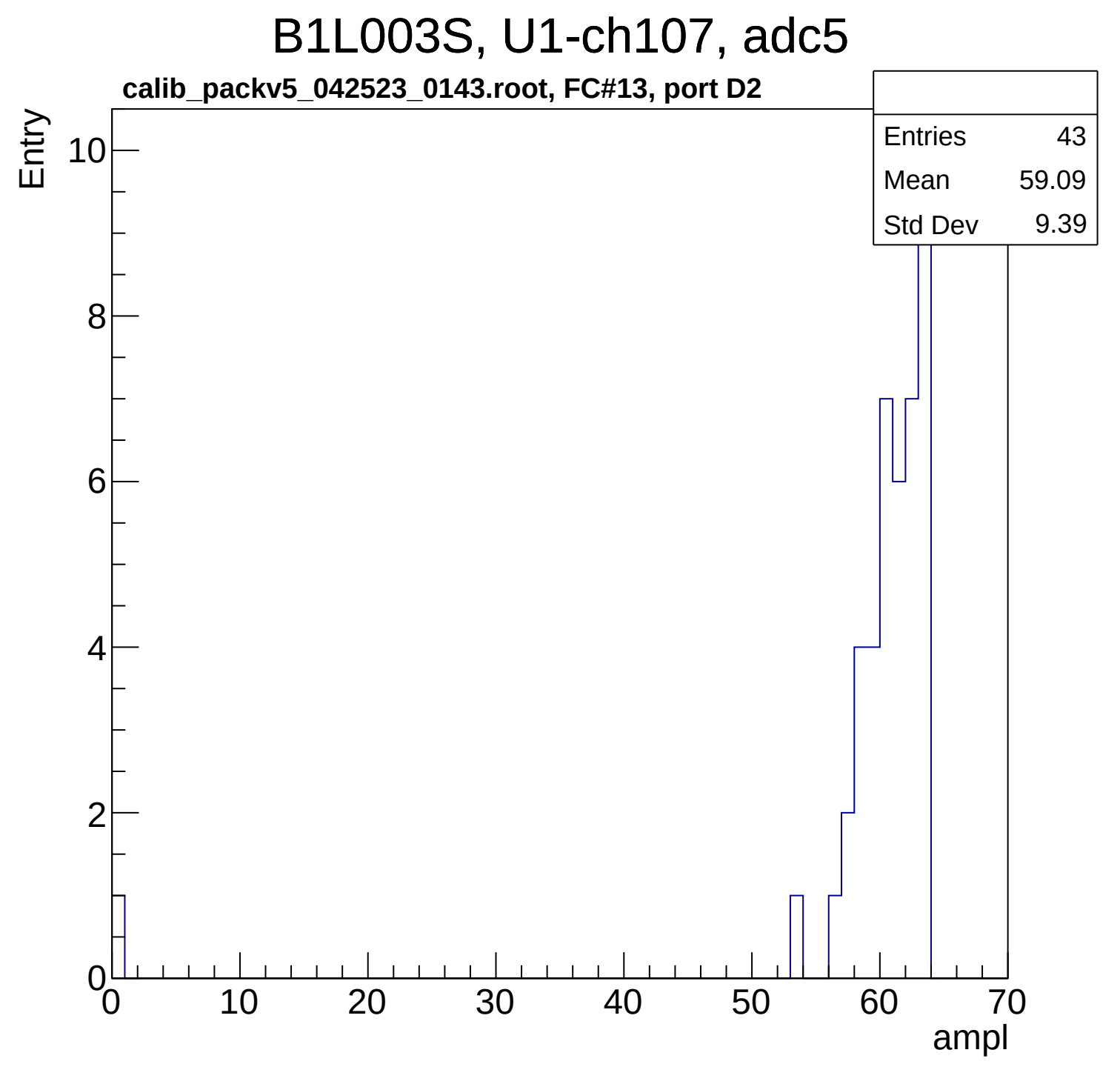
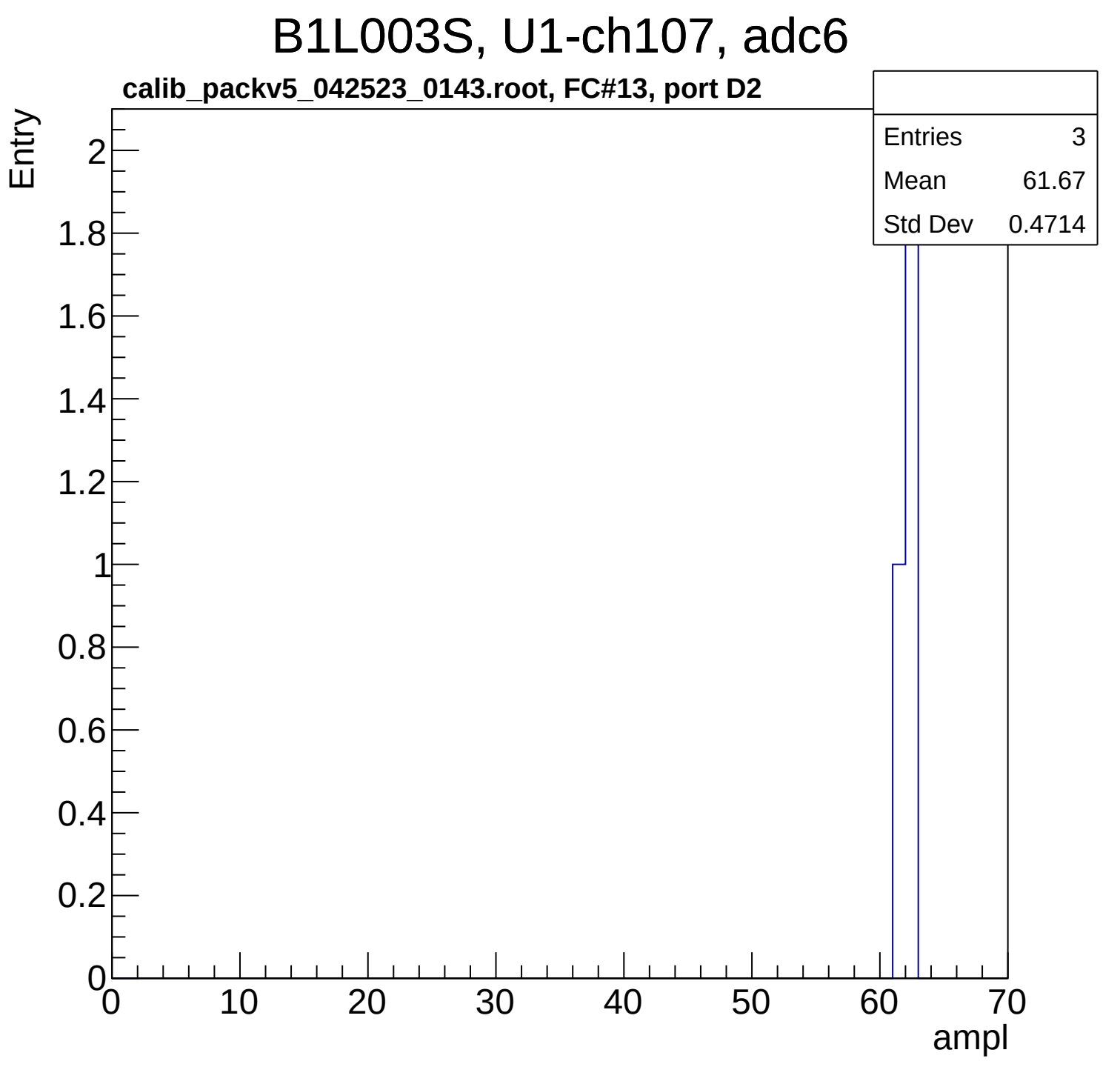
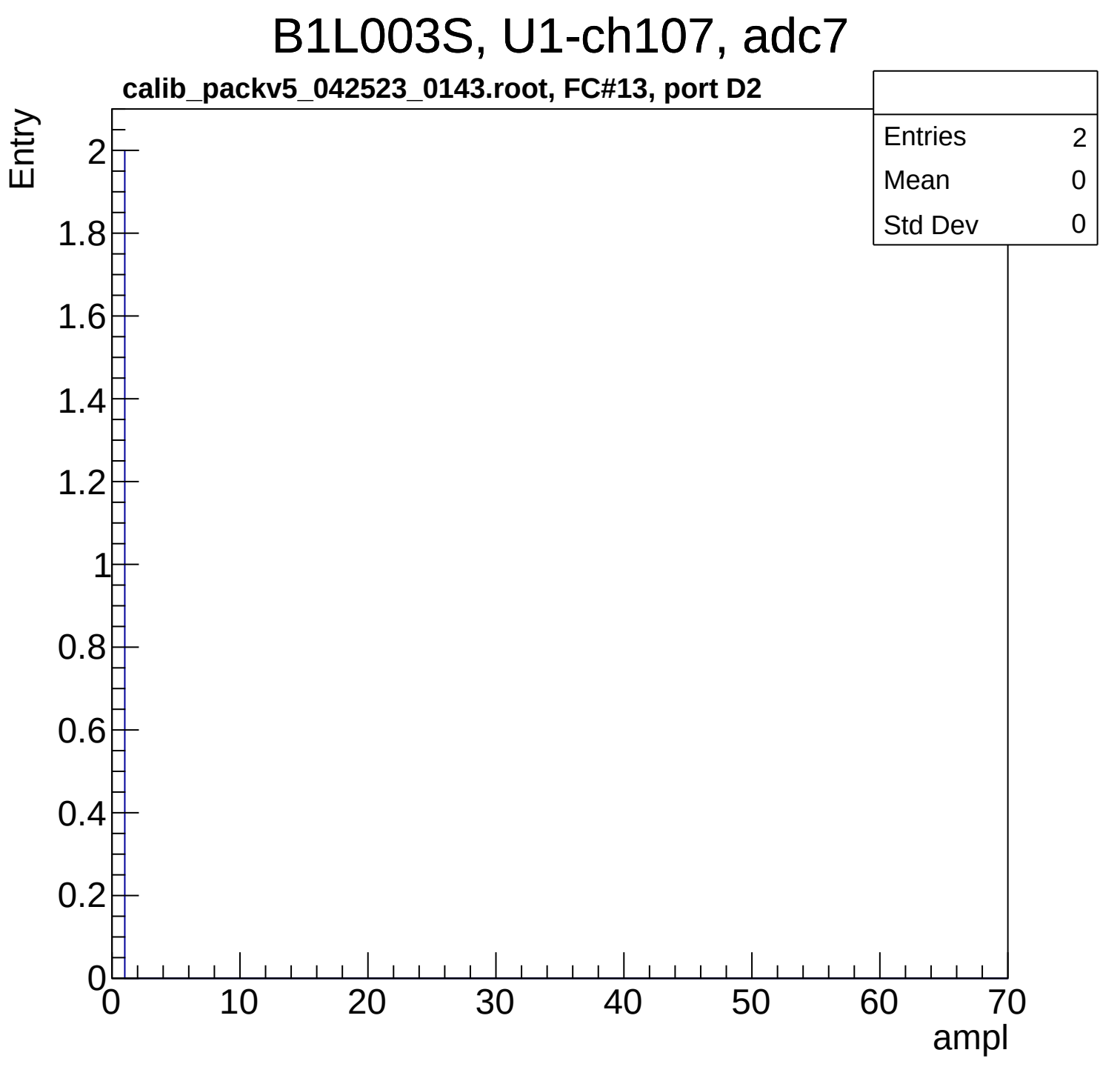
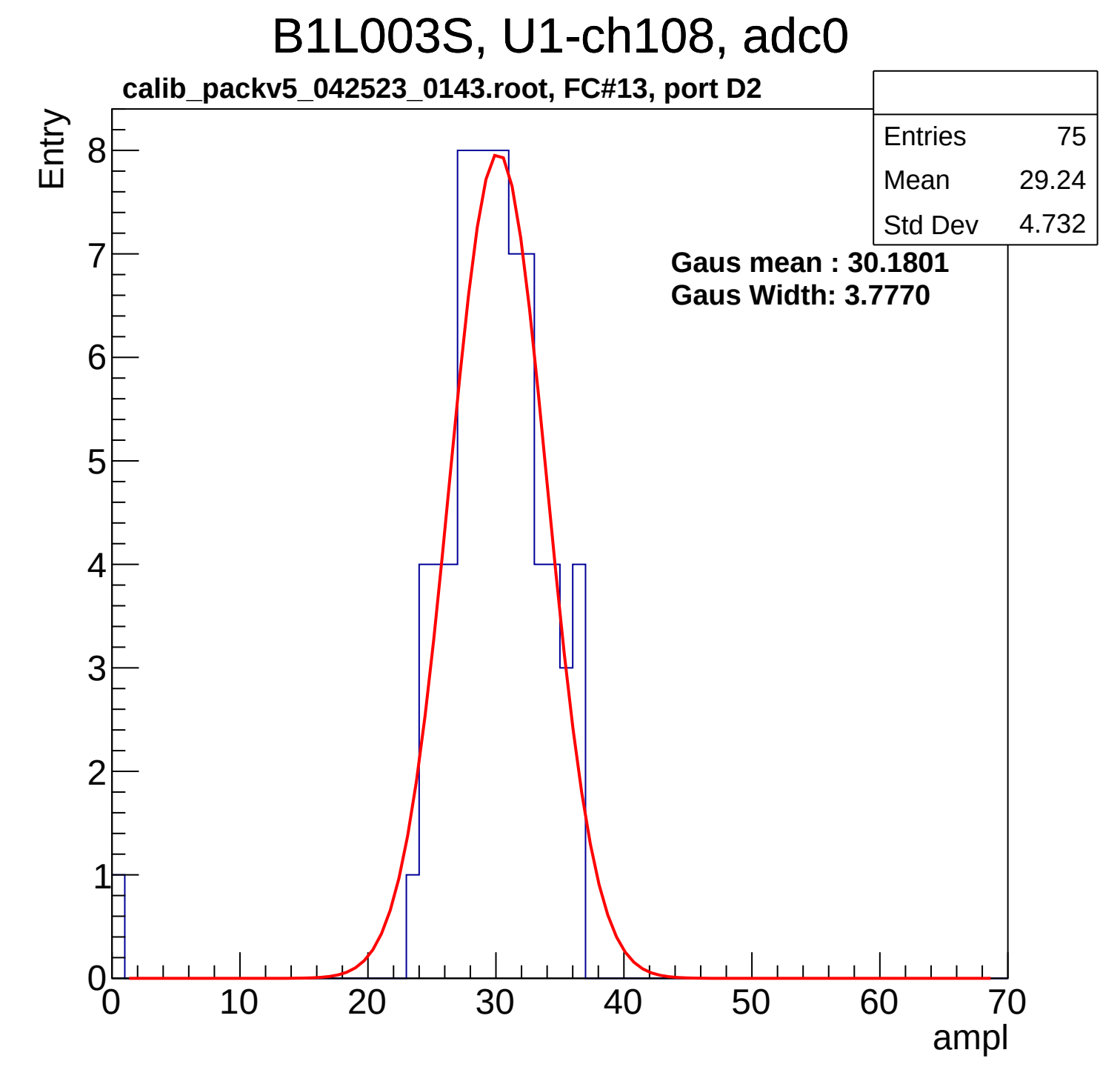

B1L003S, U1-ch107, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.72
Std Dev 3.407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

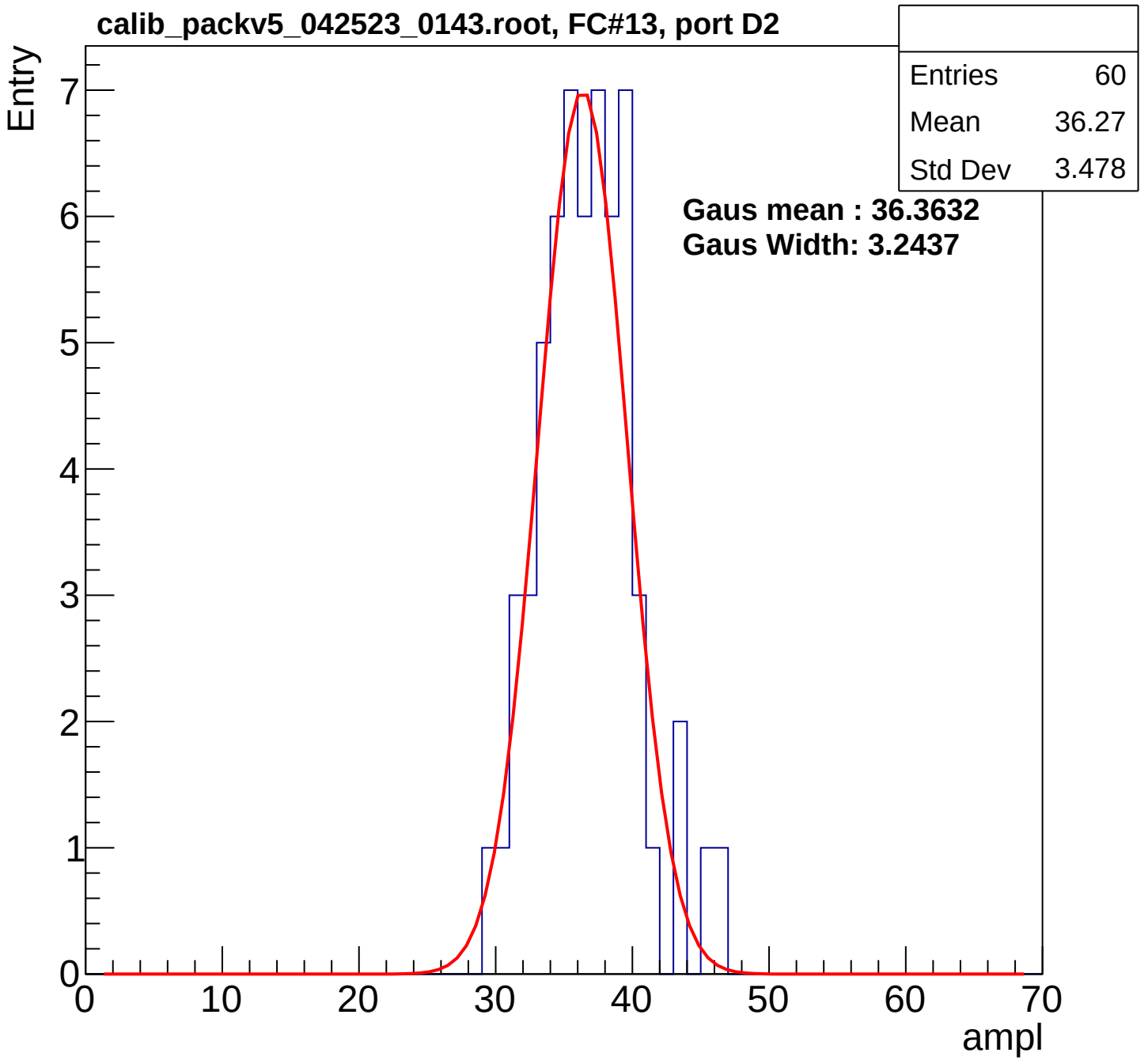

B1L003S, U1-ch107, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 55.71
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

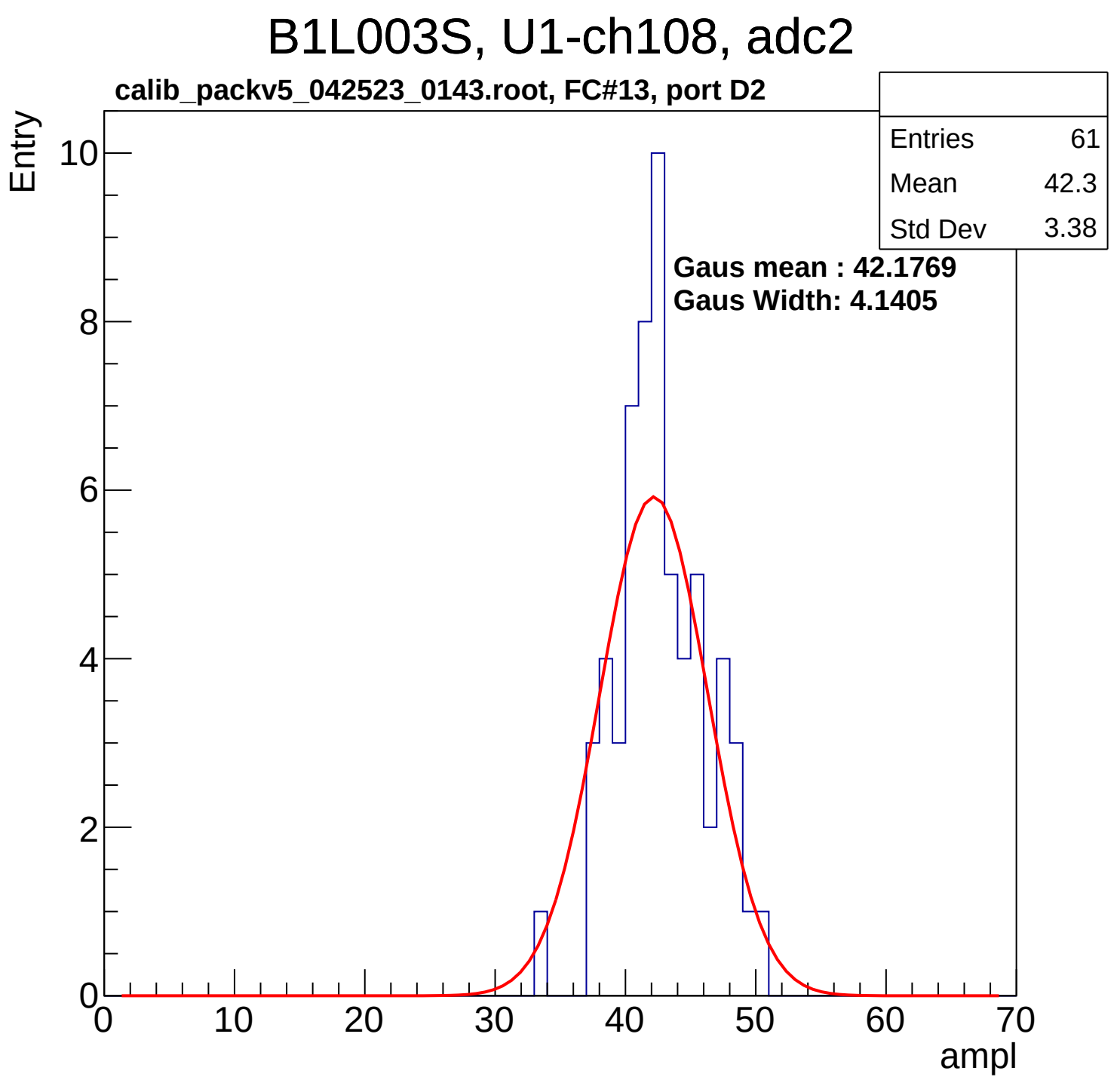

B1L003S, U1-ch107, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 59.09
Std Dev 9.39
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

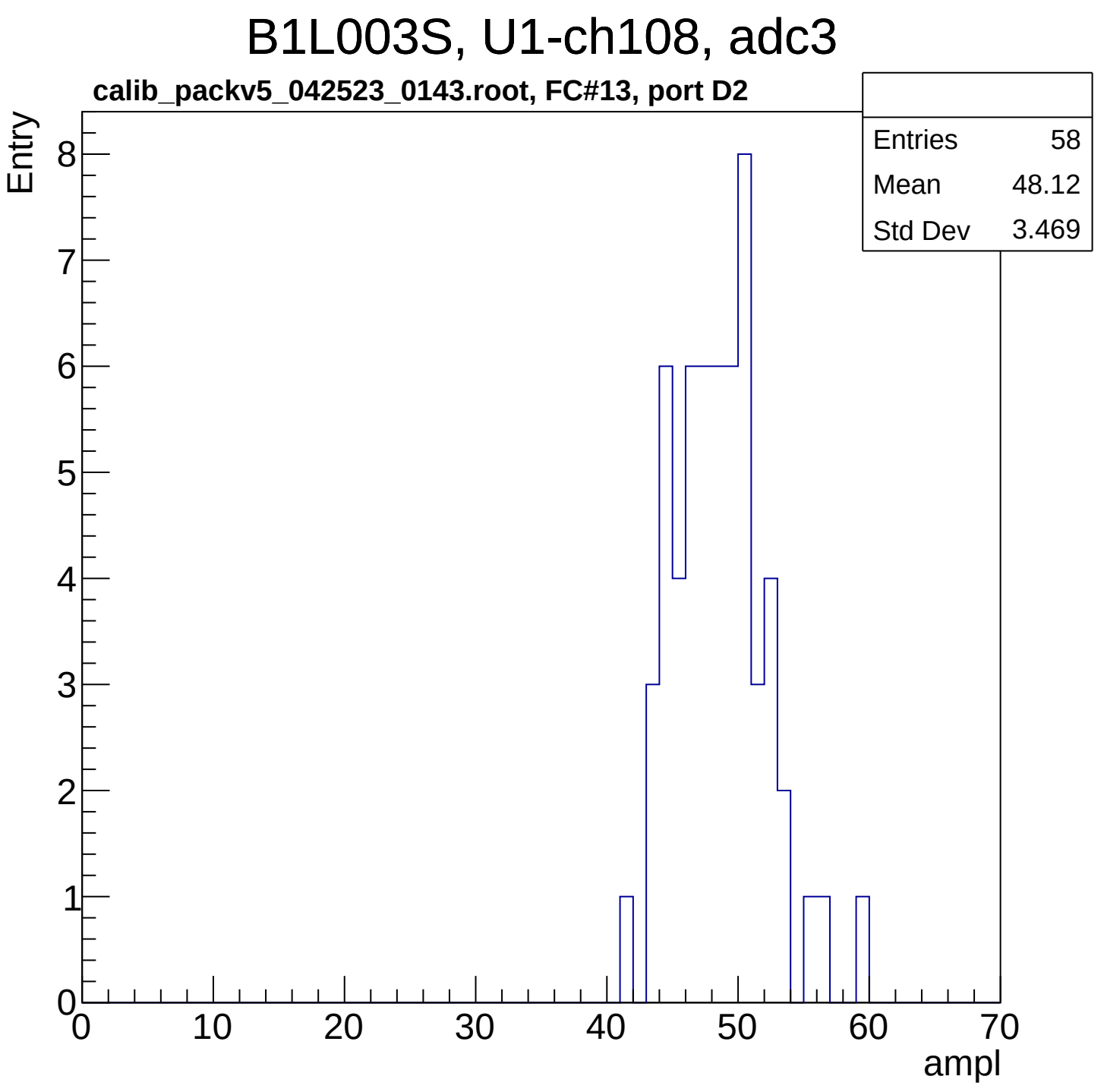

B1L003S, U1-ch107, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 61.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch107, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch108, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.24
Std Dev 4.732
Gaus mean : 30.1801
Gaus Width: 3.7770
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch108, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.27
Std Dev 3.478
Gaus mean : 36.3632
Gaus Width: 3.2437
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch108, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 42.3
Std Dev 3.38
Gaus mean : 42.1769
Gaus Width: 4.1405
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch108, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 48.12
Std Dev 3.469
Entry
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2
3
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6
7
8
0
10
20
30
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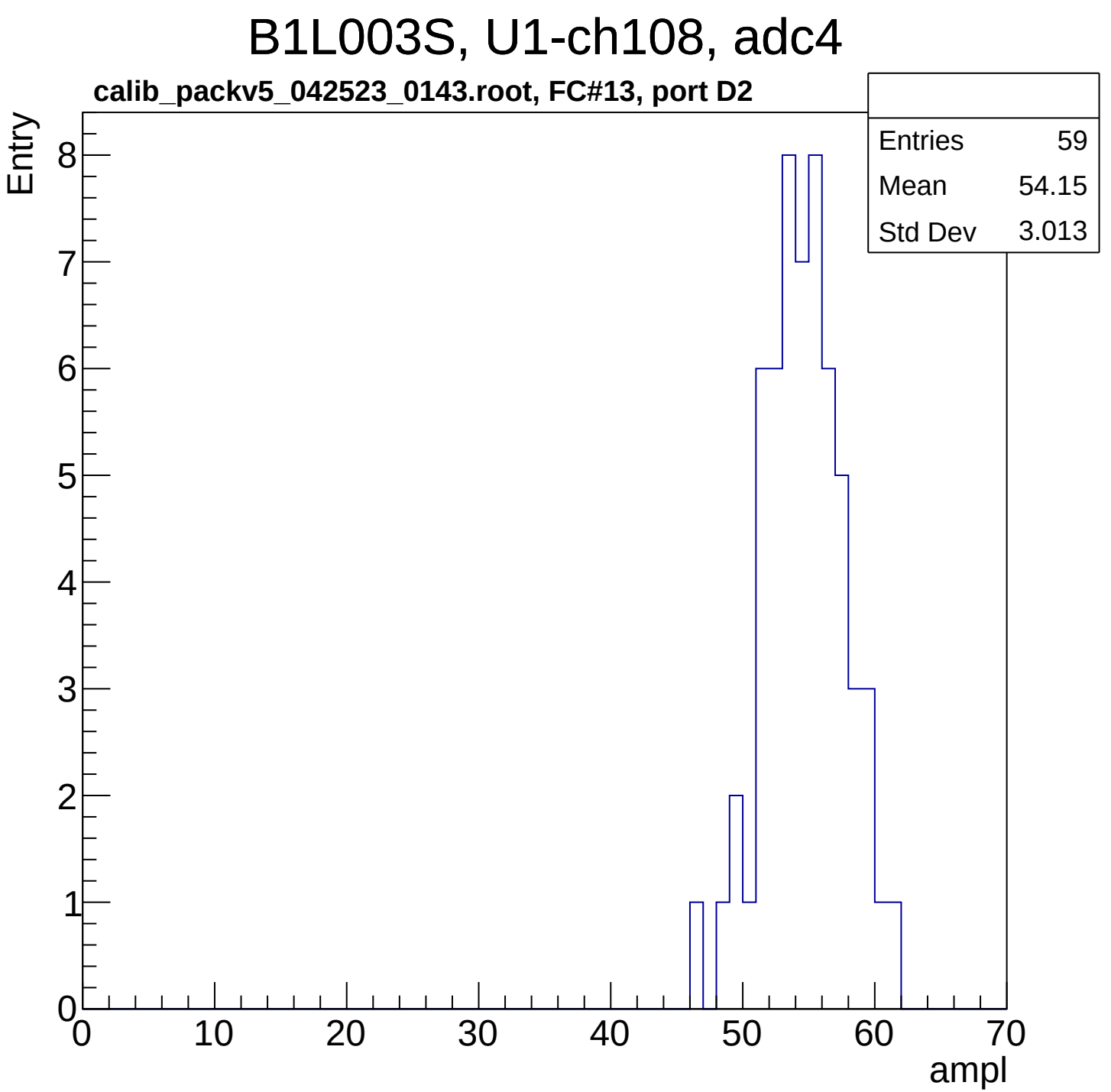
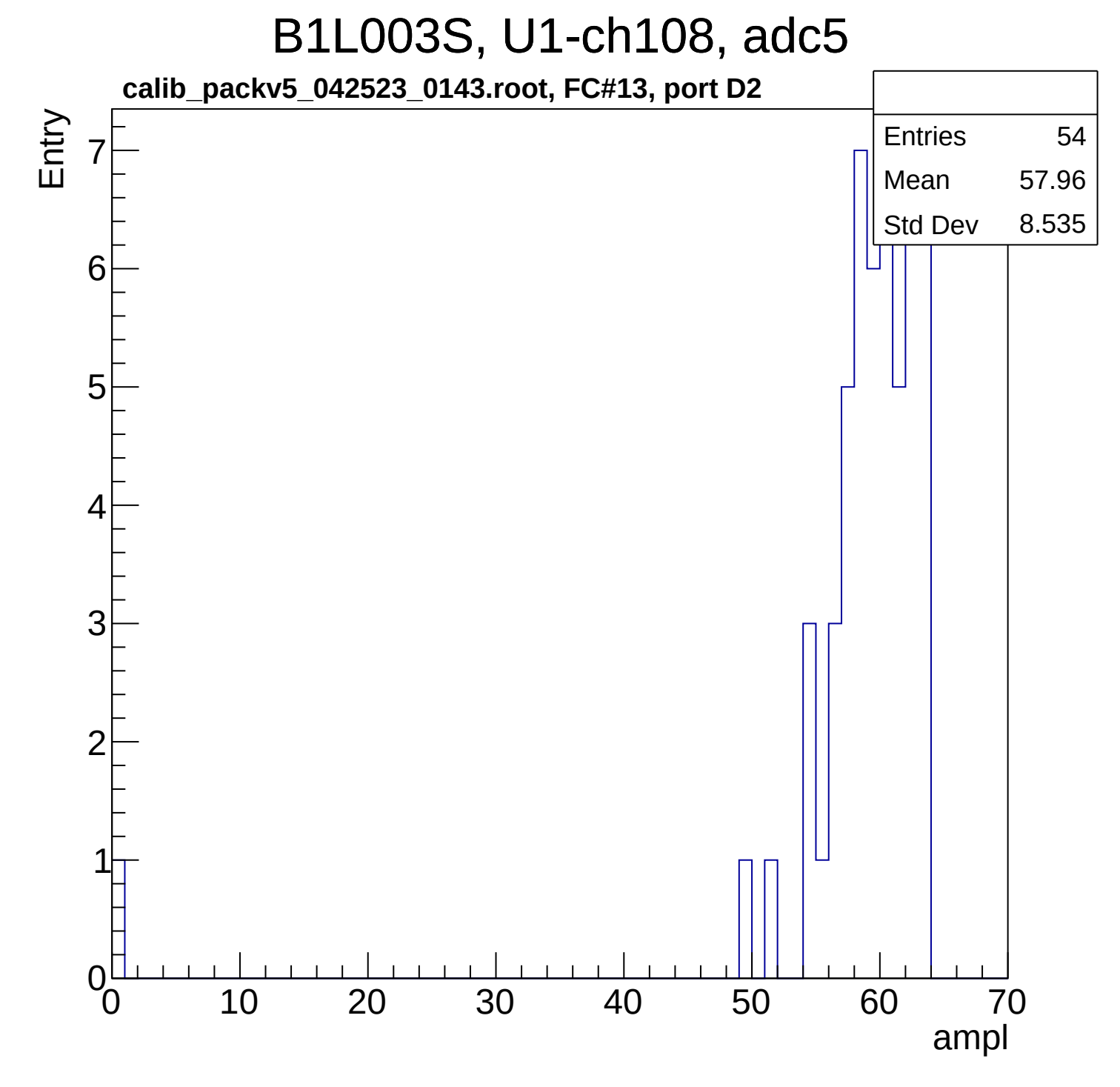
70
ampl


B1L003S, U1-ch108, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 54.15
Std Dev 3.013
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch108, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 57.96
Std Dev 8.535
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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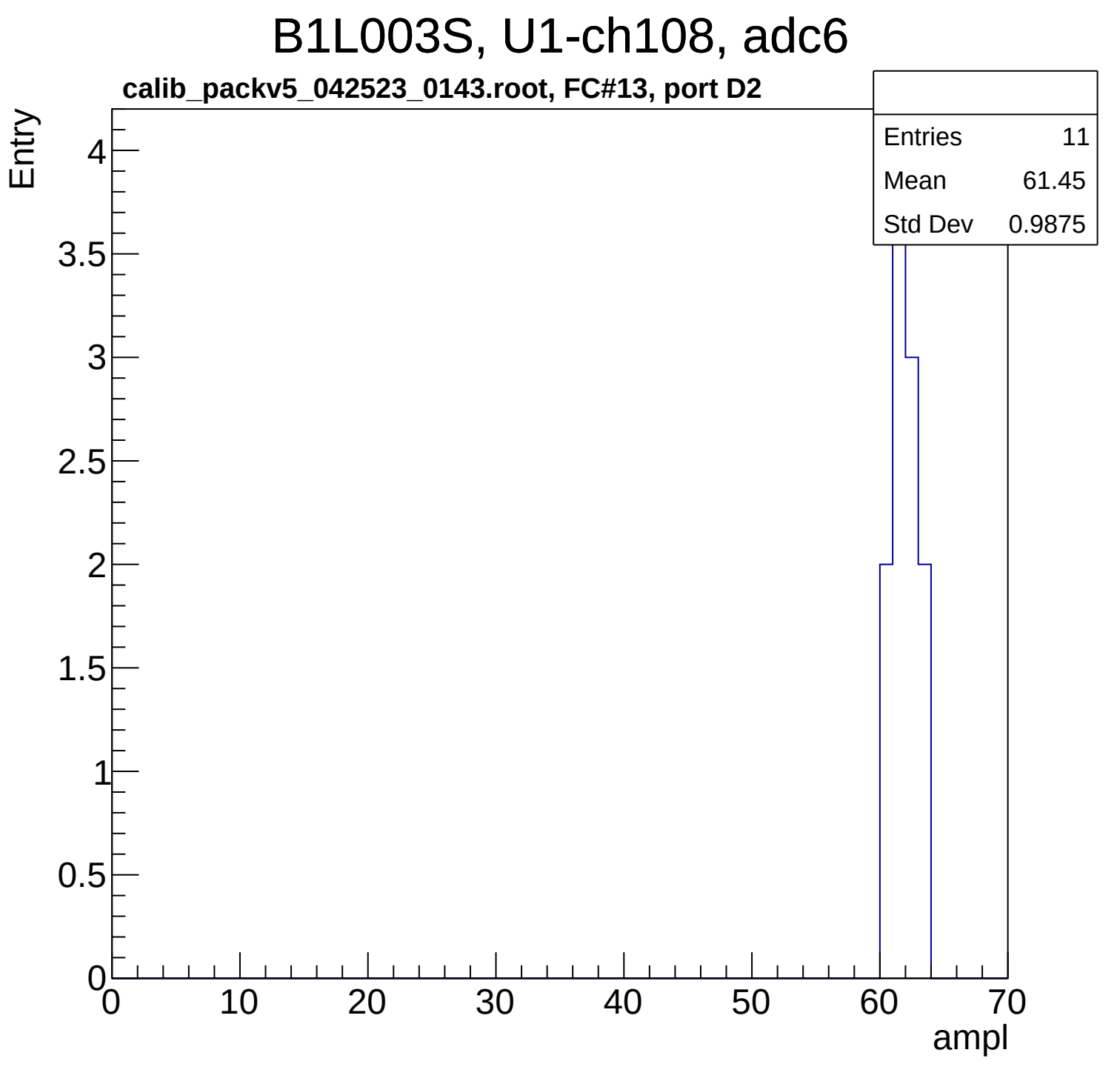
50
60
70


B1L003S, U1-ch108, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.45
Std Dev 0.9875
Entry
ampl
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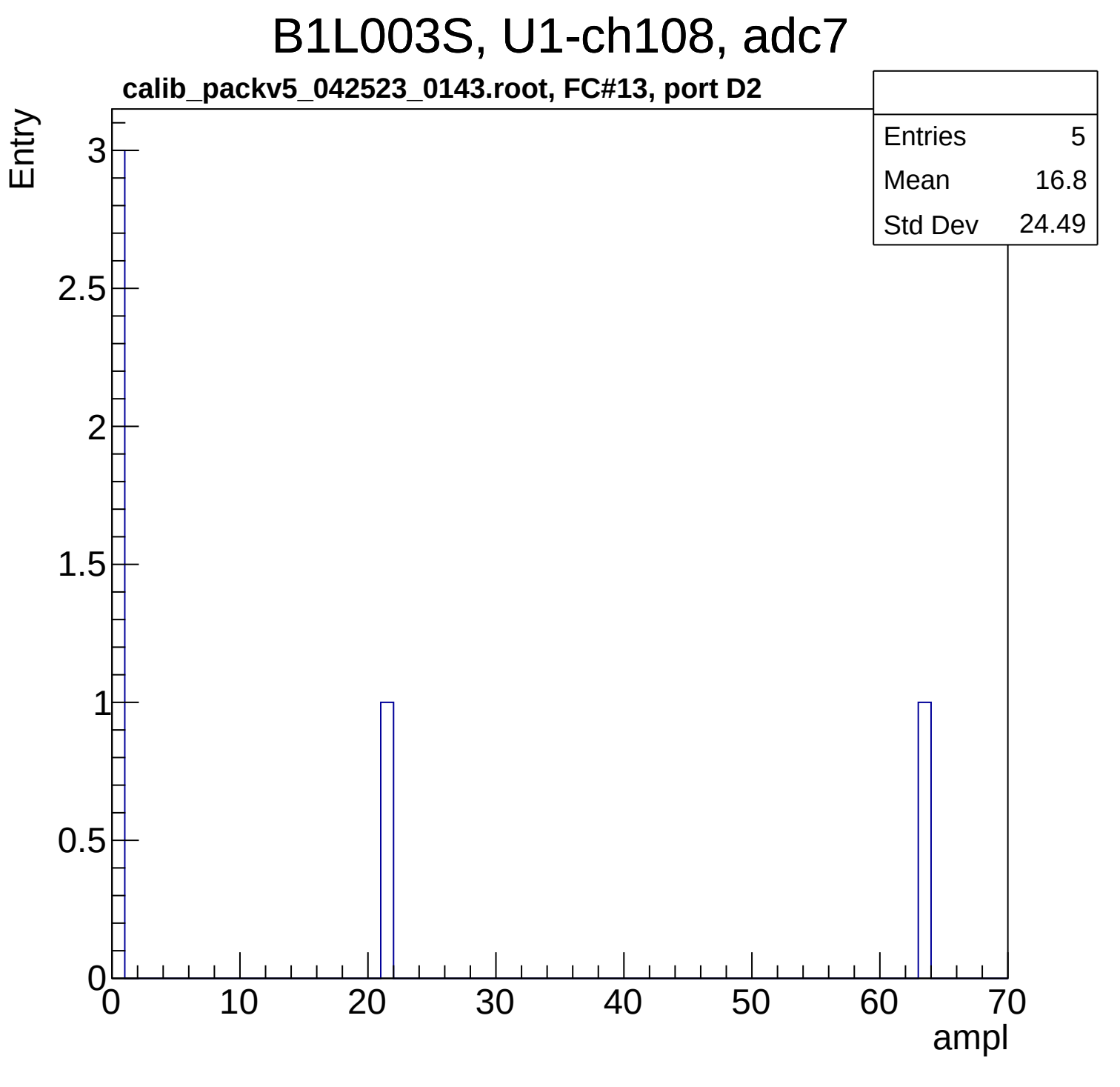
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch108, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 16.8
Std Dev 24.49
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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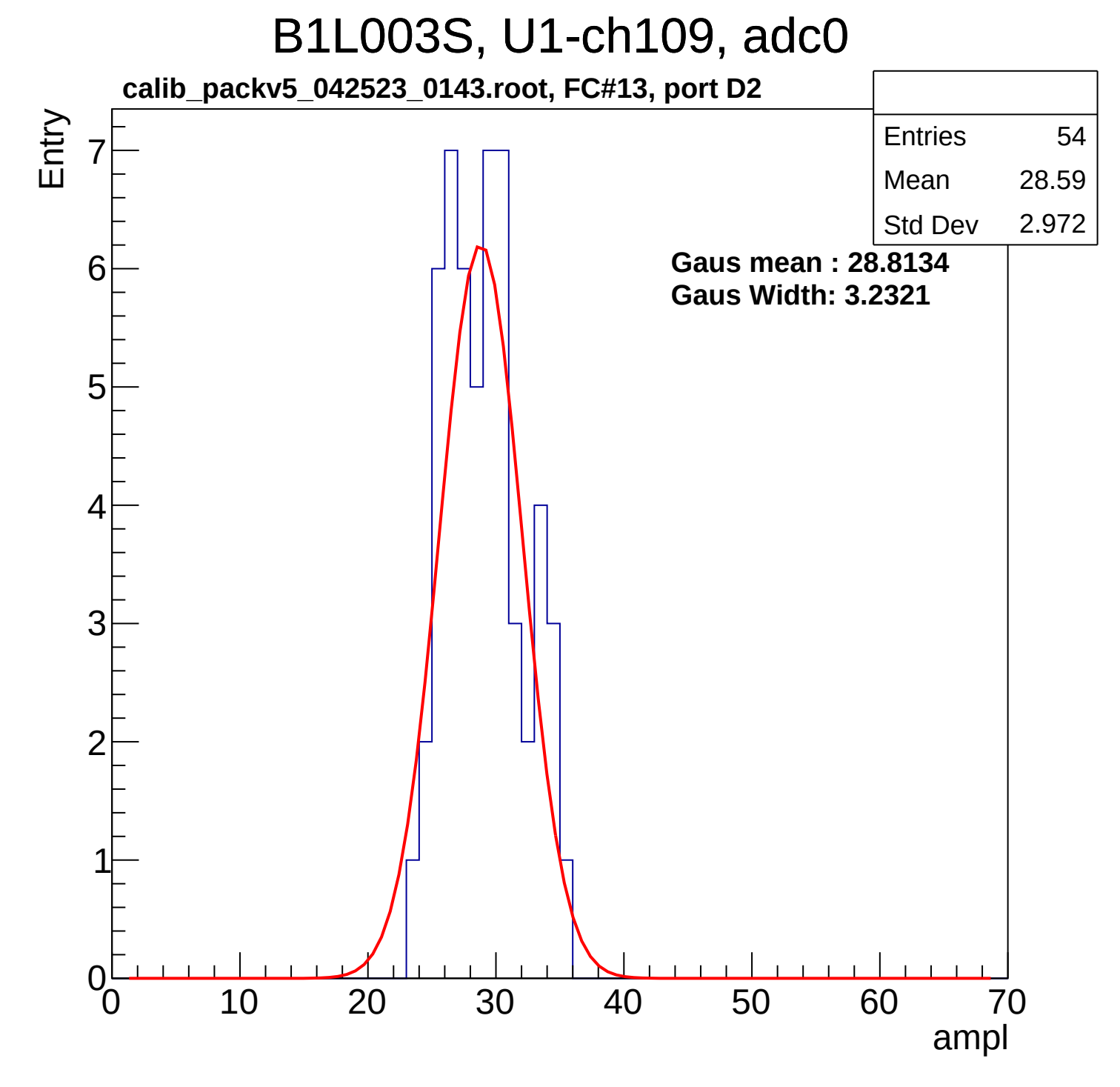
B1L003S, U1-ch109, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 28.59
Std Dev 2.972
Gaus mean : 28.8134
Gaus Width: 3.2321
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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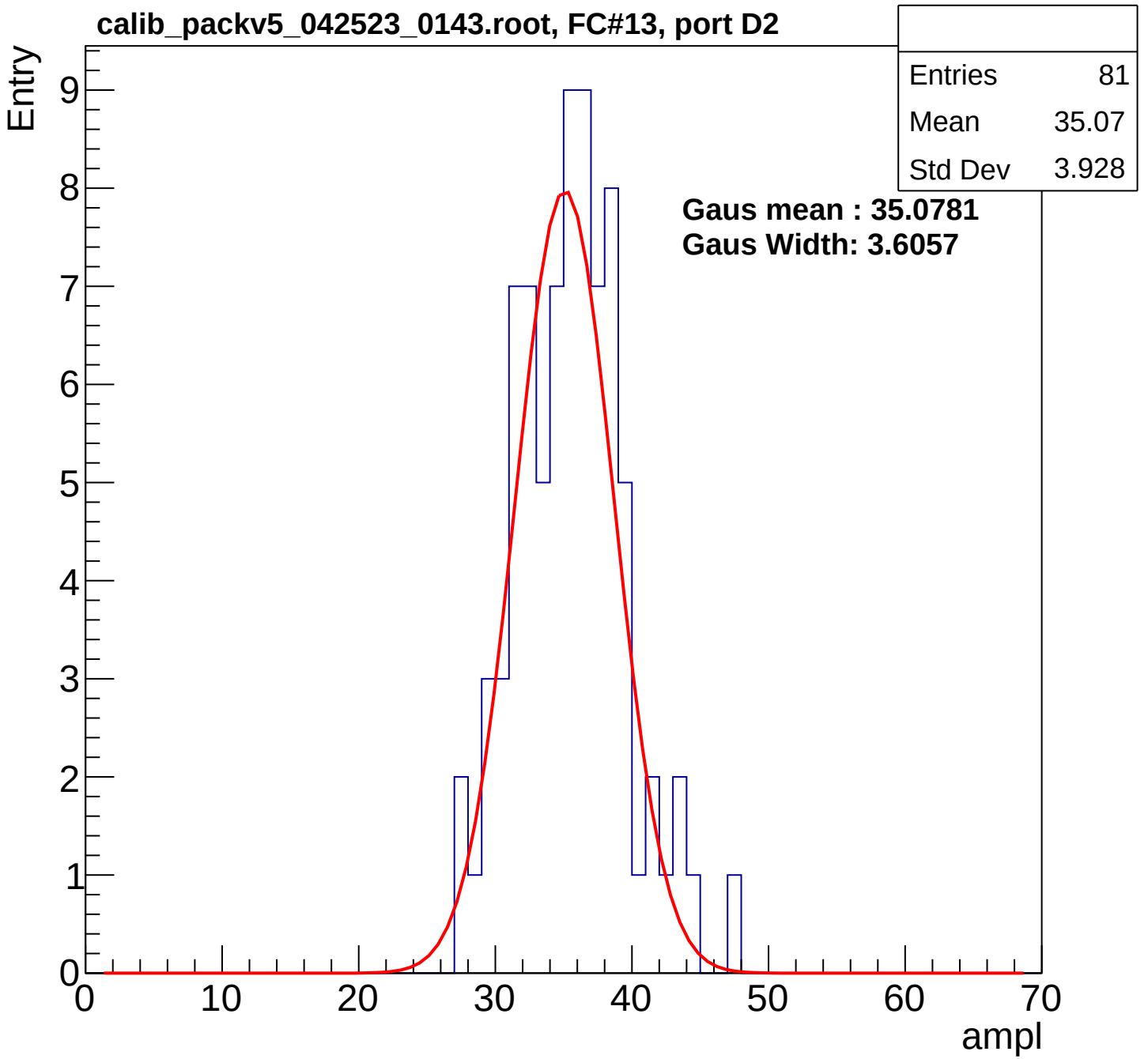
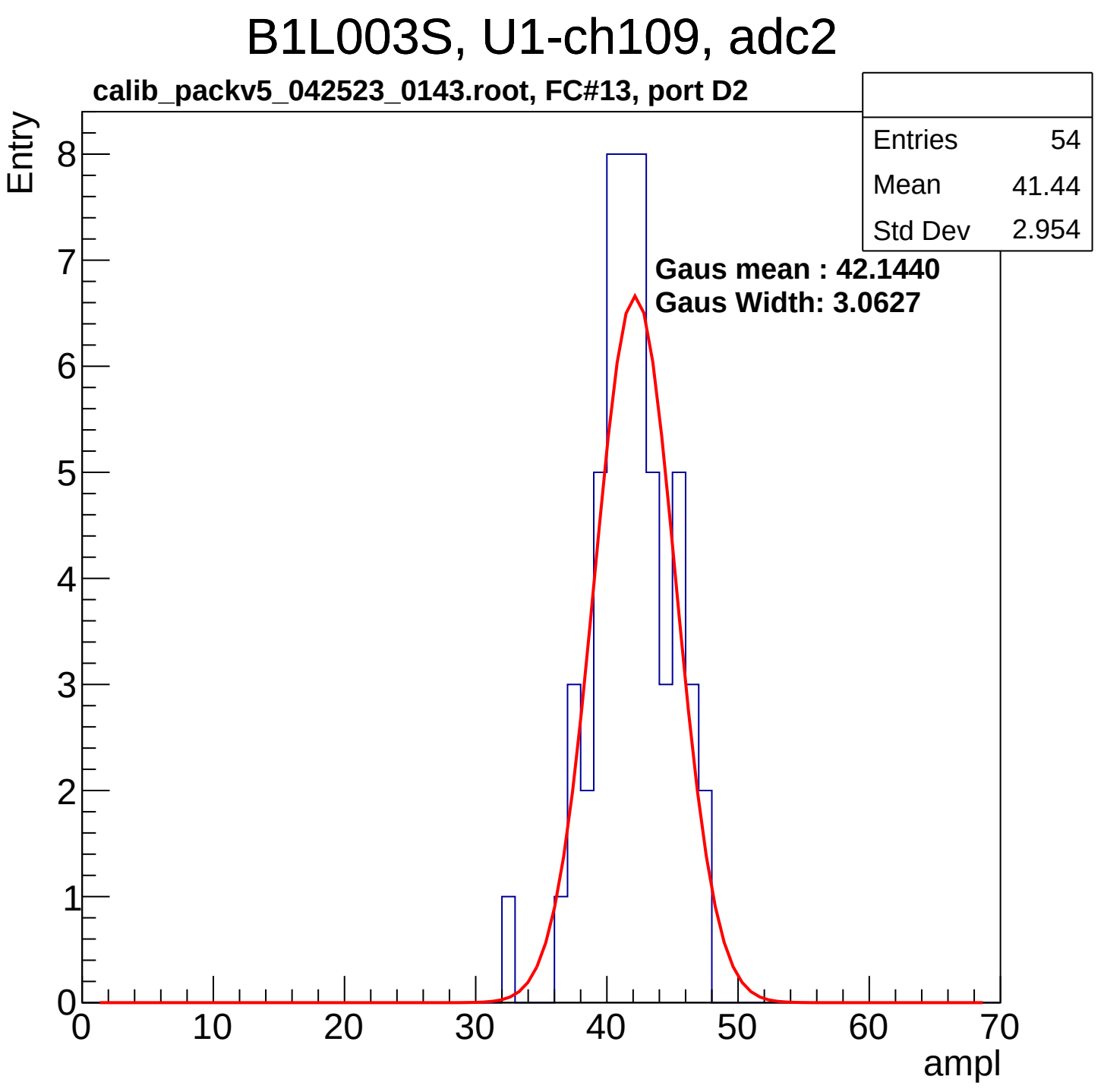

B1L003S, U1-ch109, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 35.07
Std Dev 3.928
Gaus mean : 35.0781
Gaus Width: 3.6057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch109, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 41.44
Std Dev 2.954
Gaus mean : 42.1440
Gaus Width: 3.0627
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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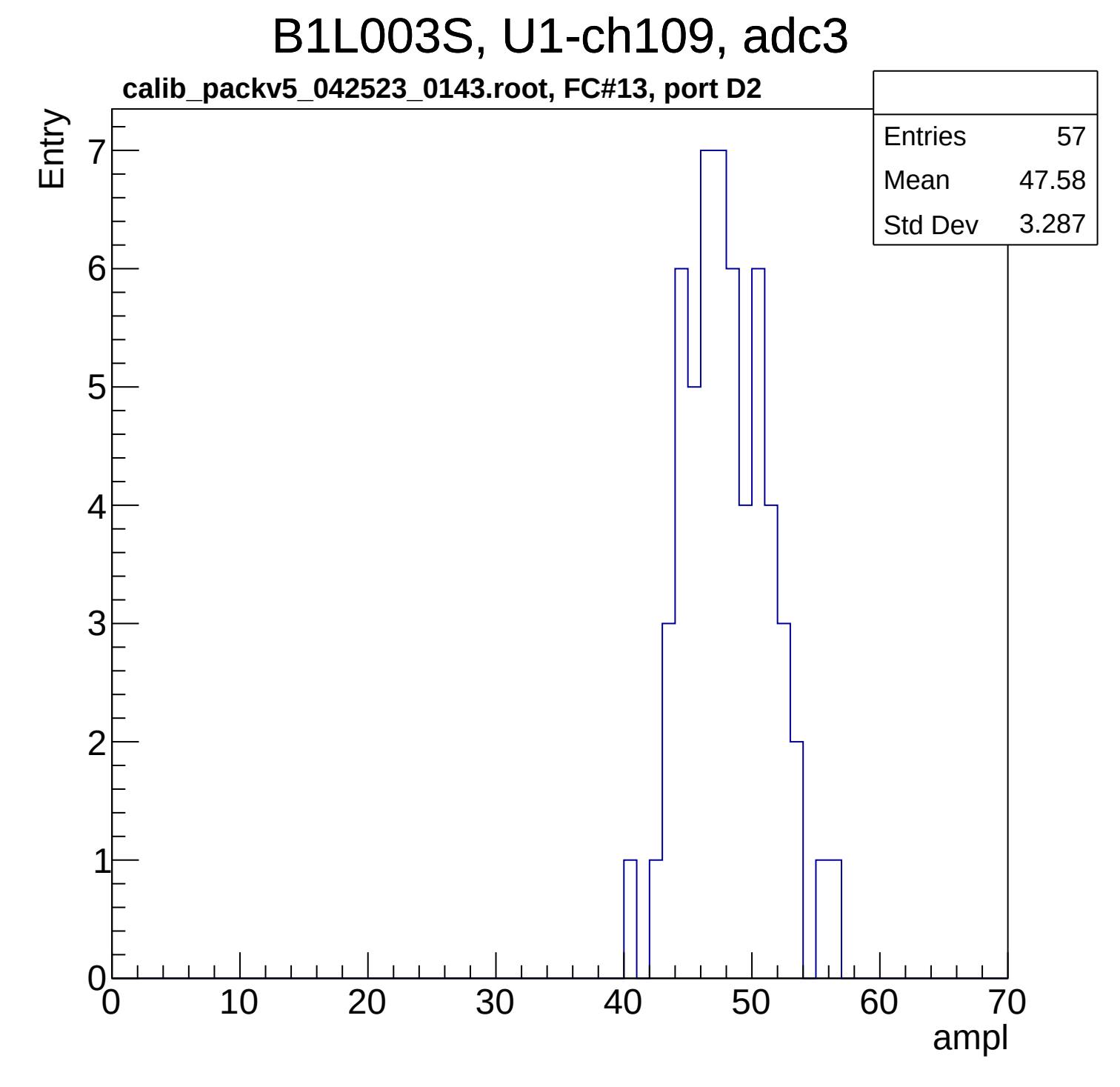
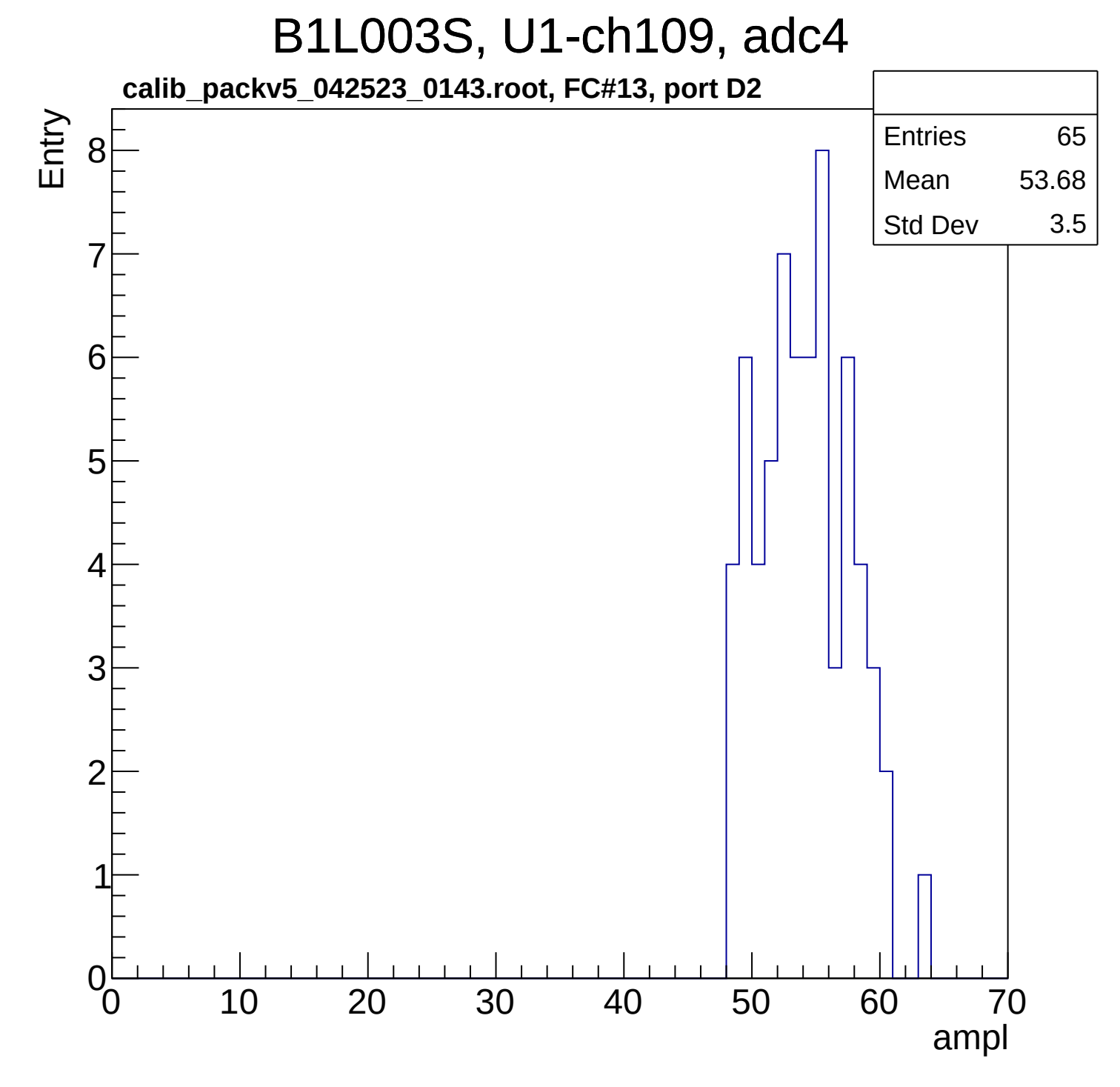
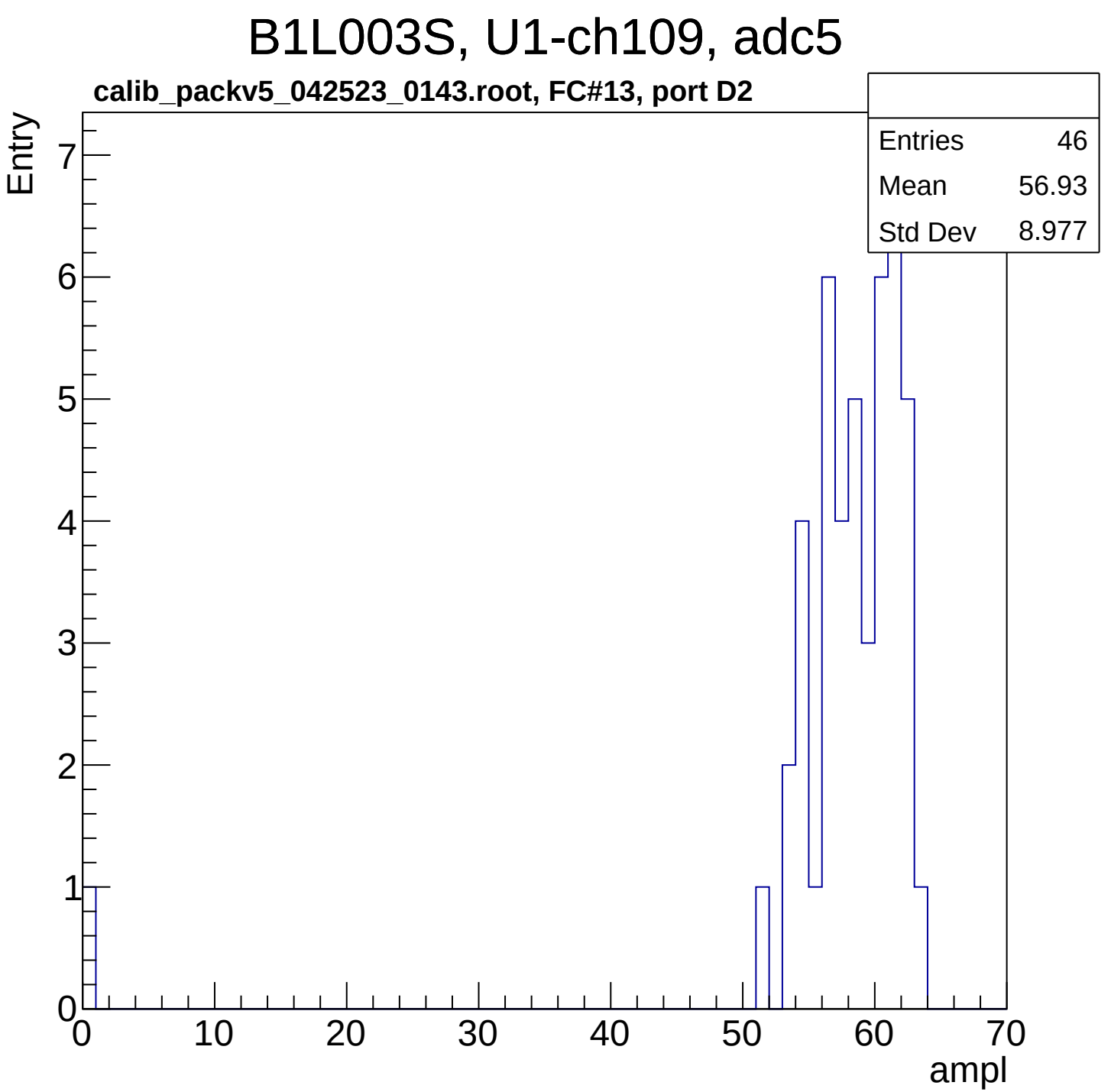
60
70


B1L003S, U1-ch109, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 47.58
Std Dev 3.287
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch109, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 53.68
Std Dev 3.5
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch109, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 46
Mean 56.93
Std Dev 8.977
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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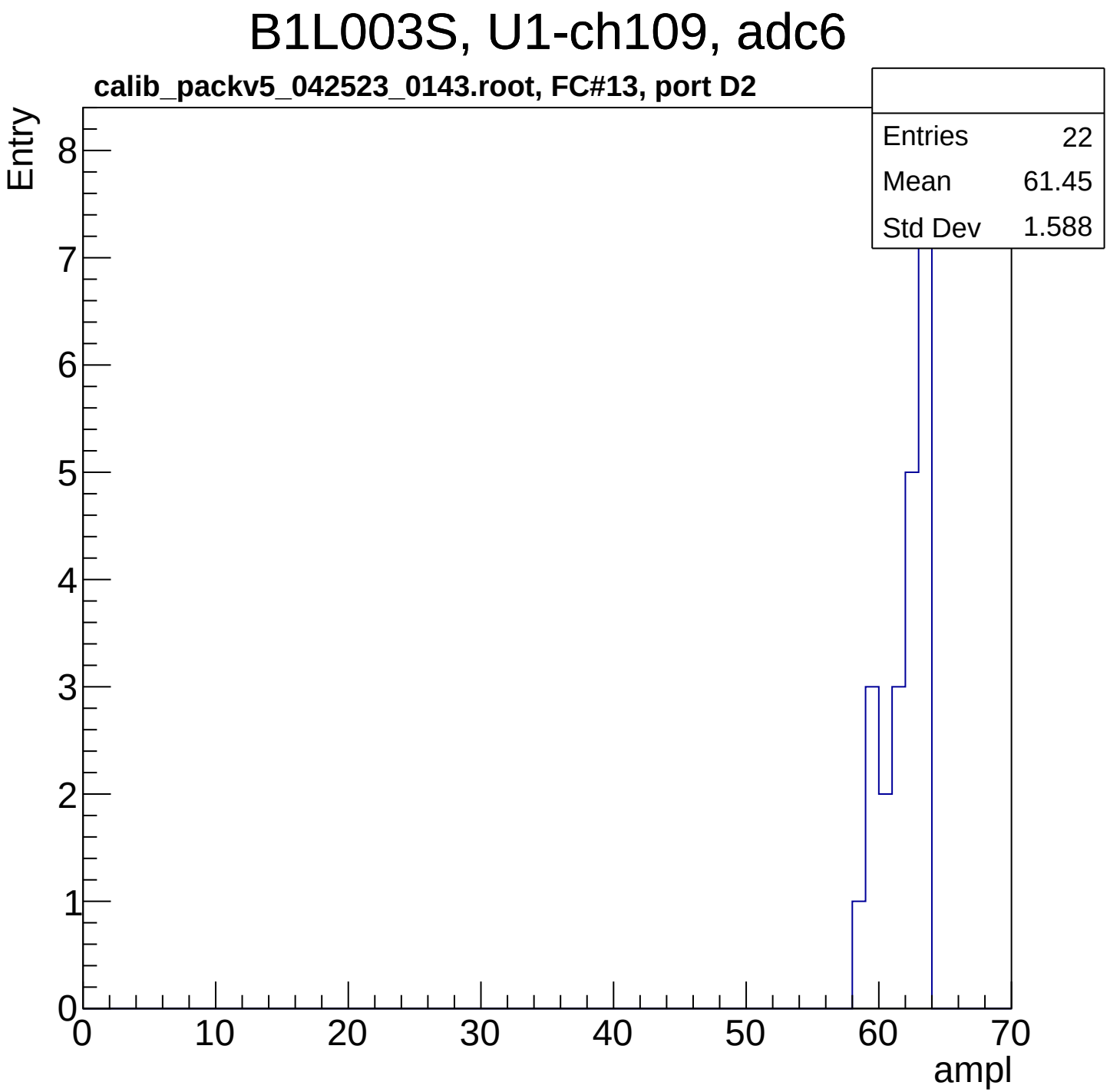
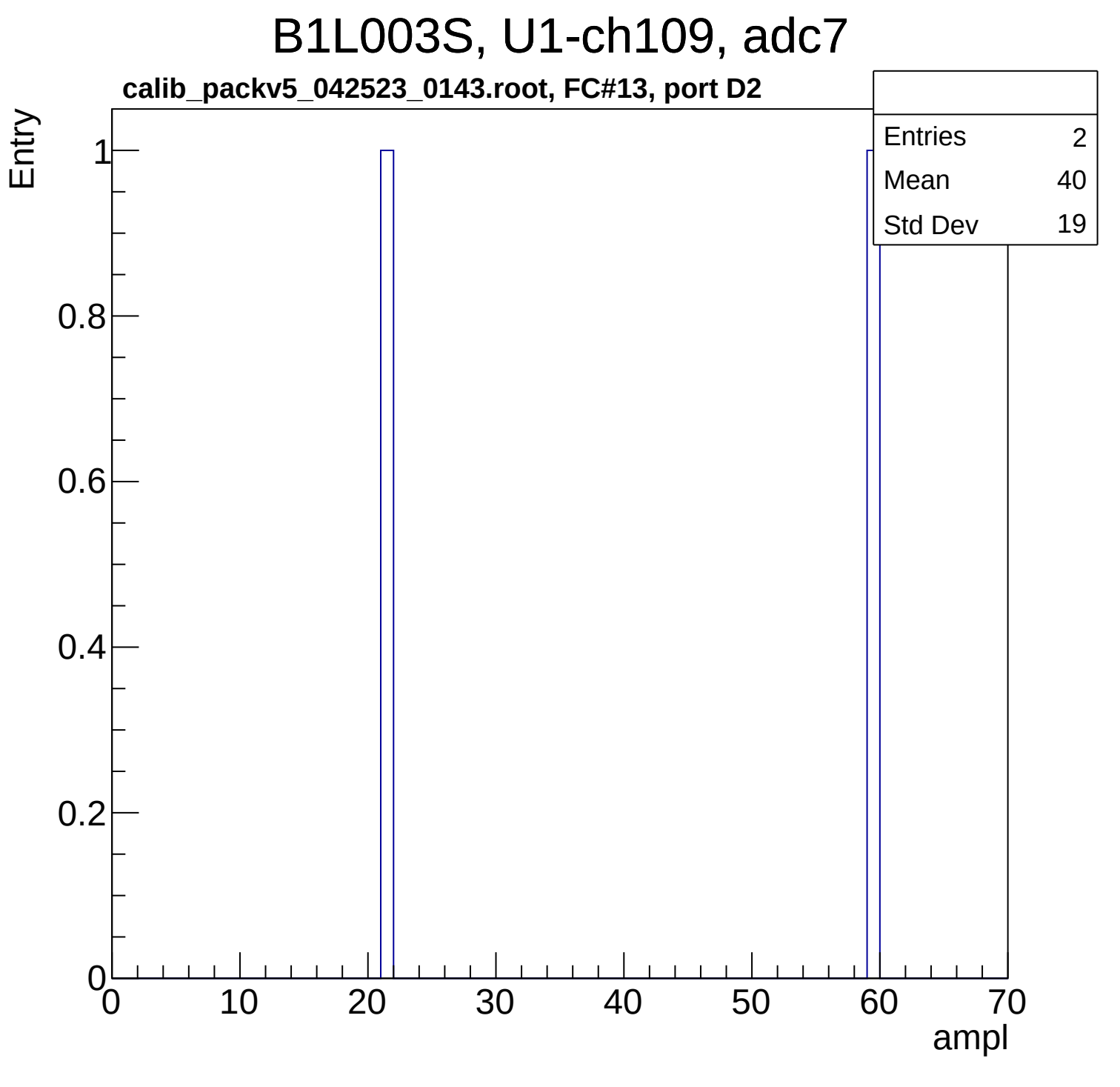
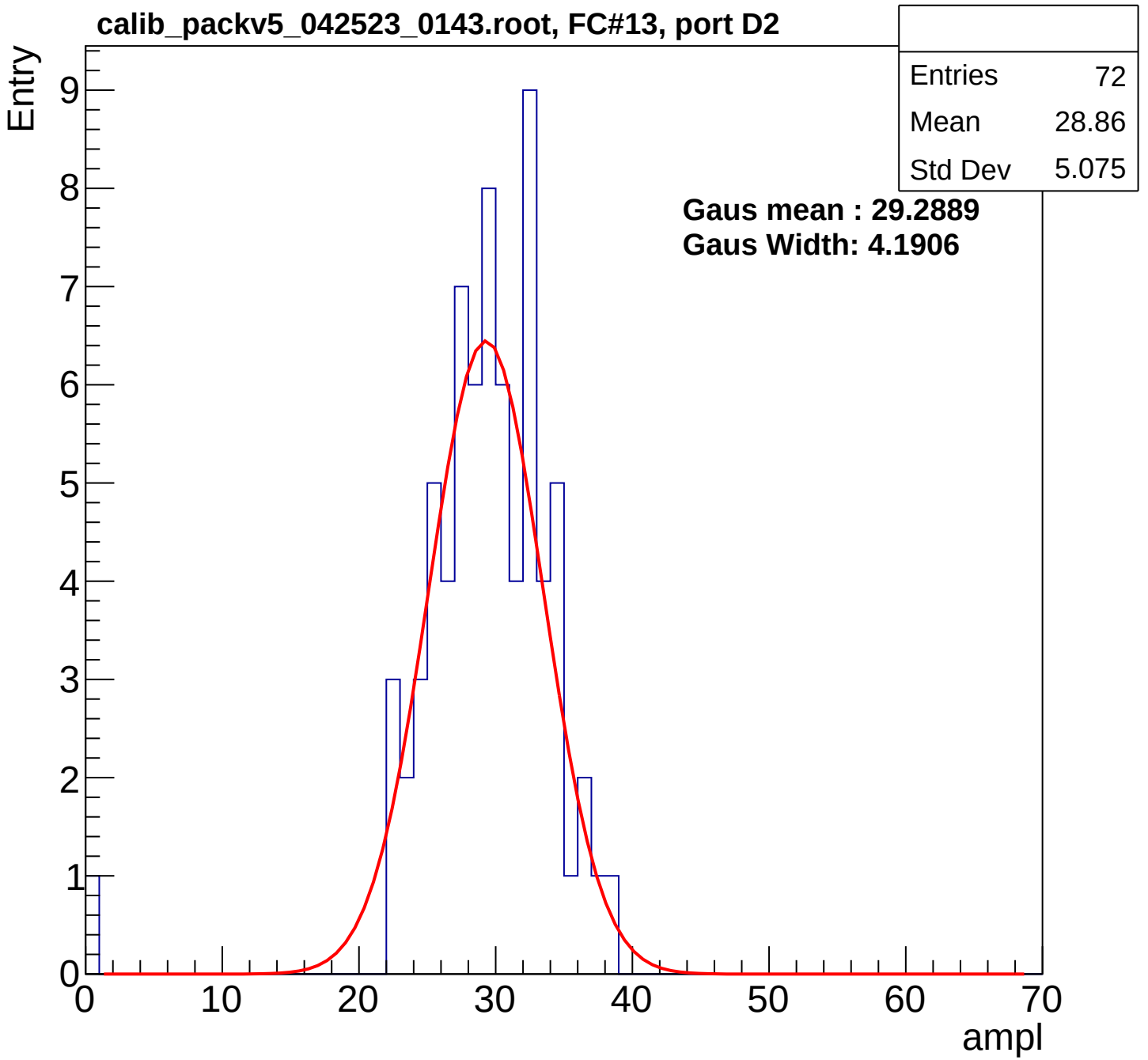
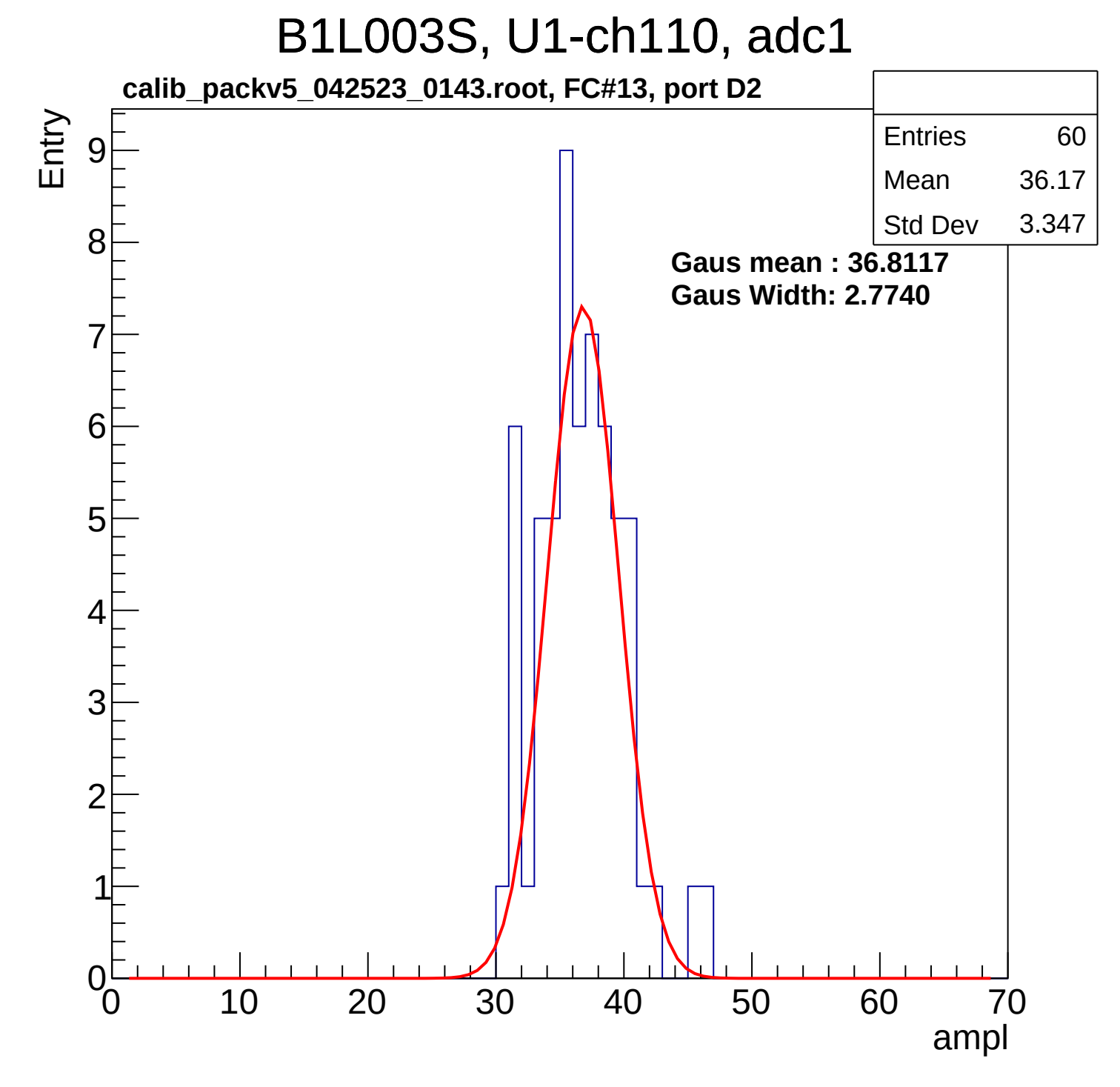
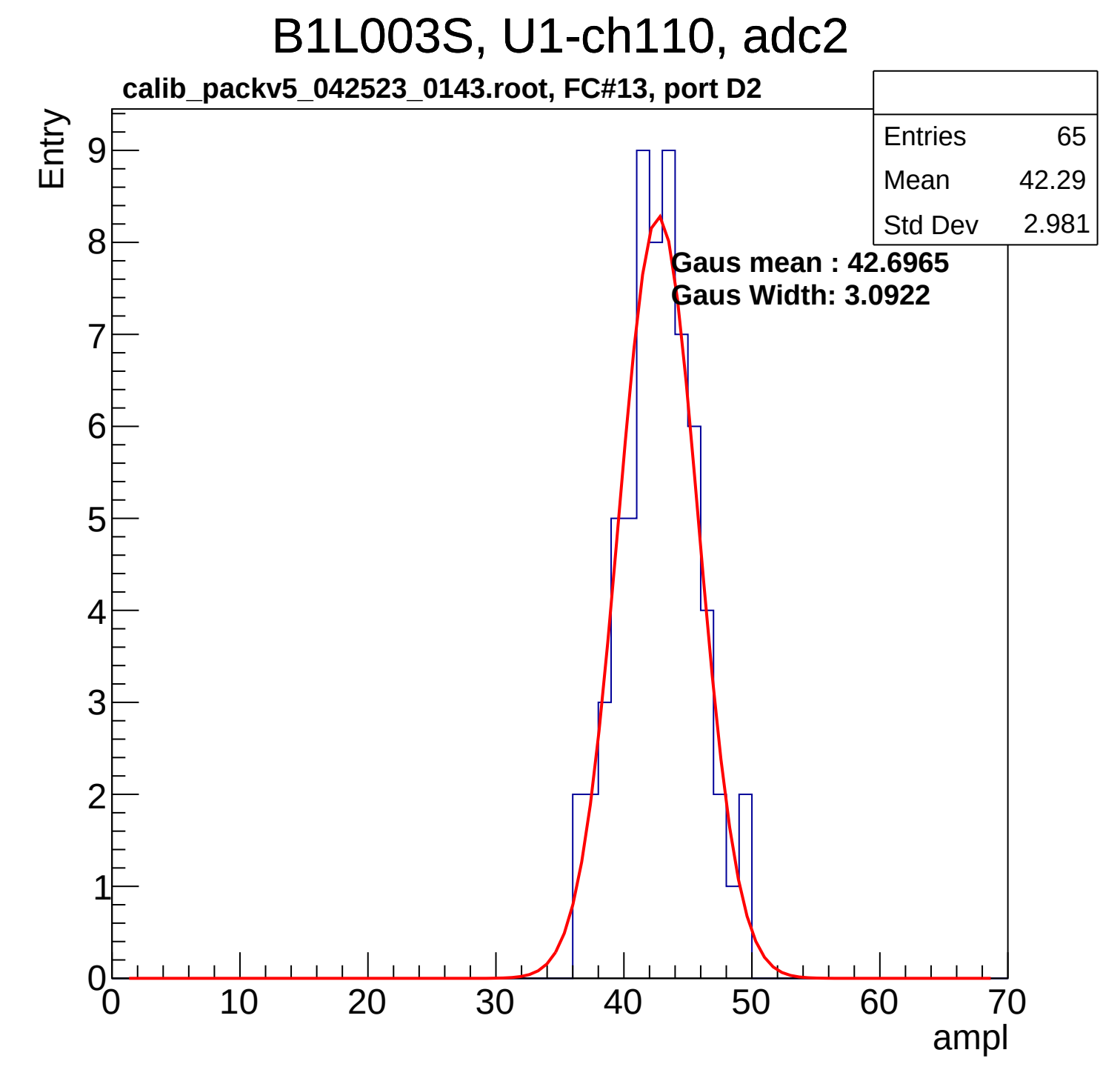
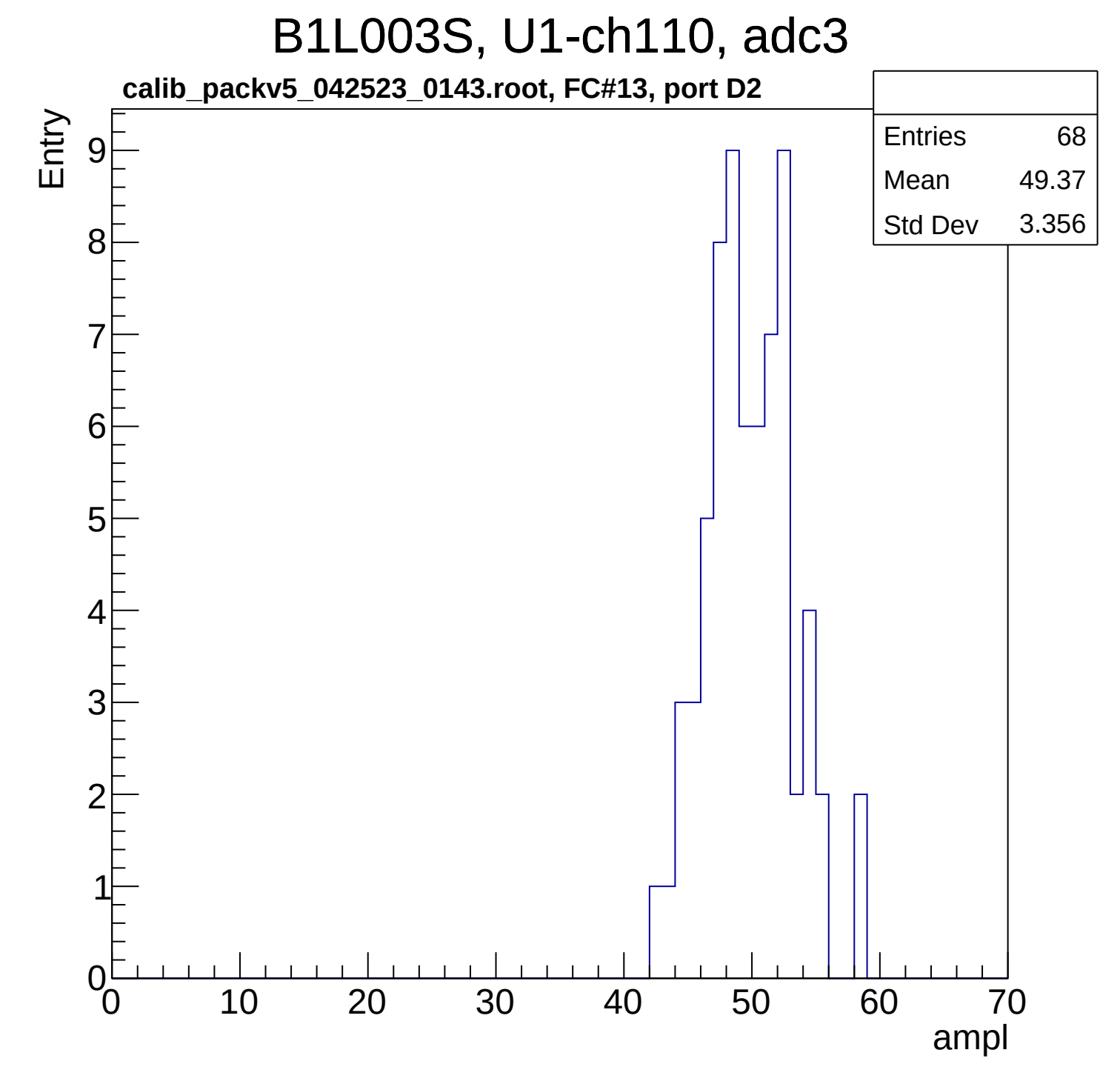

B1L003S, U1-ch109, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 61.45
Std Dev 1.588
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

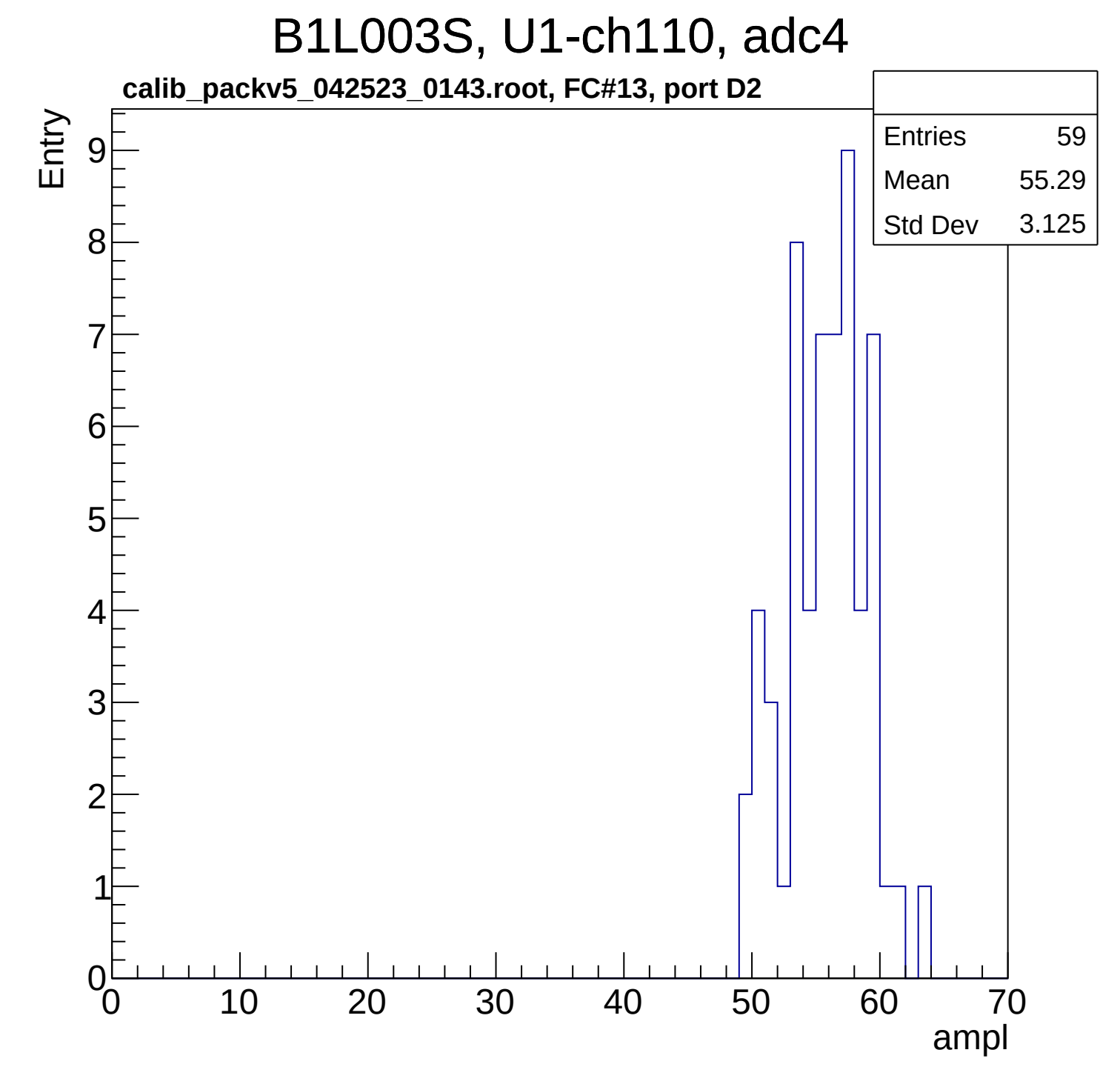

B1L003S, U1-ch109, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 40
Std Dev 19
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch110, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 28.86
Std Dev 5.075
Gaus mean : 29.2889
Gaus Width: 4.1906
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch110, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 36.17
Std Dev 3.347
Gaus mean : 36.8117
Gaus Width: 2.7740
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch110, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 42.29
Std Dev 2.981
Gaus mean : 42.6965
Gaus Width: 3.0922
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch110, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 49.37
Std Dev 3.356
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch110, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 55.29
Std Dev 3.125
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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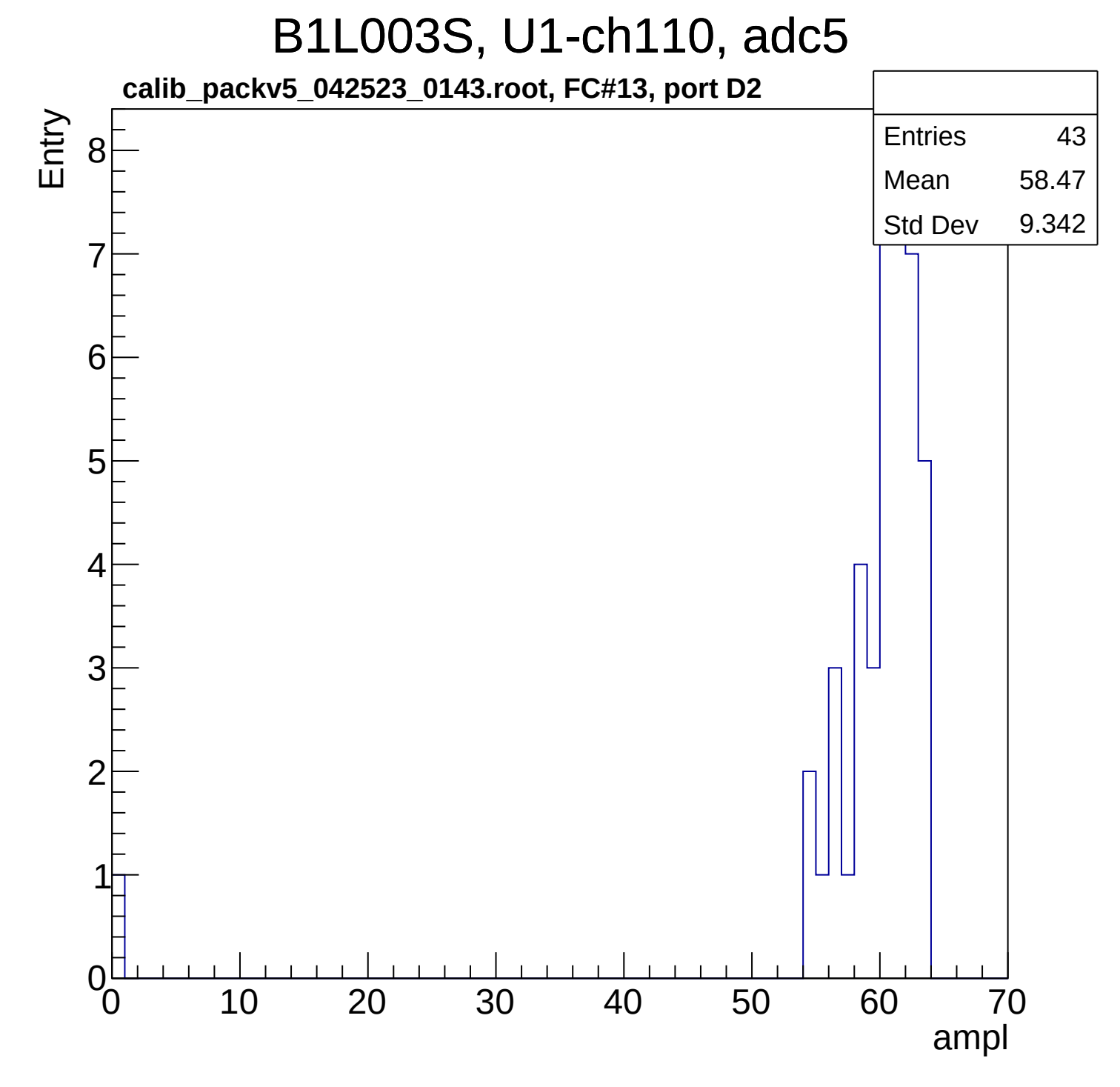
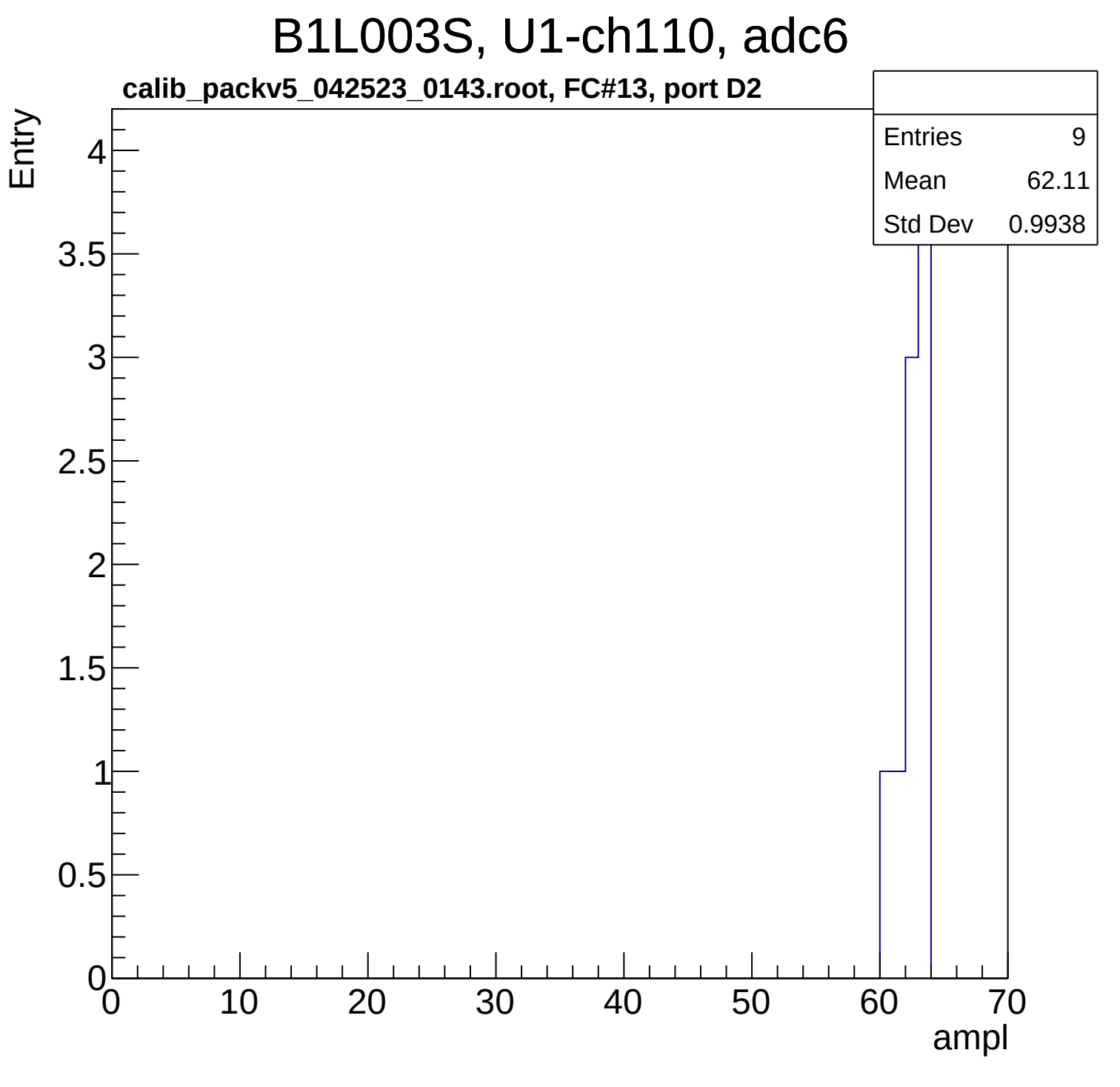
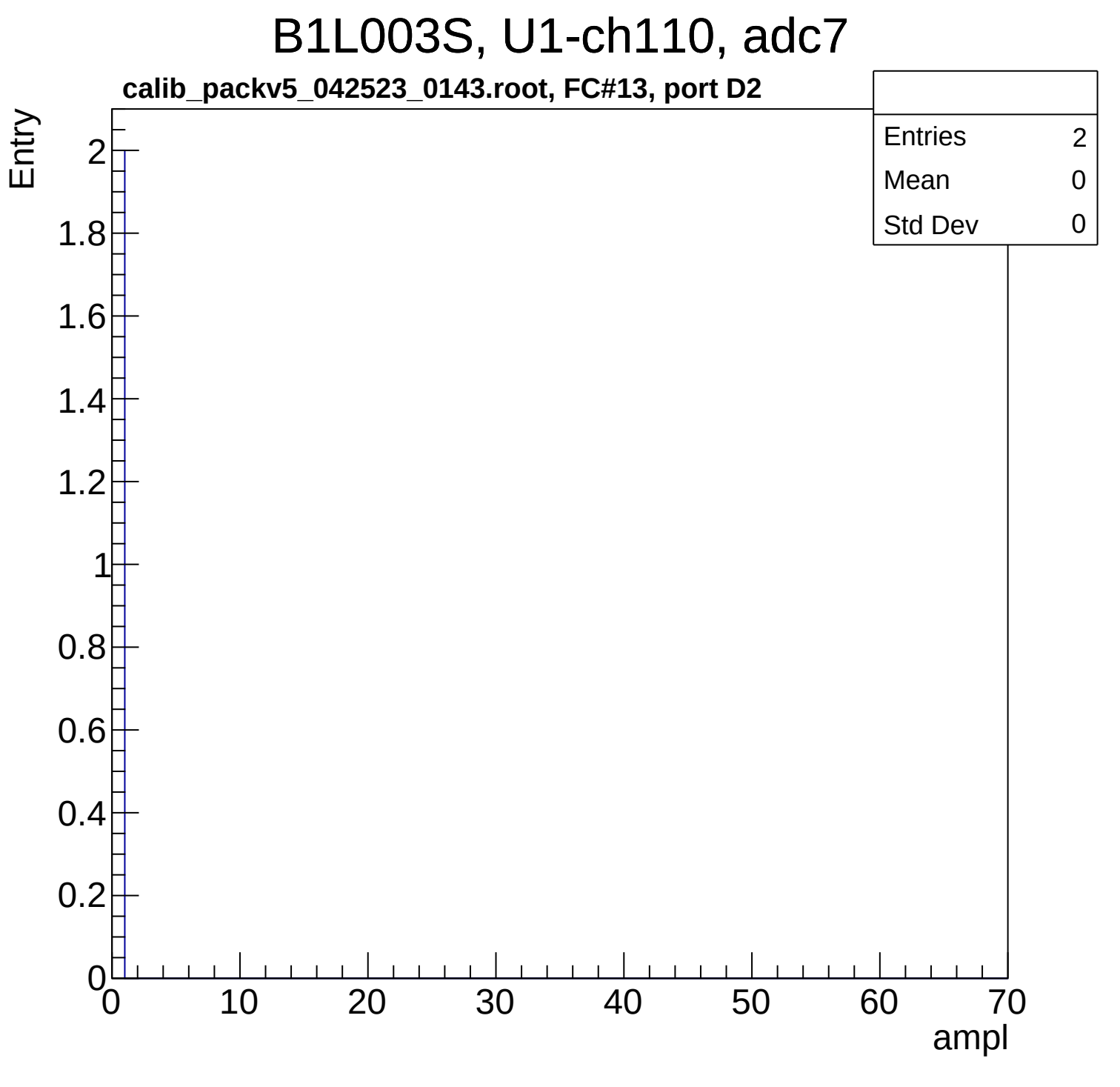
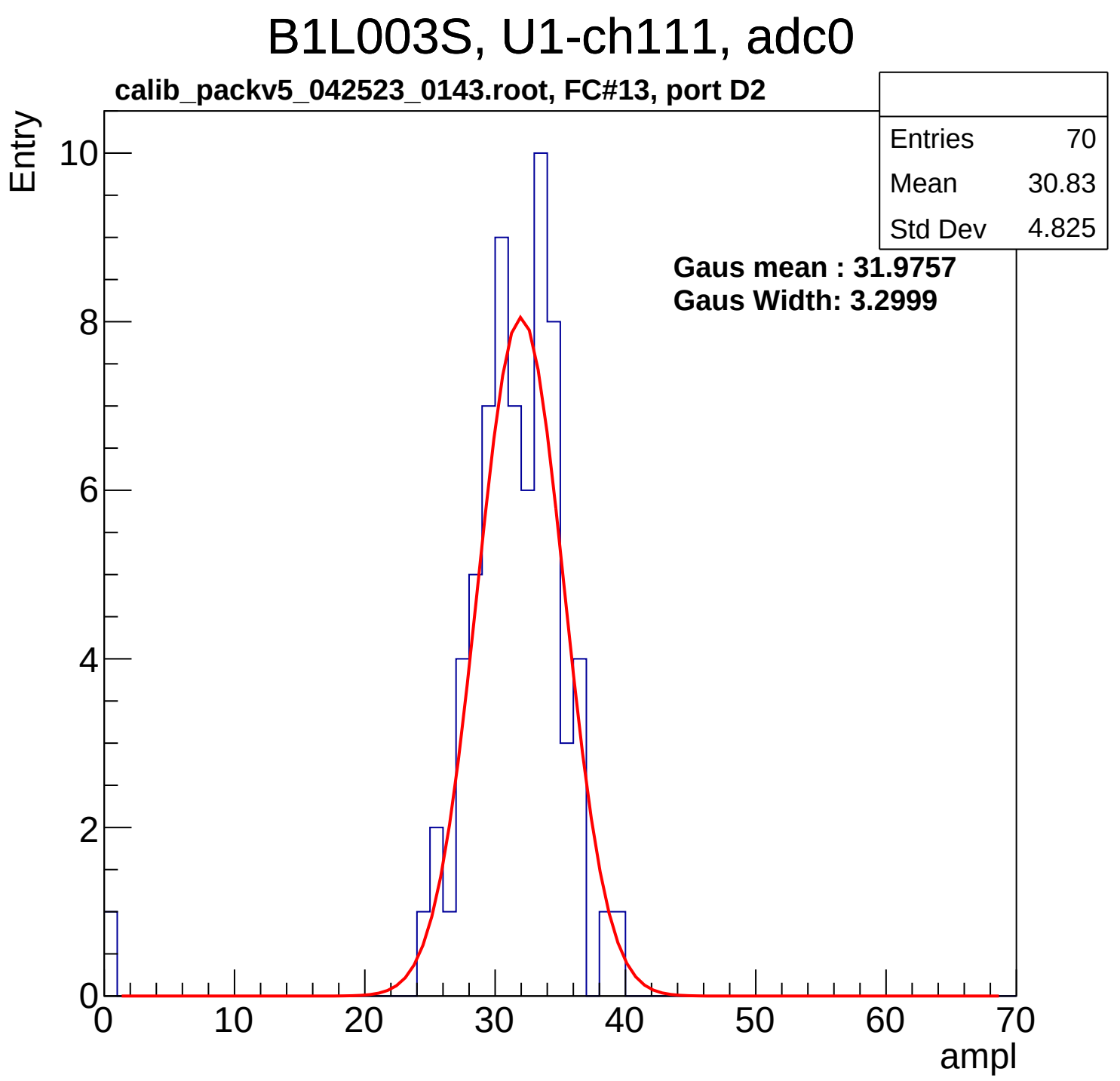
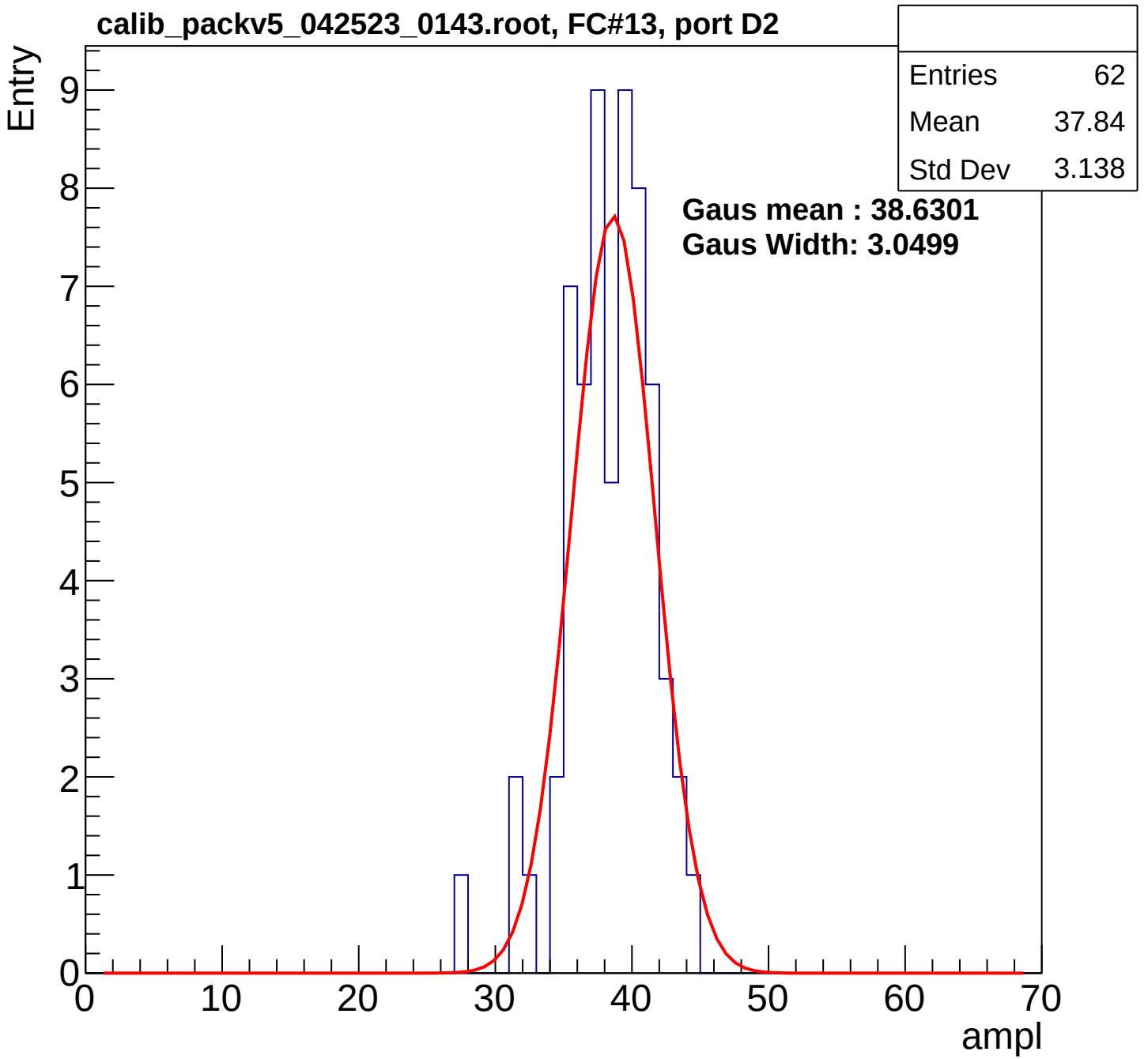
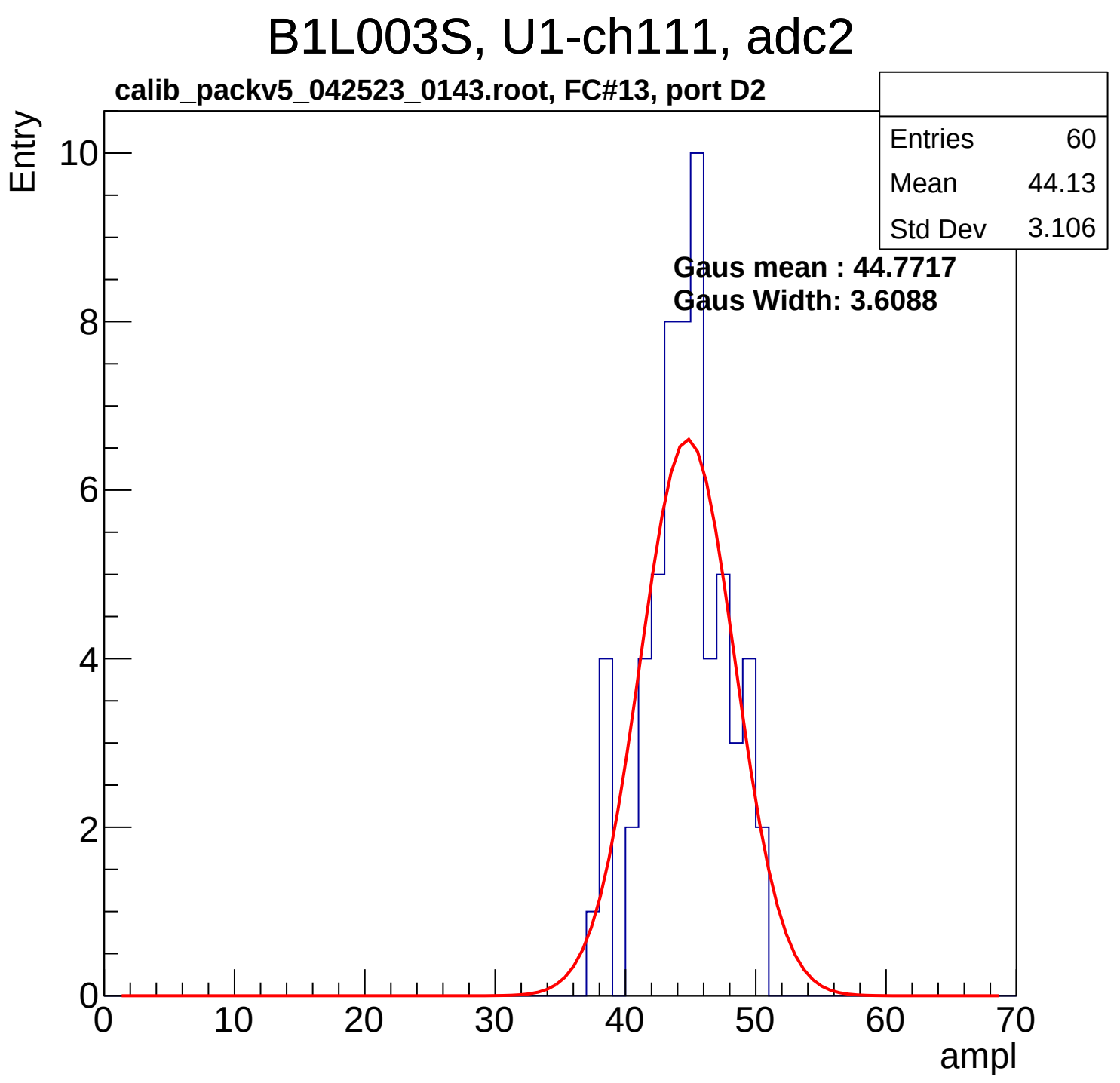

B1L003S, U1-ch110, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 43
Mean 58.47
Std Dev 9.342
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

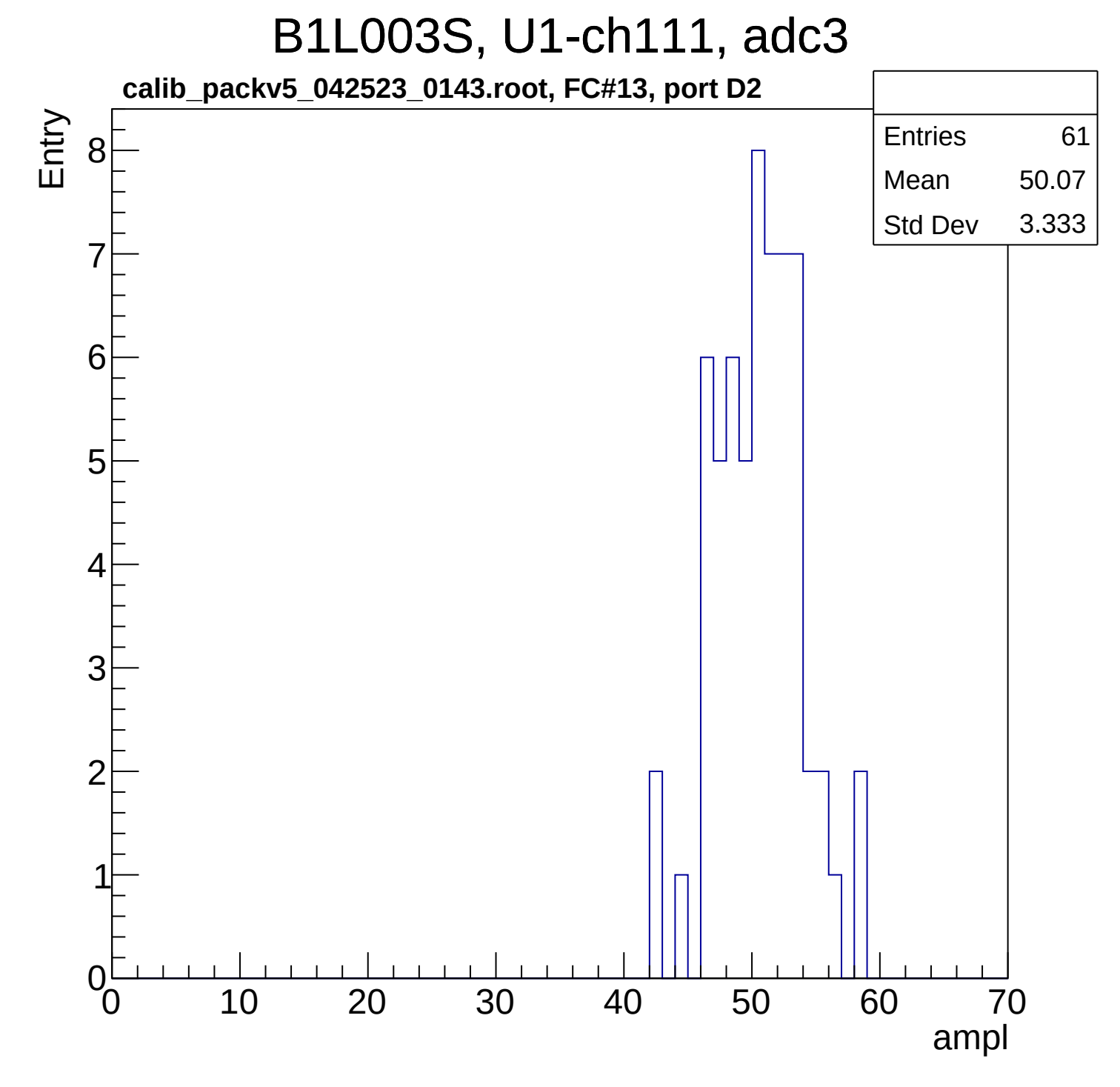

B1L003S, U1-ch110, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 62.11
Std Dev 0.9938
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

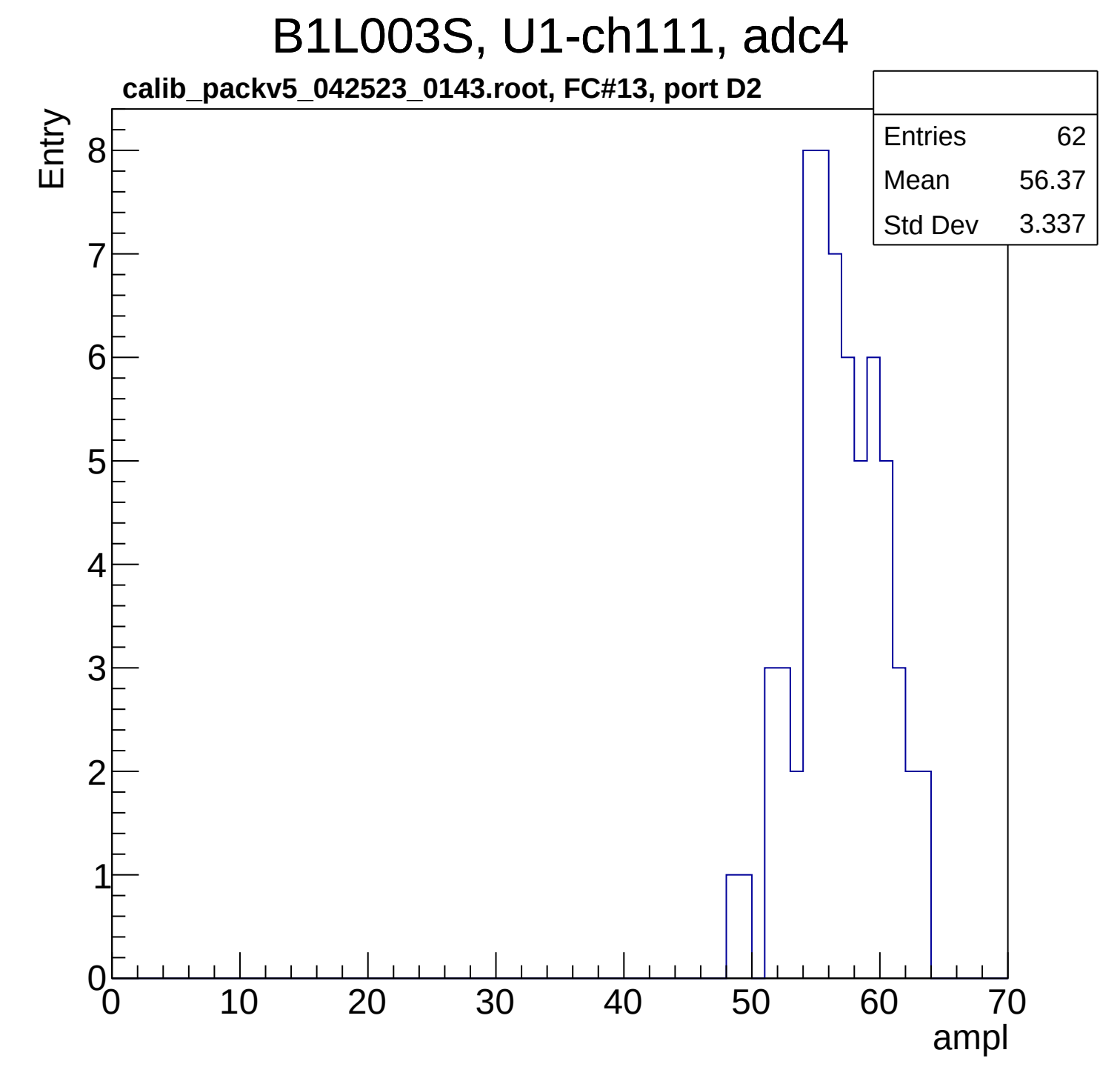

B1L003S, U1-ch110, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

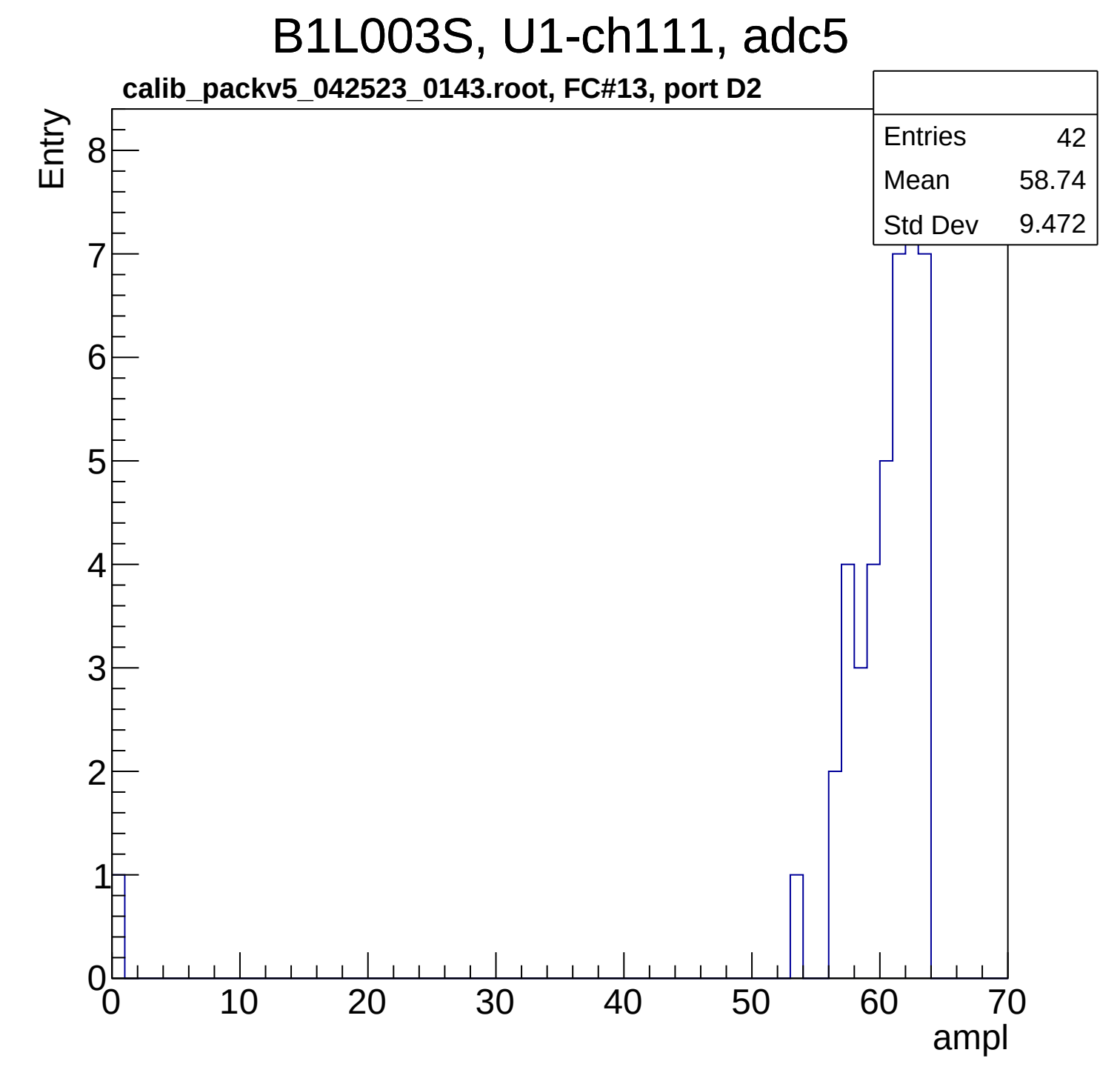

B1L003S, U1-ch111, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 30.83
Std Dev 4.825
Gaus mean : 31.9757
Gaus Width: 3.2999
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch111, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.84
Std Dev 3.138
Gaus mean : 38.6301
Gaus Width: 3.0499
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch111, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 44.13
Std Dev 3.106
Gaus mean : 44.7717
Gaus Width: 3.6088
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch111, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 50.07
Std Dev 3.333
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch111, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 56.37
Std Dev 3.337
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch111, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 58.74
Std Dev 9.472
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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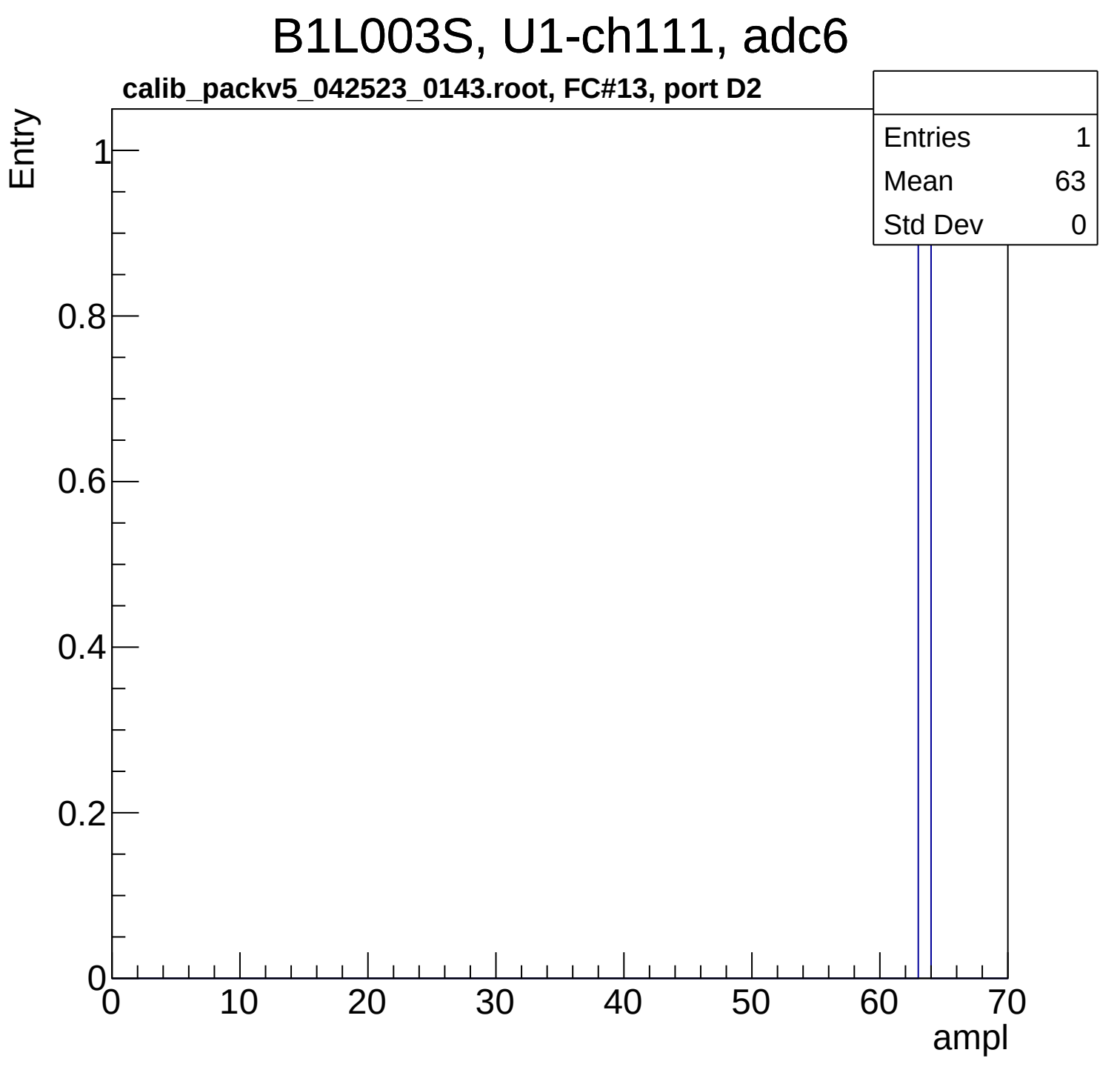
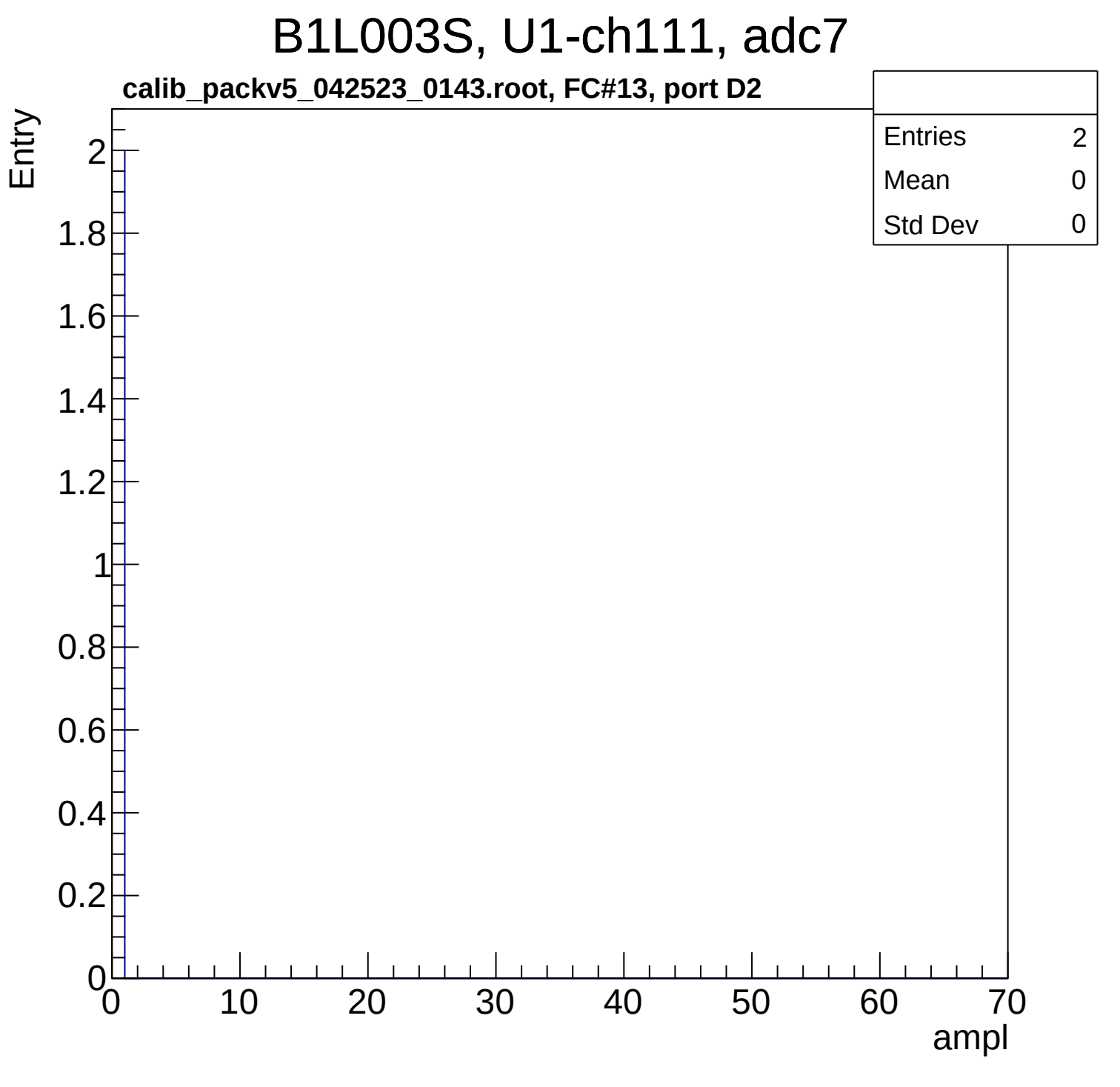
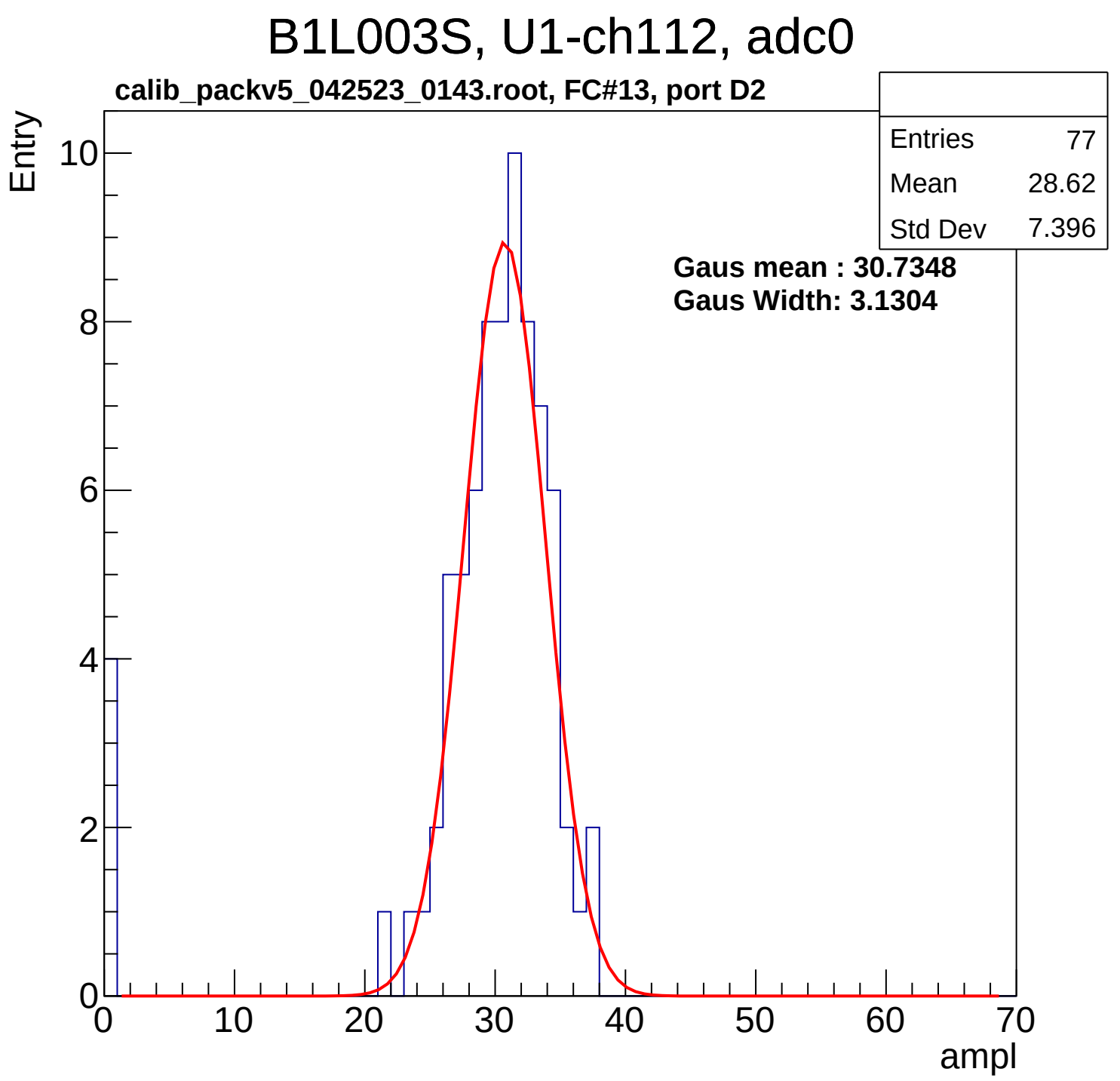
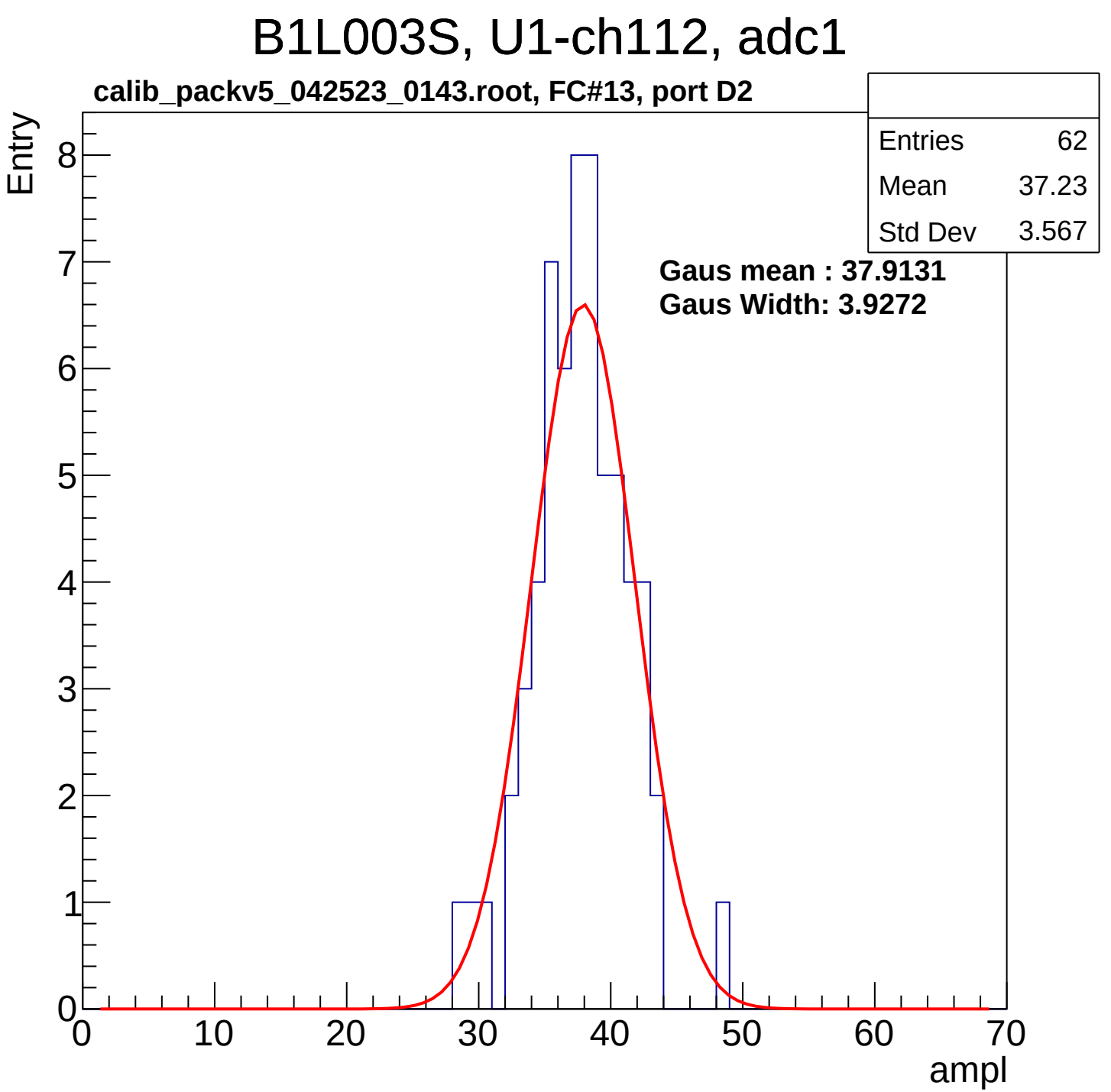
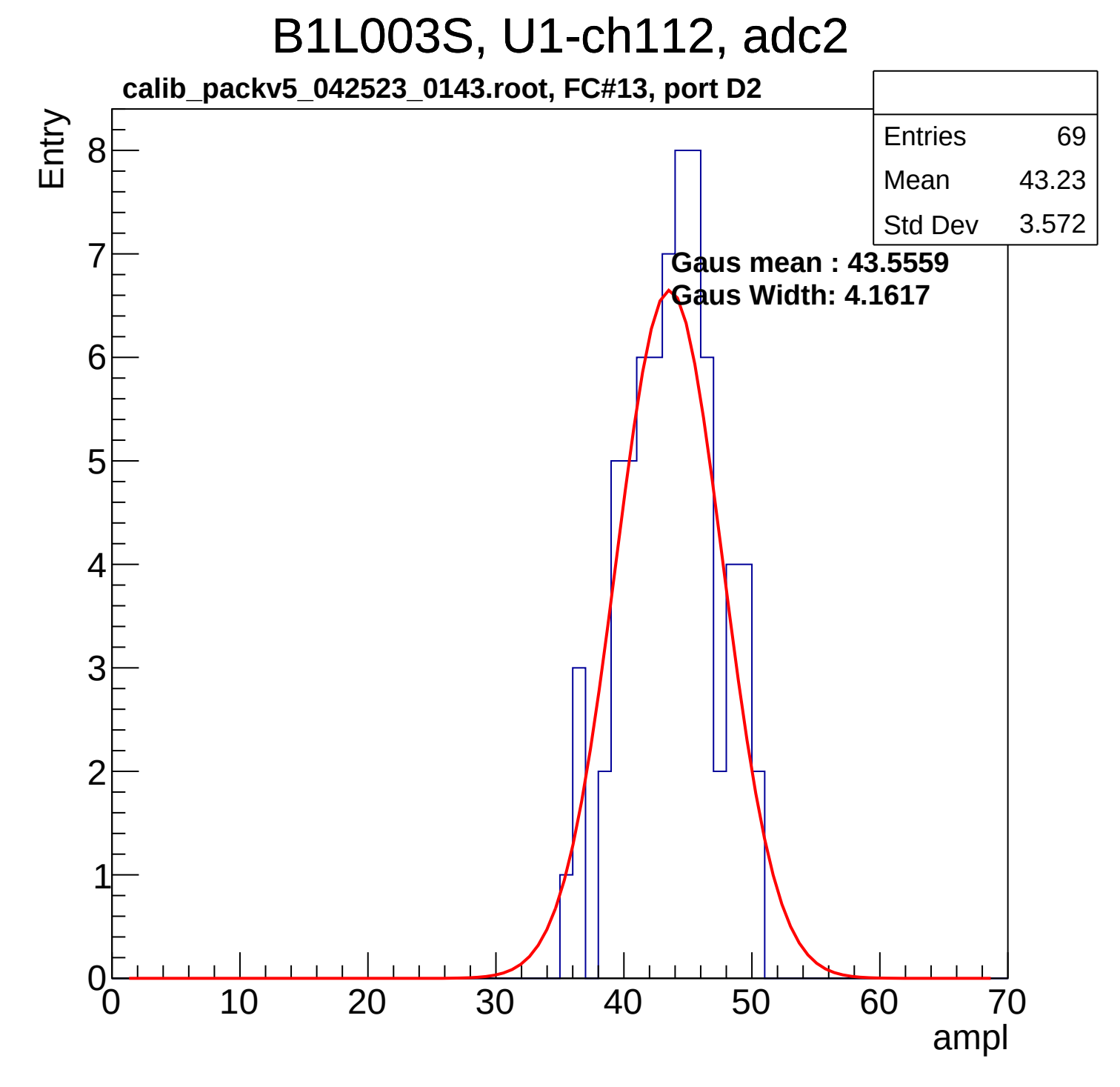
60
70


B1L003S, U1-ch111, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch111, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch112, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.62
Std Dev 7.396
Gaus mean : 30.7348
Gaus Width: 3.1304
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch112, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.23
Std Dev 3.567
Gaus mean : 37.9131
Gaus Width: 3.9272
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch112, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 69
Mean 43.23
Std Dev 3.572
Gaus mean : 43.5559
Gaus Width: 4.1617
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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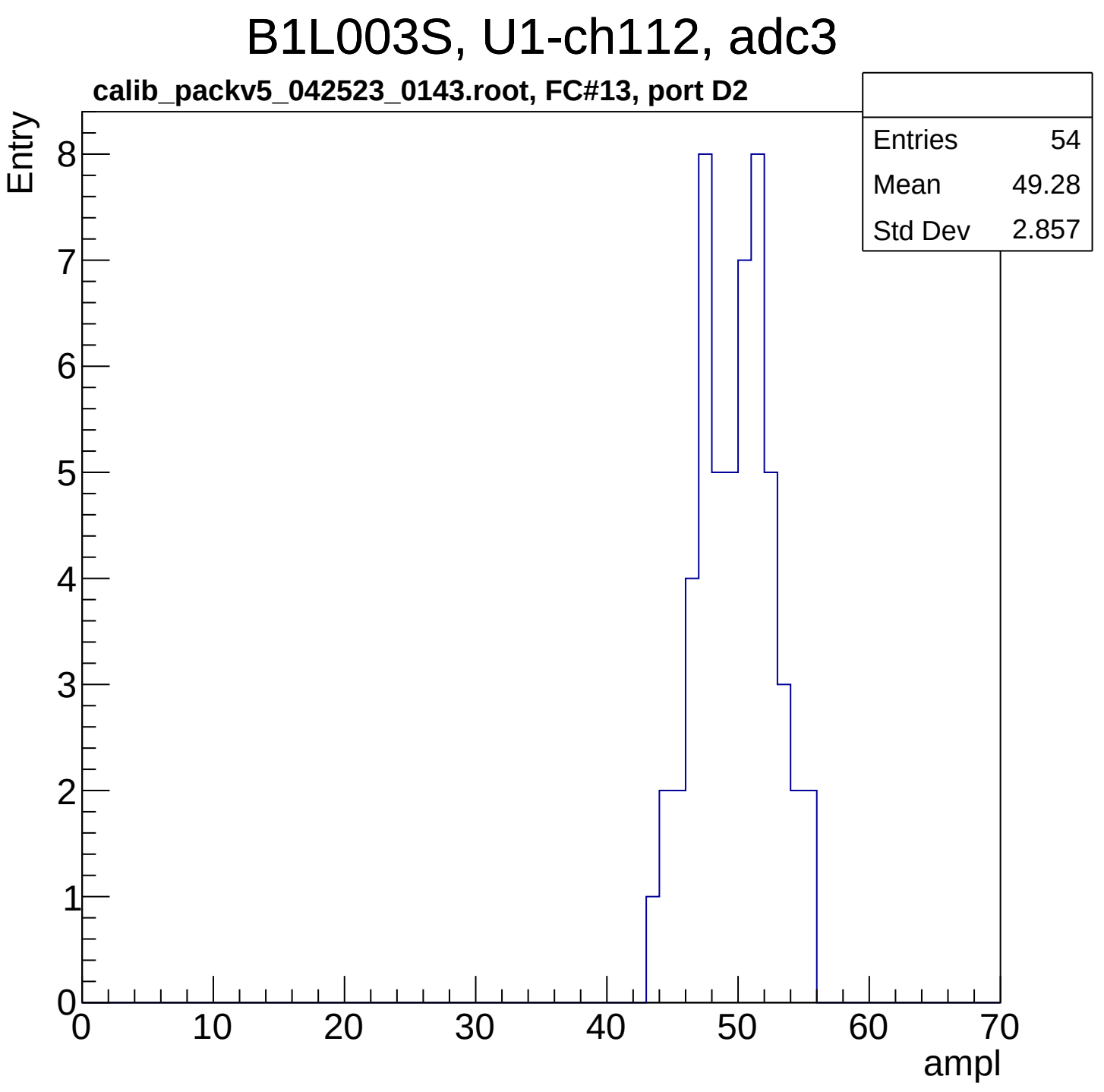
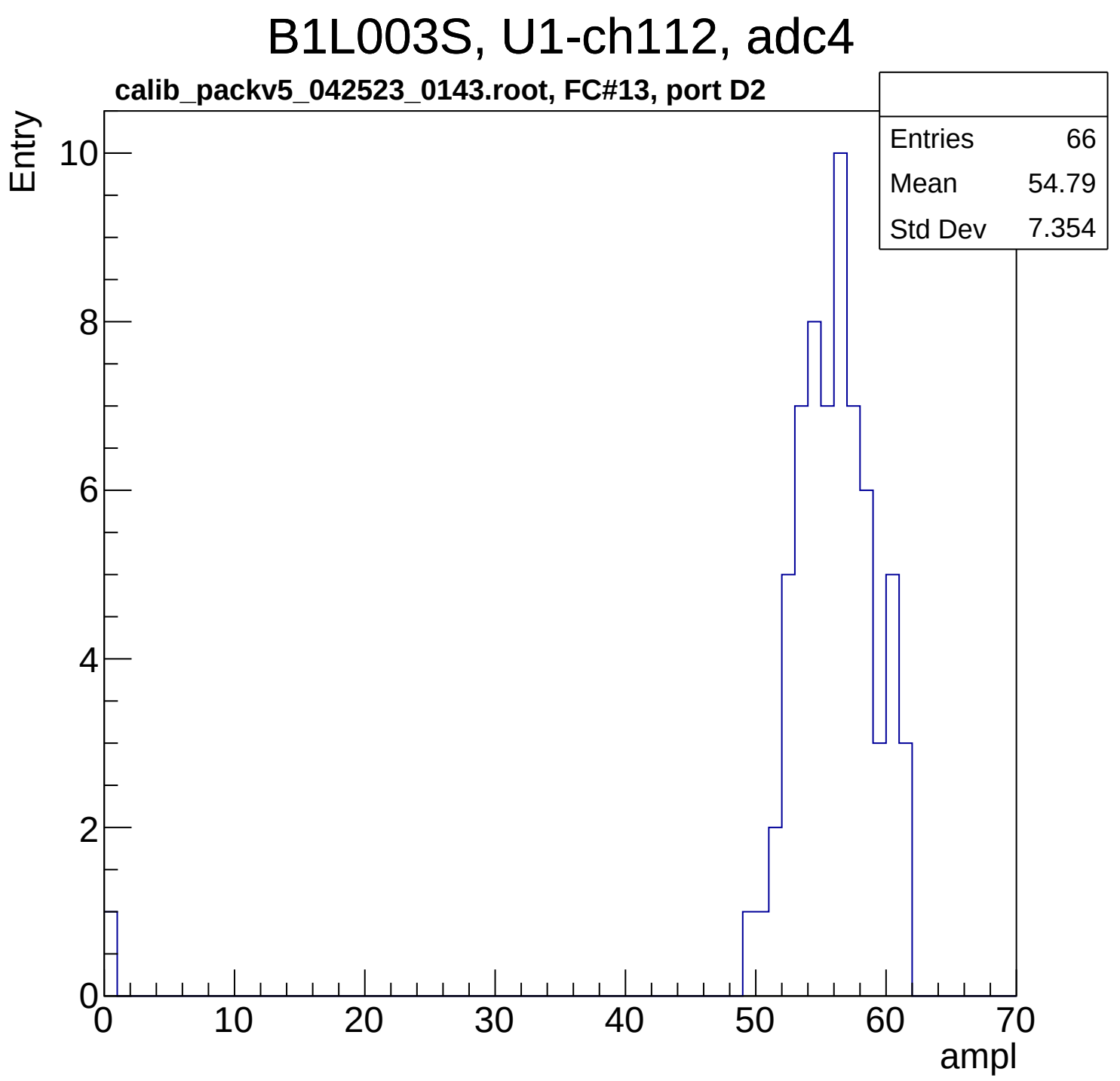
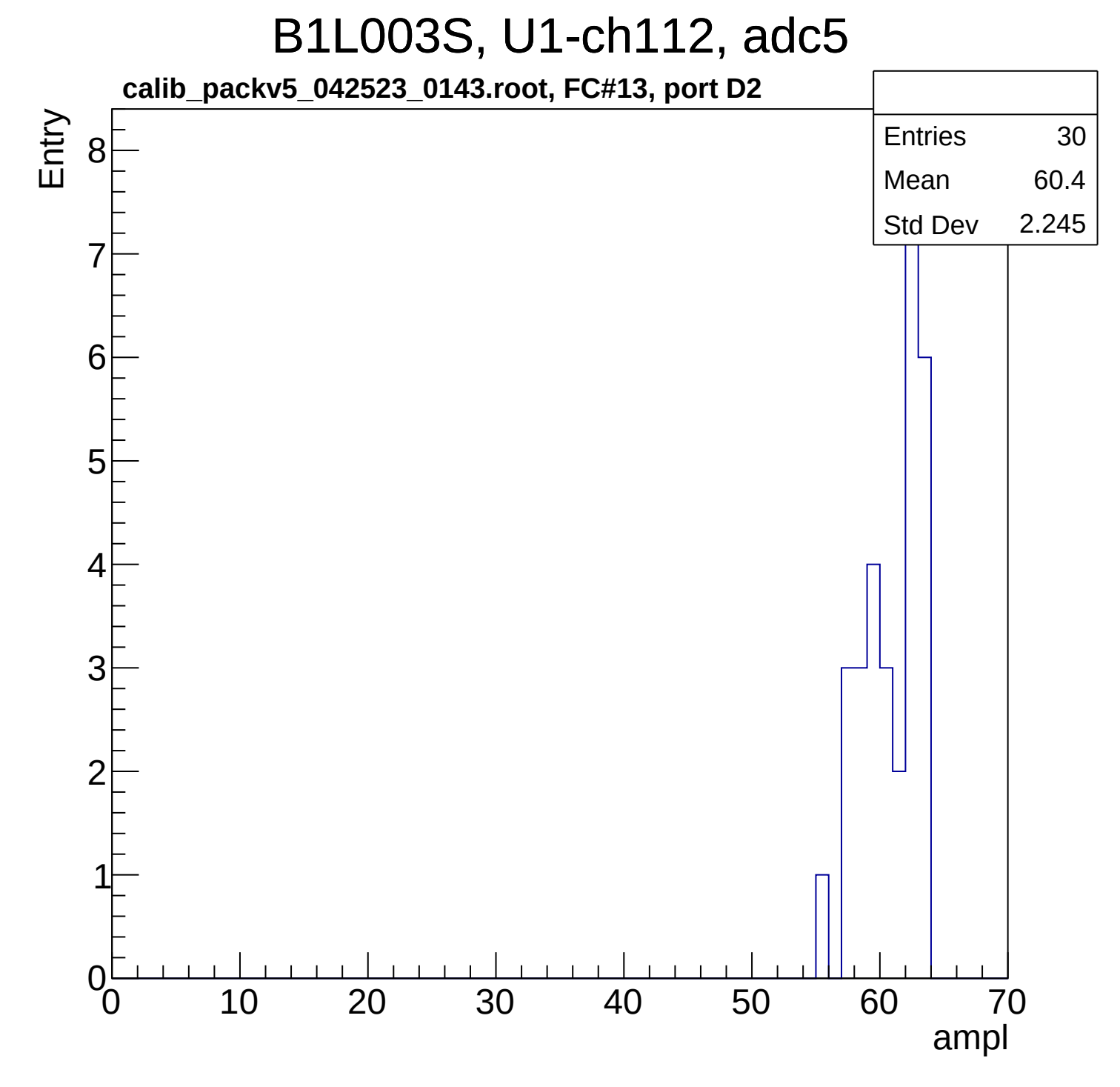
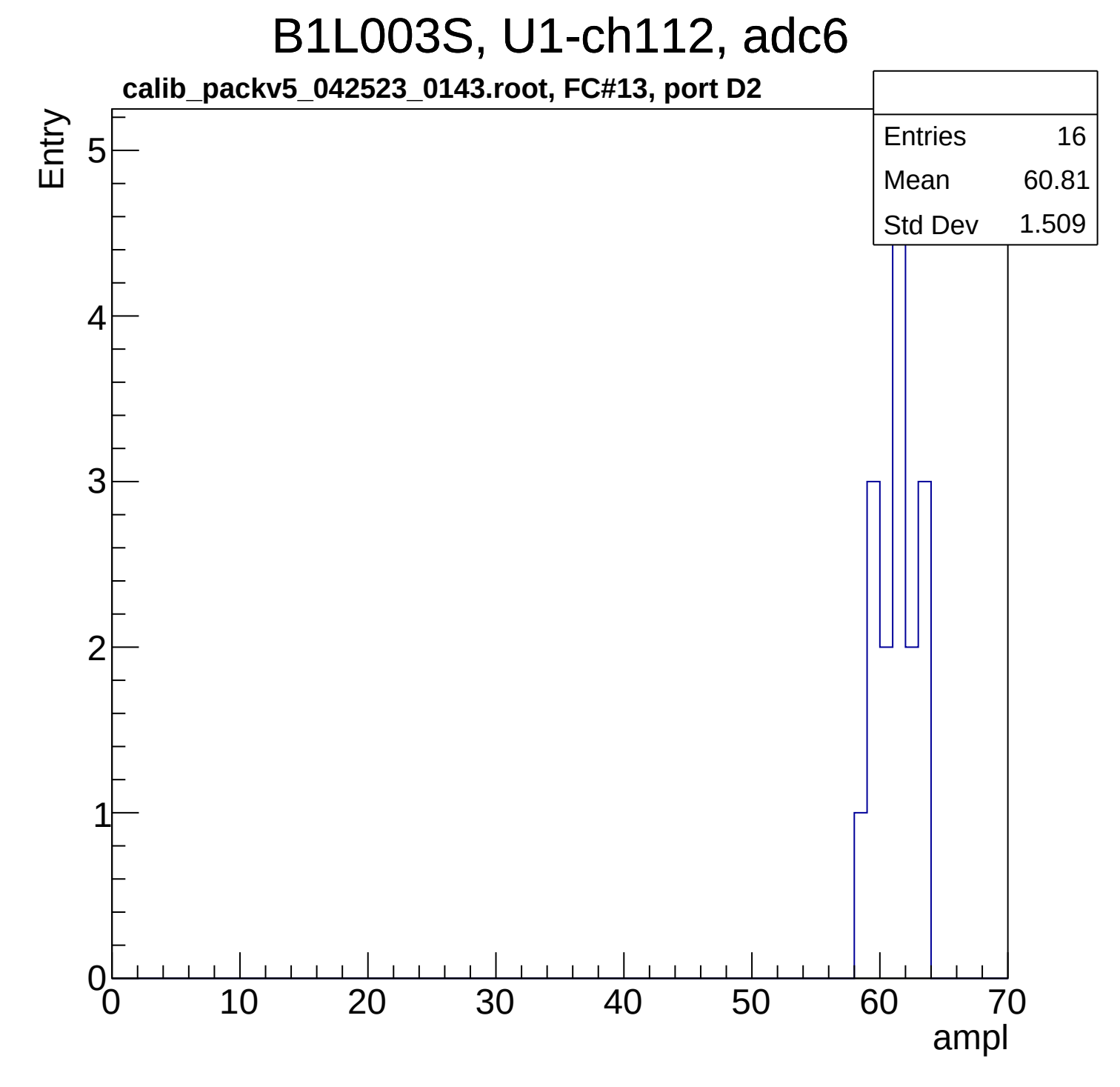
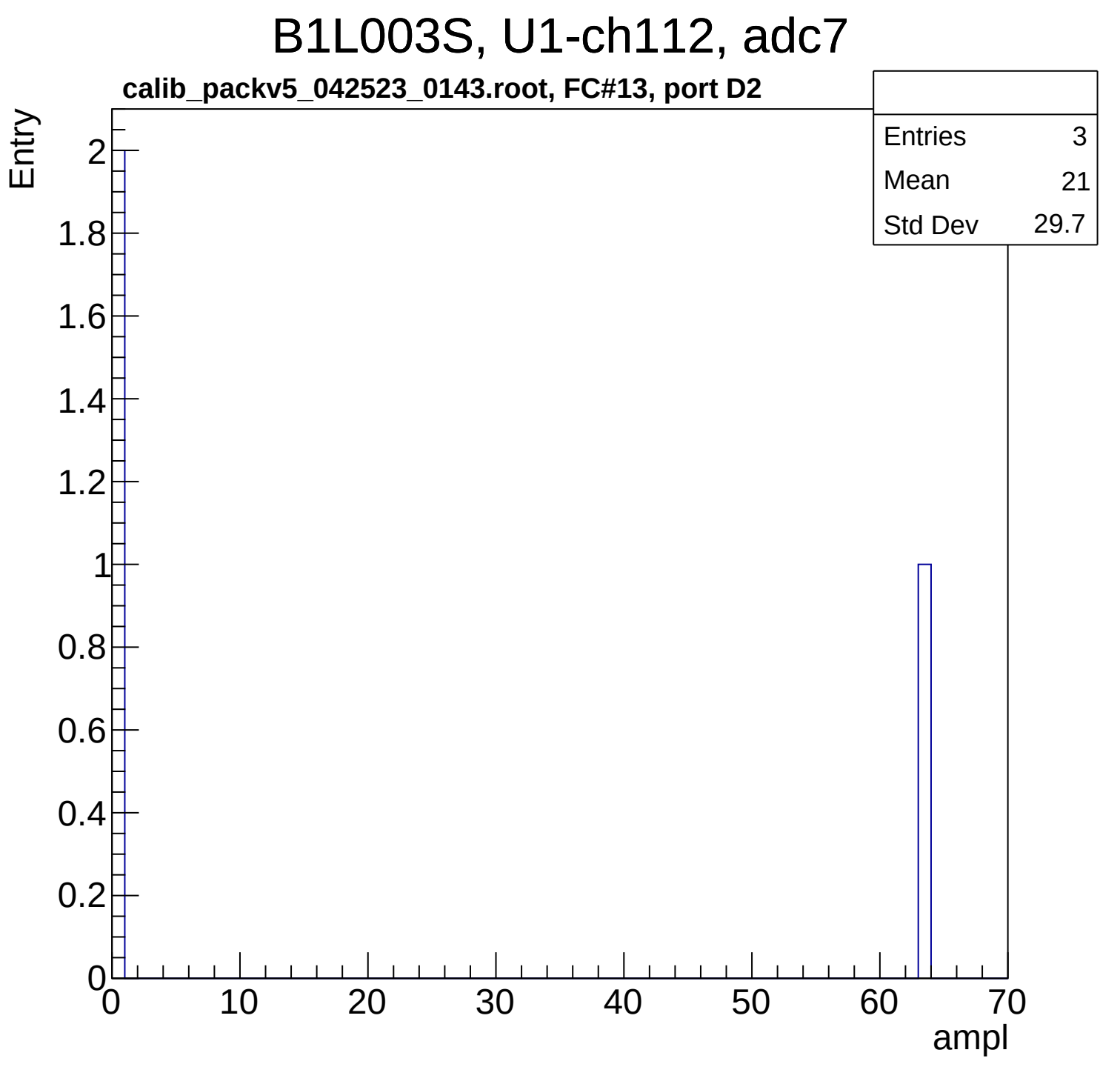
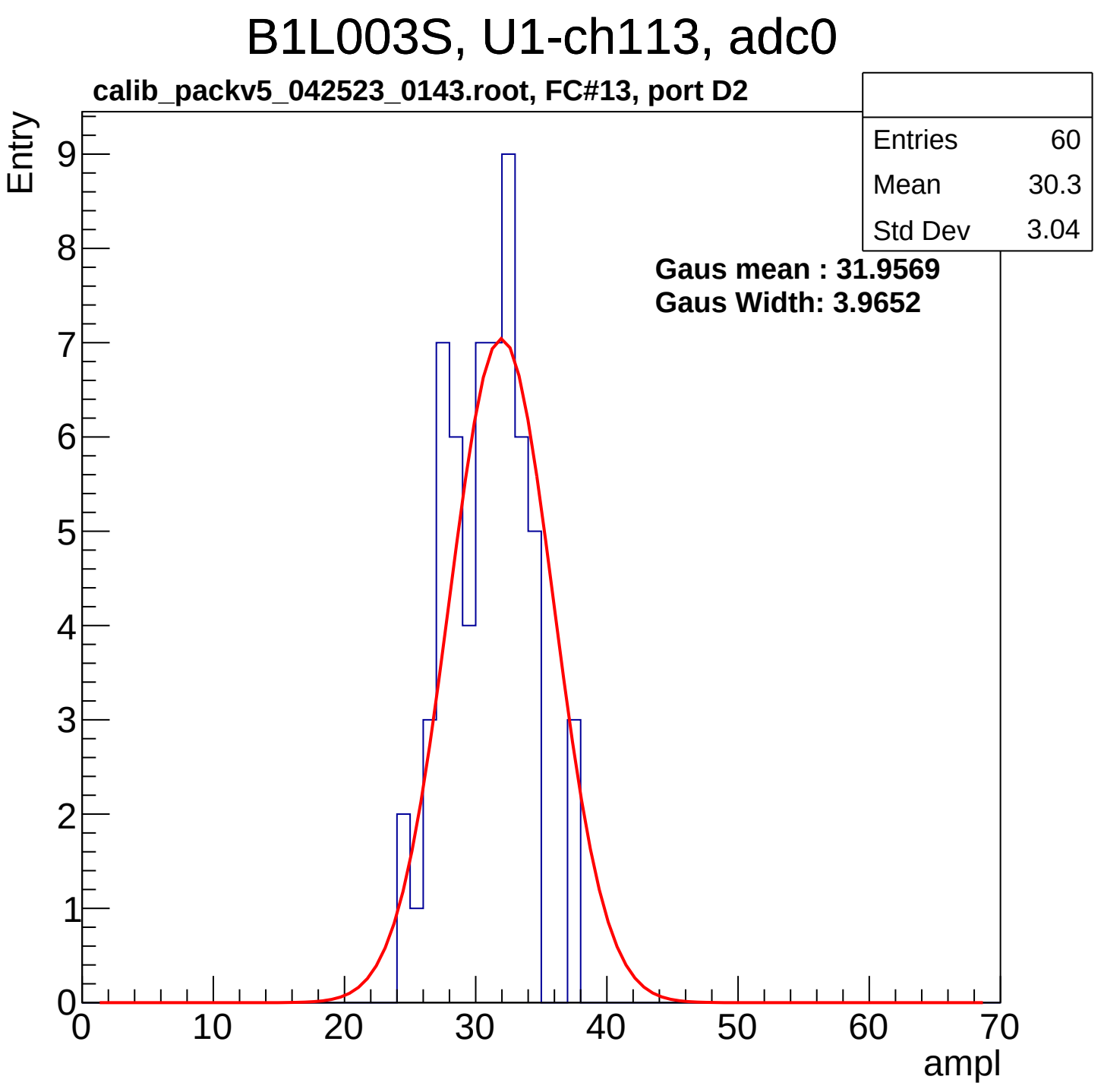

B1L003S, U1-ch112, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 49.28
Std Dev 2.857
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch112, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 54.79
Std Dev 7.354
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch112, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 30
Mean 60.4
Std Dev 2.245
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch112, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 60.81
Std Dev 1.509
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch112, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch113, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 30.3
Std Dev 3.04
Gaus mean : 31.9569
Gaus Width: 3.9652
Entry
ampl
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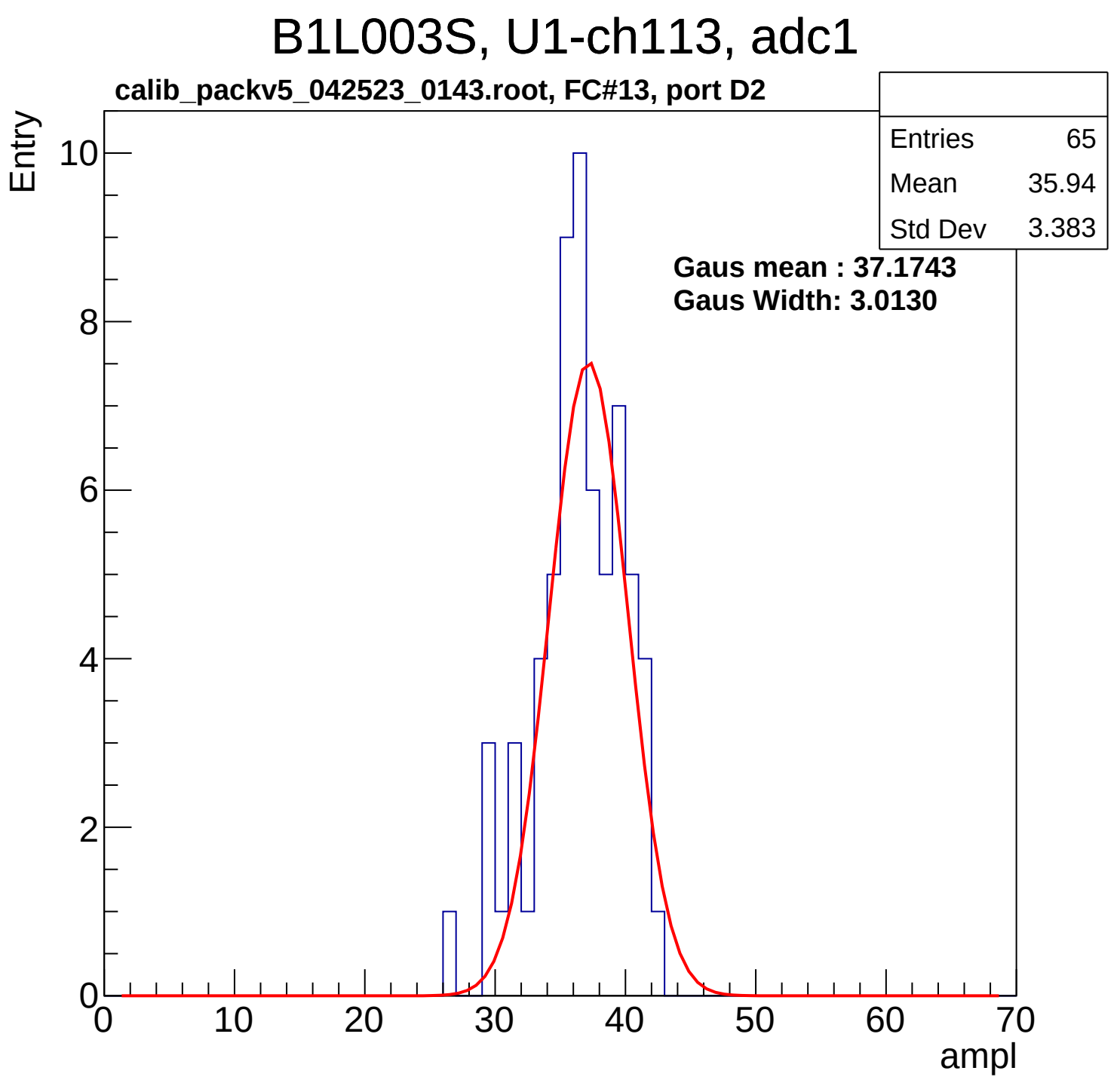
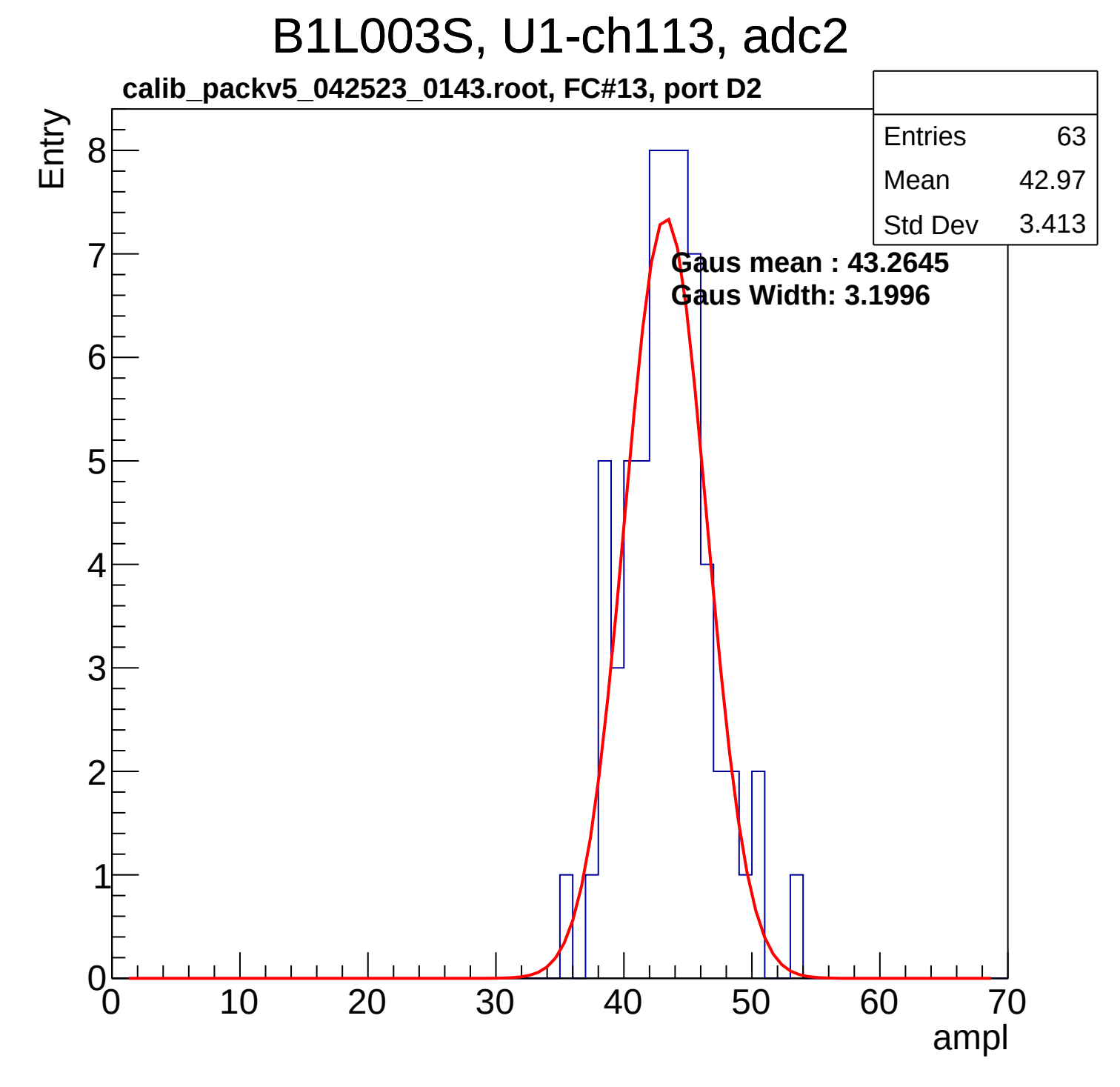
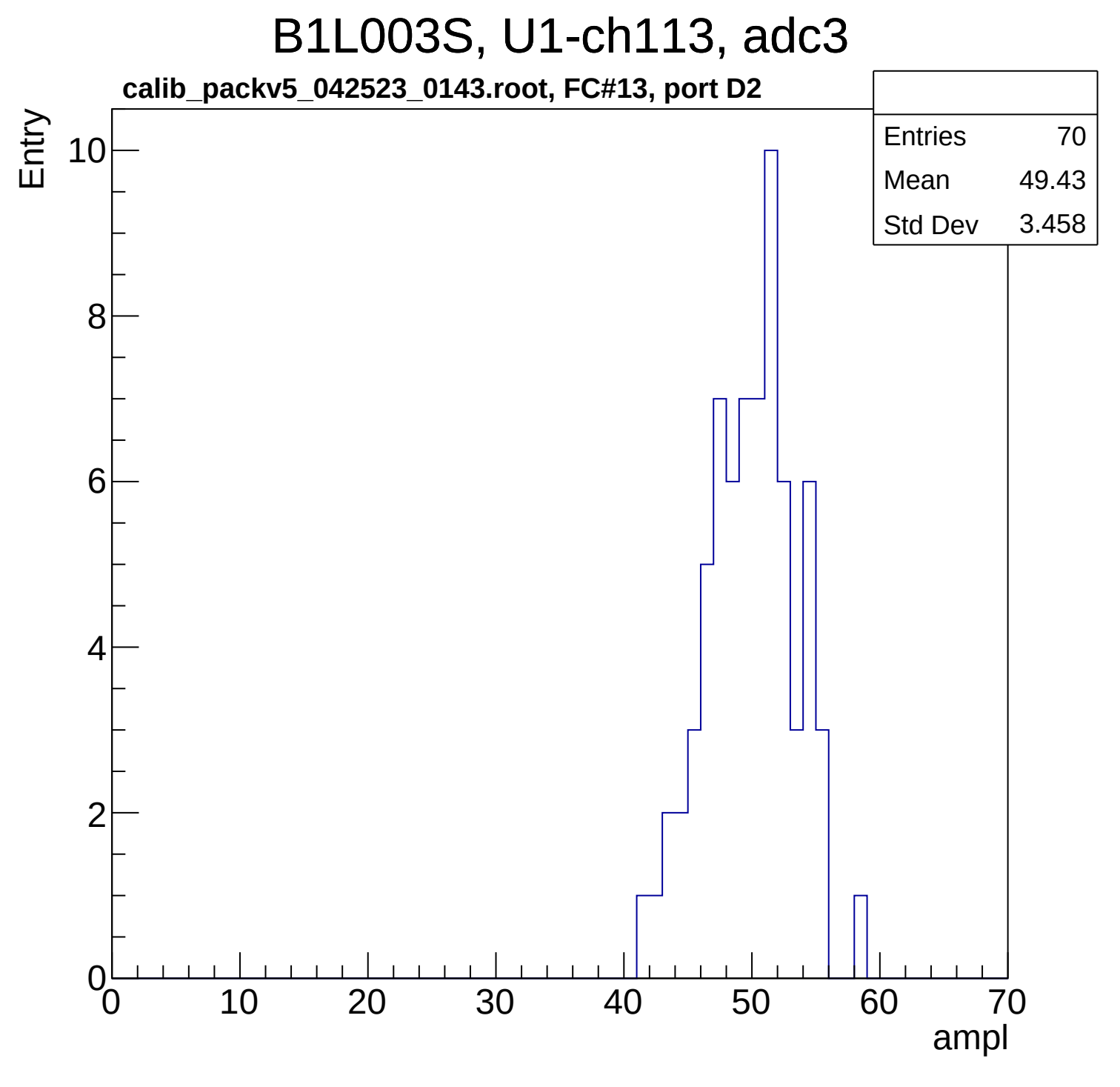
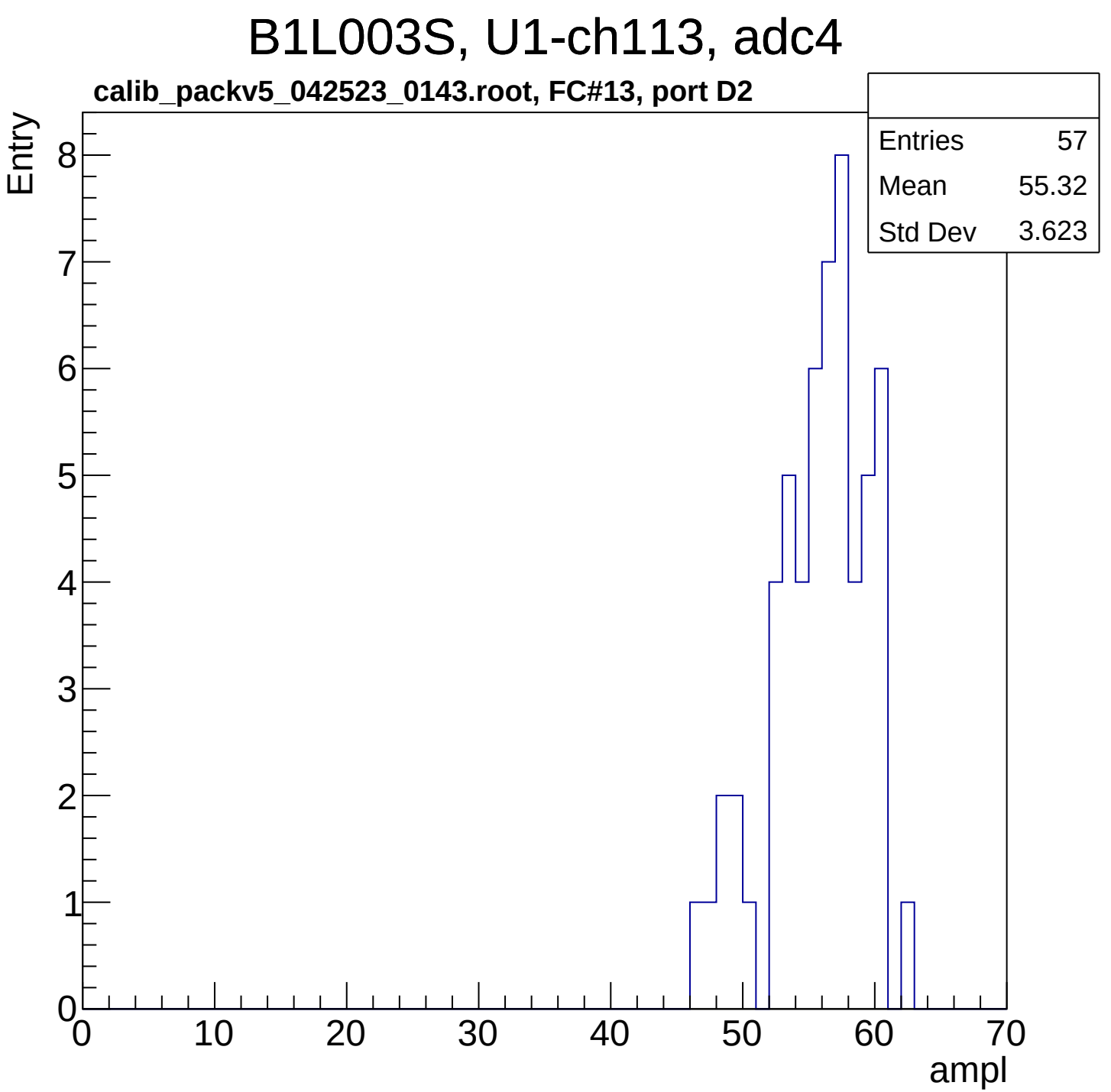
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L003S, U1-ch113, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.94
Std Dev 3.383
Gaus mean : 37.1743
Gaus Width: 3.0130
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch113, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 42.97
Std Dev 3.413
Gaus mean : 43.2645
Gaus Width: 3.1996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch113, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 49.43
Std Dev 3.458
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch113, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 55.32
Std Dev 3.623
Entry
ampl
0
1
2
3
4
5
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7
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0
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40
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60
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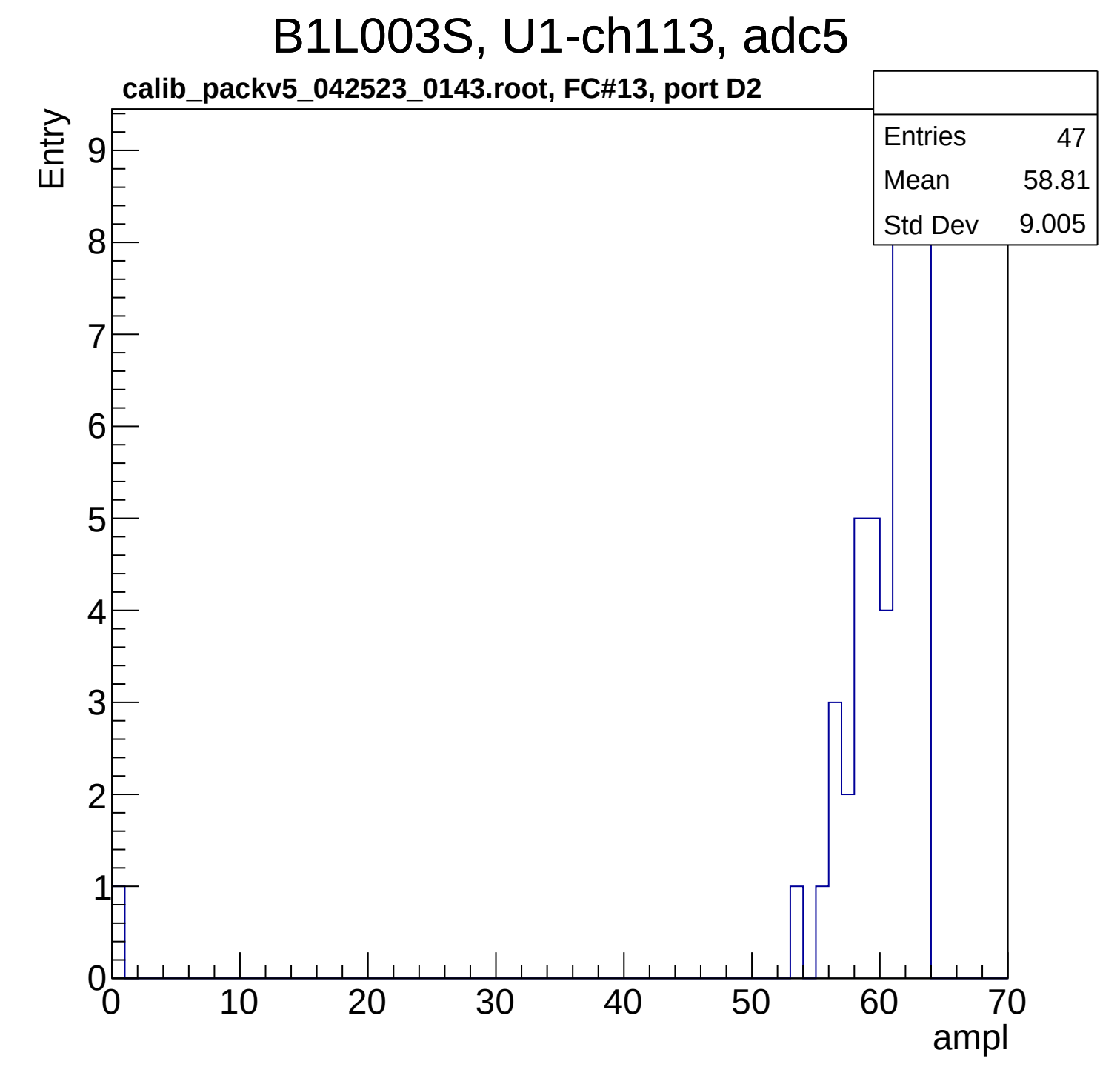
B1L003S, U1-ch113, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 58.81
Std Dev 9.005
Entry
ampl
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3
4
5
6
7
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0
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60
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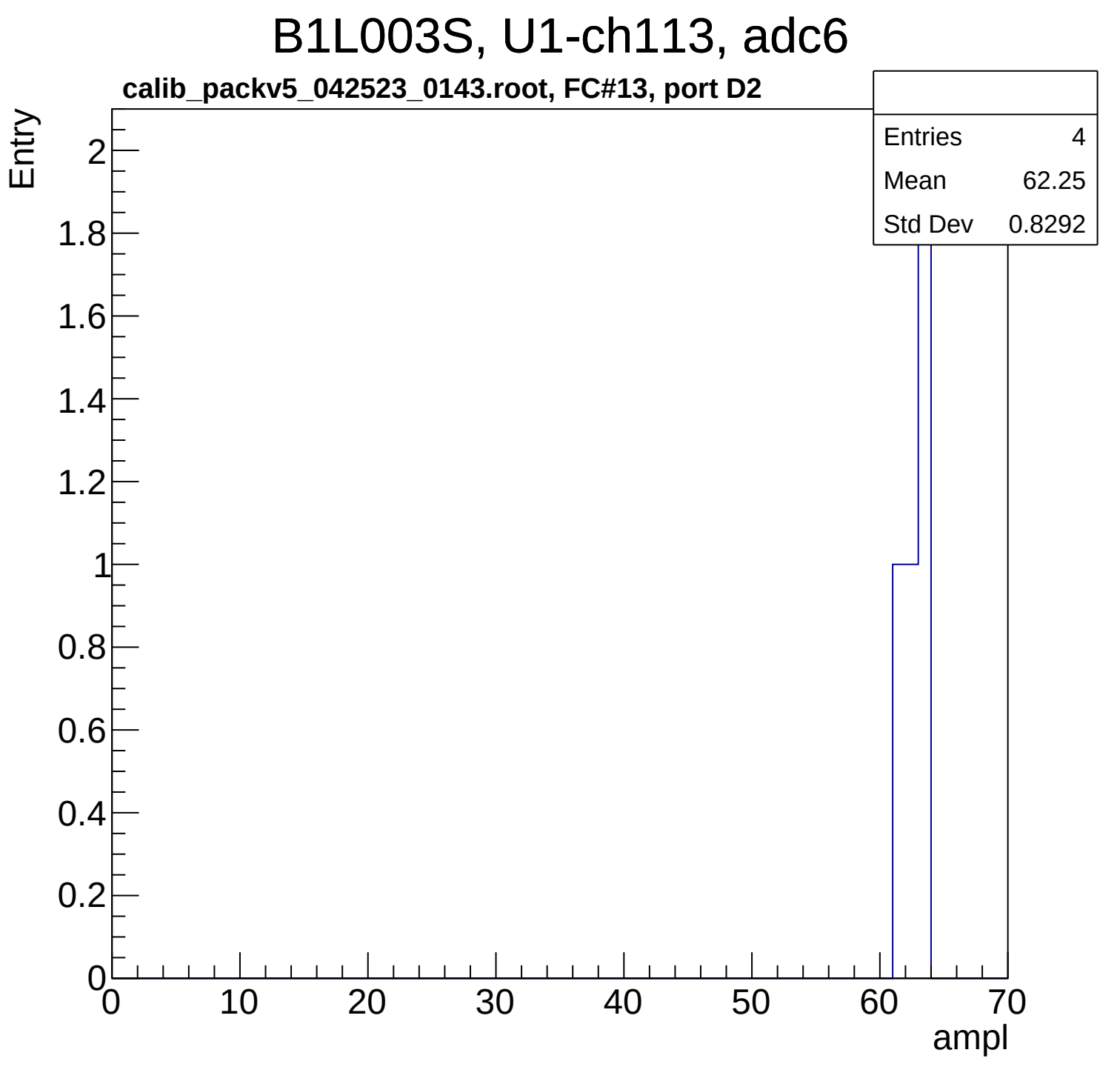
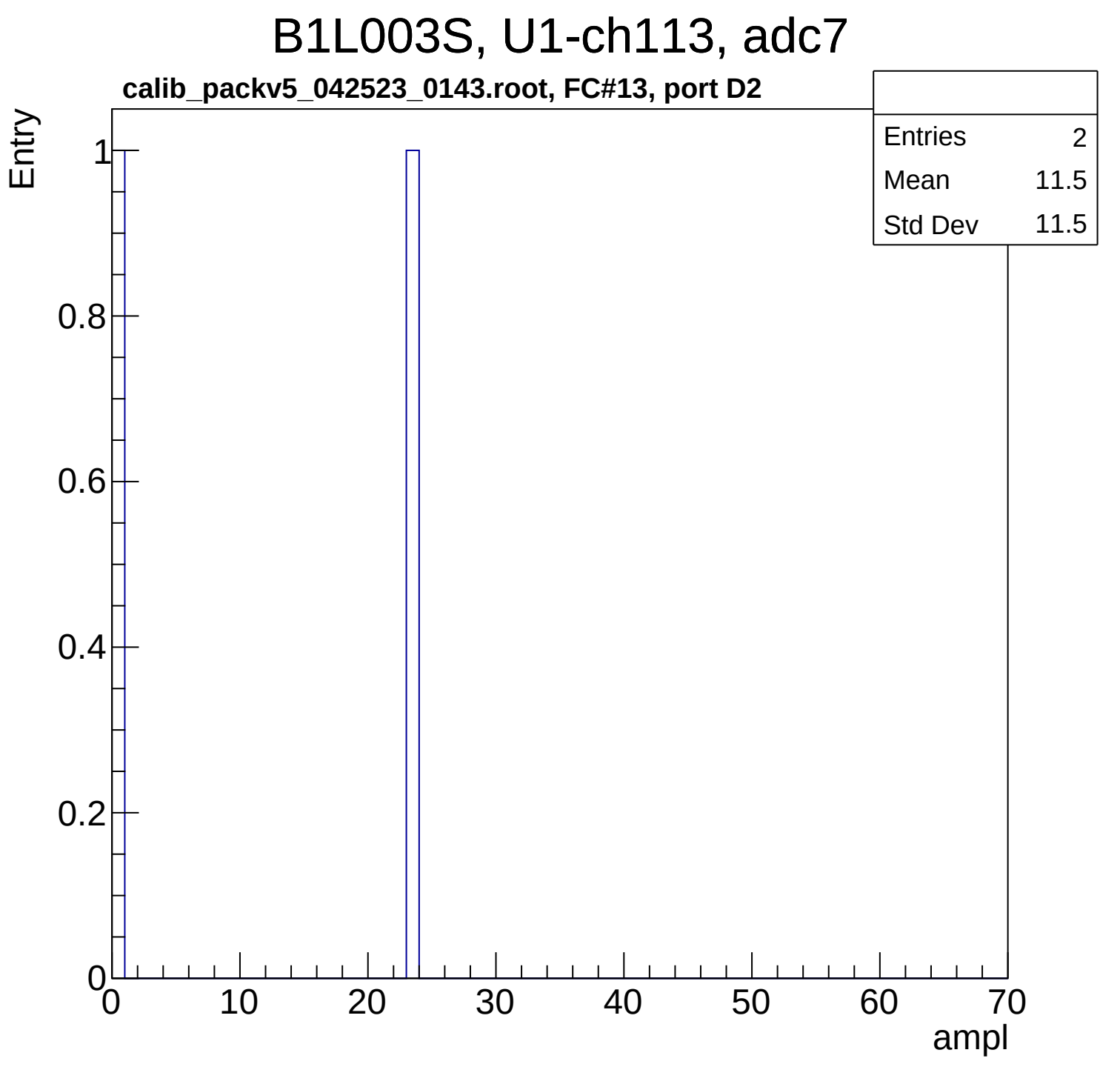
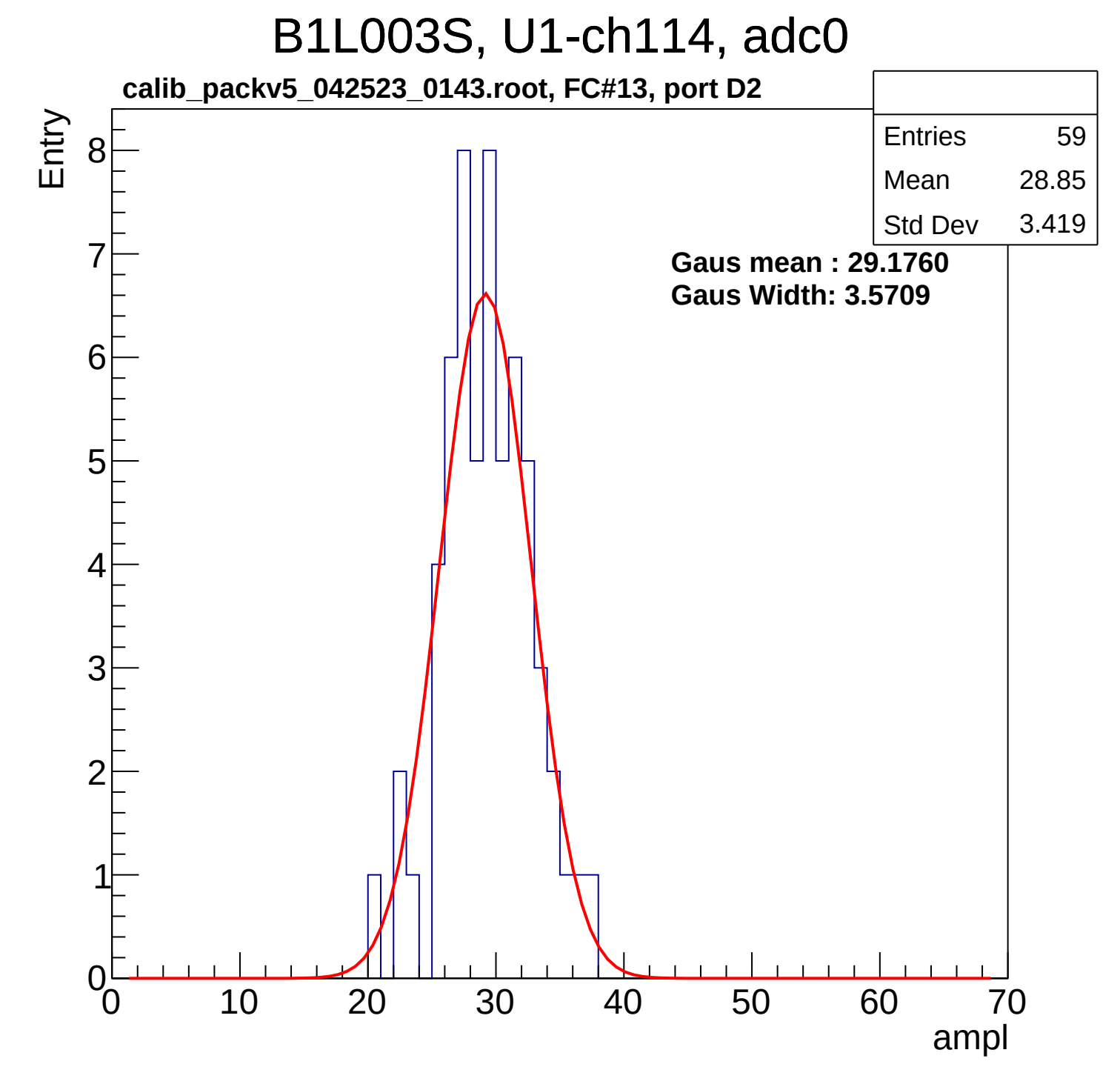
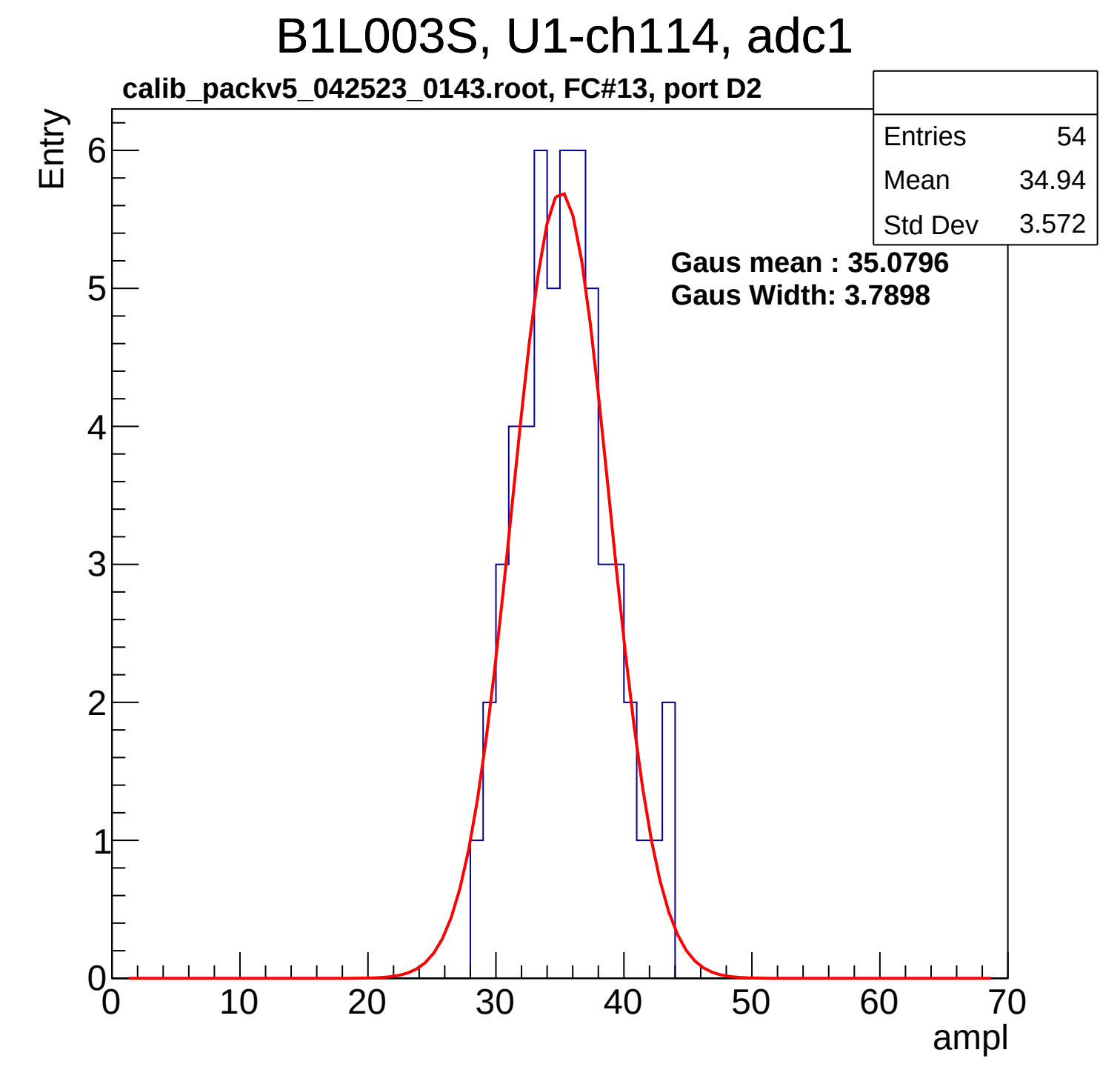
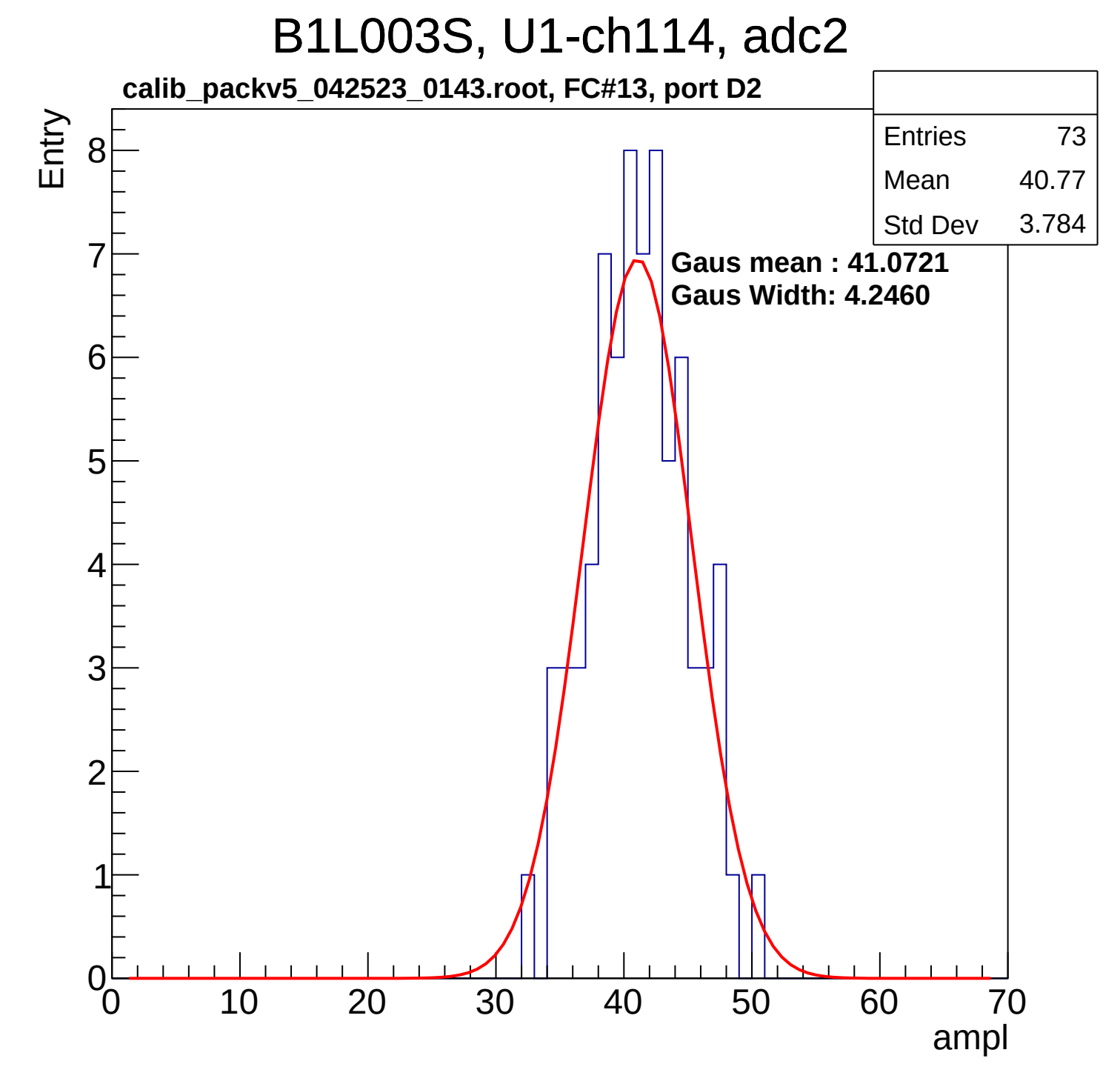

B1L003S, U1-ch113, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch113, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch114, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 28.85
Std Dev 3.419
Gaus mean : 29.1760
Gaus Width: 3.5709
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch114, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 34.94
Std Dev 3.572
Gaus mean : 35.0796
Gaus Width: 3.7898
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch114, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 40.77
Std Dev 3.784
Gaus mean : 41.0721
Gaus Width: 4.2460
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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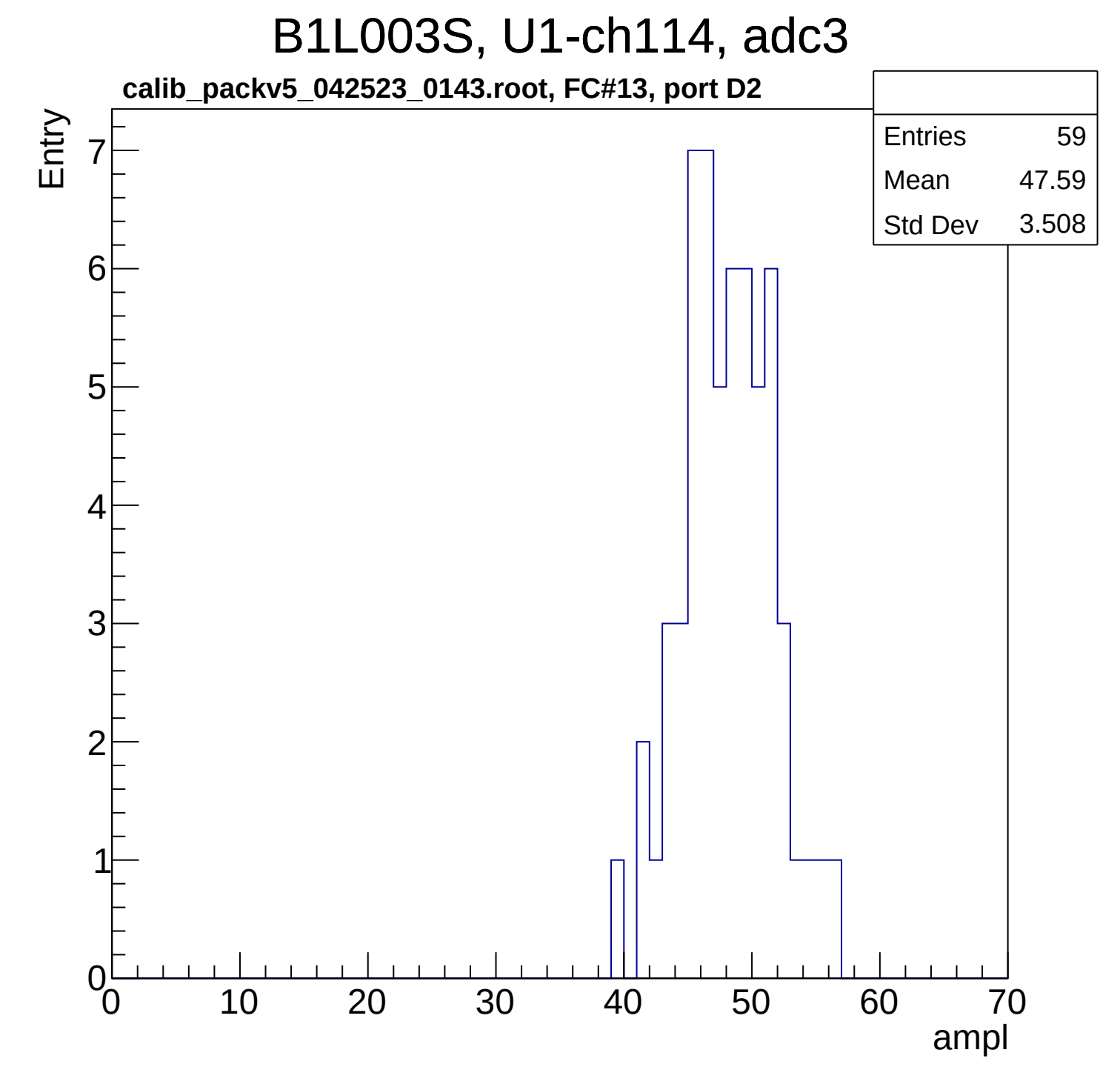
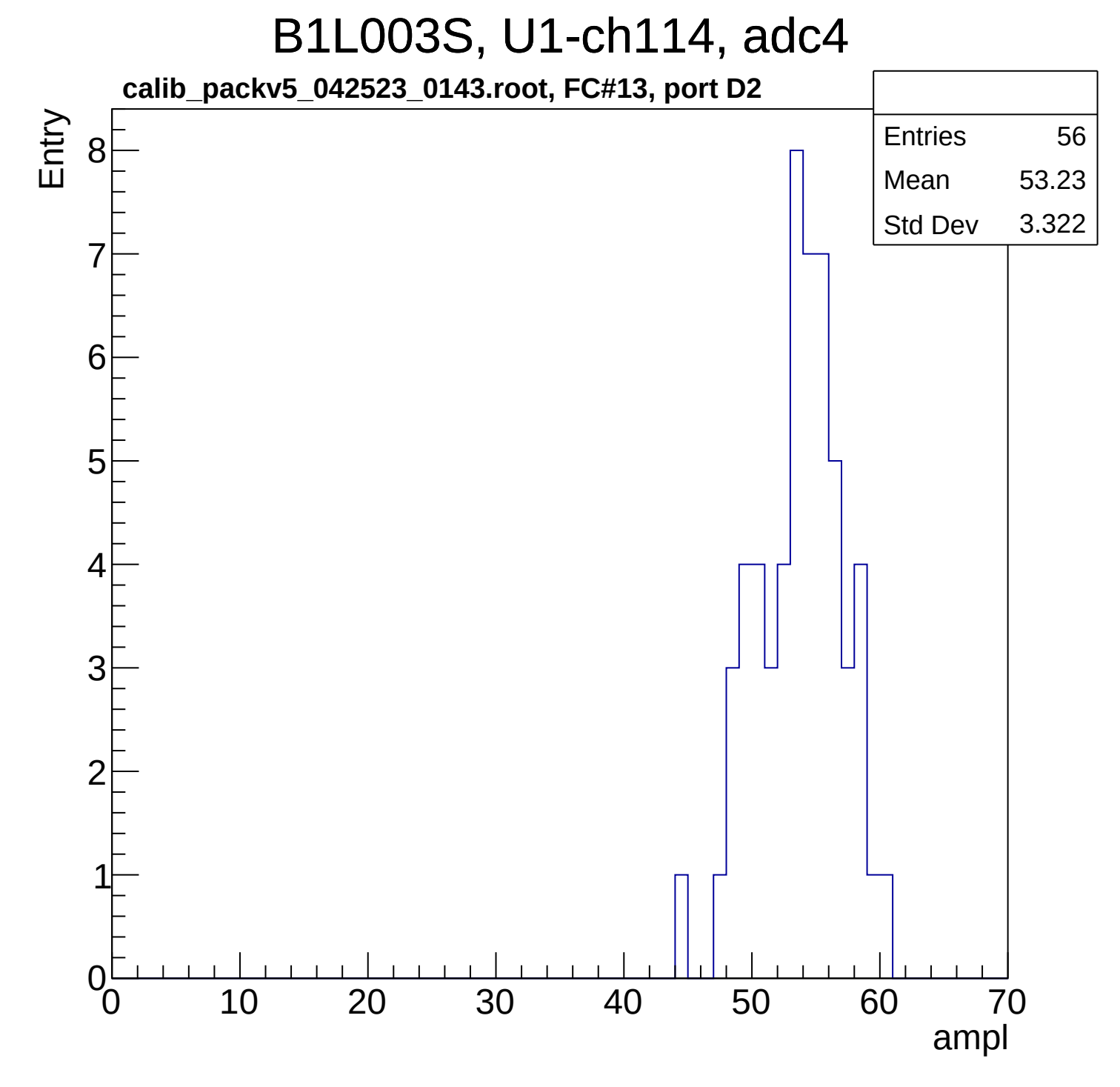

B1L003S, U1-ch114, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 47.59
Std Dev 3.508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch114, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 53.23
Std Dev 3.322
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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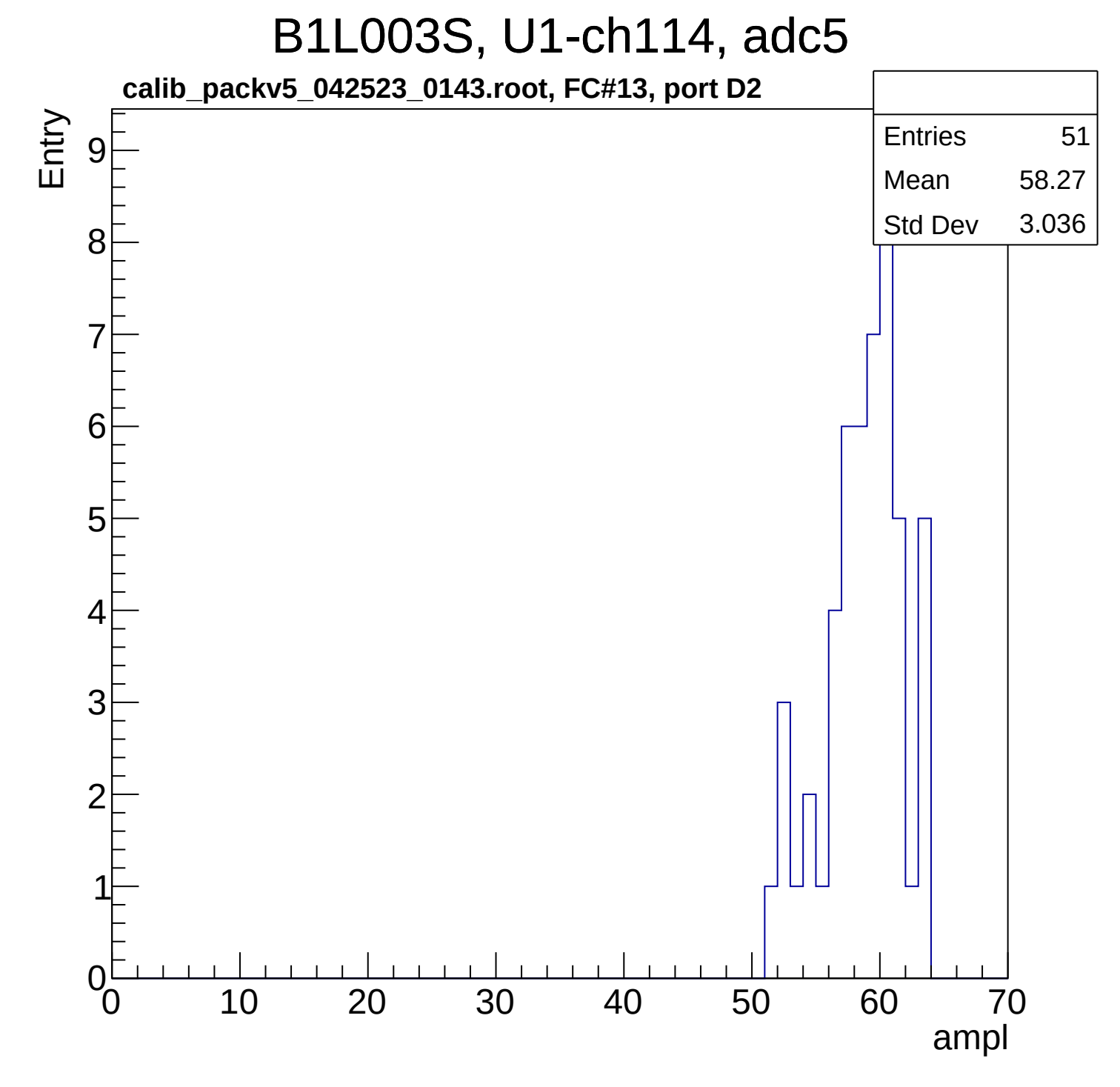
B1L003S, U1-ch114, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 51
Mean 58.27
Std Dev 3.036
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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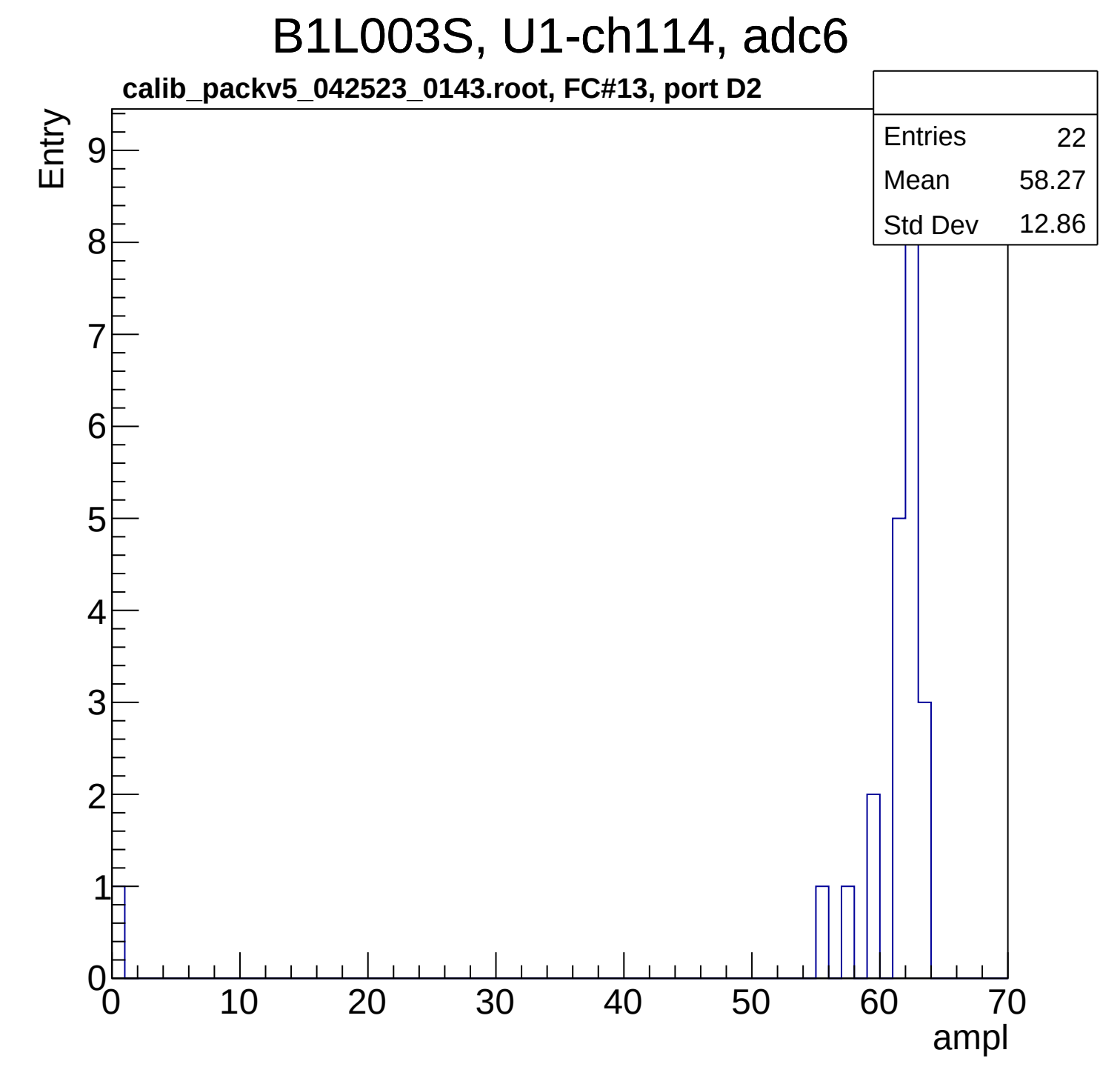
B1L003S, U1-ch114, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 22
Mean 58.27
Std Dev 12.86
Entry
0
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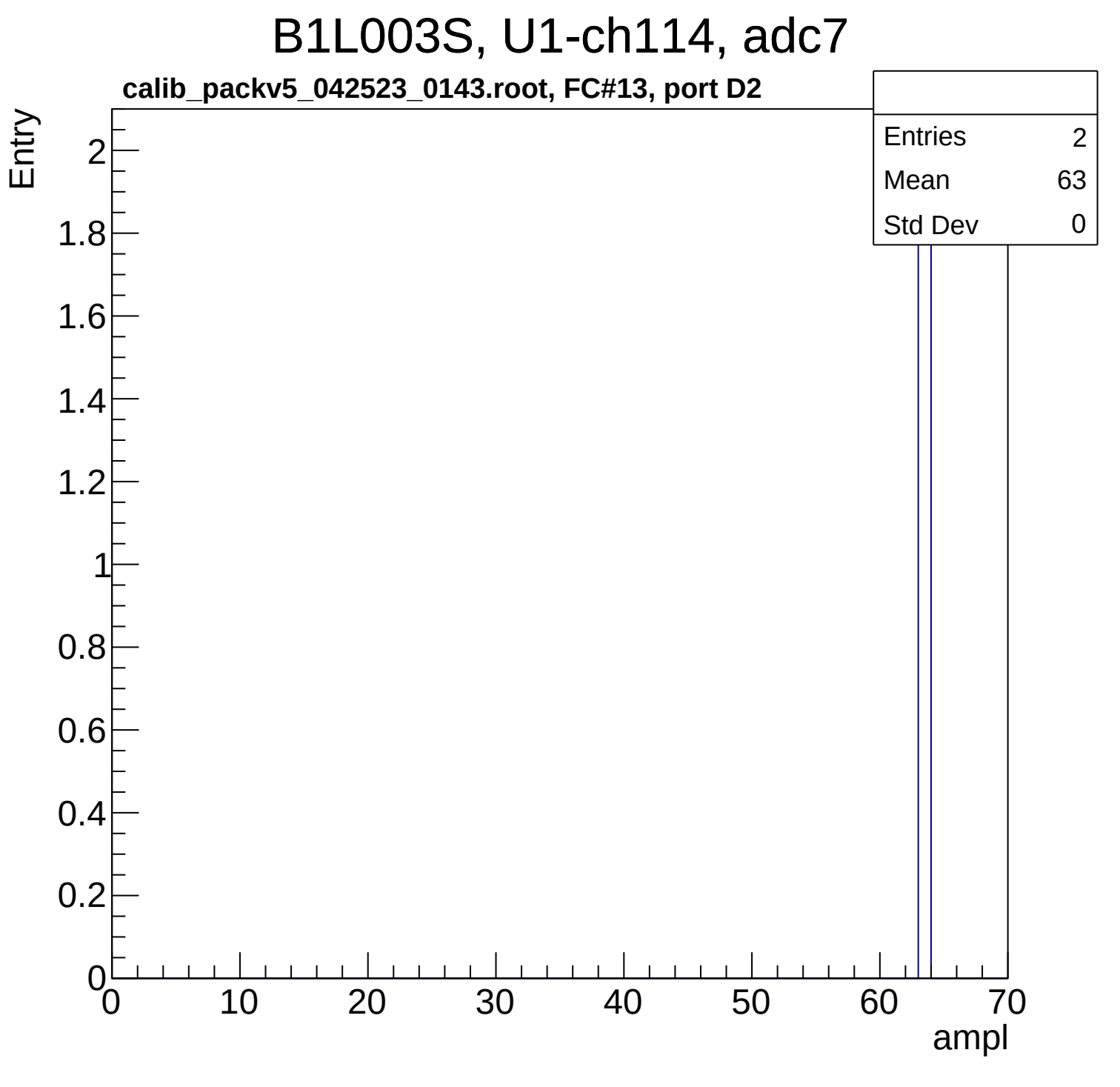
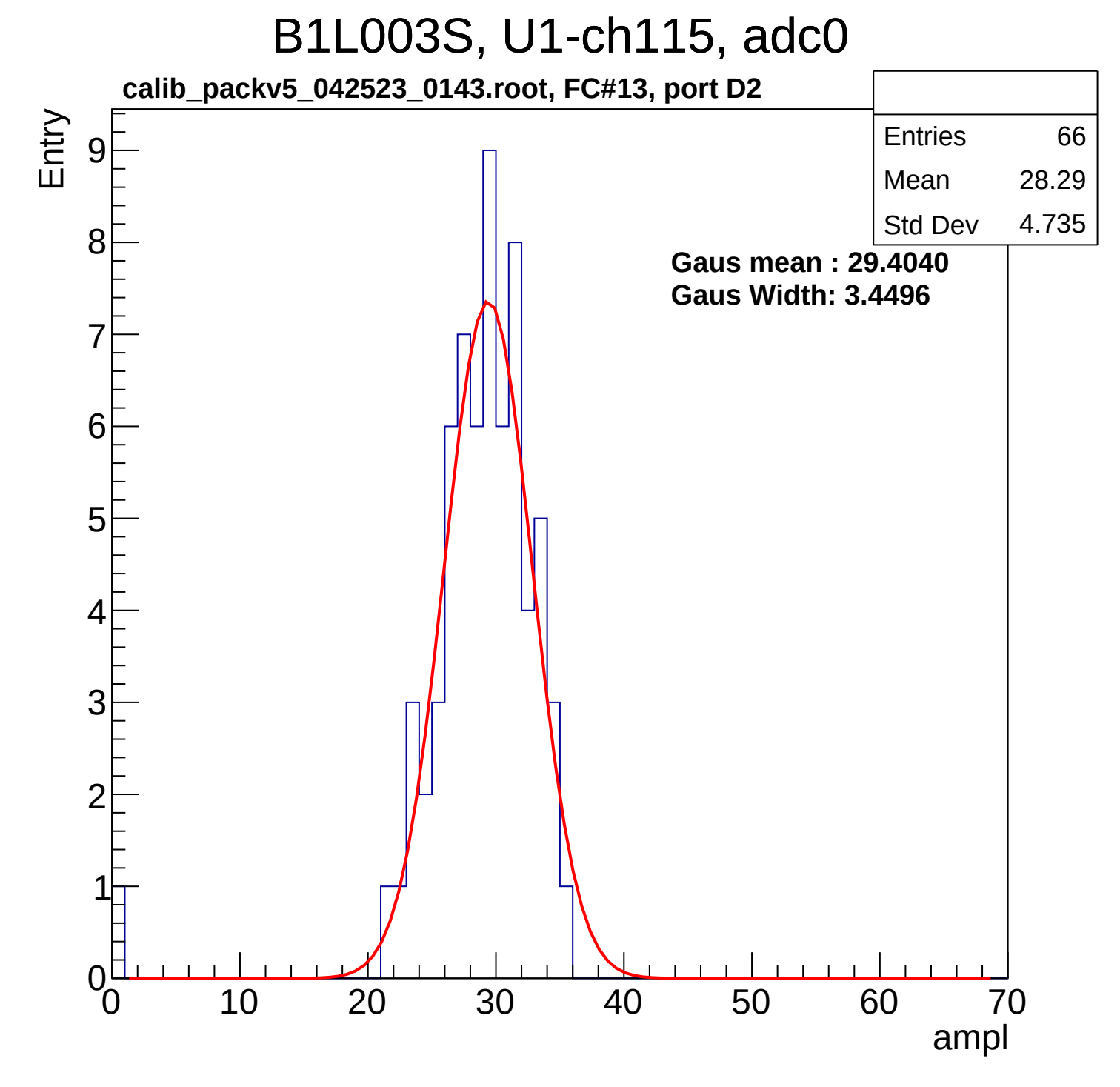
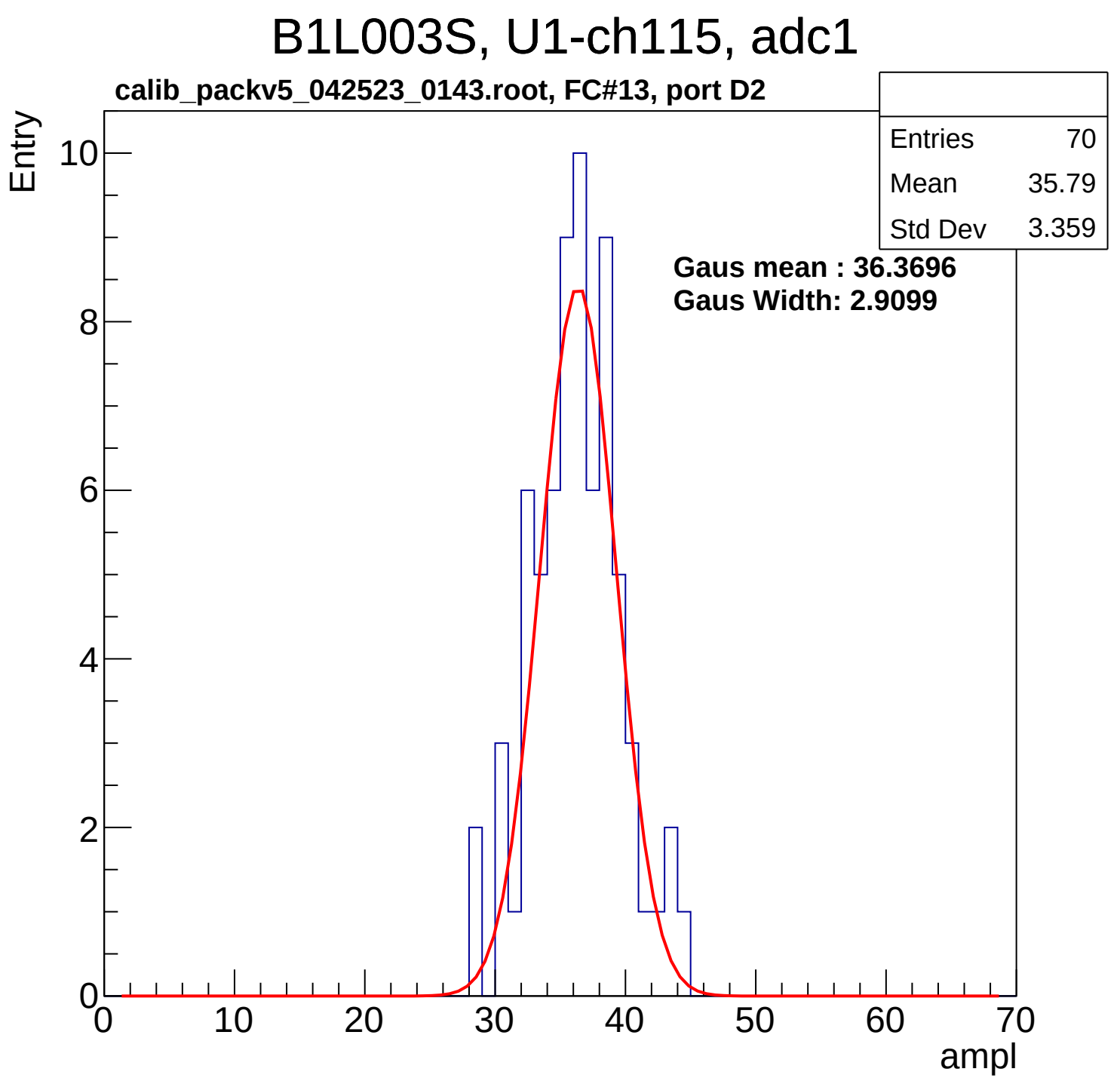
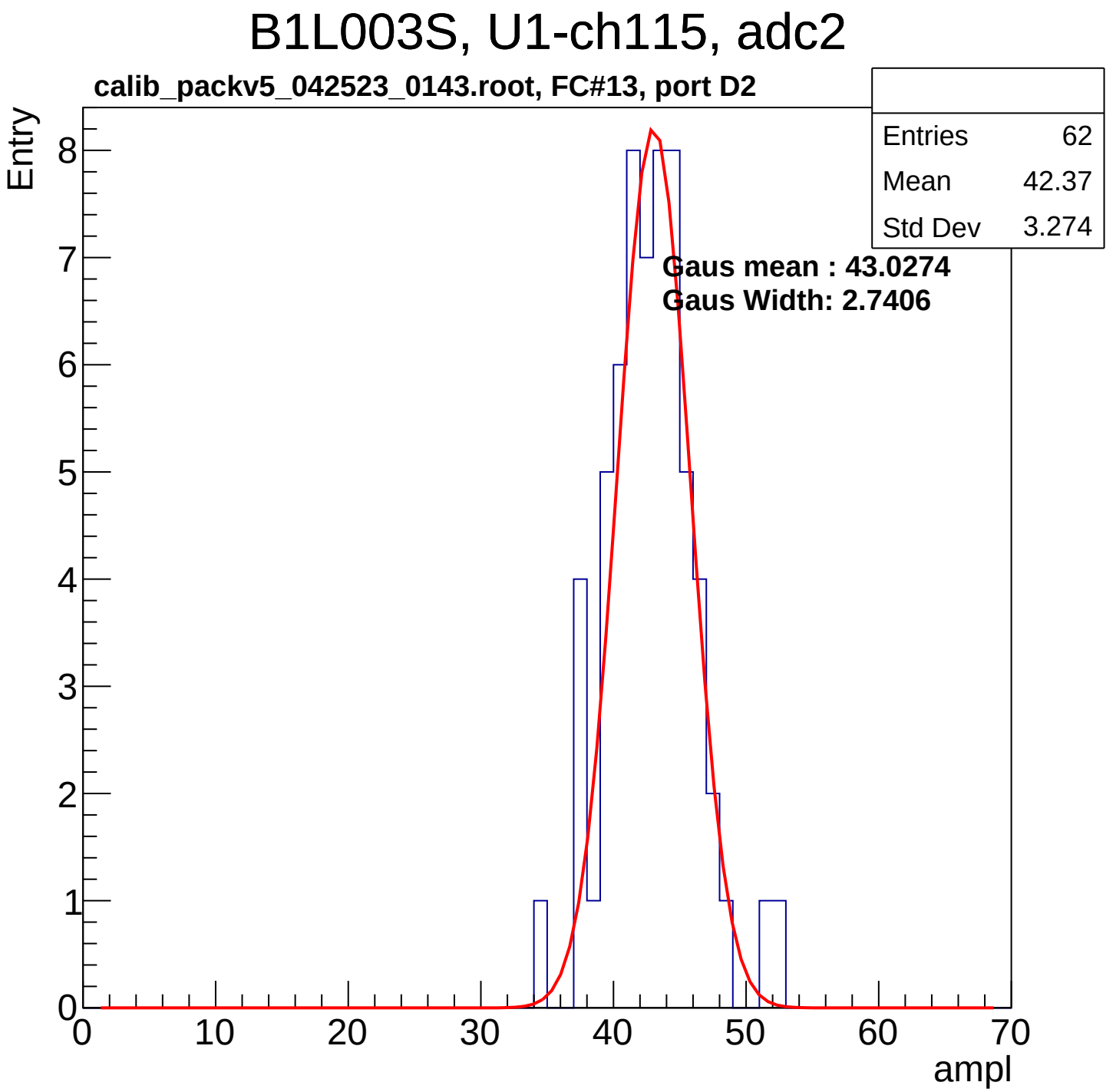
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch114, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch115, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 28.29
Std Dev 4.735
Gaus mean : 29.4040
Gaus Width: 3.4496
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch115, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 70
Mean 35.79
Std Dev 3.359
Gaus mean : 36.3696
Gaus Width: 2.9099
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch115, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 42.37
Std Dev 3.274
Gaus mean : 43.0274
Gaus Width: 2.7406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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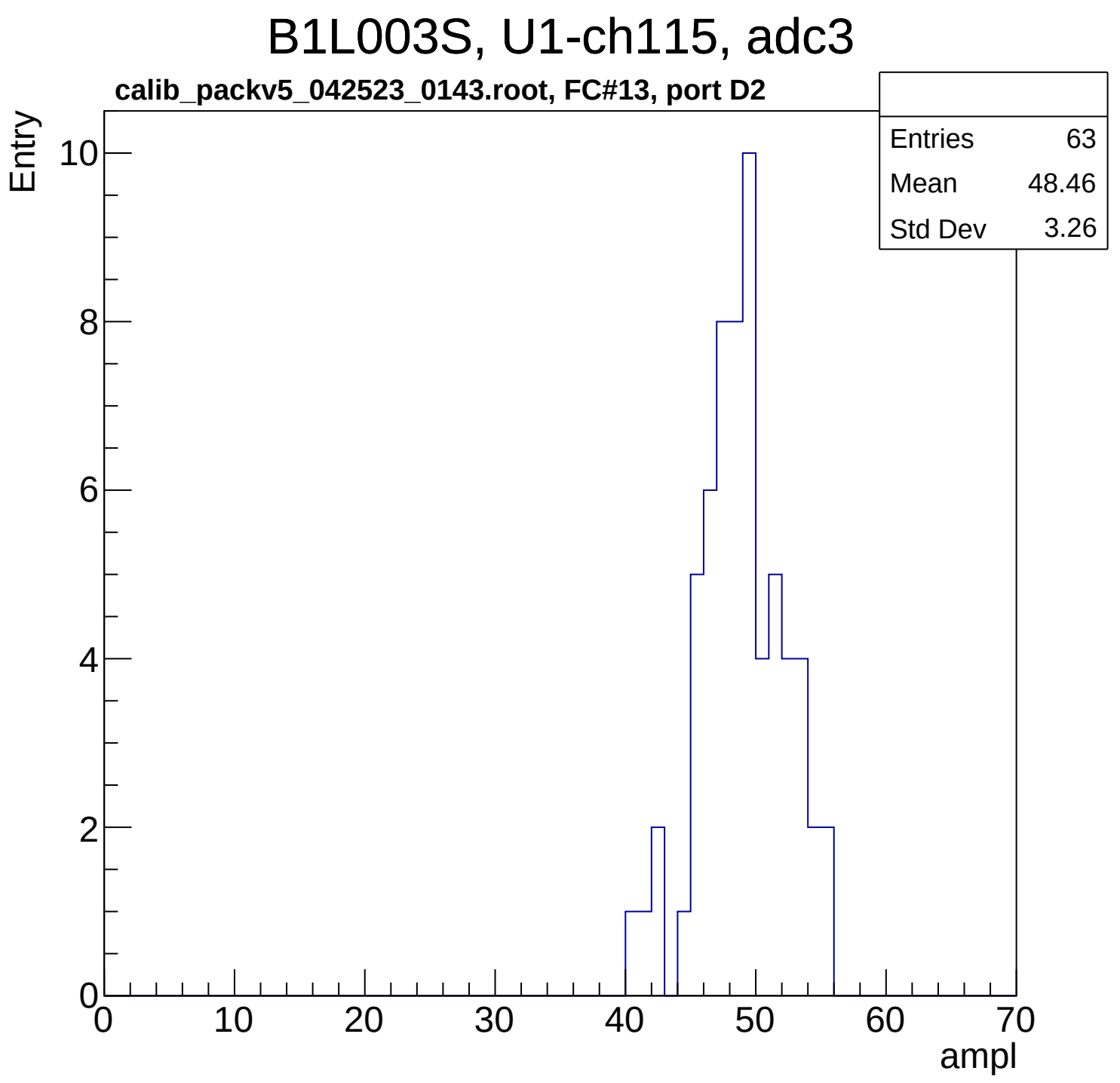
B1L003S, U1-ch115, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 48.46
Std Dev 3.26
Entry
0
2
4
6
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10
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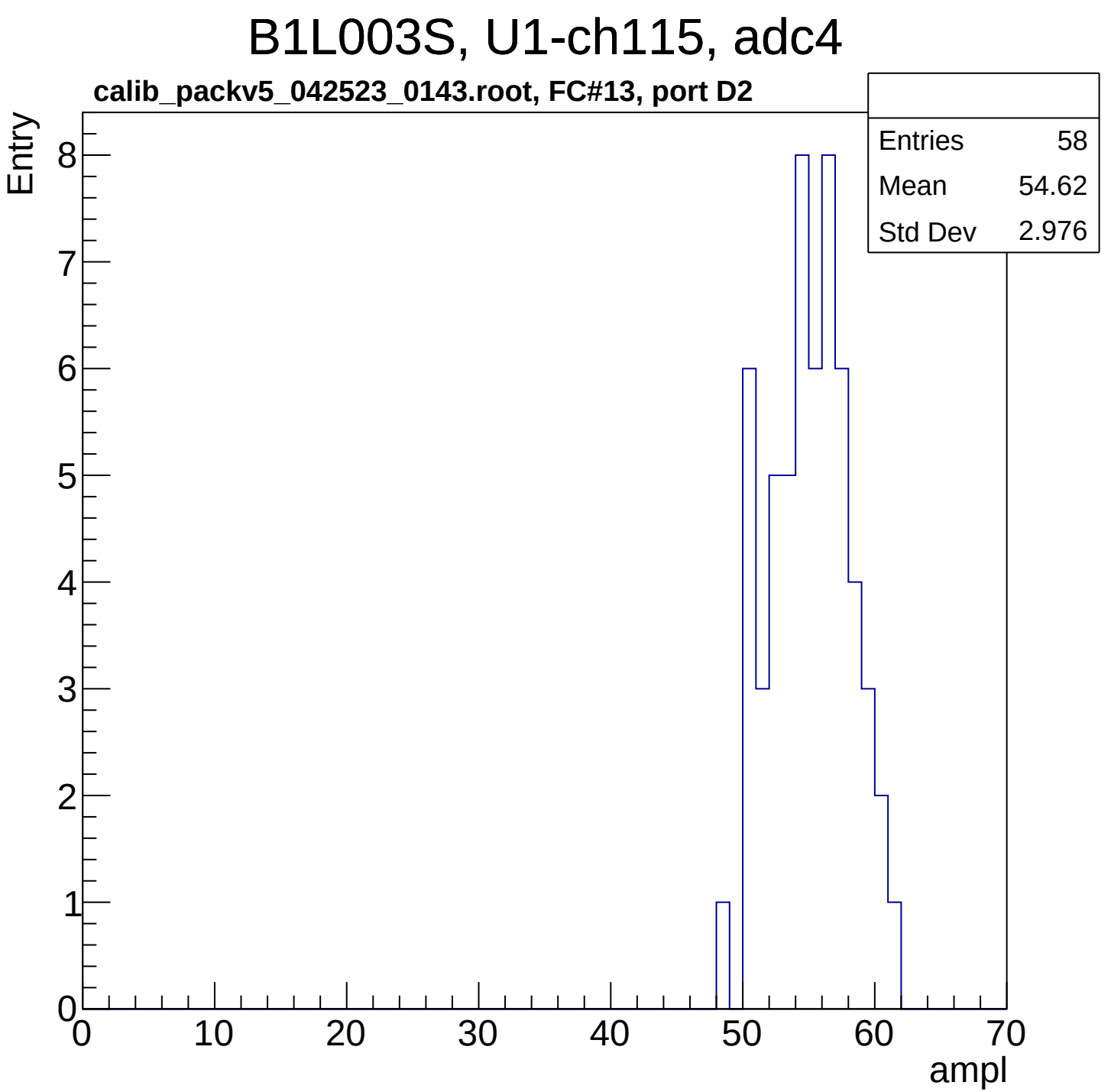
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch115, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.62
Std Dev 2.976
Entry
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2
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4
5
6
7
8
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10
20
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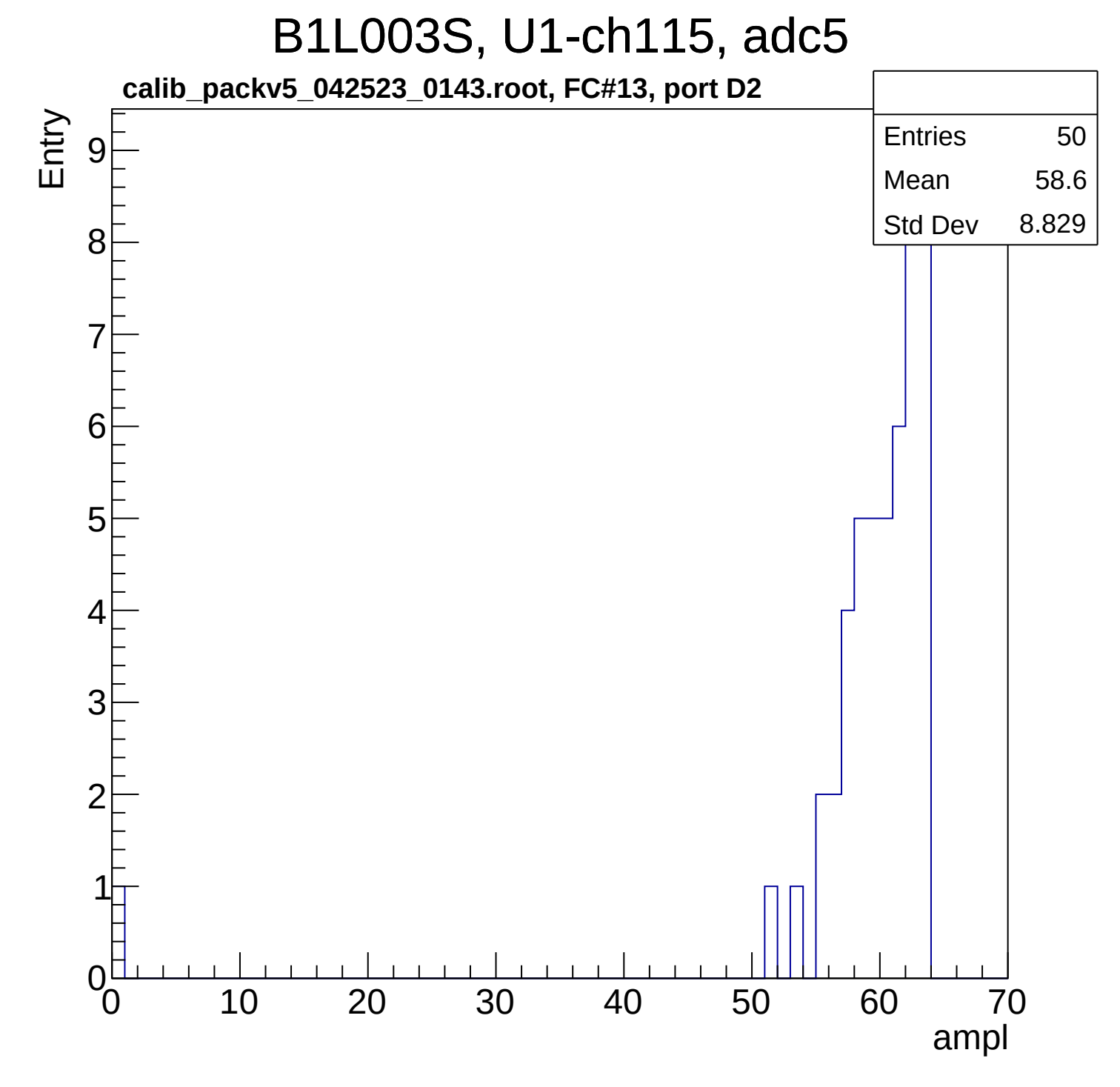
50
60
70
ampl


B1L003S, U1-ch115, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 58.6
Std Dev 8.829
Entry
ampl
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2
3
4
5
6
7
8
9
0
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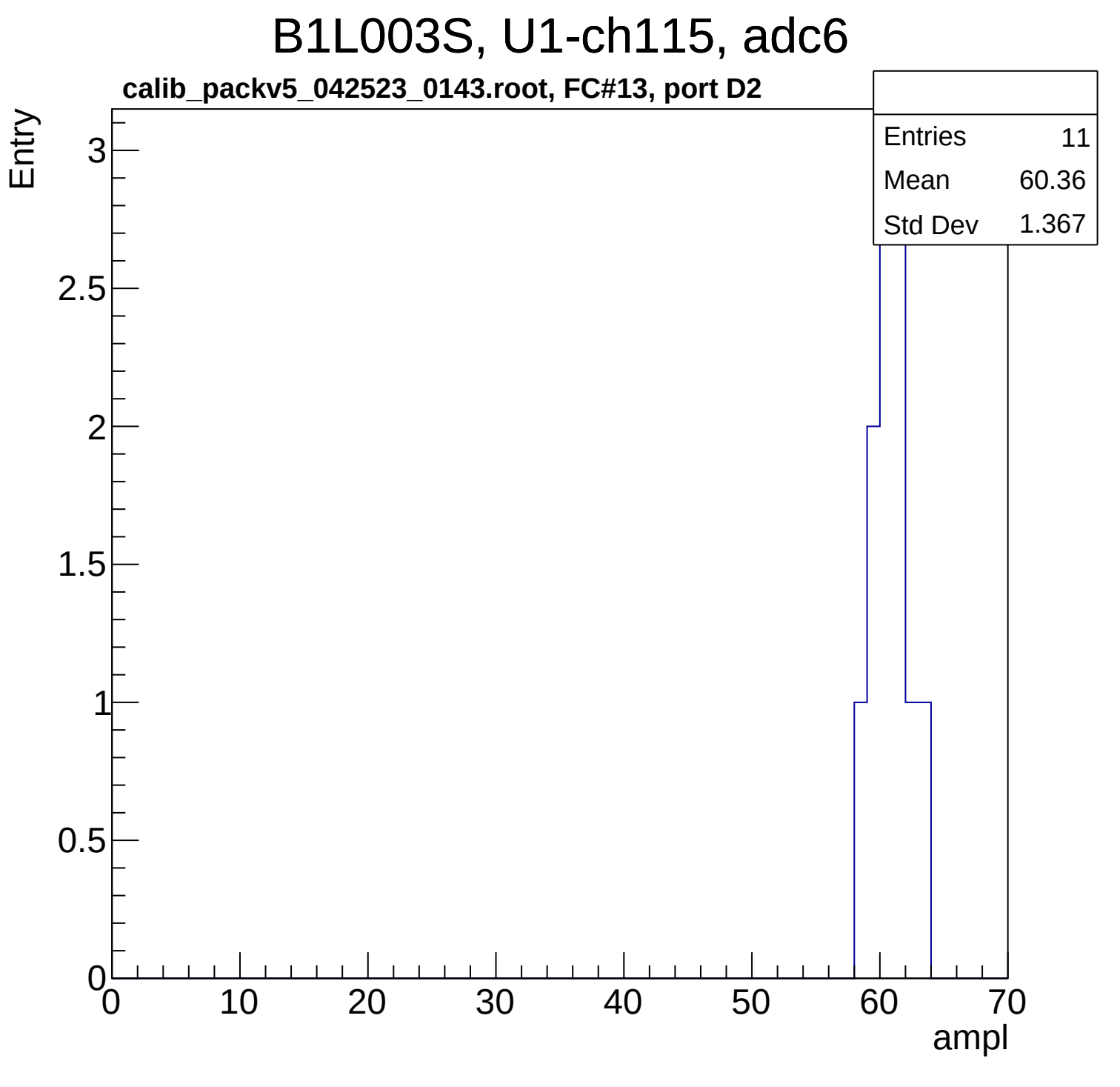
50
60
70


B1L003S, U1-ch115, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 60.36
Std Dev 1.367
Entry
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1
1.5
2
2.5
3
0
10
20
30
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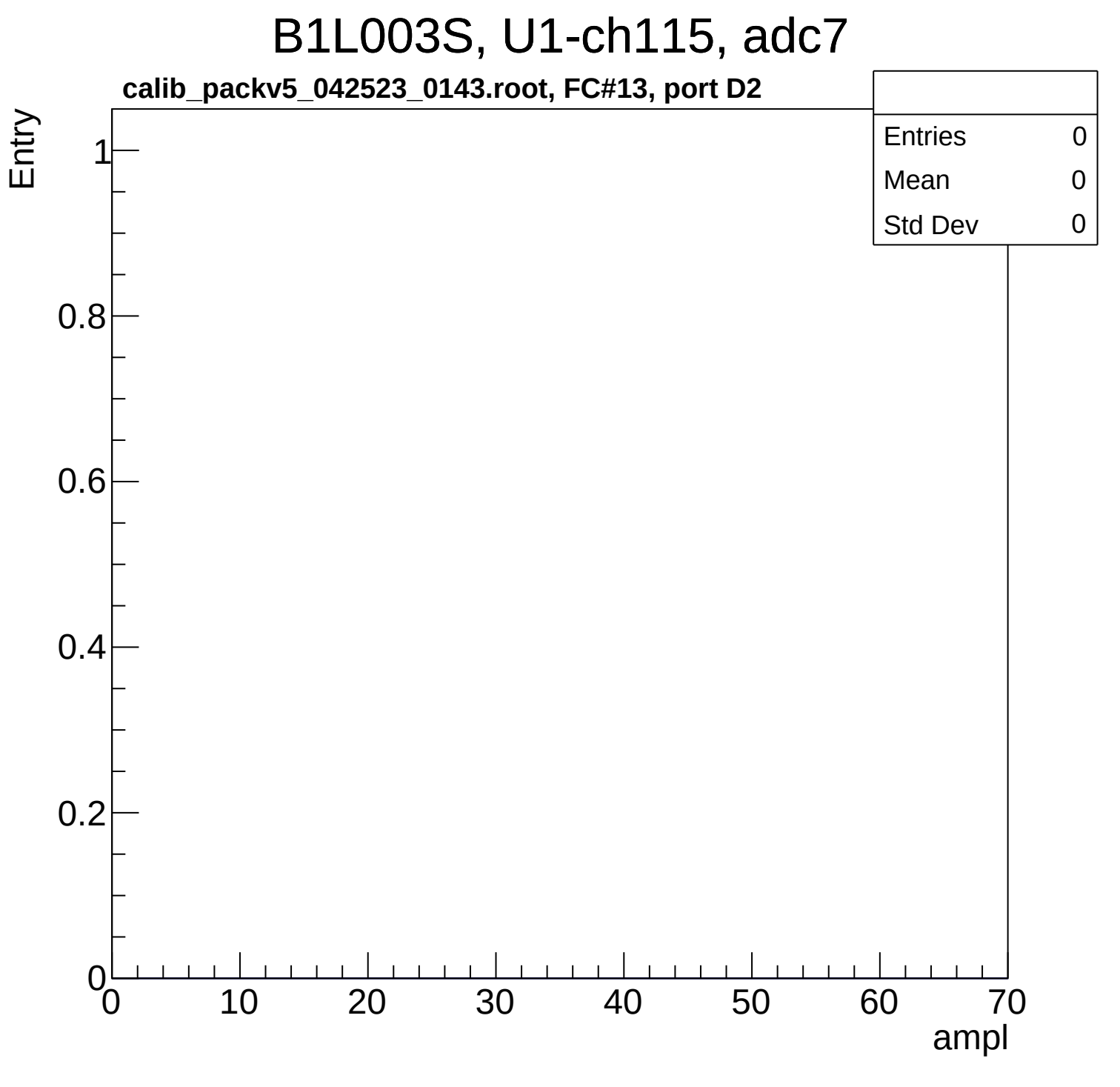
50
60
70
ampl


B1L003S, U1-ch115, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
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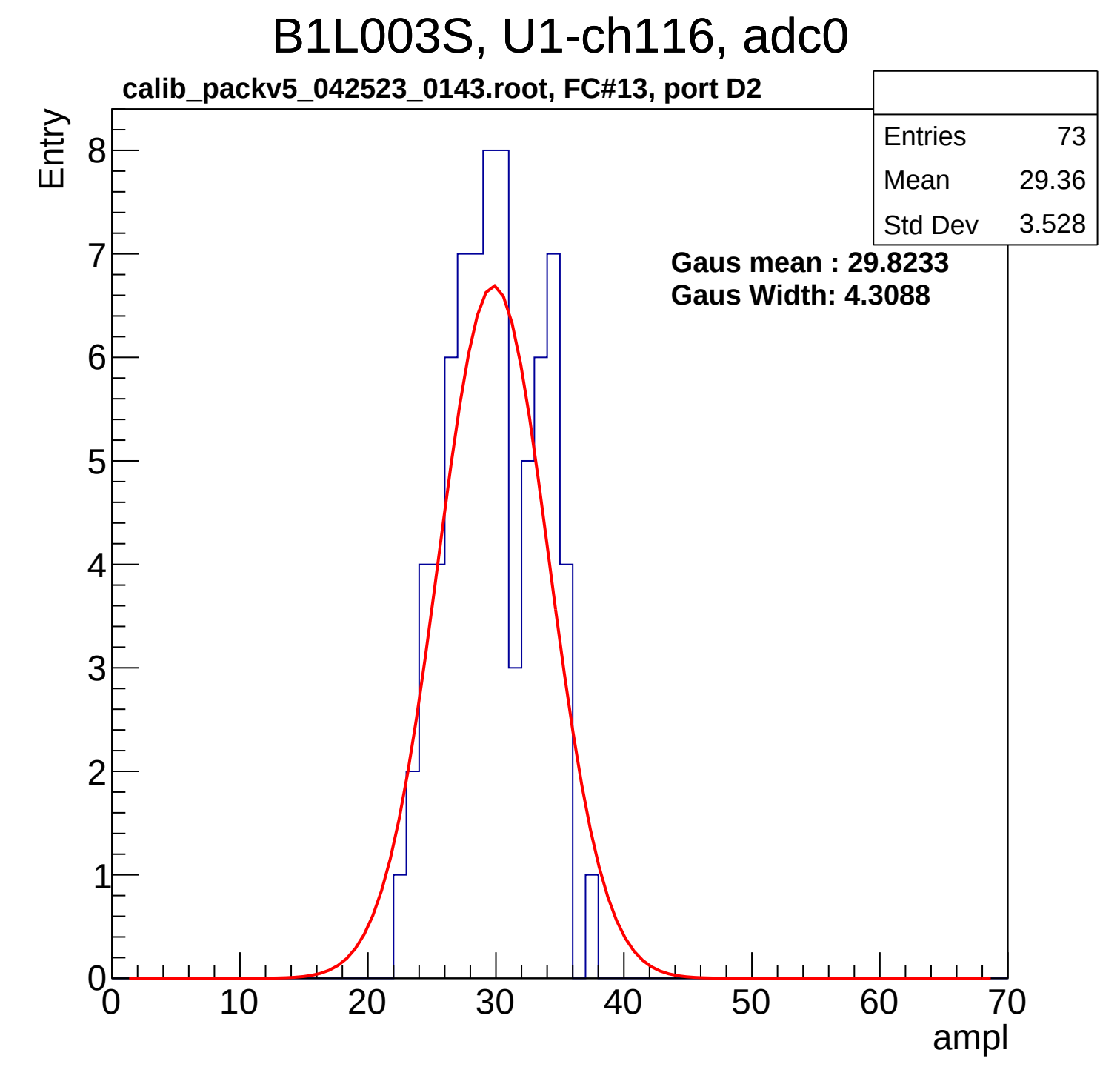
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch116, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 29.36
Std Dev 3.528
Gaus mean : 29.8233
Gaus Width: 4.3088
Entry
ampl
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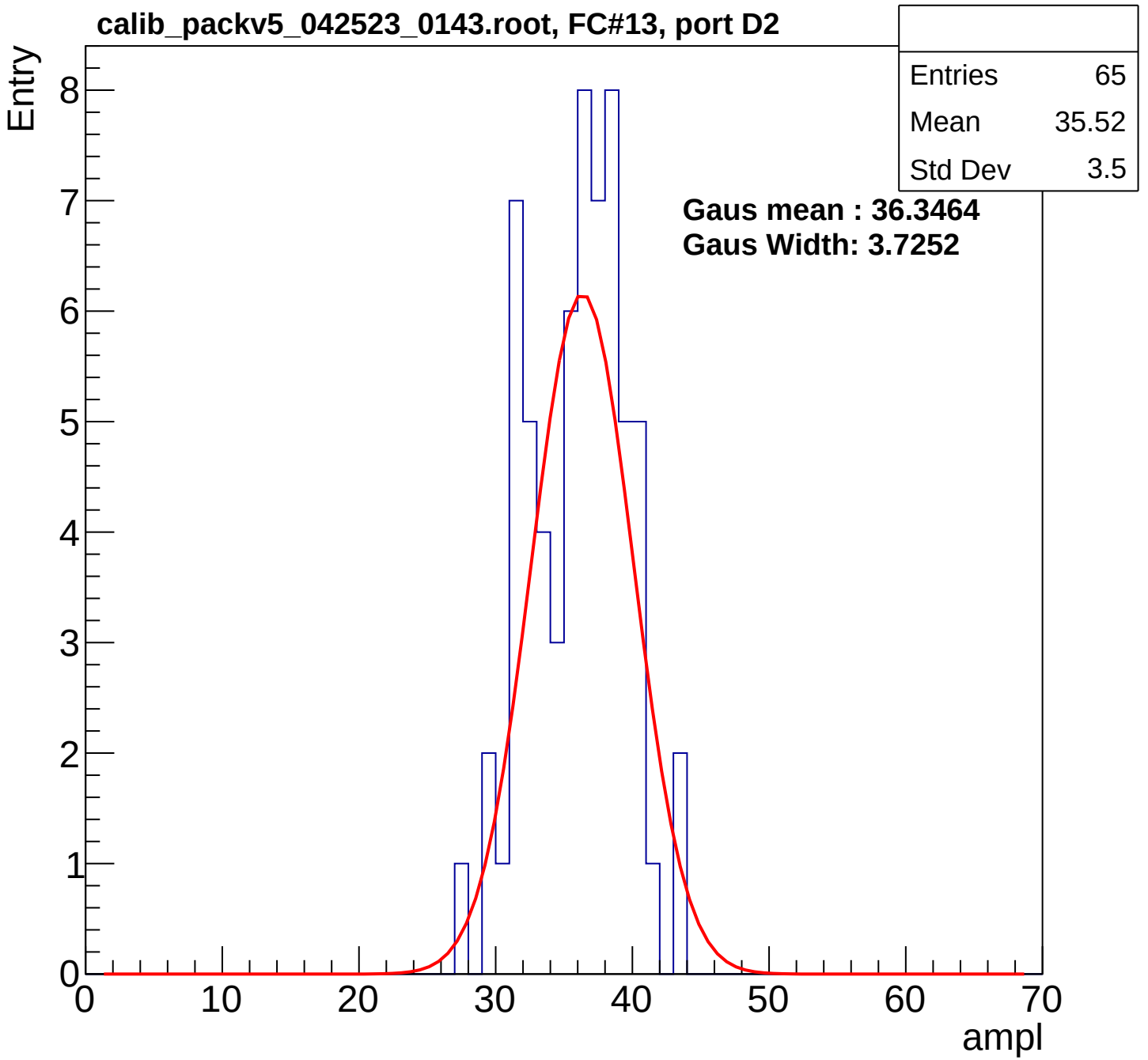
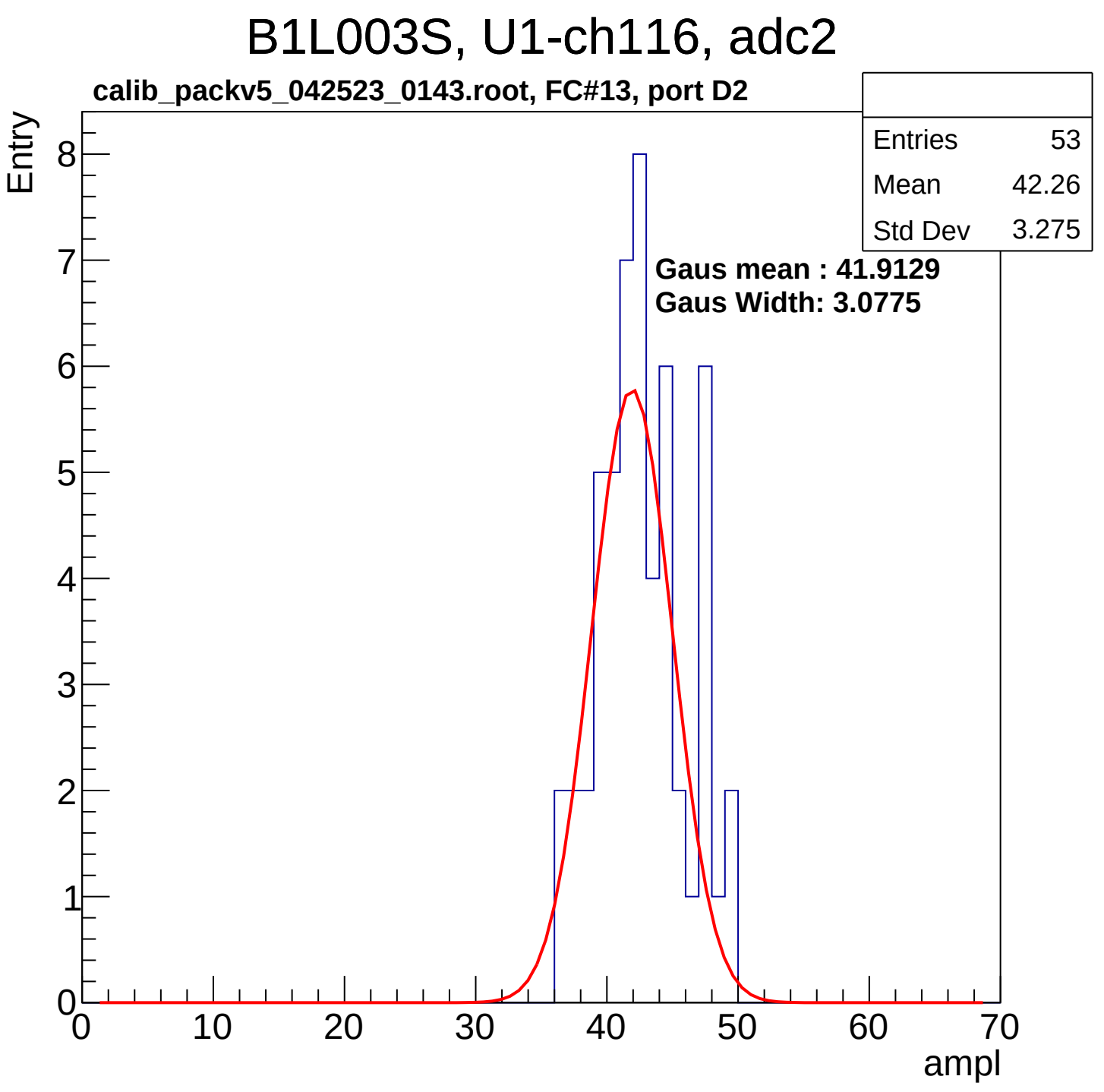
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch116, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 35.52
Std Dev 3.5
Gaus mean : 36.3464
Gaus Width: 3.7252
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch116, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 42.26
Std Dev 3.275
Gaus mean : 41.9129
Gaus Width: 3.0775
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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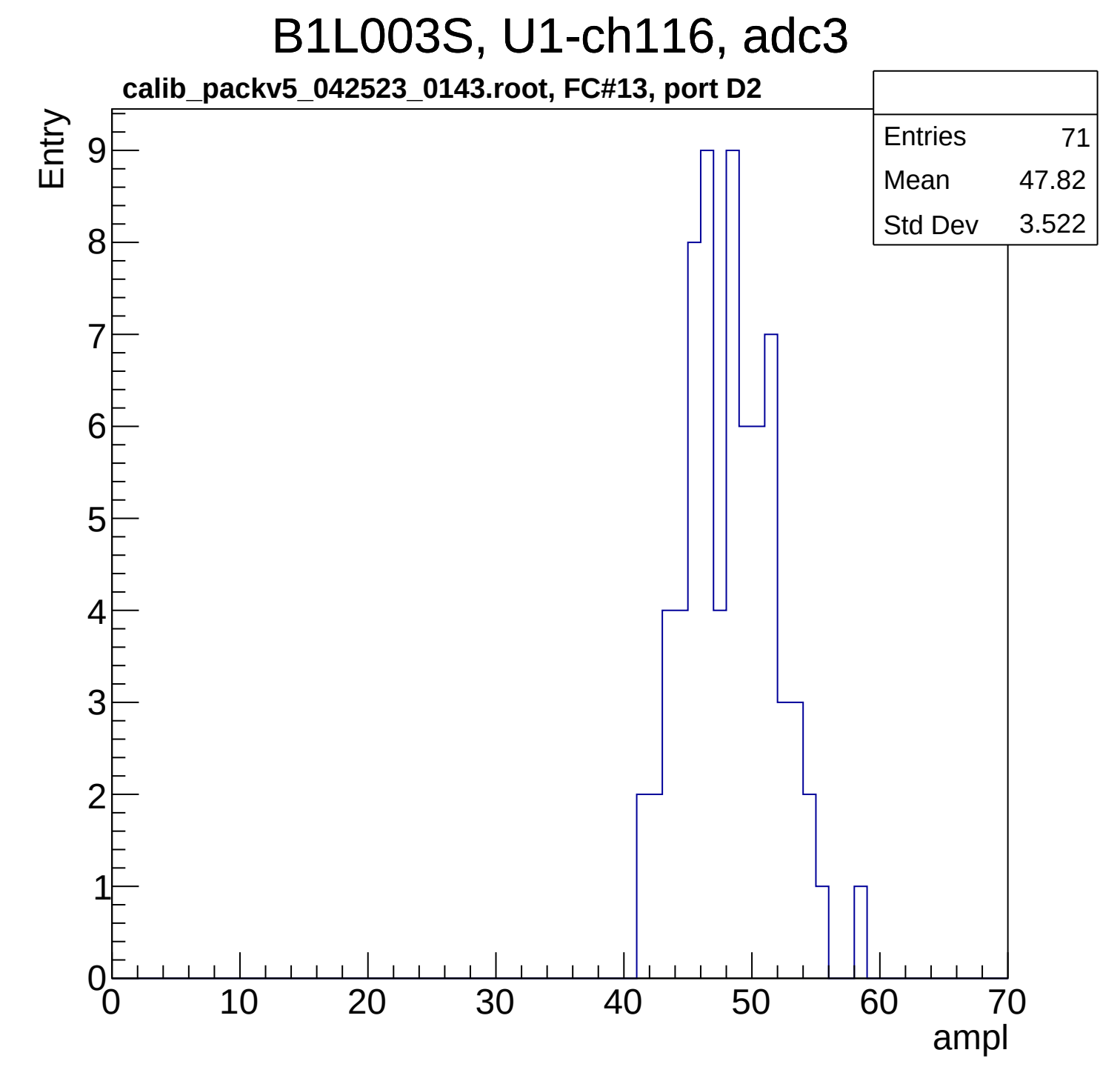
B1L003S, U1-ch116, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 47.82
Std Dev 3.522
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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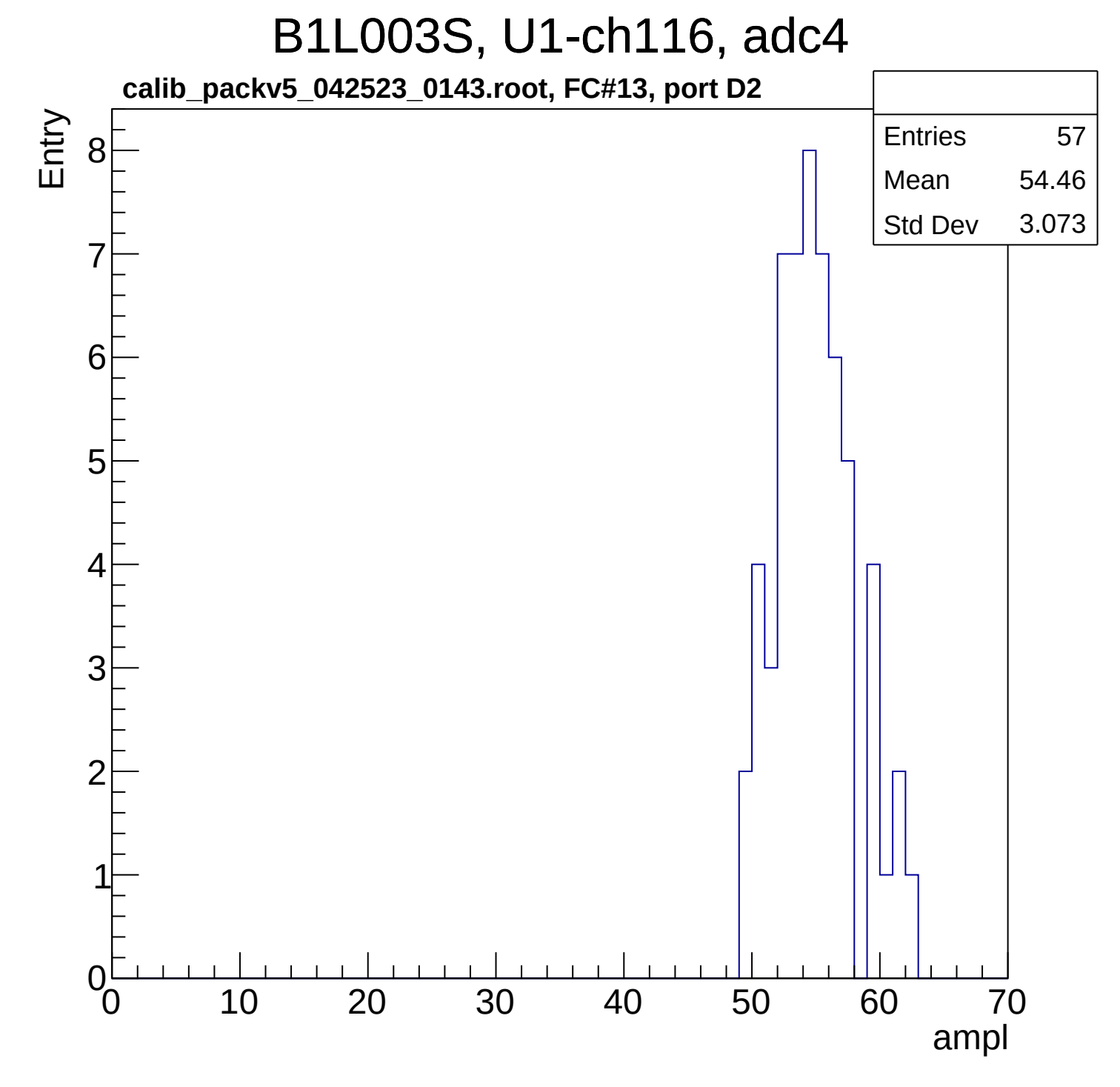
B1L003S, U1-ch116, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 54.46
Std Dev 3.073
Entry
ampl
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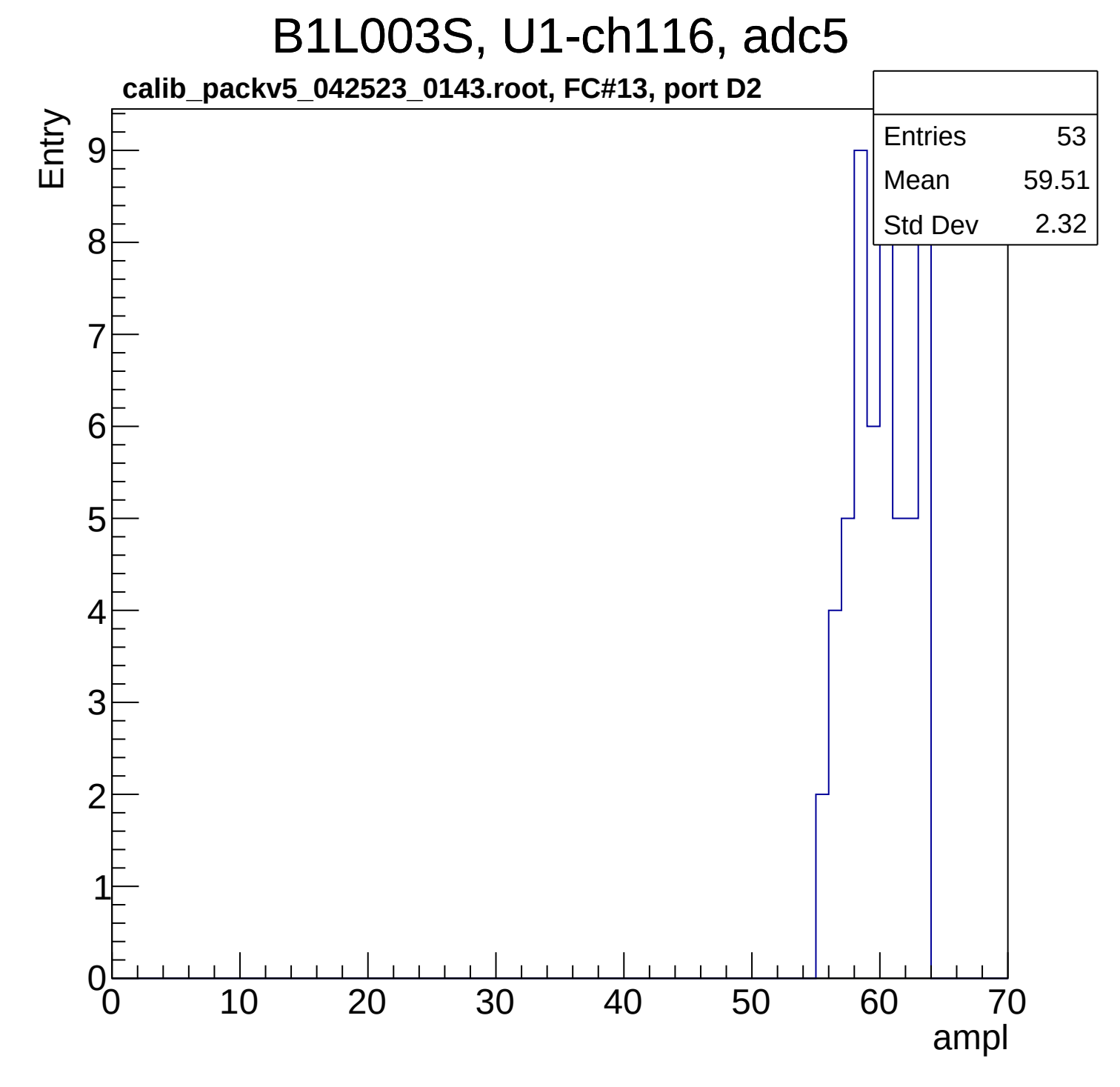
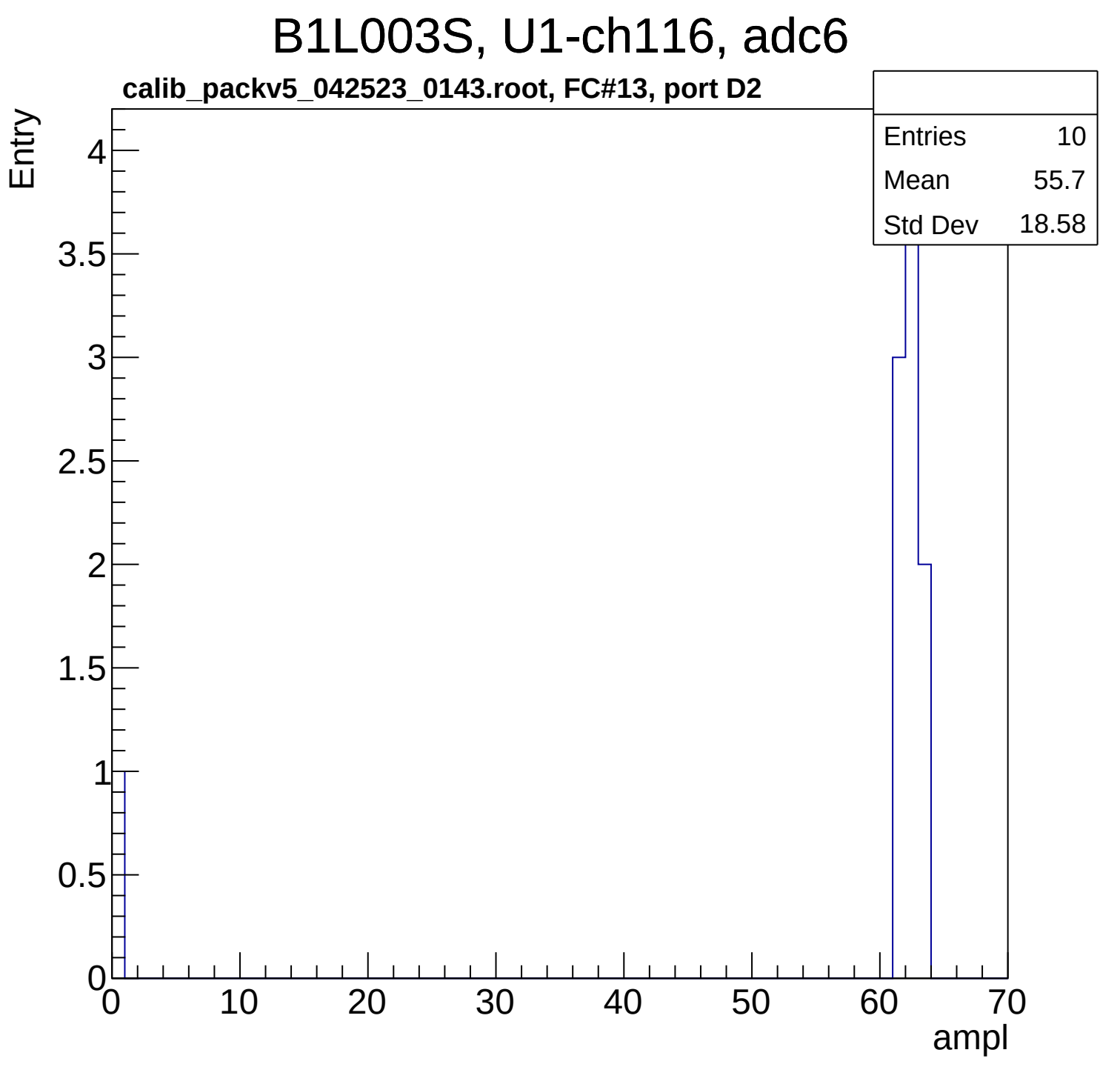
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch116, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 59.51
Std Dev 2.32
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch116, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.7
Std Dev 18.58
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
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40
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60
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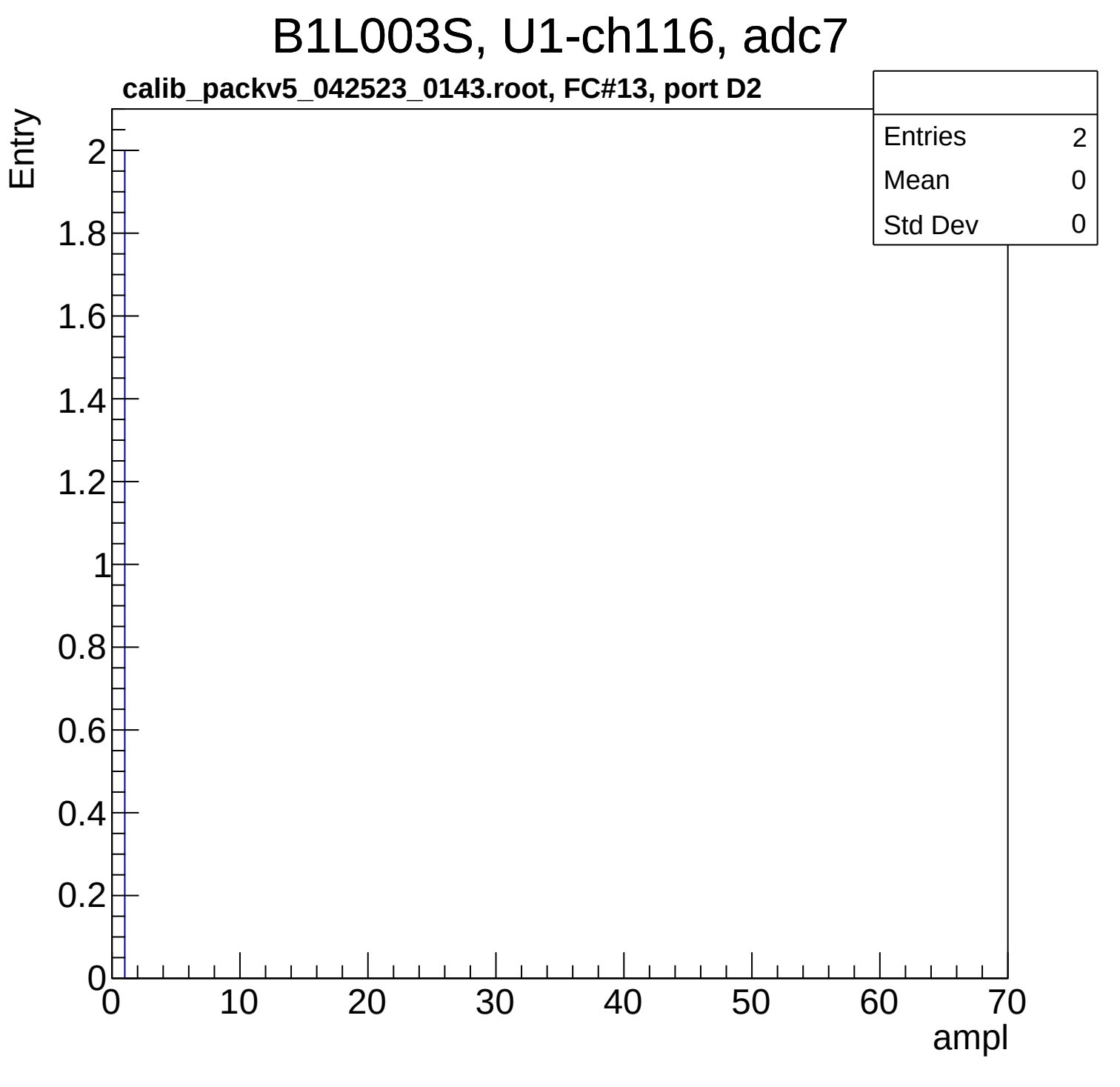
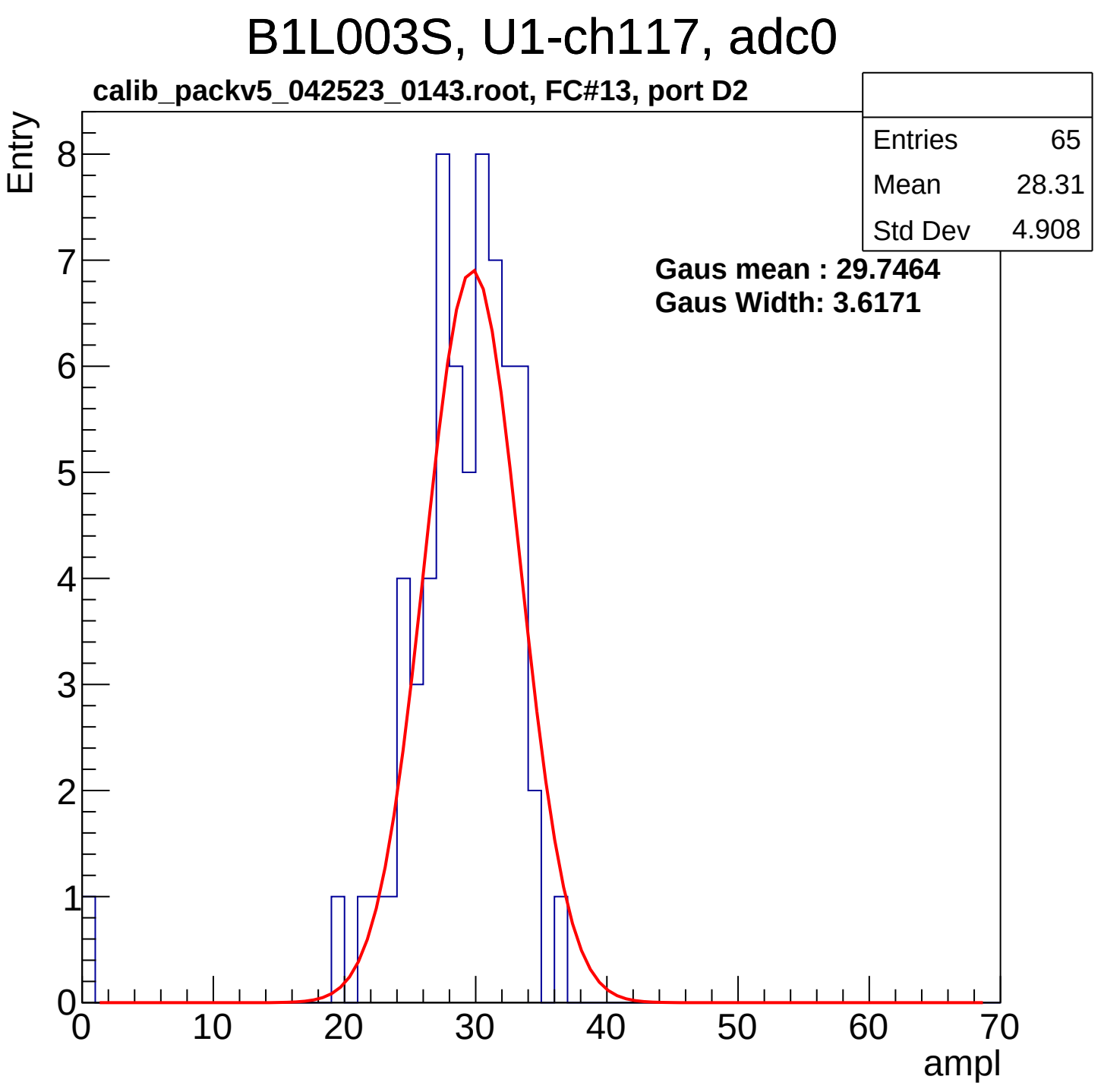
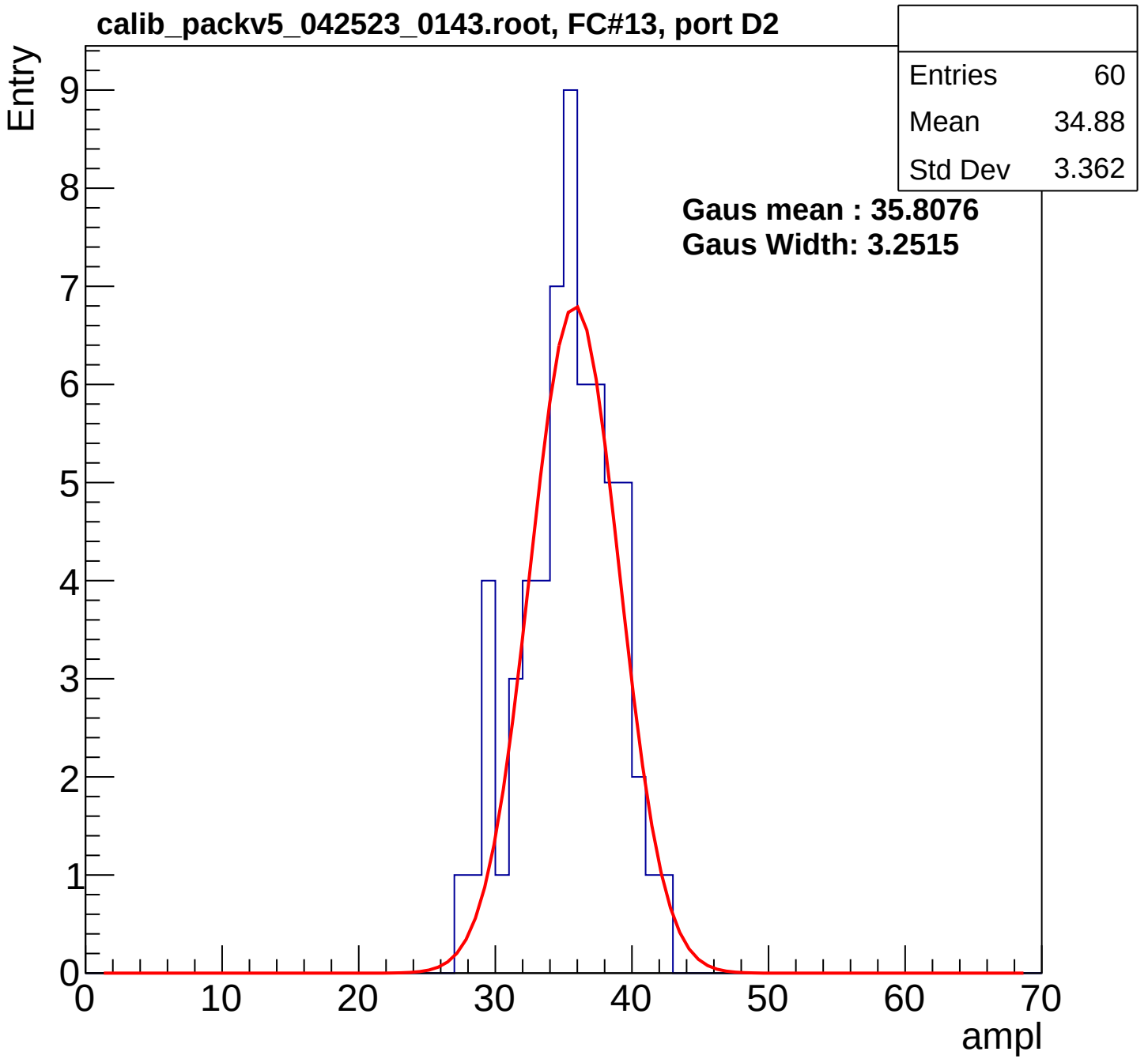
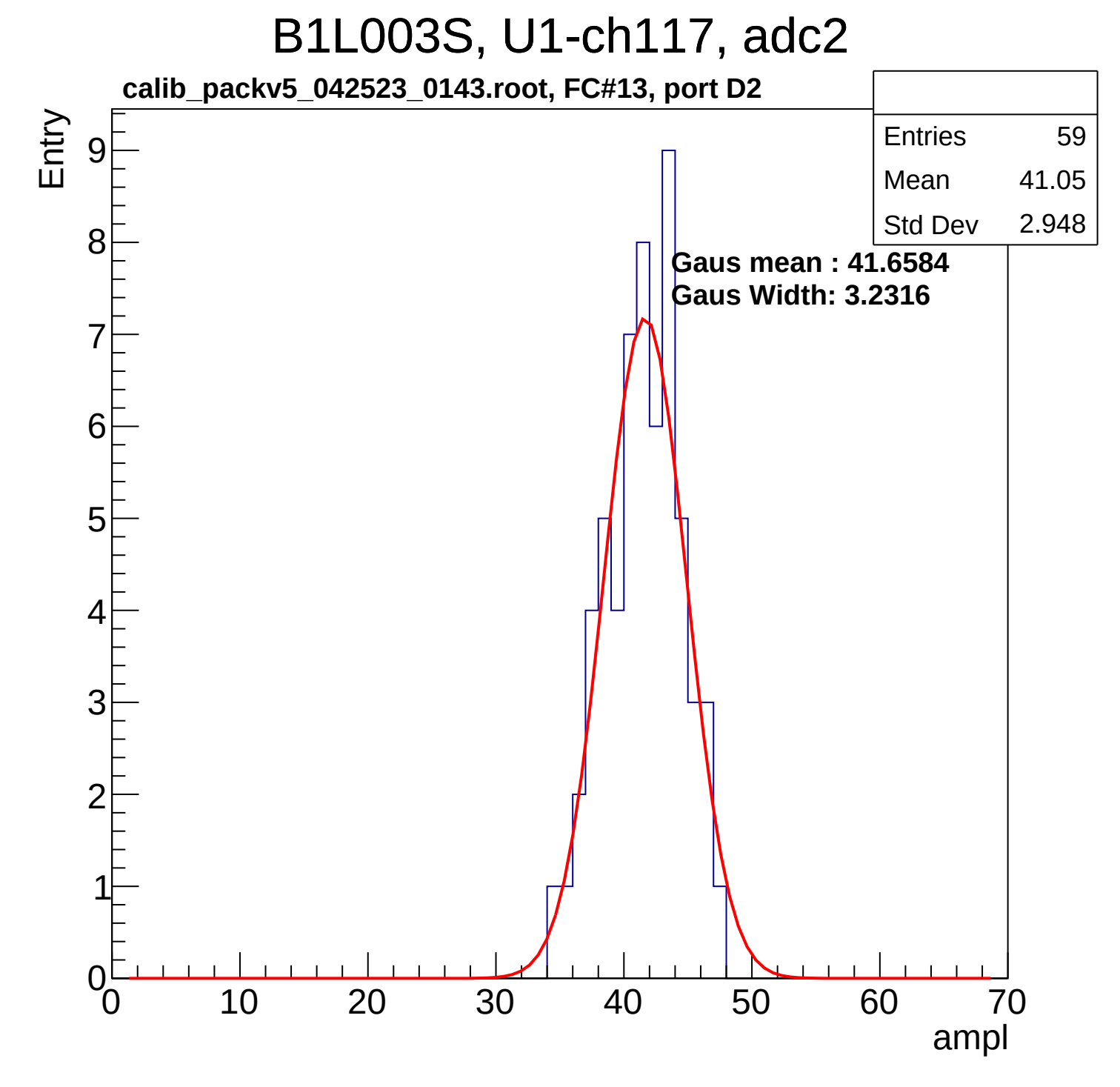

B1L003S, U1-ch116, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch117, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 28.31
Std Dev 4.908
Gaus mean : 29.7464
Gaus Width: 3.6171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 34.88
Std Dev 3.362
Gaus mean : 35.8076
Gaus Width: 3.2515
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 41.05
Std Dev 2.948
Gaus mean : 41.6584
Gaus Width: 3.2316
Entry
ampl
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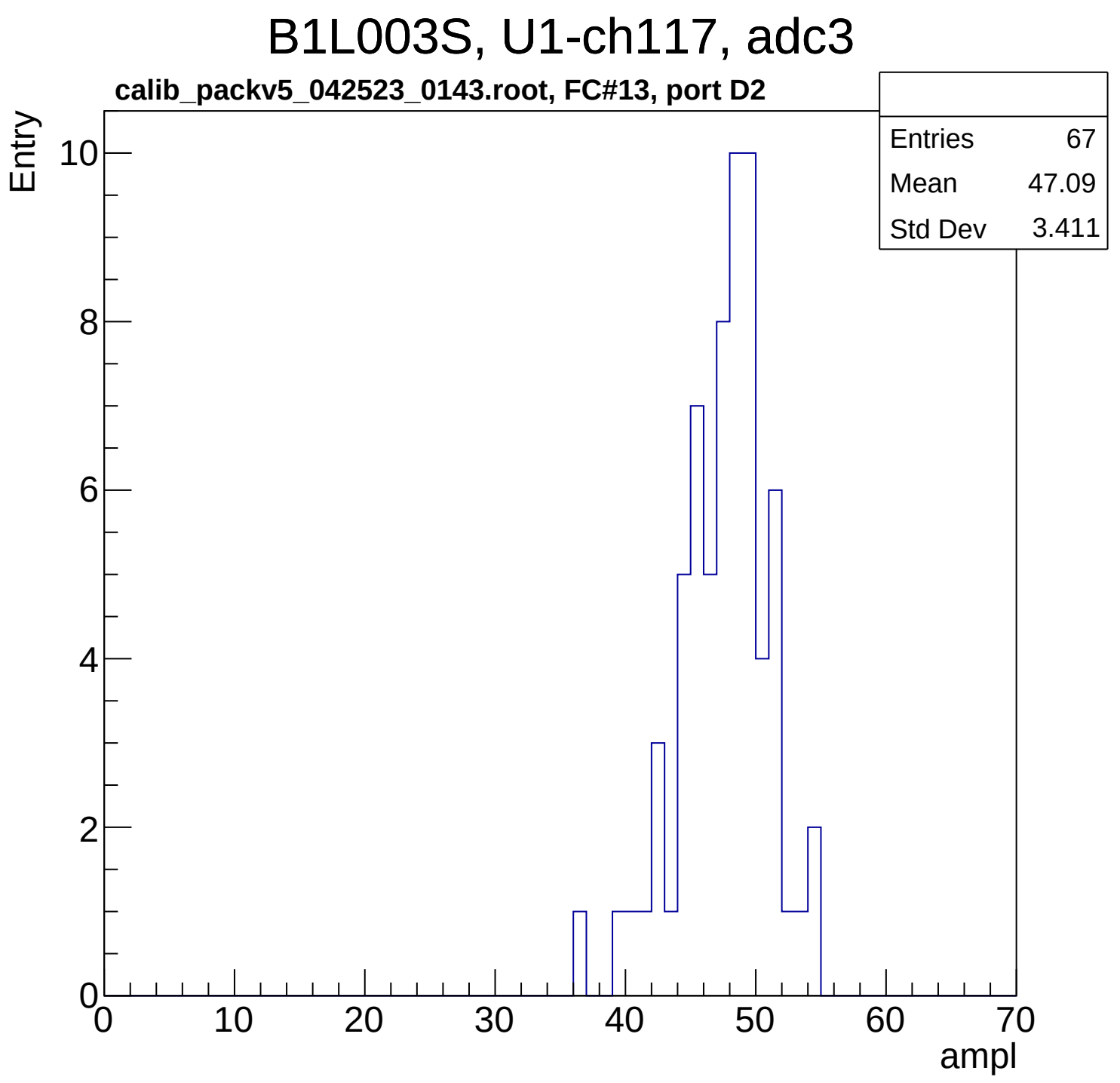
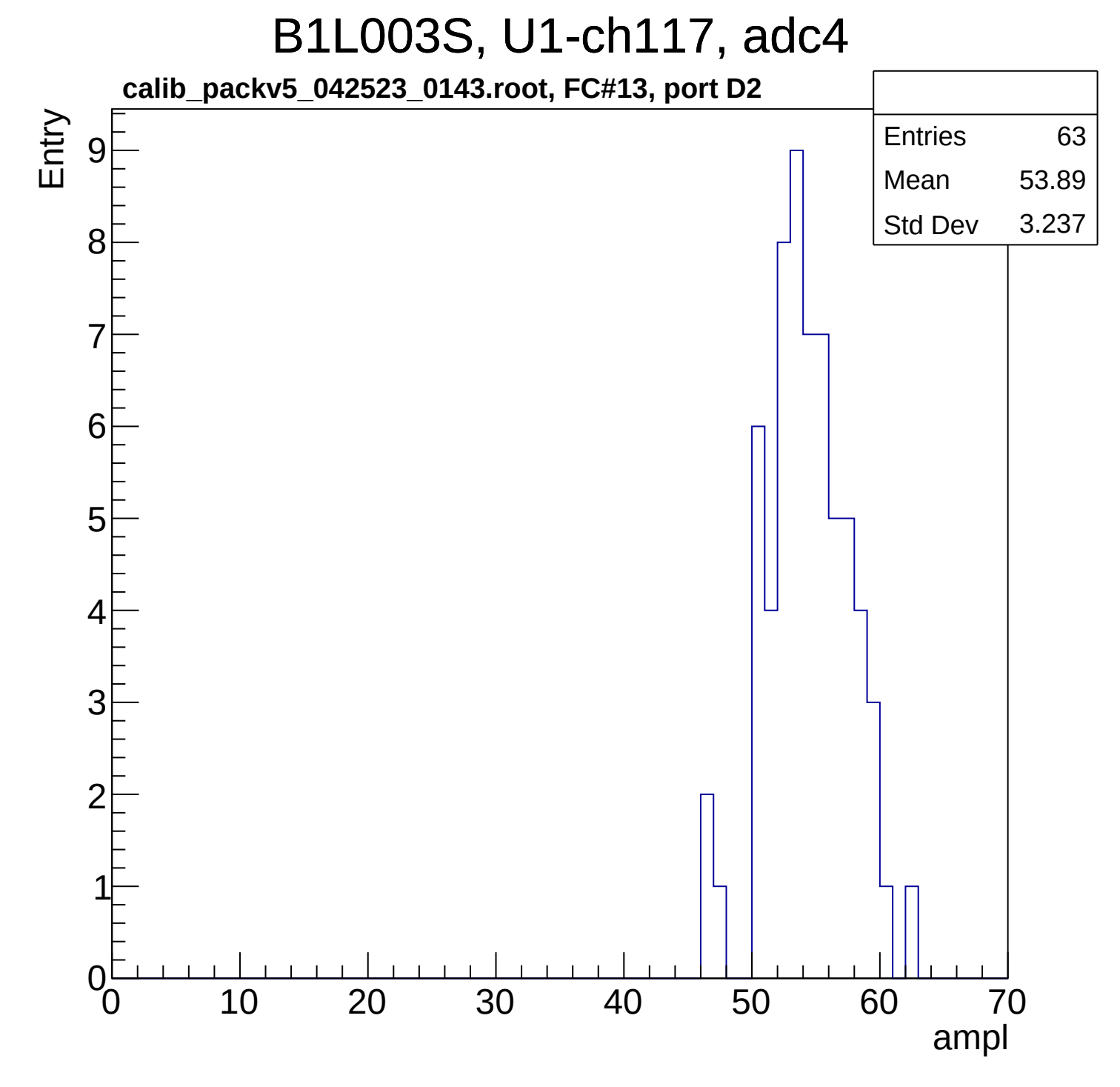
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 47.09
Std Dev 3.411
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch117, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 53.89
Std Dev 3.237
Entry
ampl
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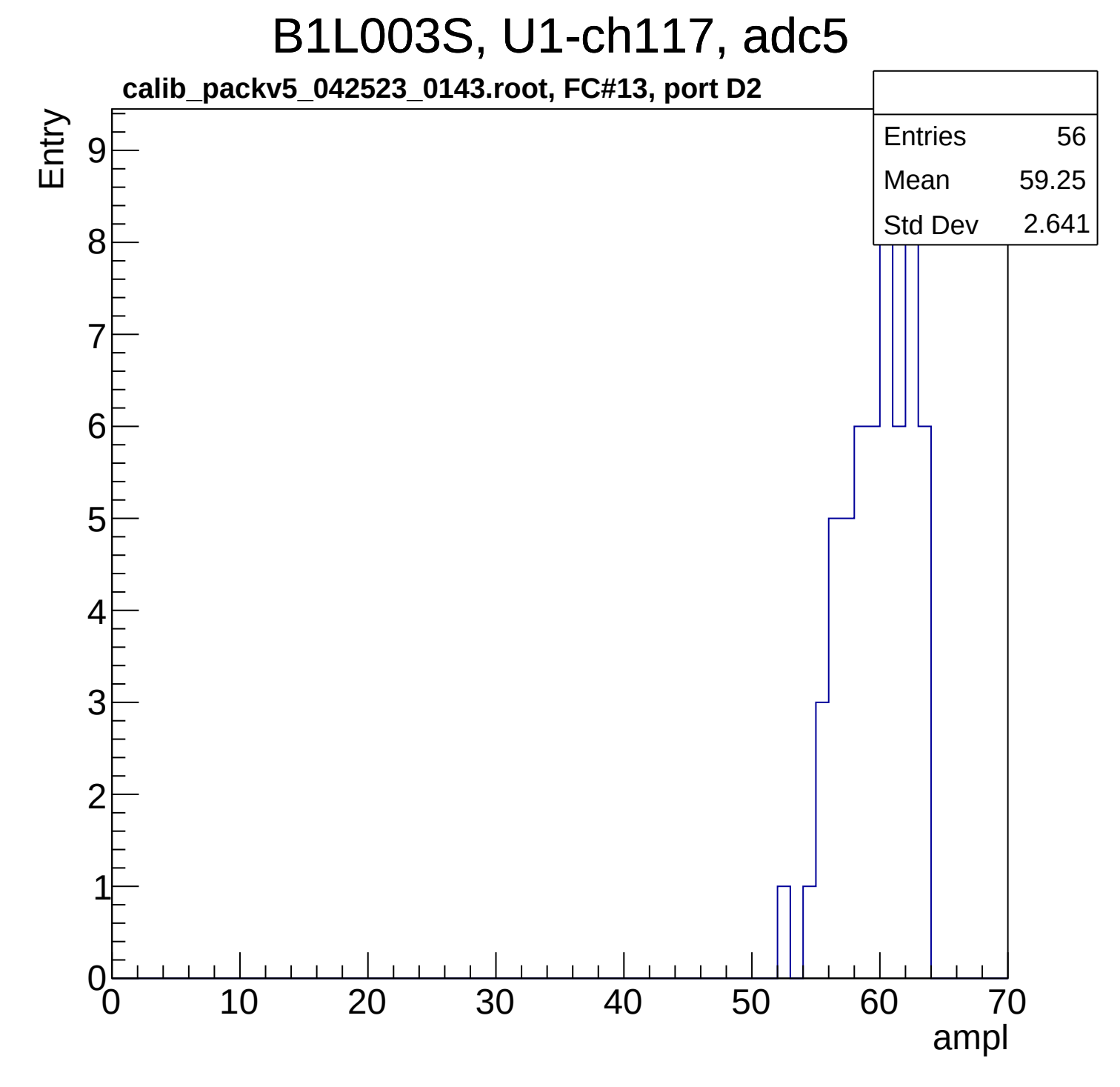
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 59.25
Std Dev 2.641
Entry
ampl
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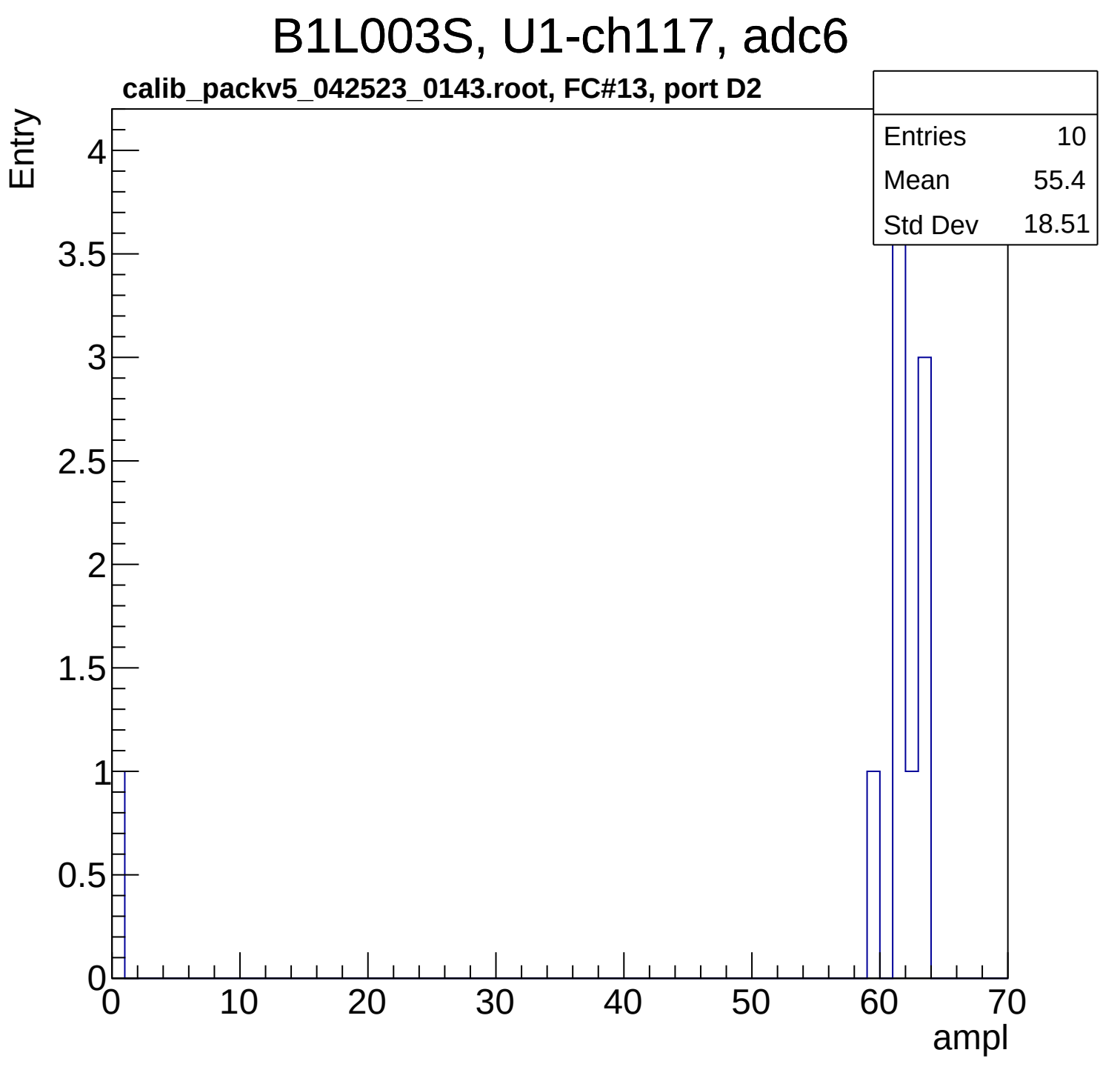
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 10
Mean 55.4
Std Dev 18.51
Entry
ampl
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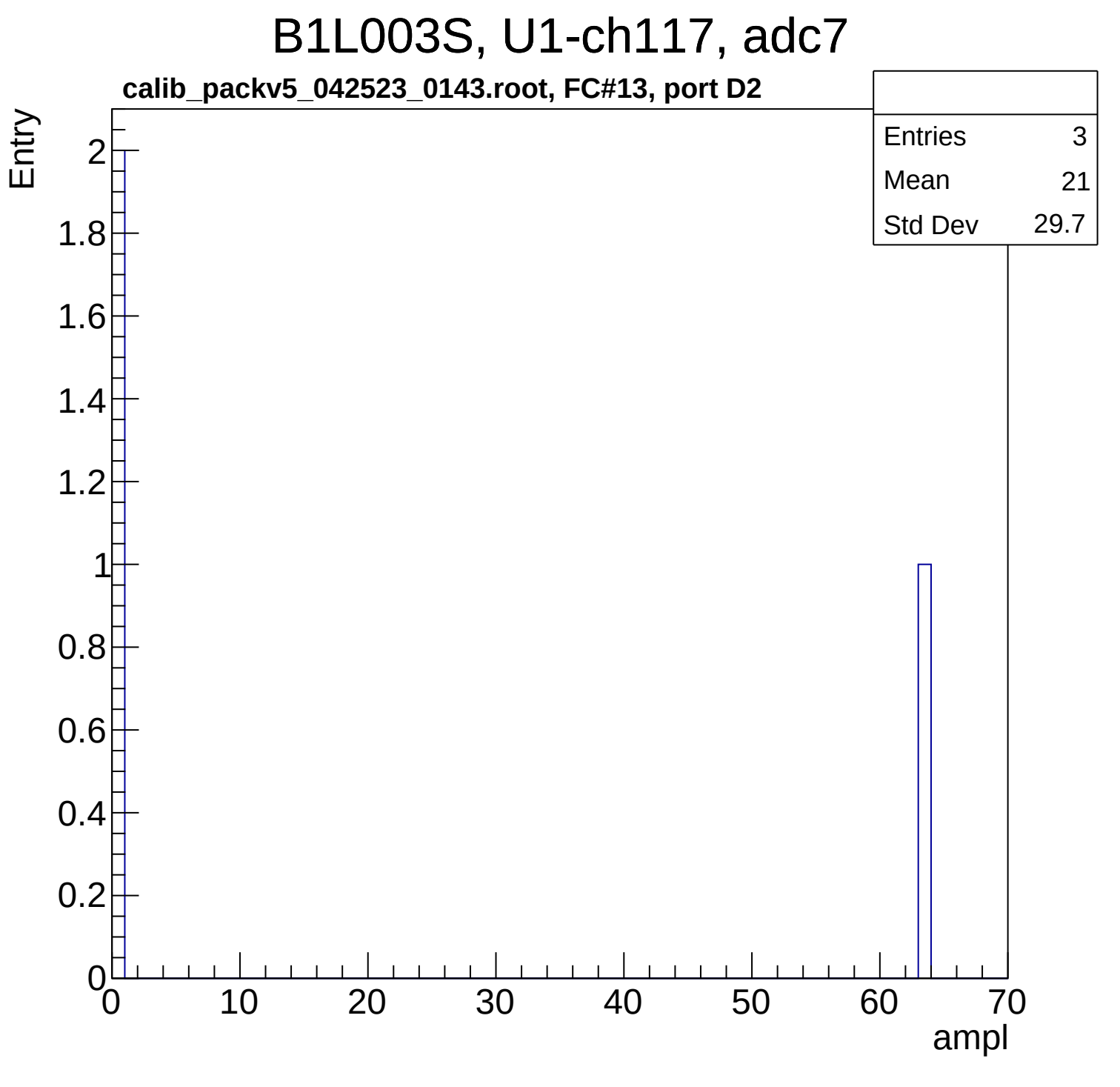
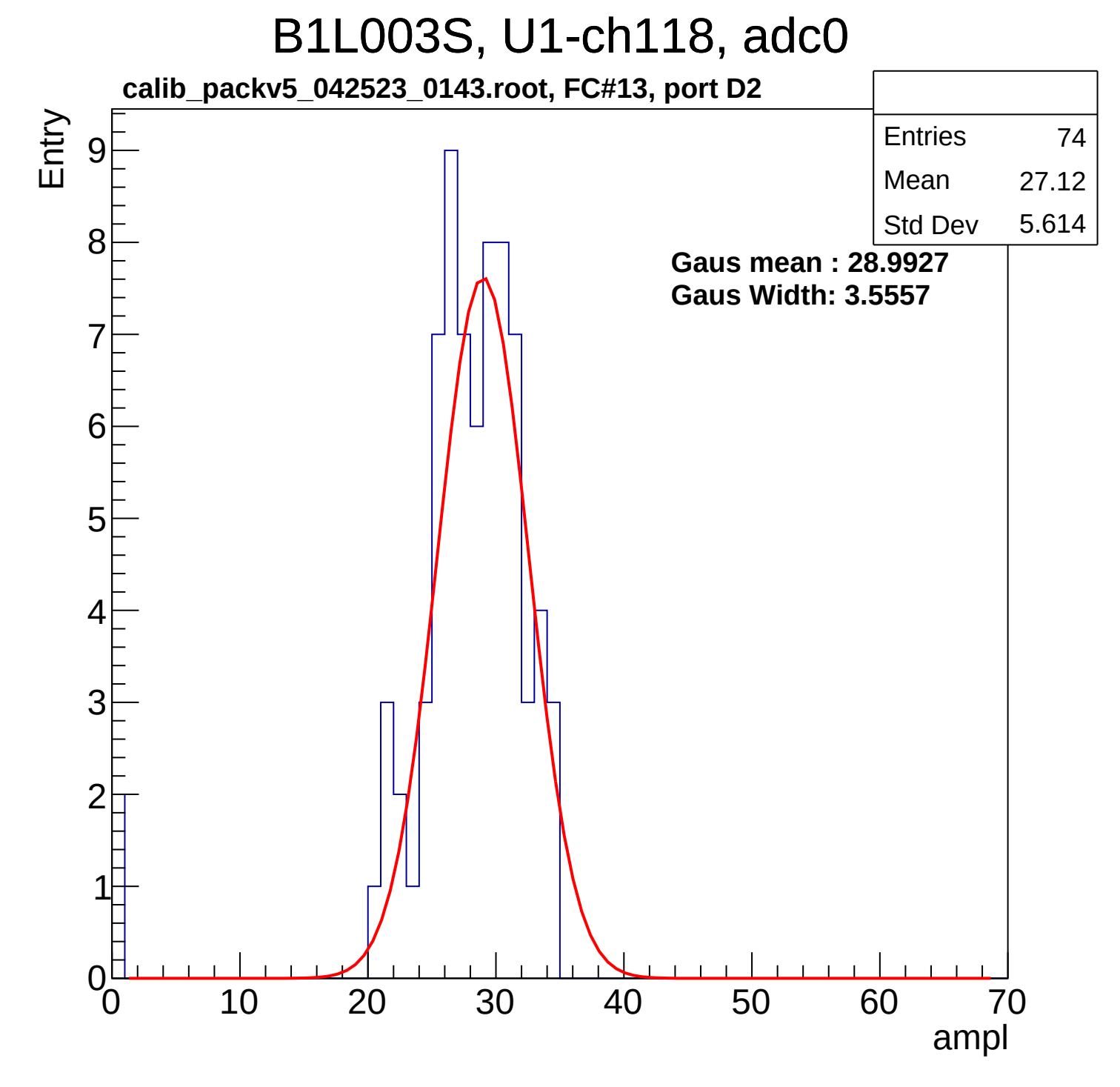
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L003S, U1-ch117, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch118, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 27.12
Std Dev 5.614
Gaus mean : 28.9927
Gaus Width: 3.5557
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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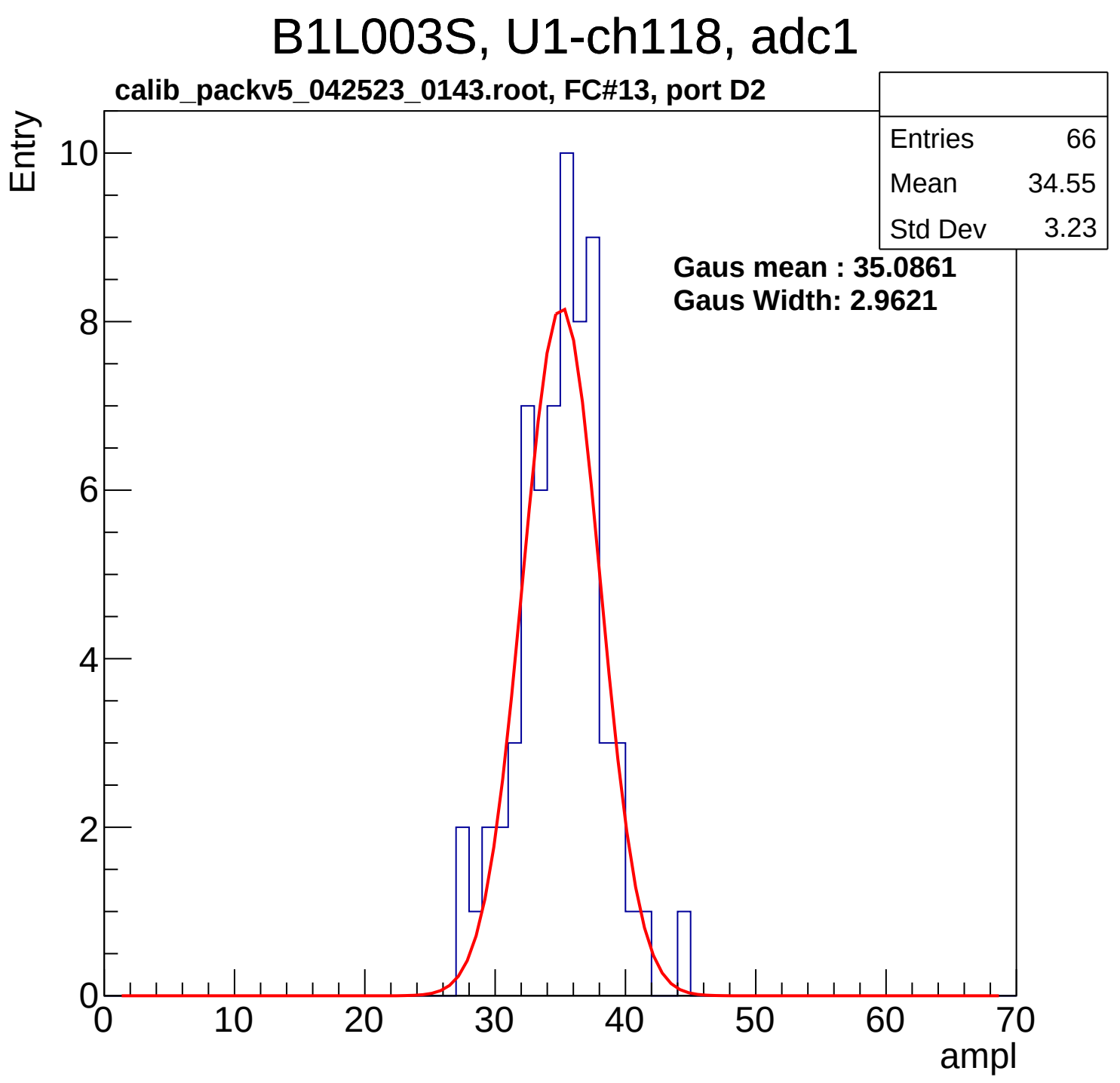
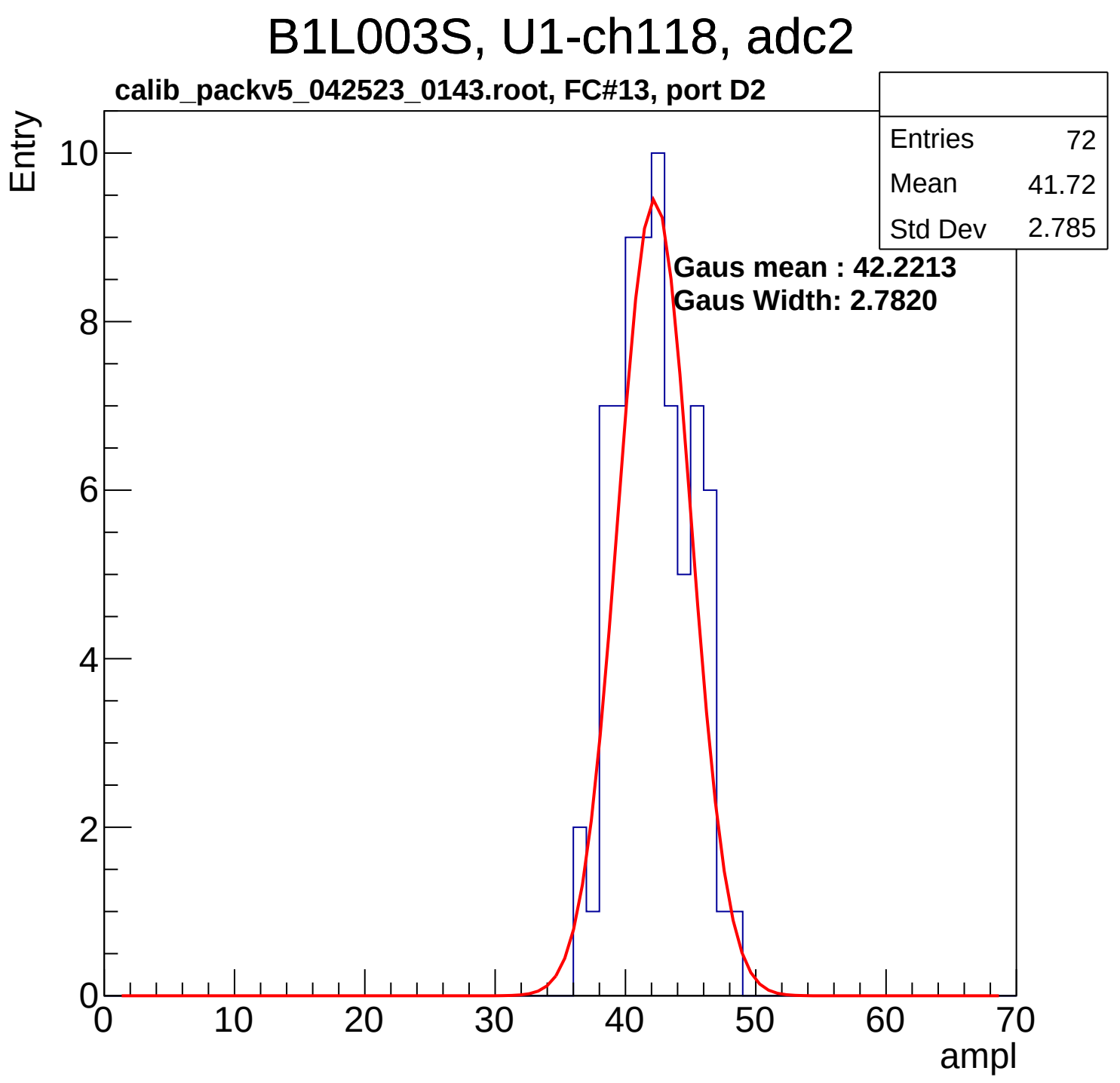
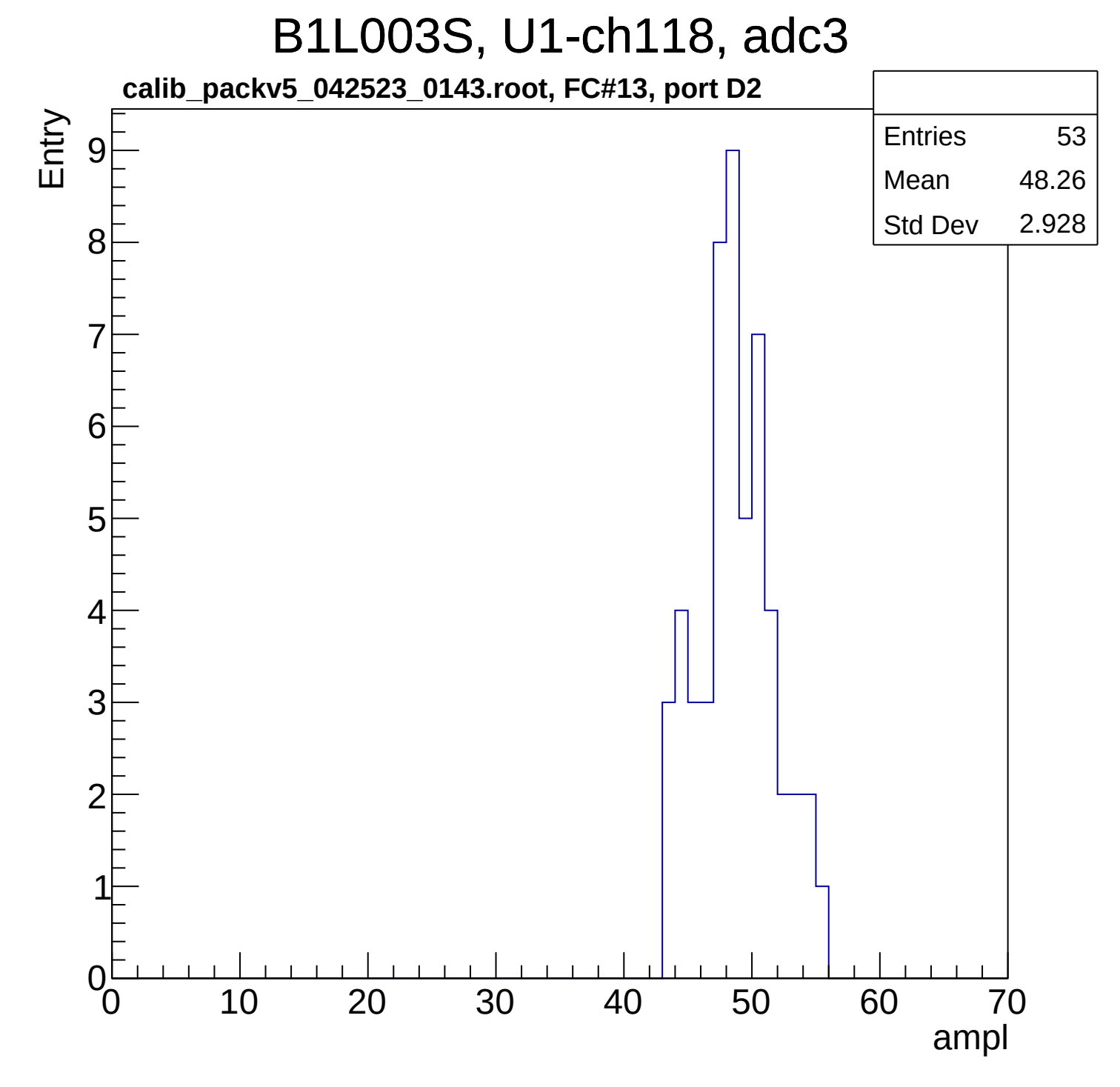

B1L003S, U1-ch118, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 34.55
Std Dev 3.23
Gaus mean : 35.0861
Gaus Width: 2.9621
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch118, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 72
Mean 41.72
Std Dev 2.785
Gaus mean : 42.2213
Gaus Width: 2.7820
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch118, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 48.26
Std Dev 2.928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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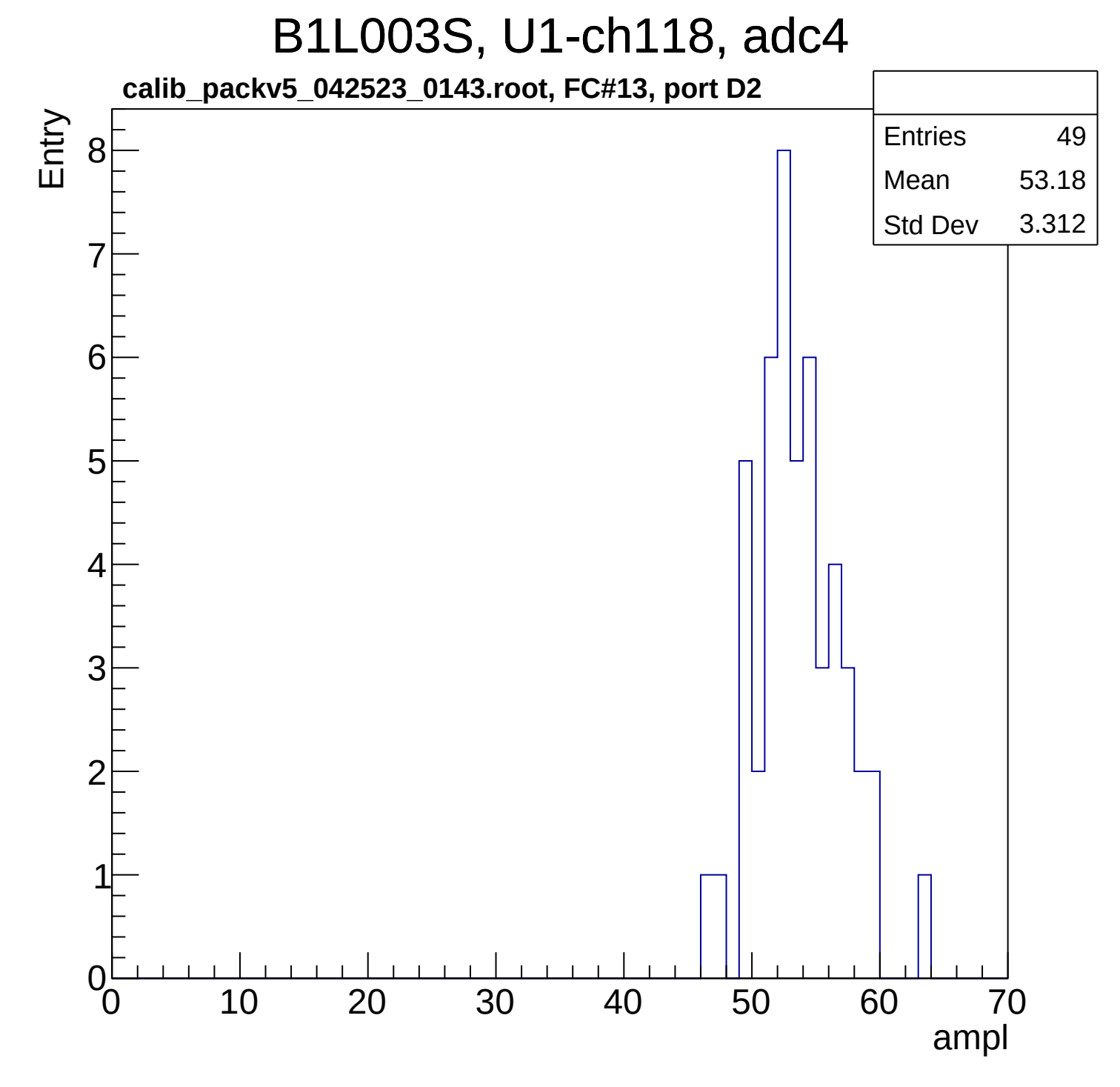
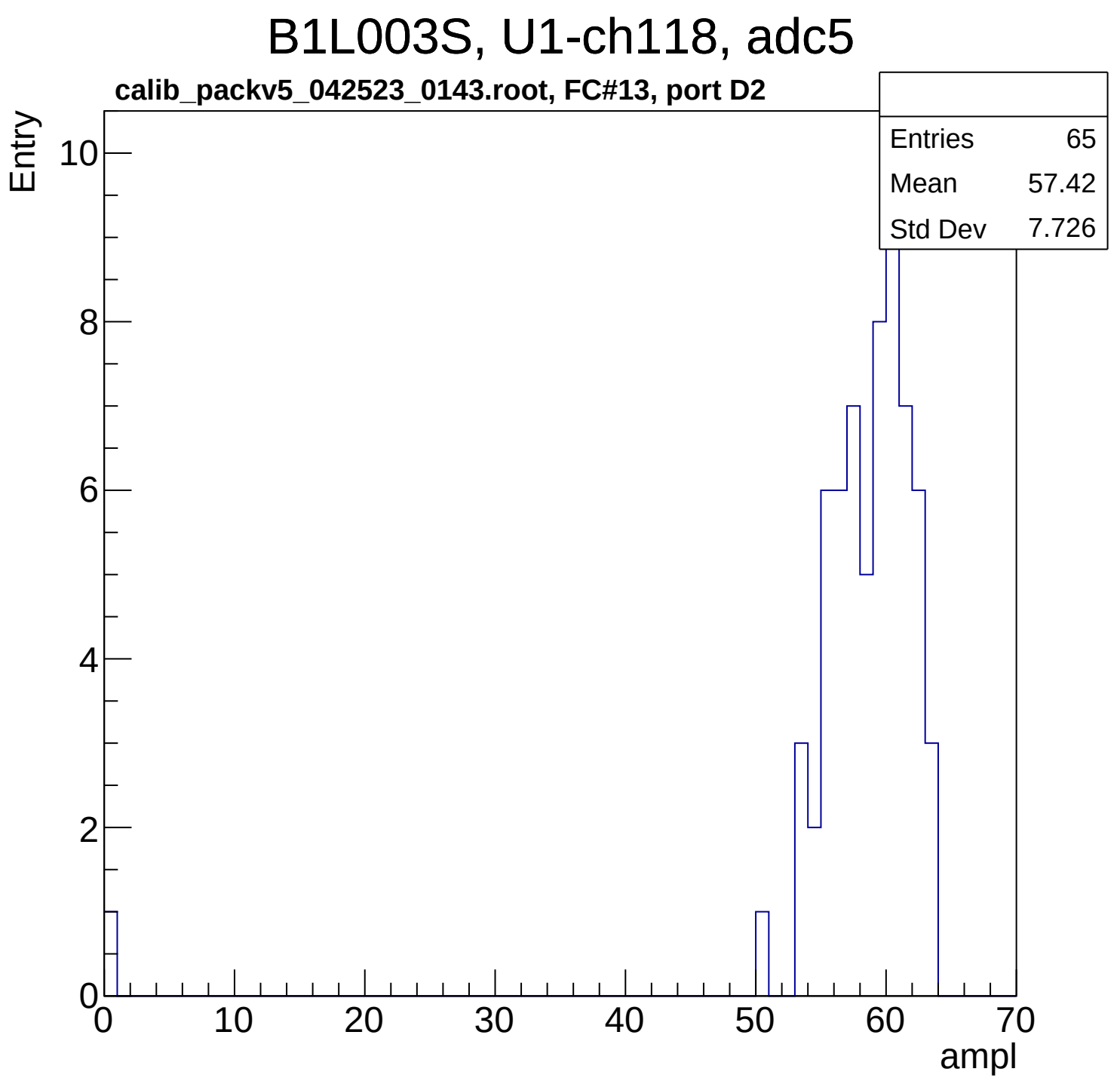
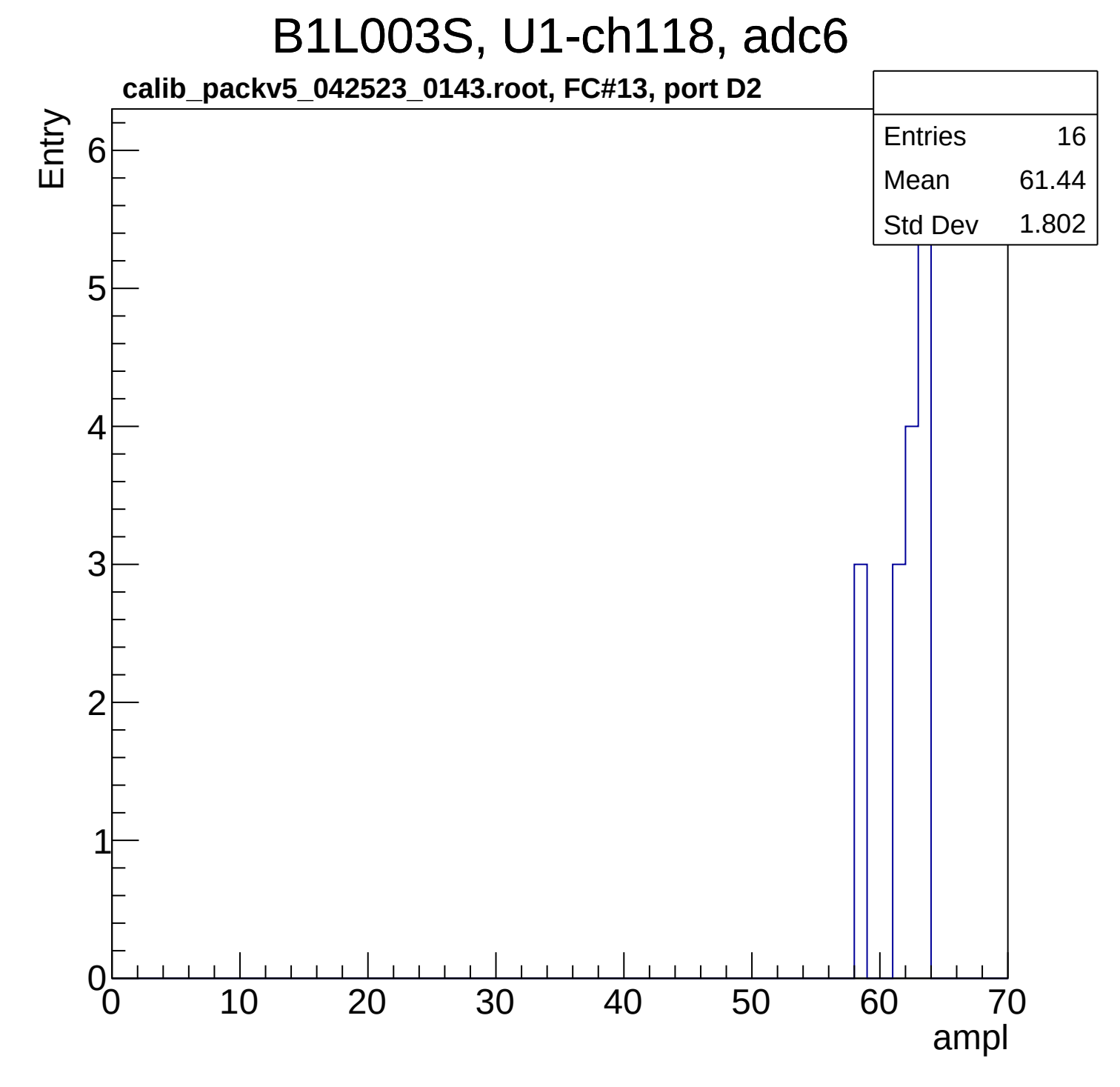
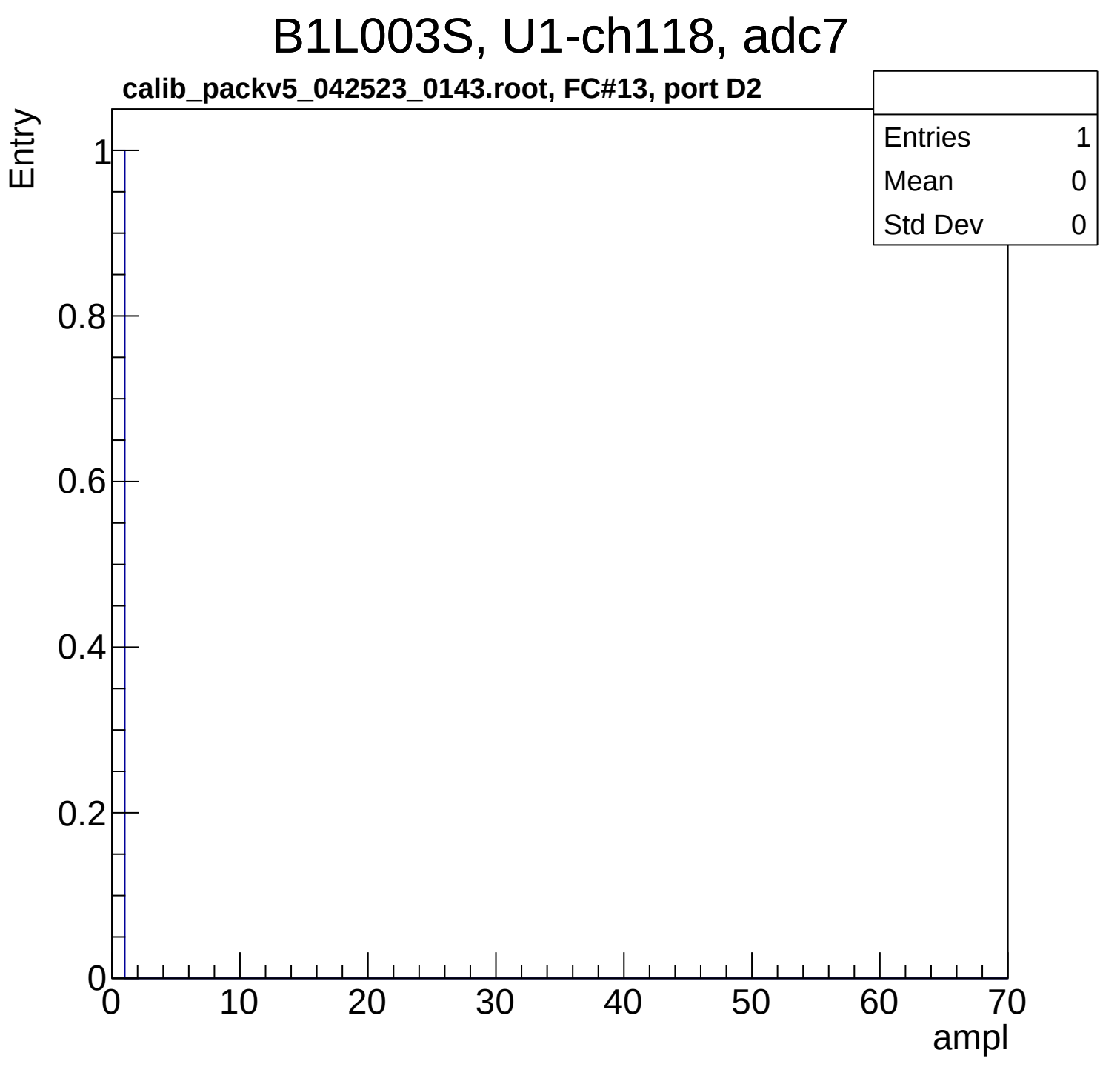
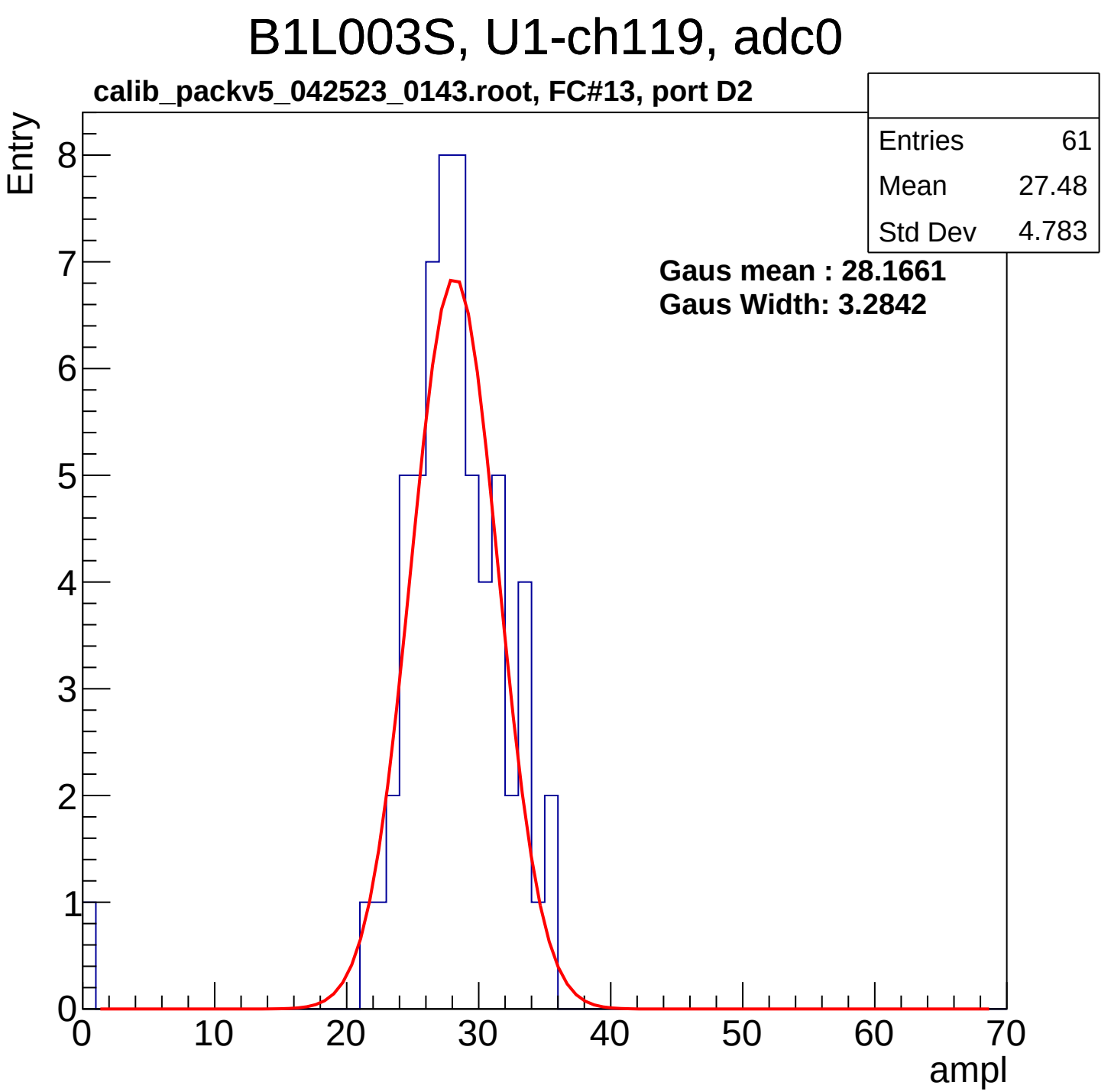
60
70


B1L003S, U1-ch118, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 53.18
Std Dev 3.312
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch118, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 65
Mean 57.42
Std Dev 7.726
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch118, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 16
Mean 61.44
Std Dev 1.802
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch118, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch119, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 27.48
Std Dev 4.783
Gaus mean : 28.1661
Gaus Width: 3.2842
Entry
ampl
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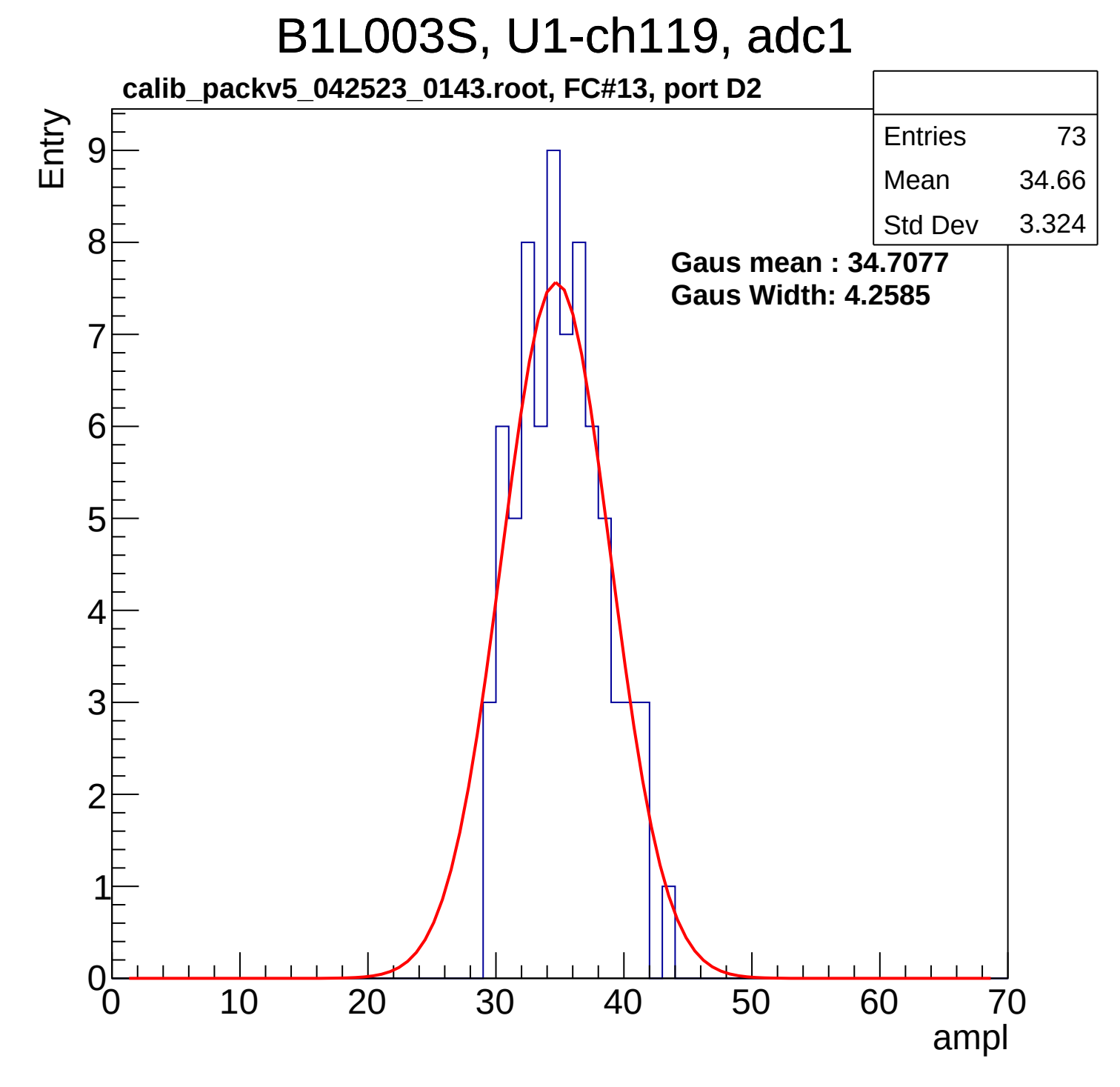
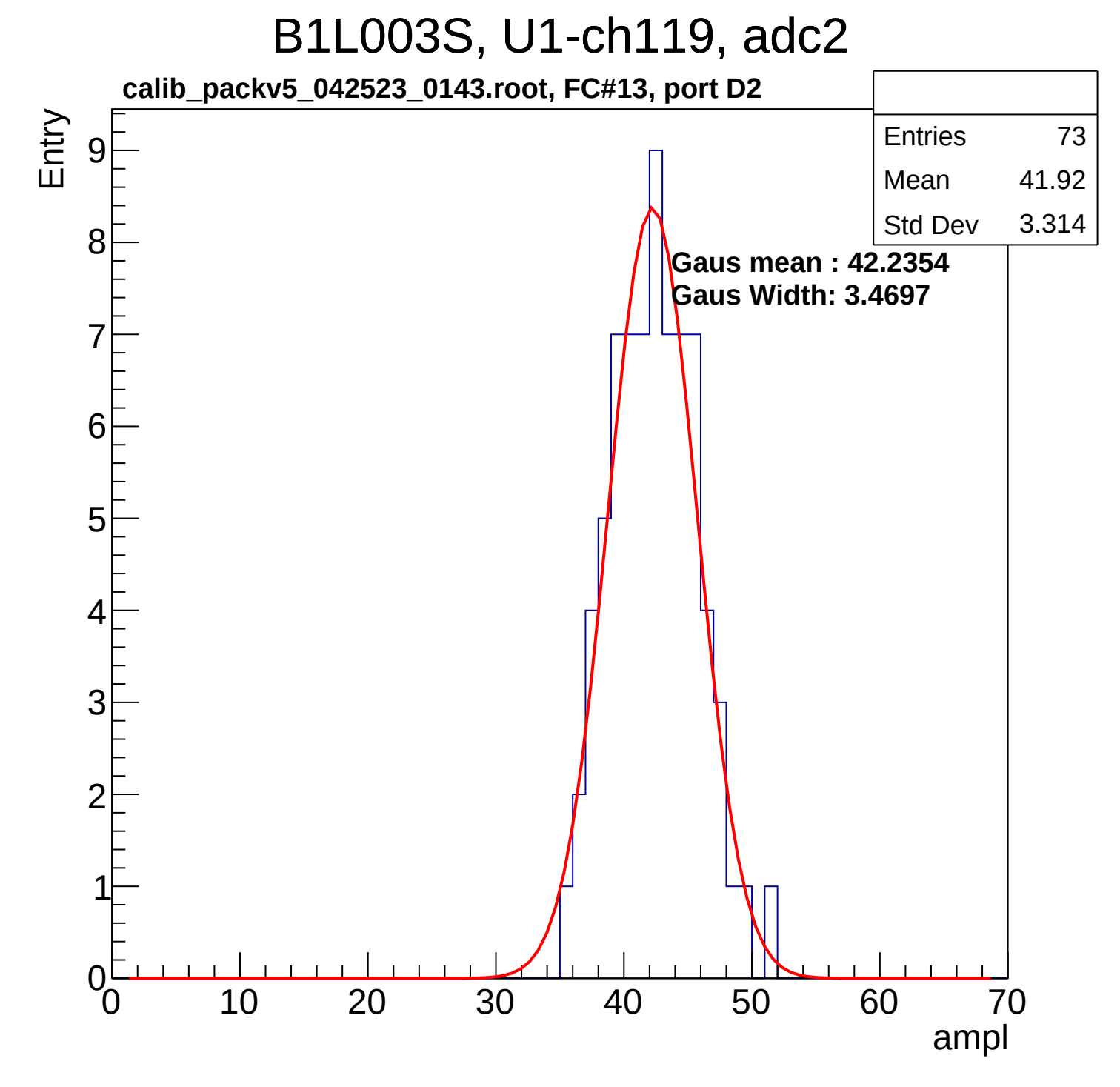
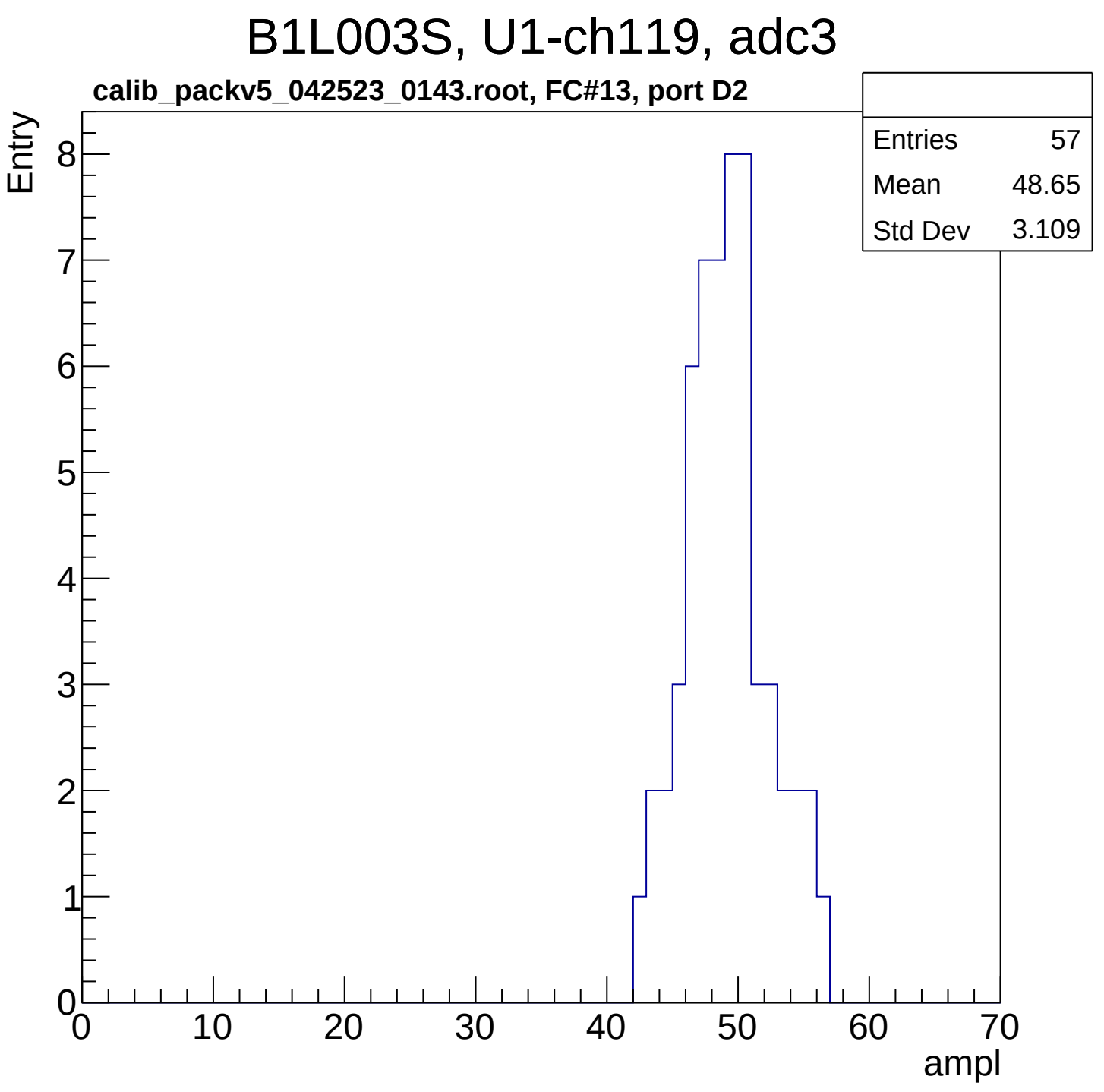
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch119, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 34.66
Std Dev 3.324
Gaus mean : 34.7077
Gaus Width: 4.2585
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch119, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 41.92
Std Dev 3.314
Gaus mean : 42.2354
Gaus Width: 3.4697
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch119, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 48.65
Std Dev 3.109
Entry
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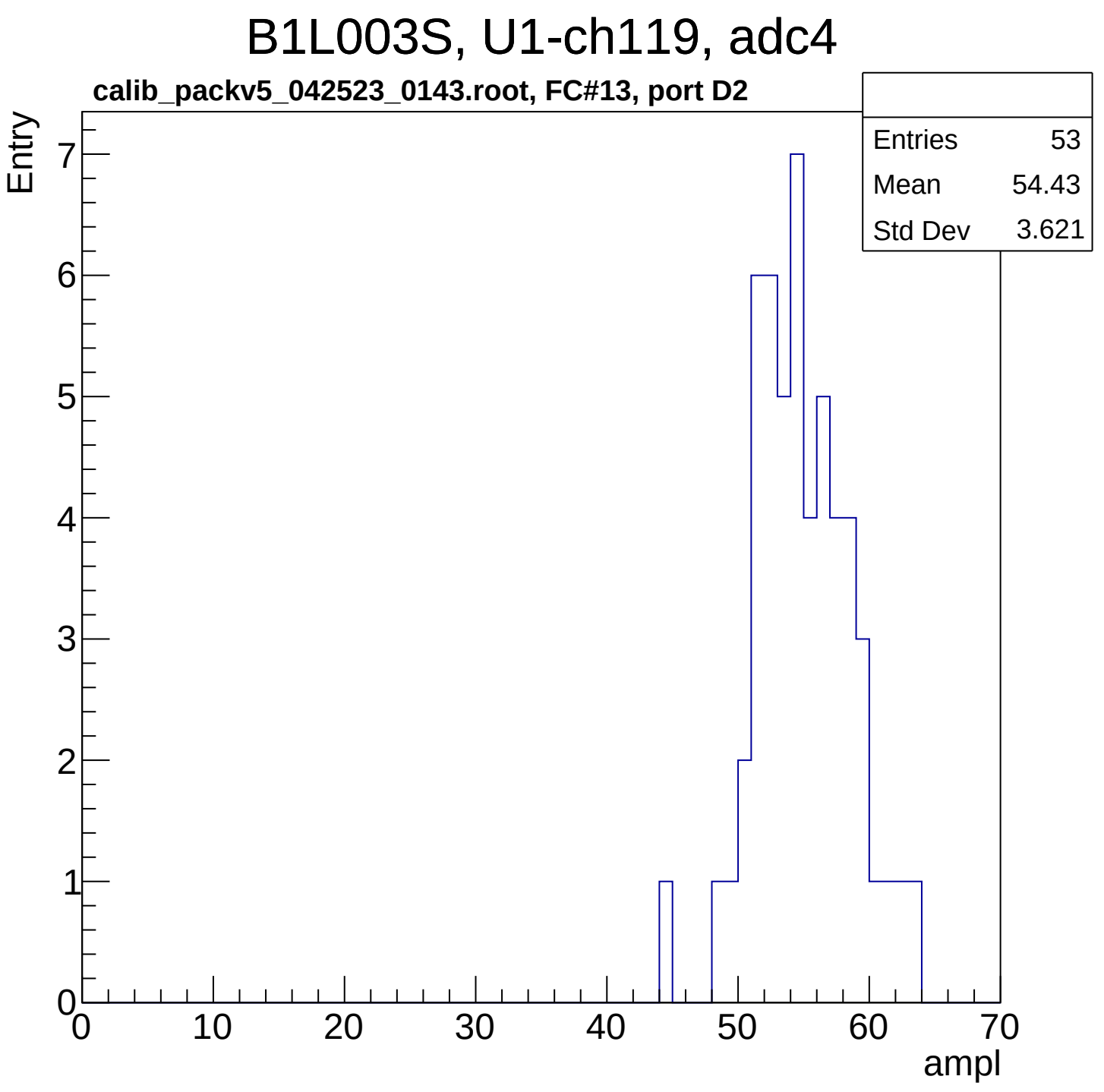
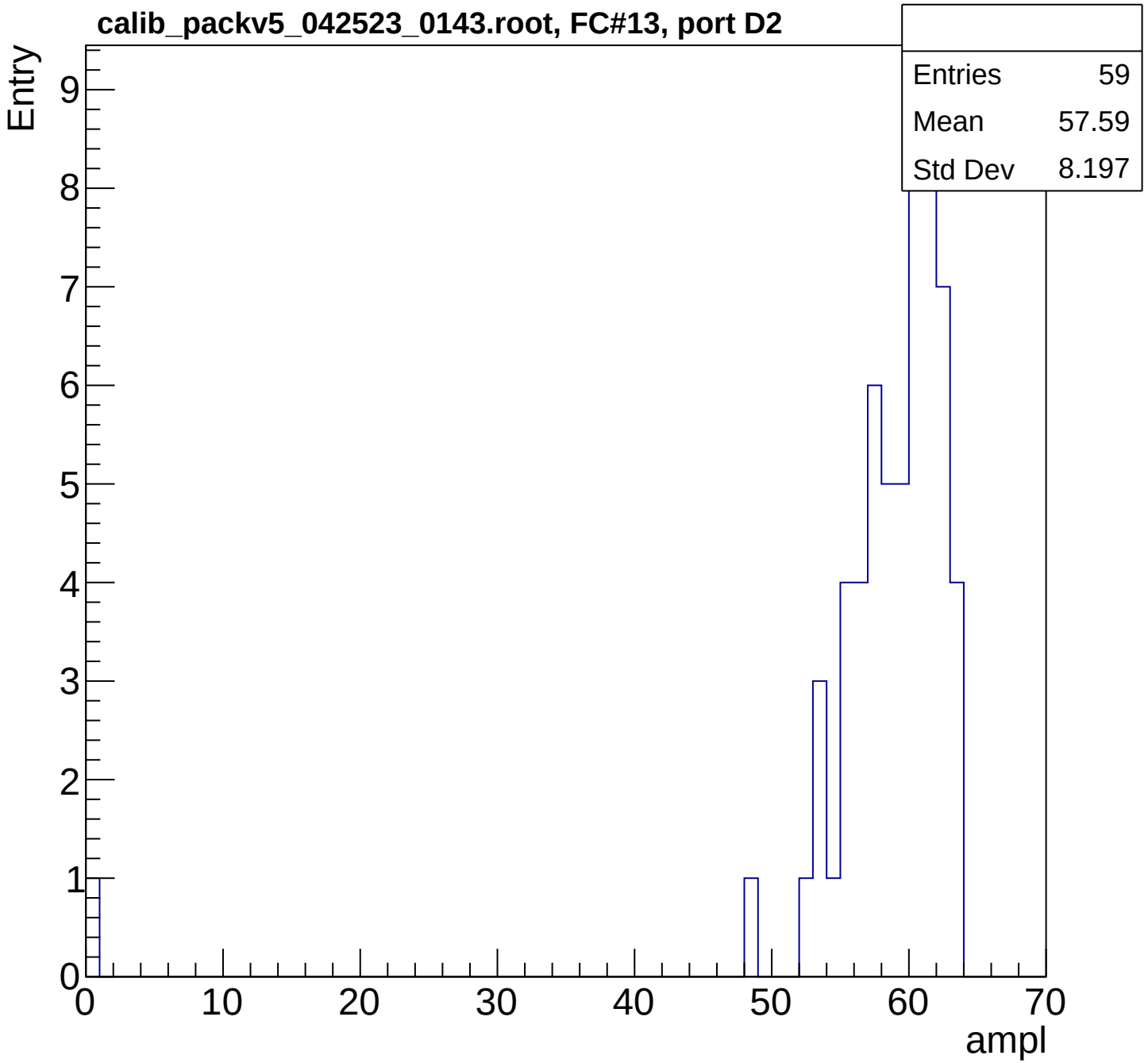
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch119, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 53
Mean 54.43
Std Dev 3.621
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch119, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 57.59
Std Dev 8.197
Entry
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3
4
5
6
7
8
9
0
10
20
30
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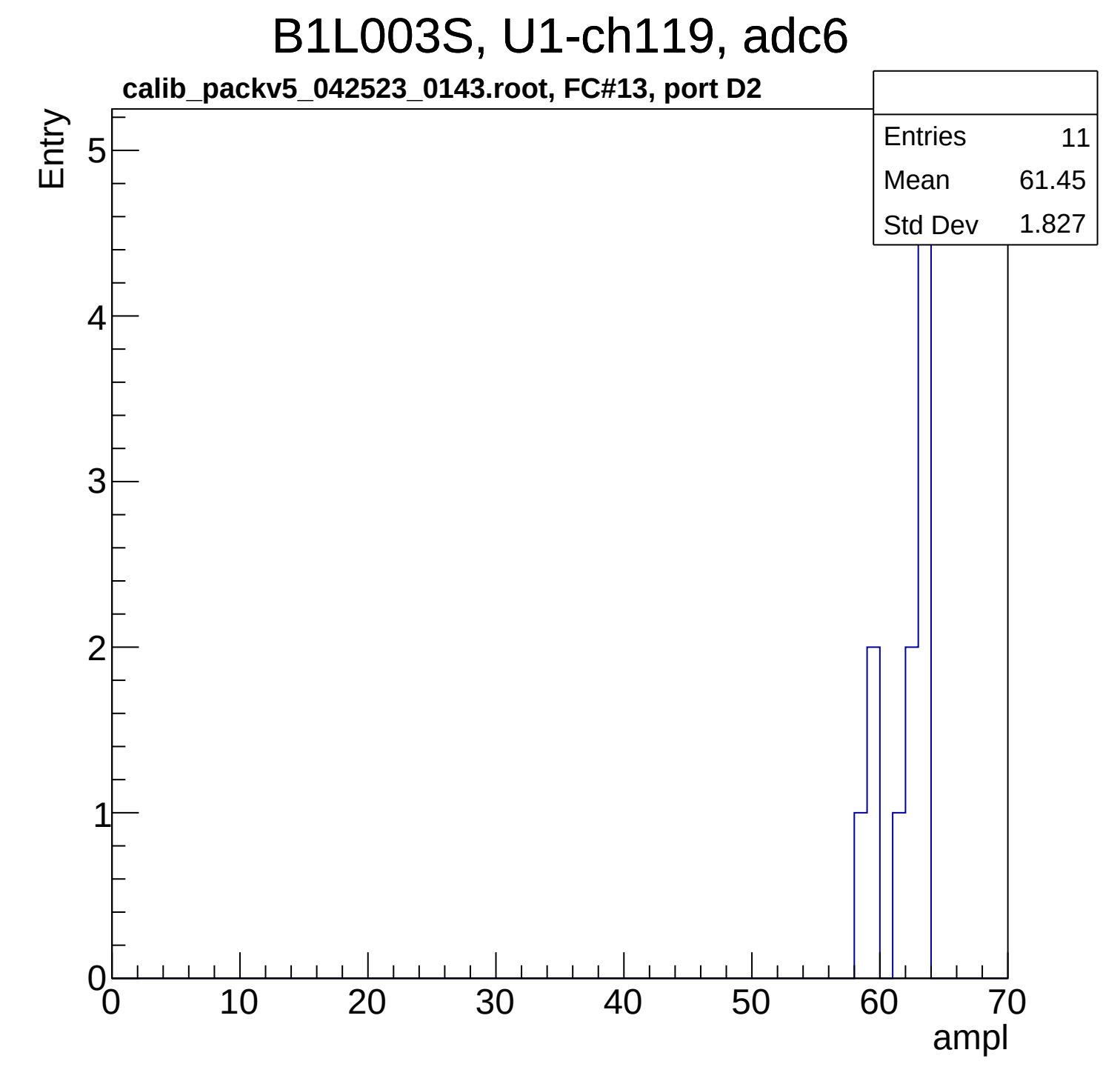
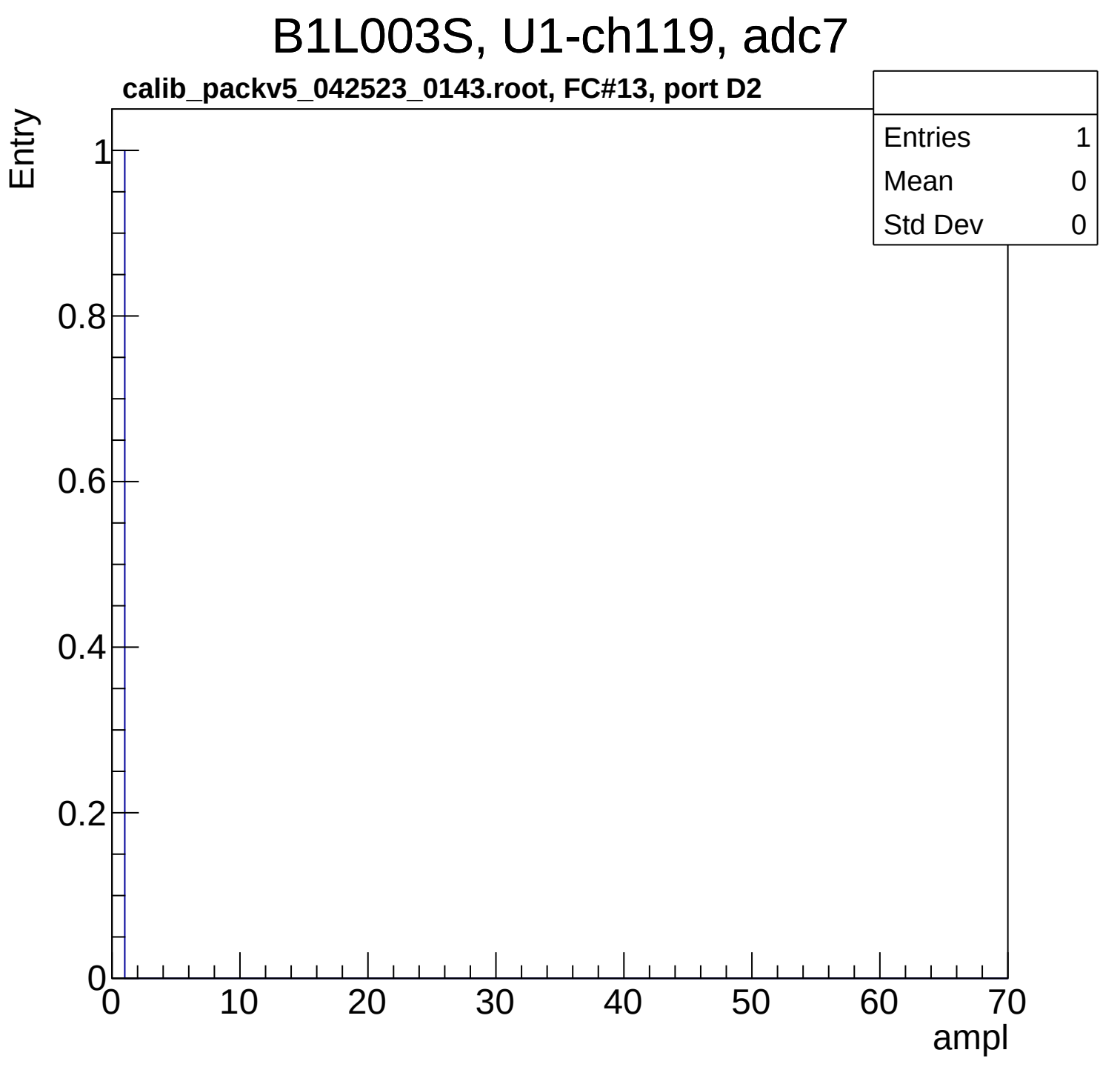
50
60
70
ampl


B1L003S, U1-ch119, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.45
Std Dev 1.827
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch119, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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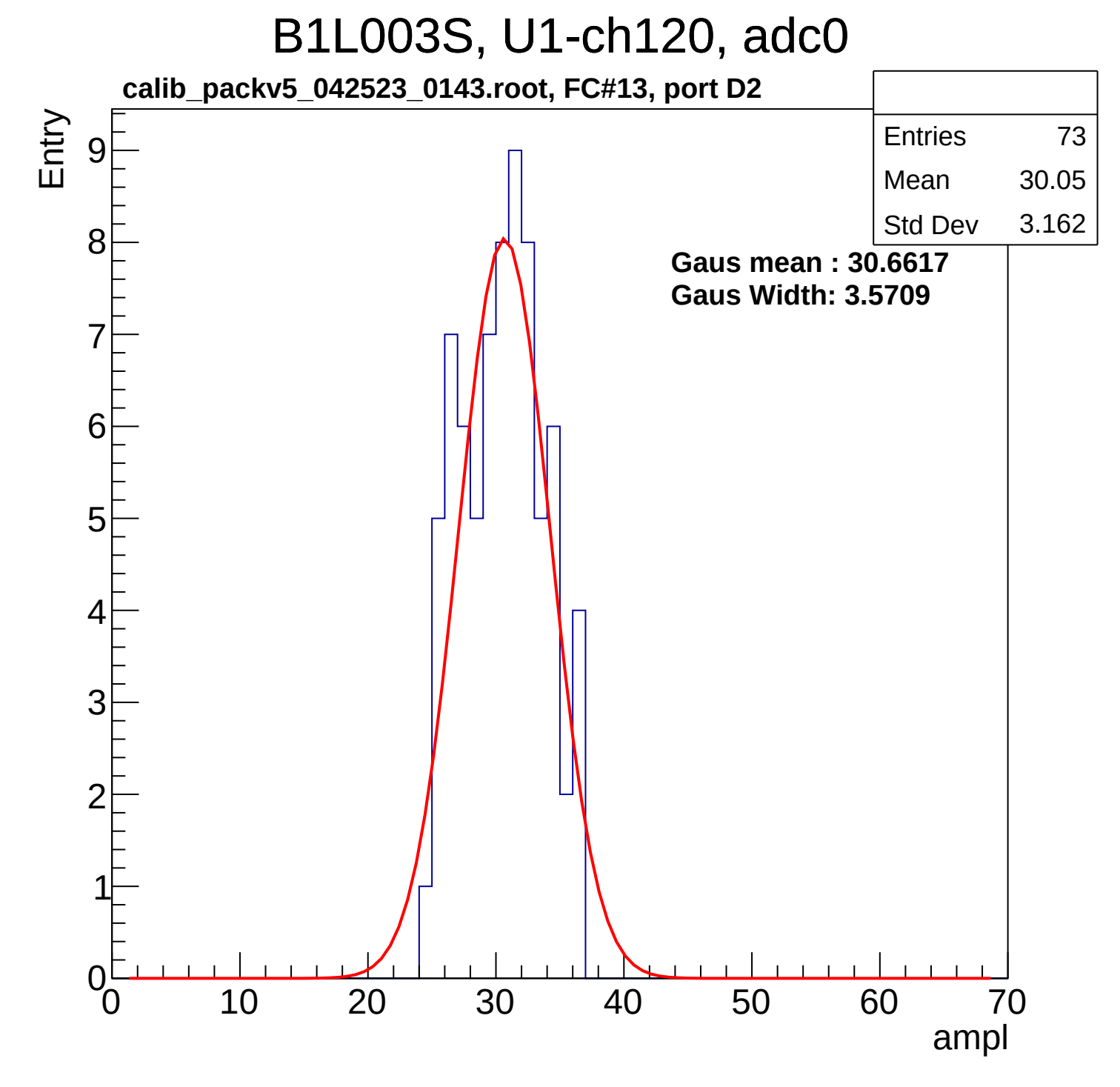
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch120, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 30.05
Std Dev 3.162
Gaus mean : 30.6617
Gaus Width: 3.5709
Entry
ampl
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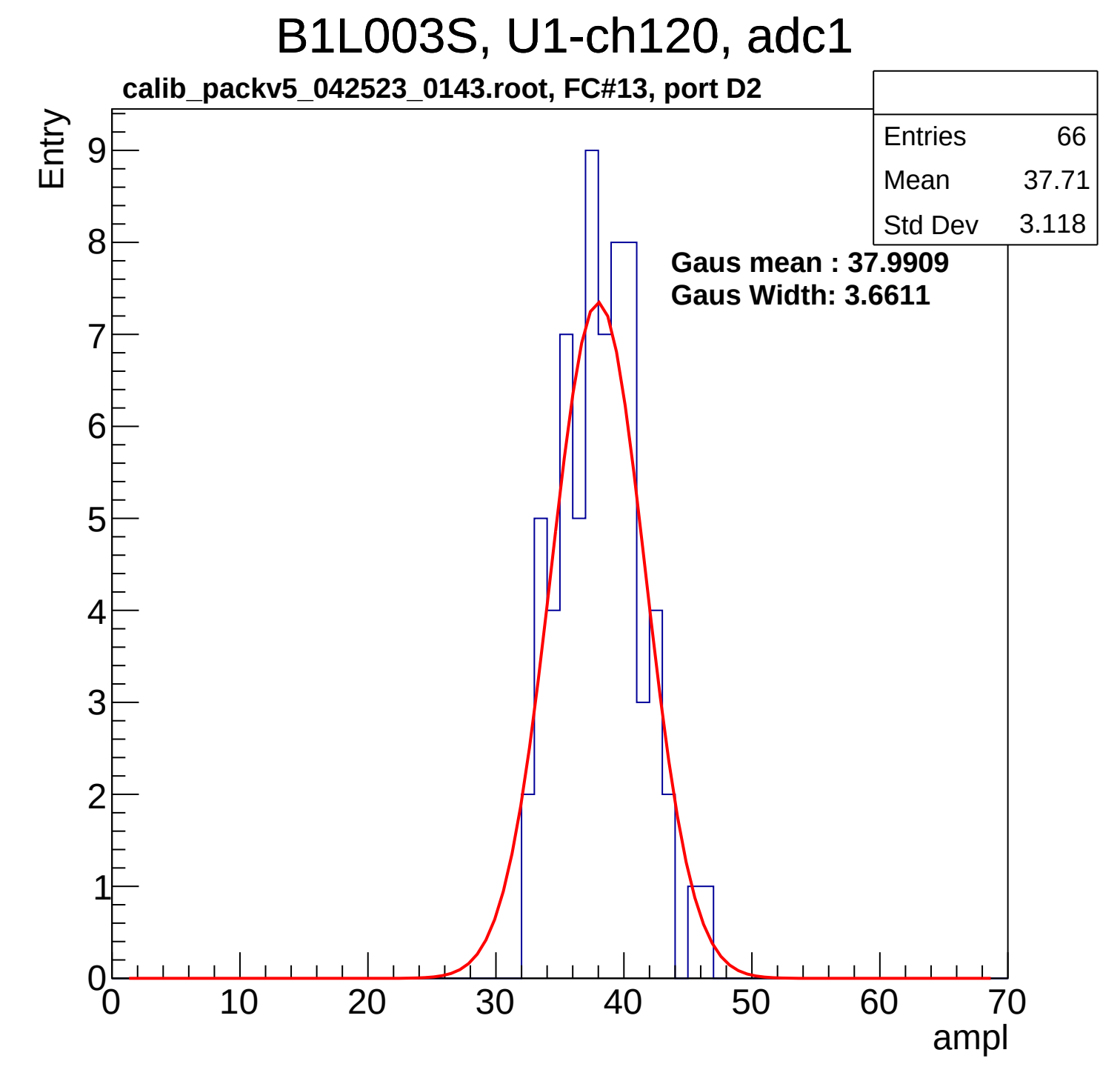
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch120, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 37.71
Std Dev 3.118
Gaus mean : 37.9909
Gaus Width: 3.6611
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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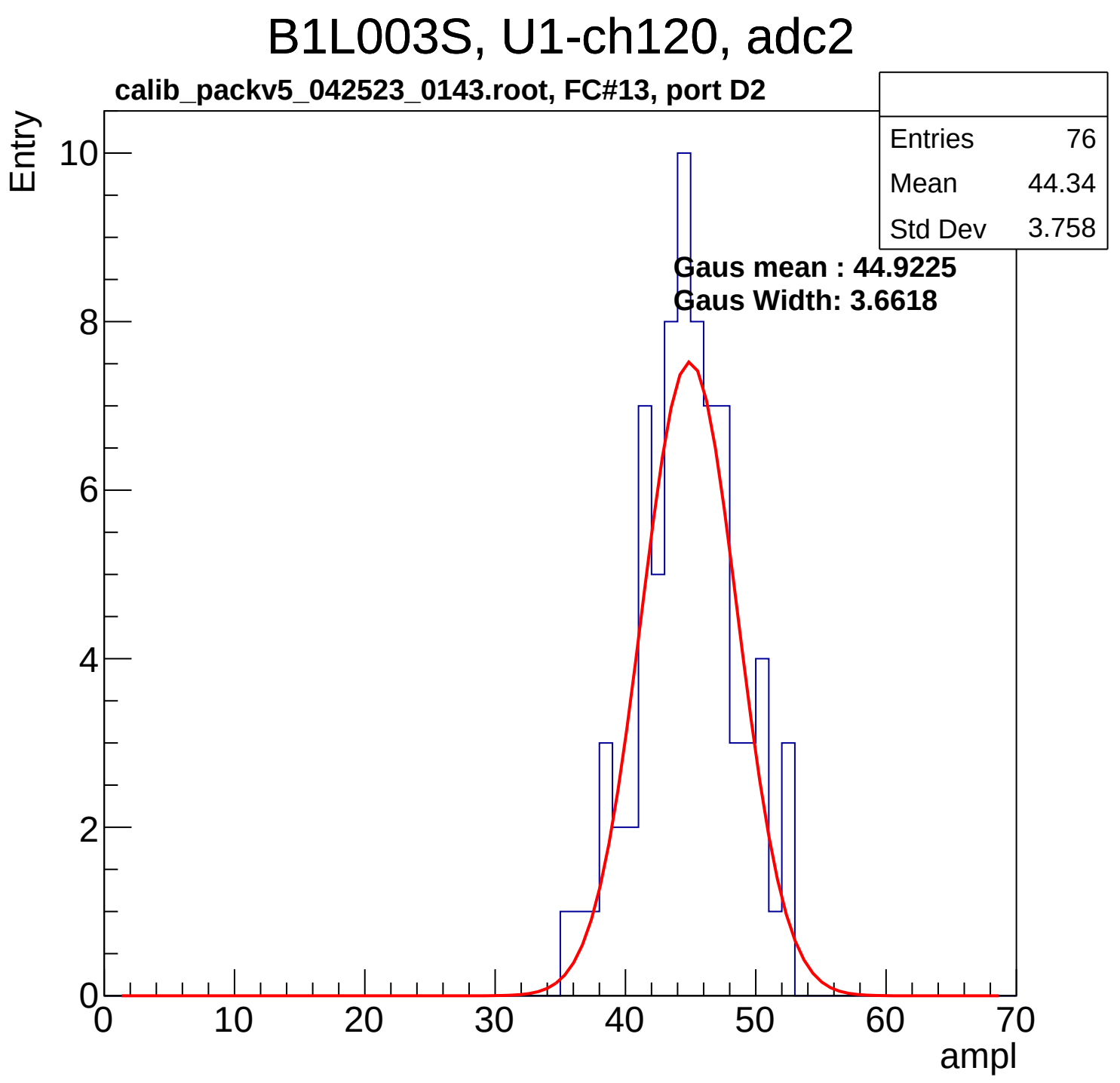
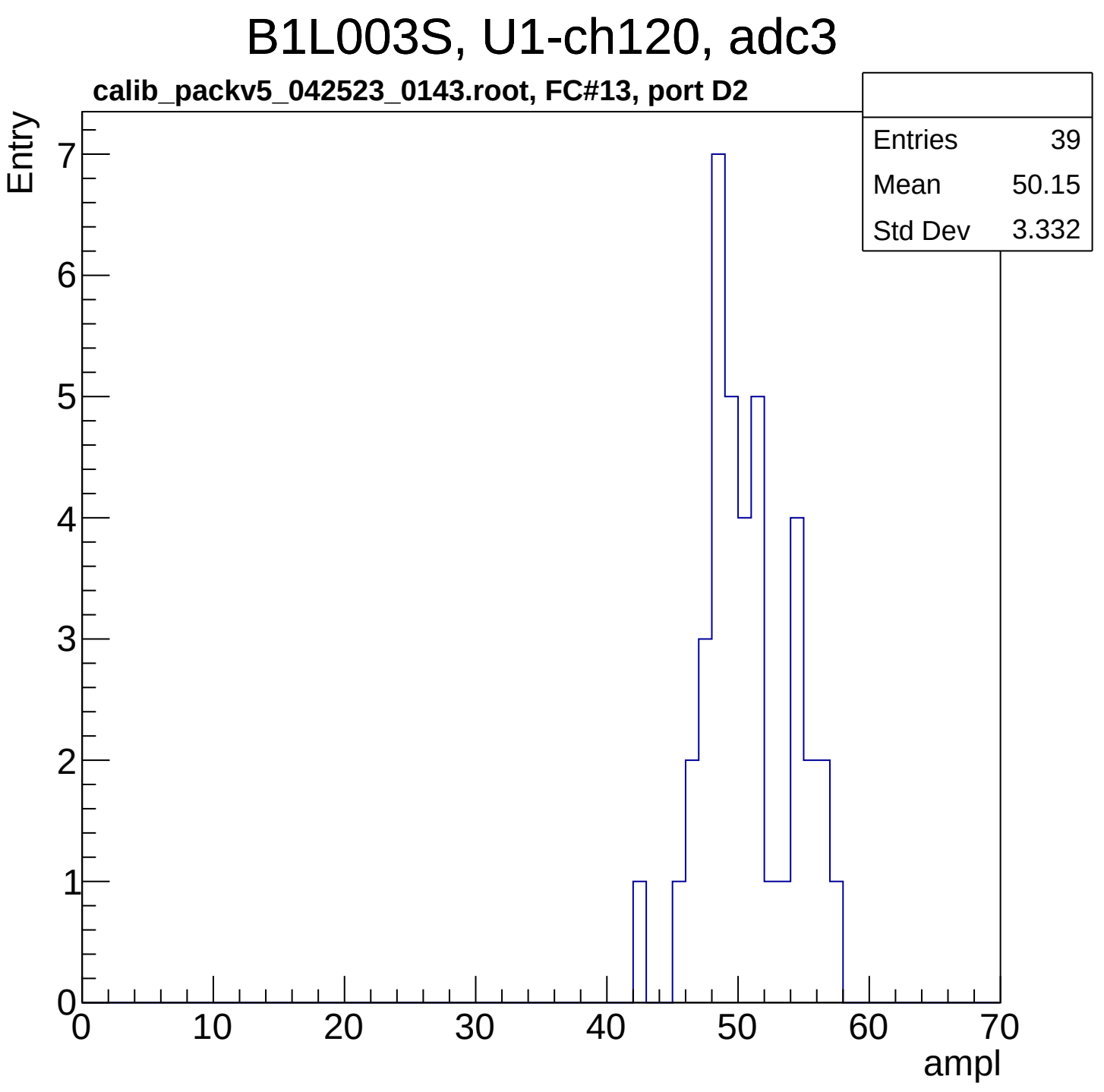
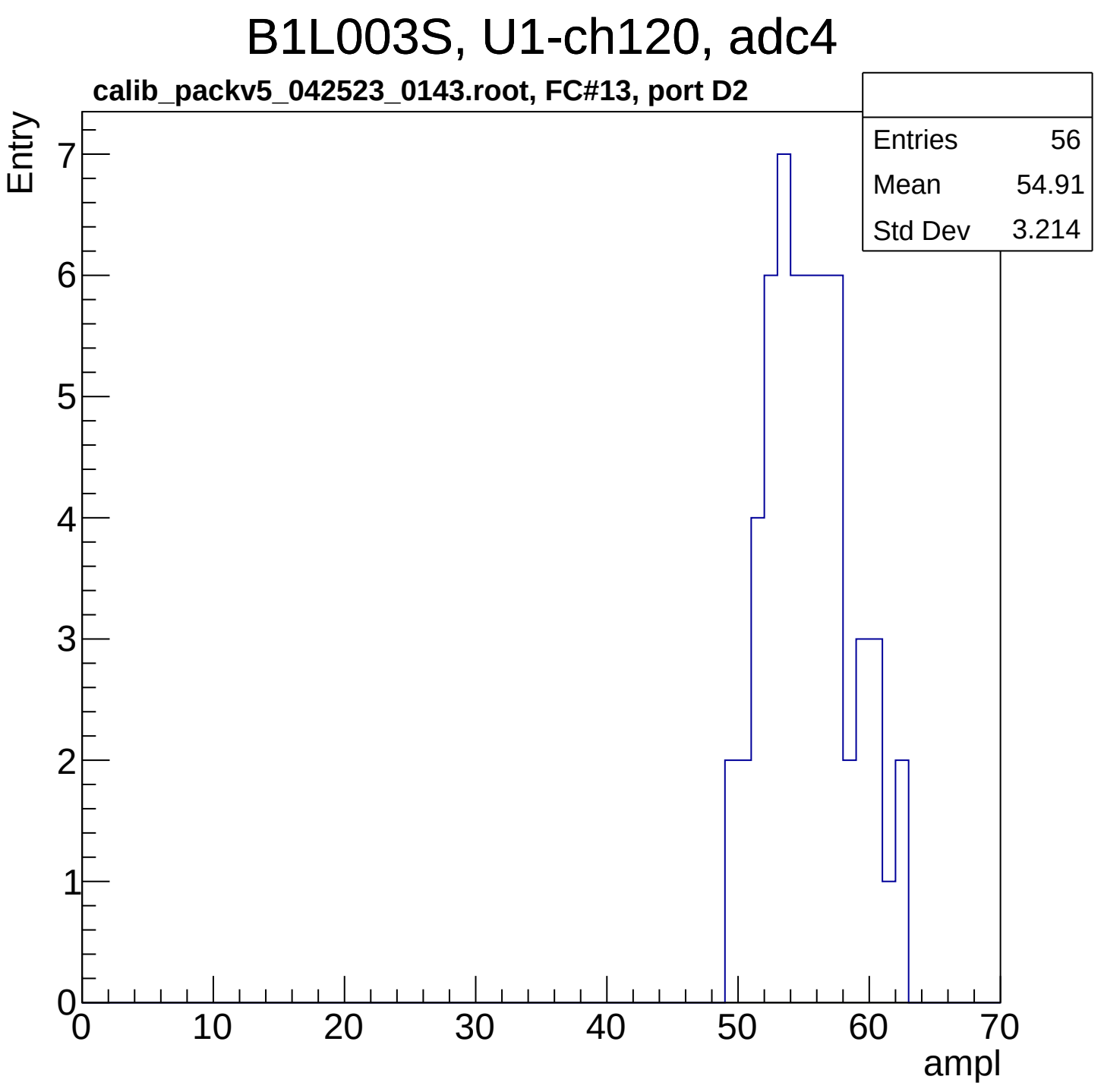

B1L003S, U1-ch120, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 76
Mean 44.34
Std Dev 3.758
Gaus mean : 44.9225
Gaus Width: 3.6618
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch120, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 39
Mean 50.15
Std Dev 3.332
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch120, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 54.91
Std Dev 3.214
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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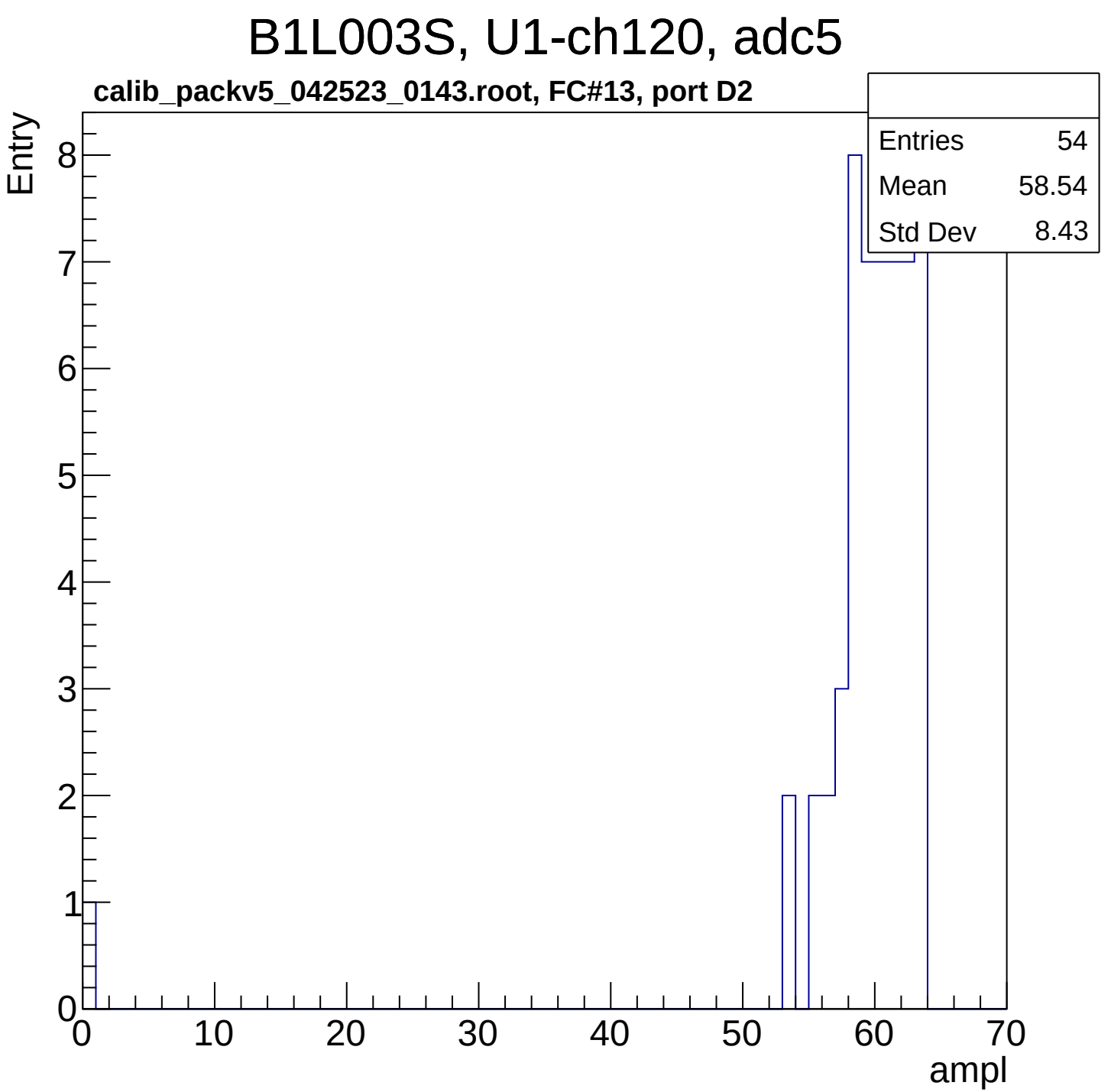
60
70


B1L003S, U1-ch120, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 54
Mean 58.54
Std Dev 8.43
Entry
ampl
0
1
2
3
4
5
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7
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0
10
20
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40
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60
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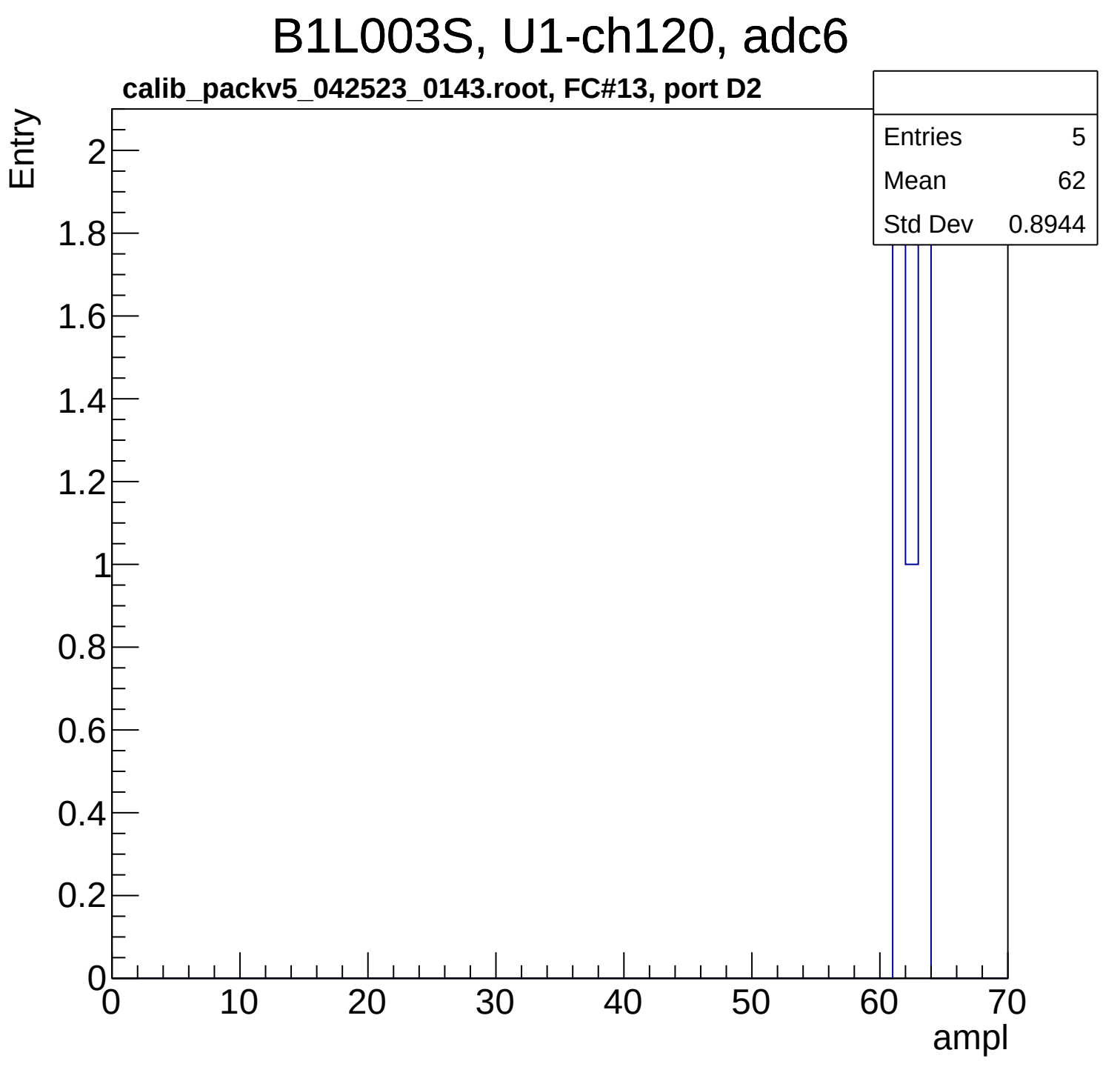
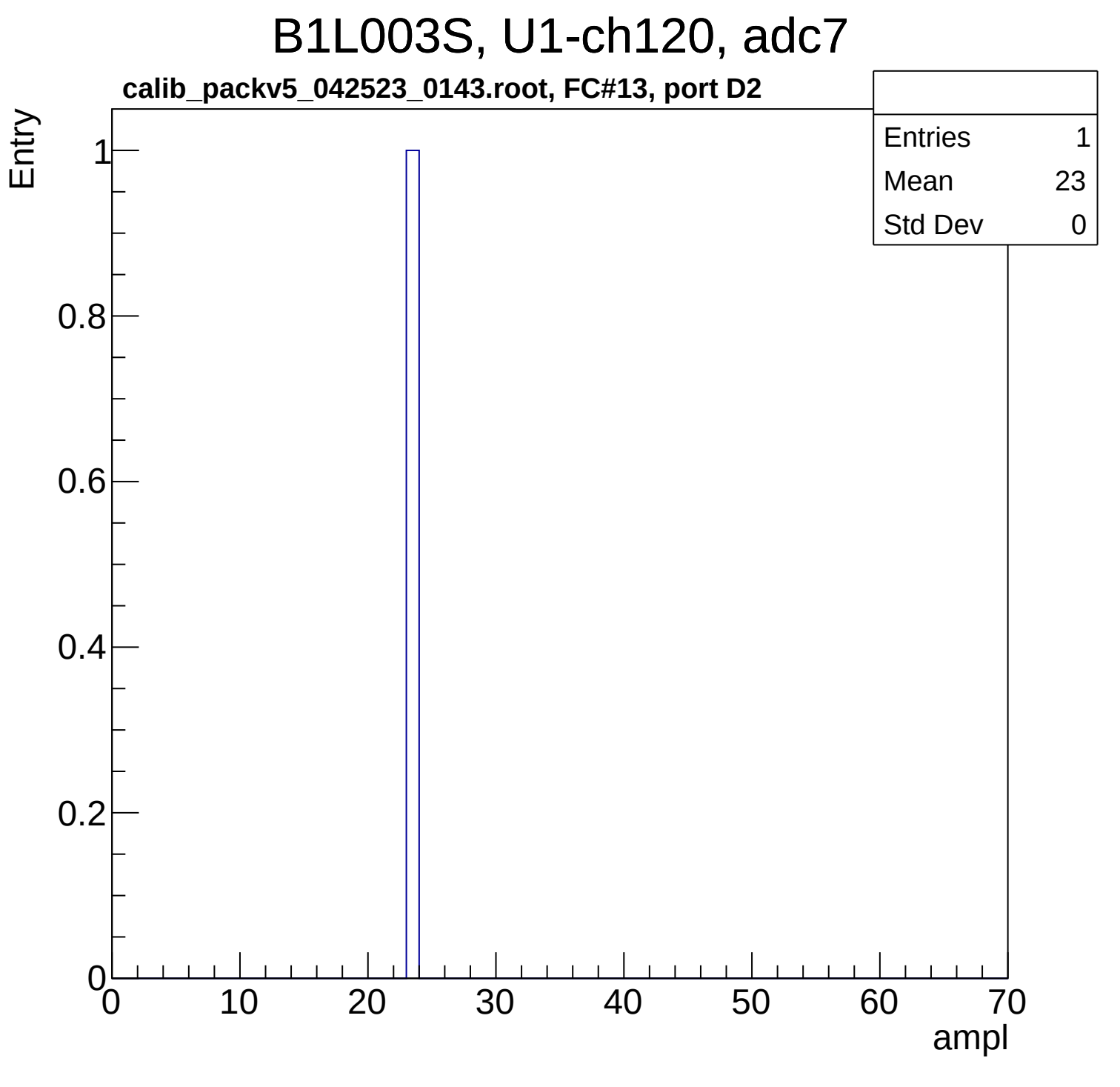
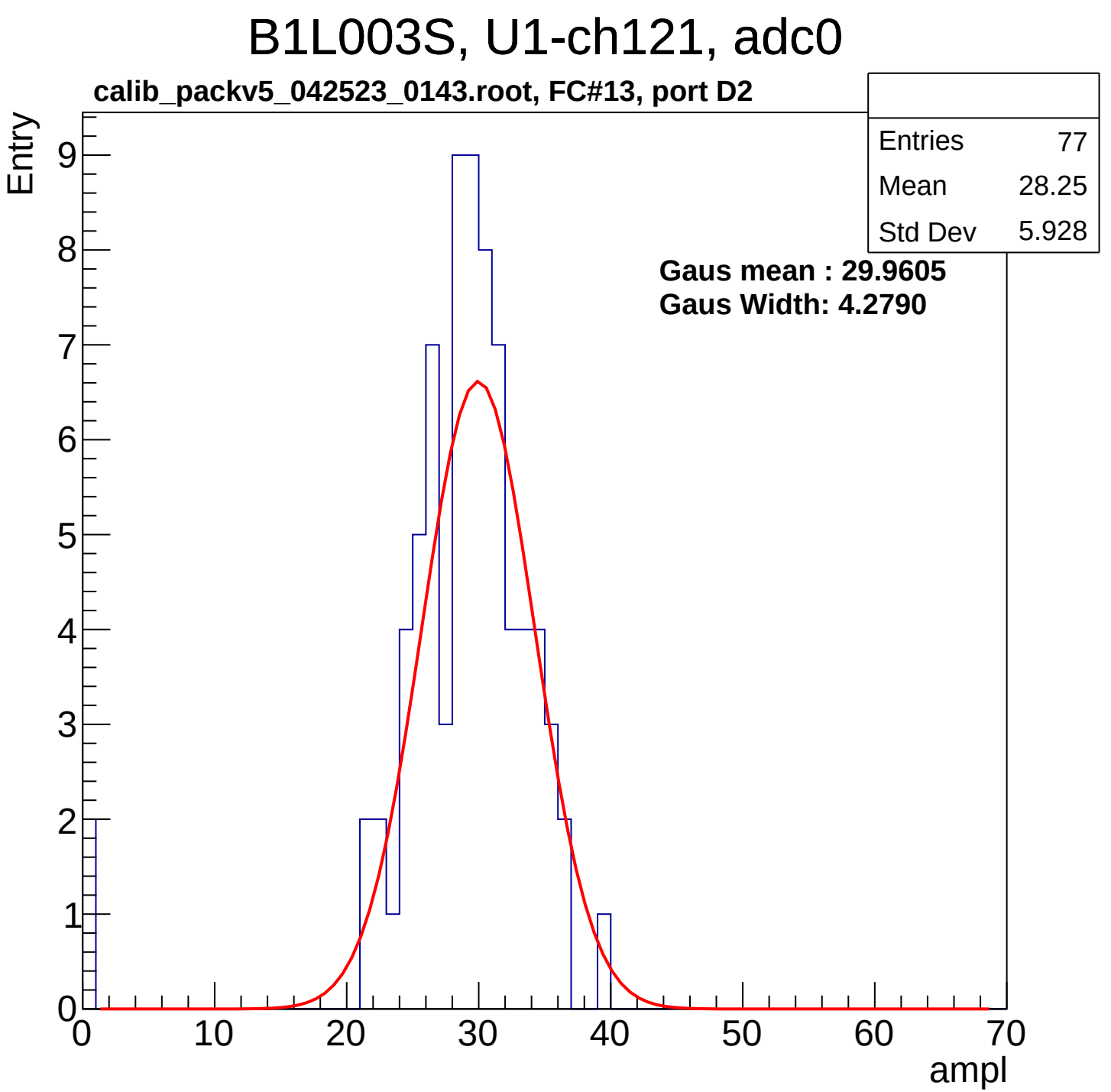
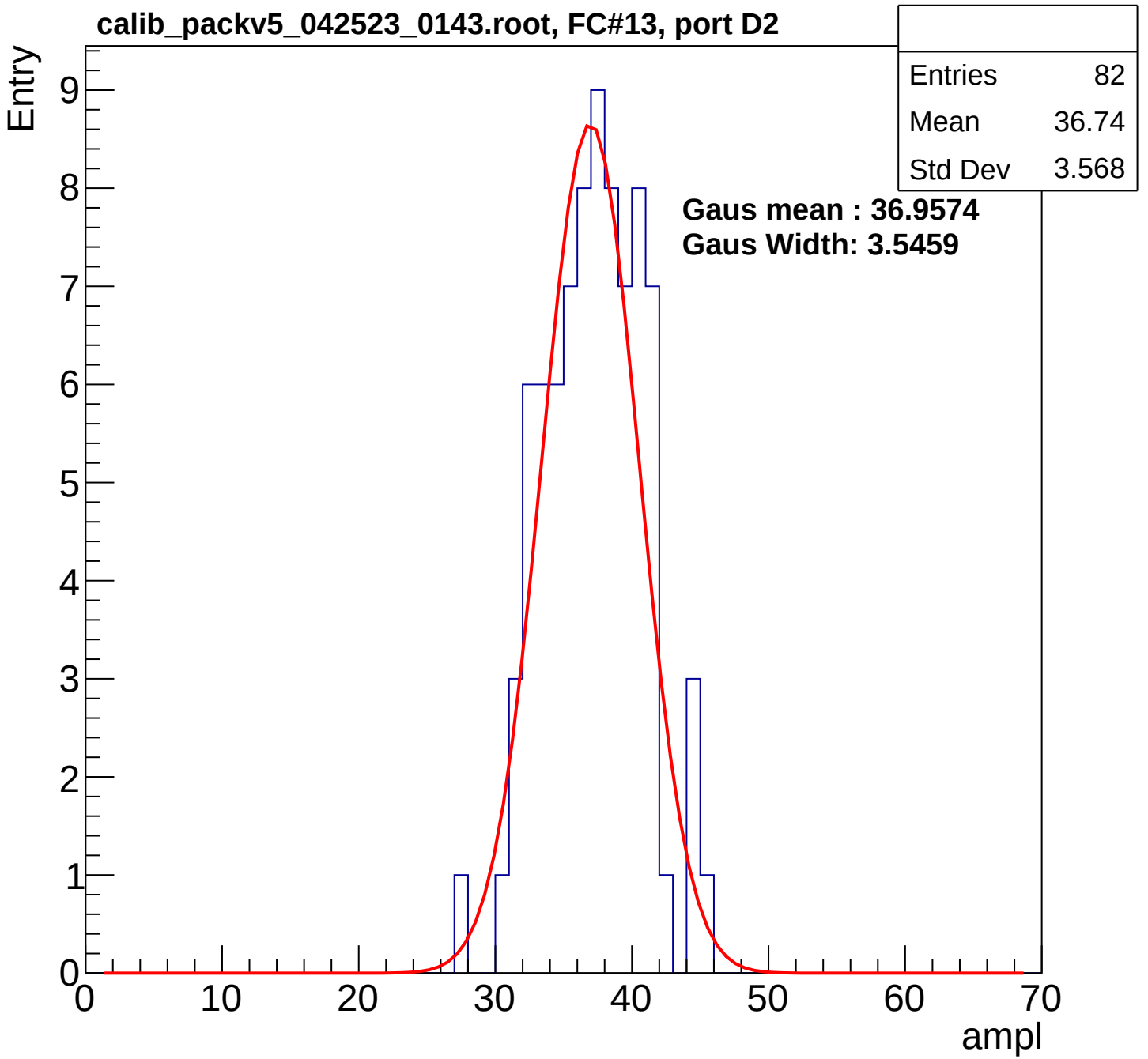
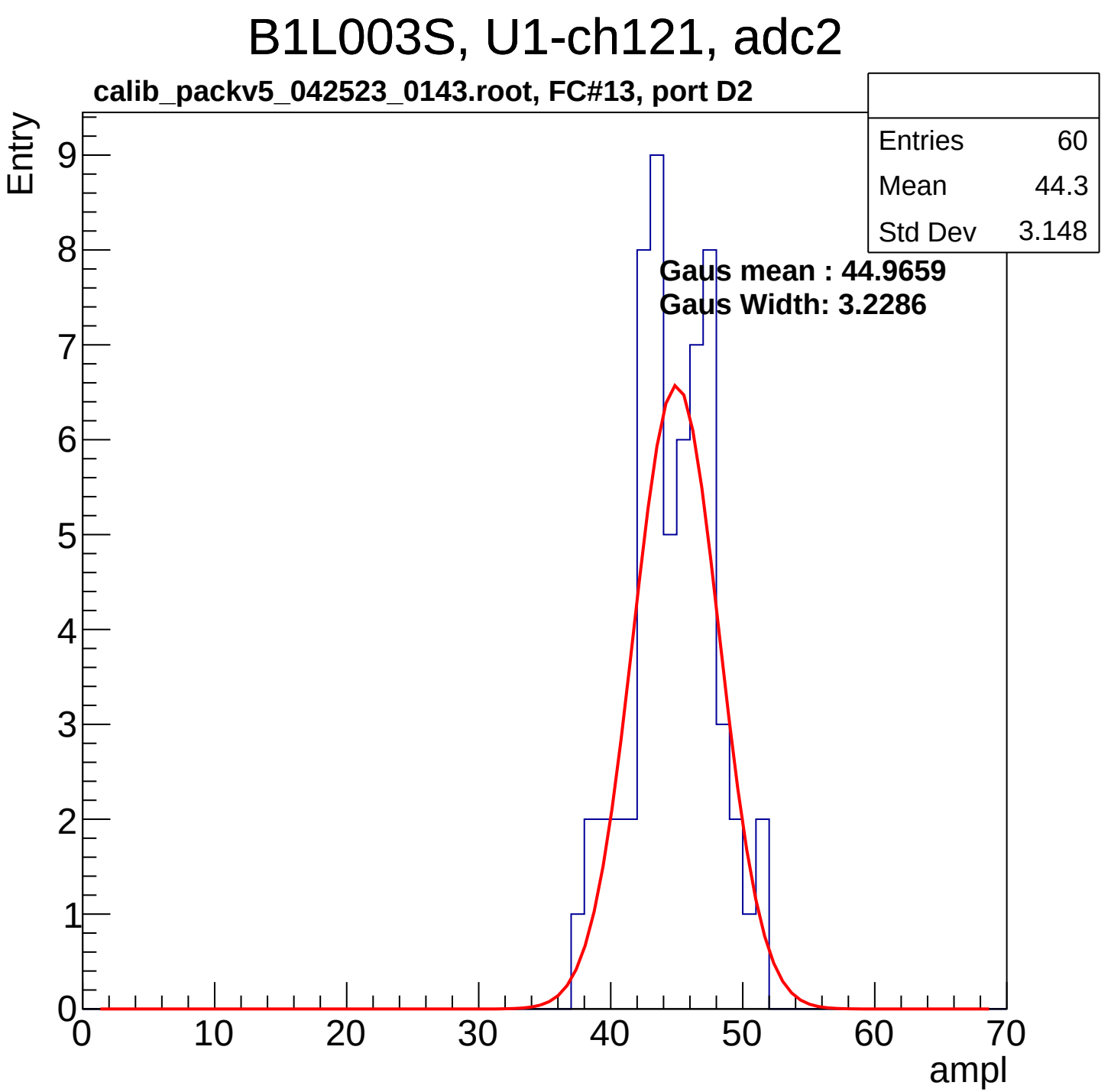
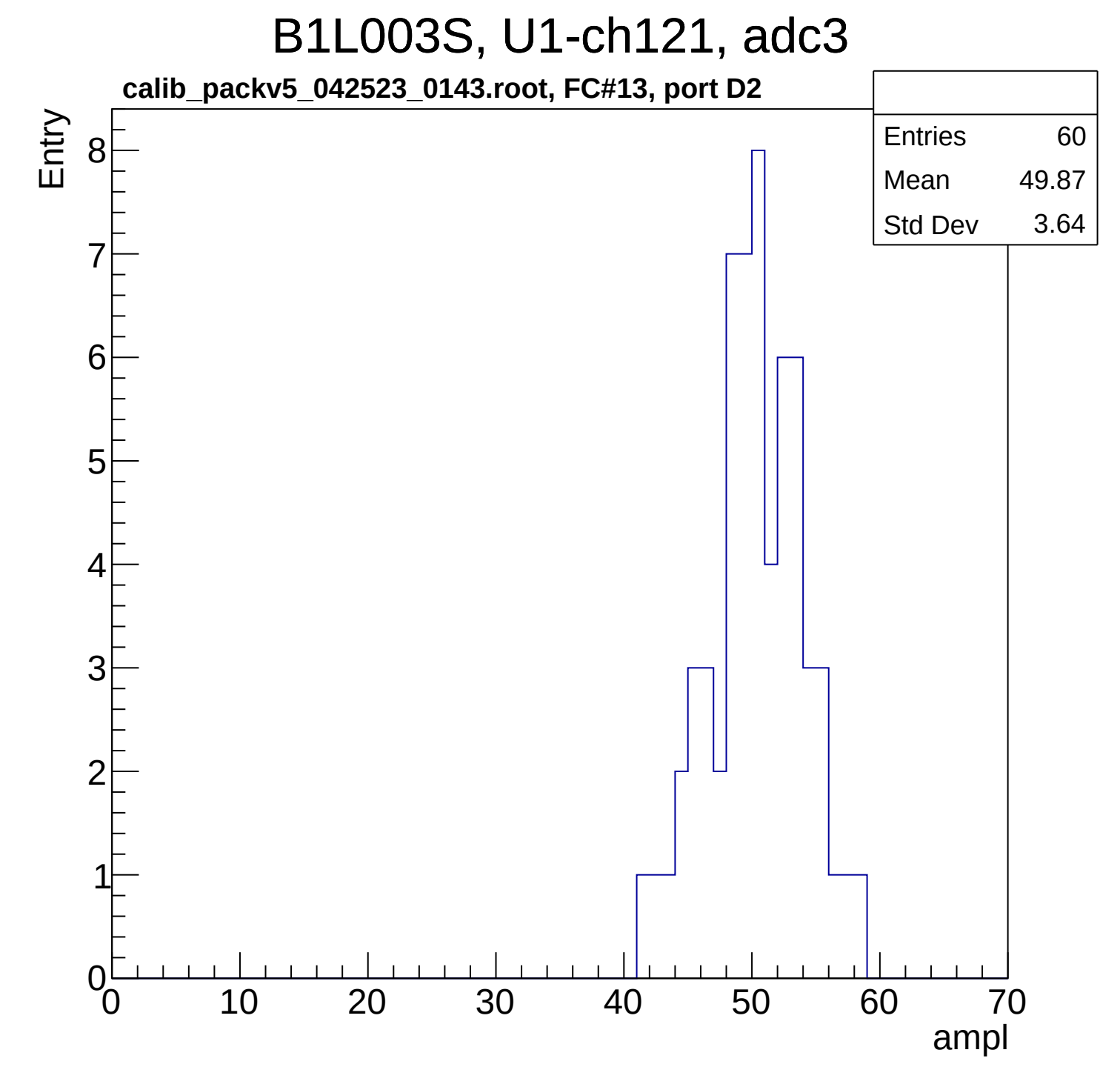

B1L003S, U1-ch120, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 0.8944
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

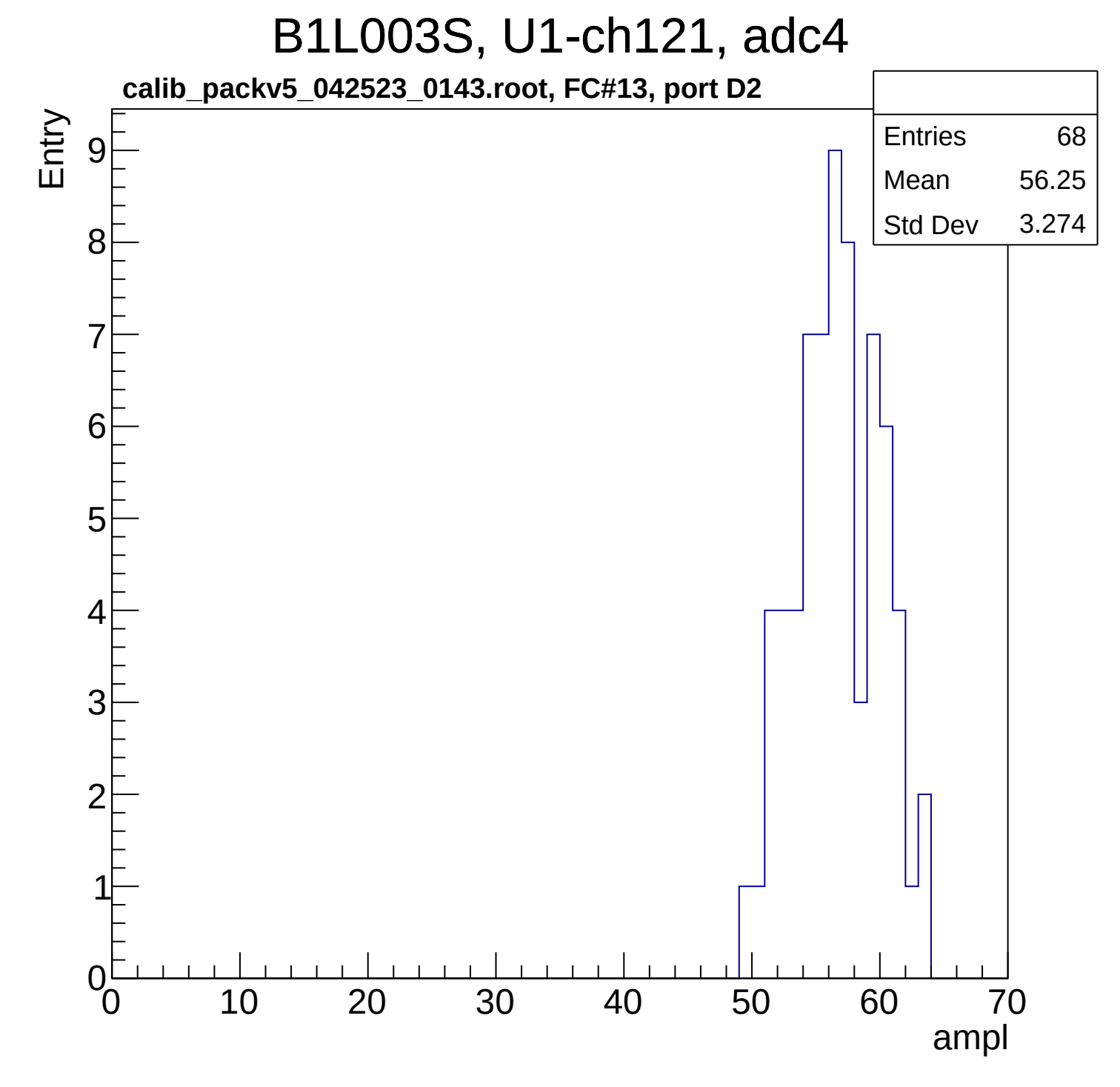

B1L003S, U1-ch120, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

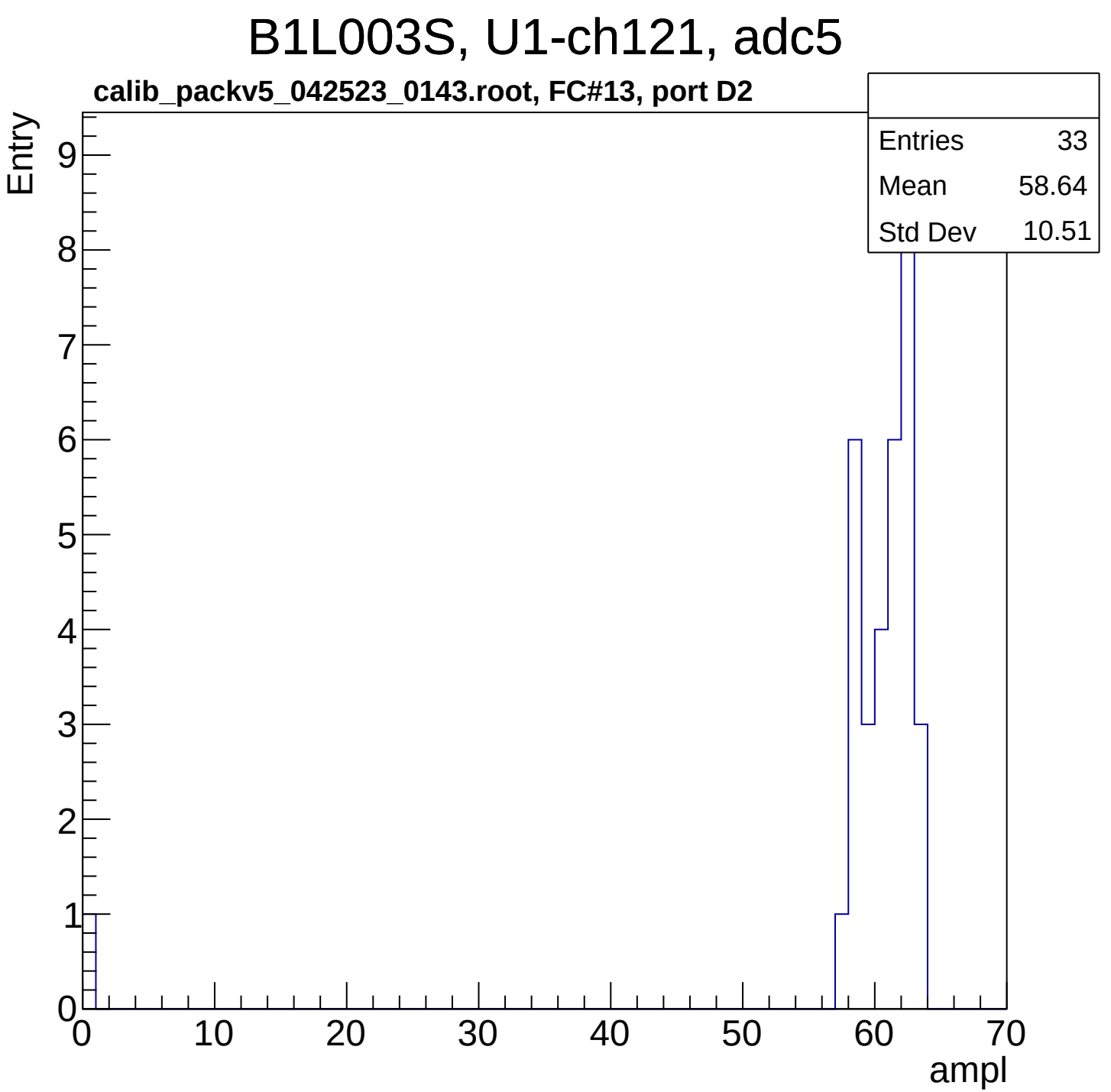

B1L003S, U1-ch121, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 77
Mean 28.25
Std Dev 5.928
Gaus mean : 29.9605
Gaus Width: 4.2790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

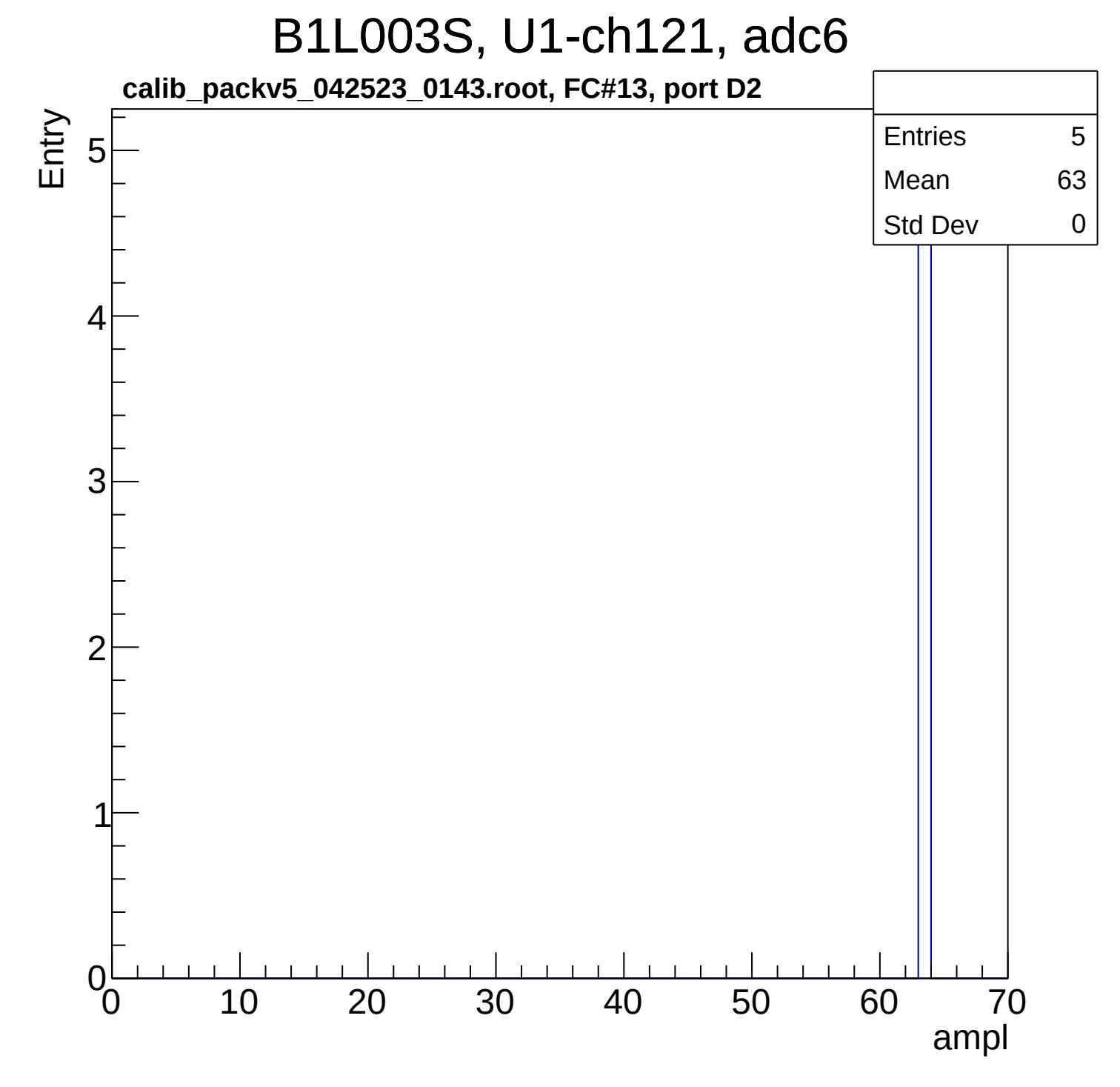

B1L003S, U1-ch121, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 82
Mean 36.74
Std Dev 3.568
Gaus mean : 36.9574
Gaus Width: 3.5459
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

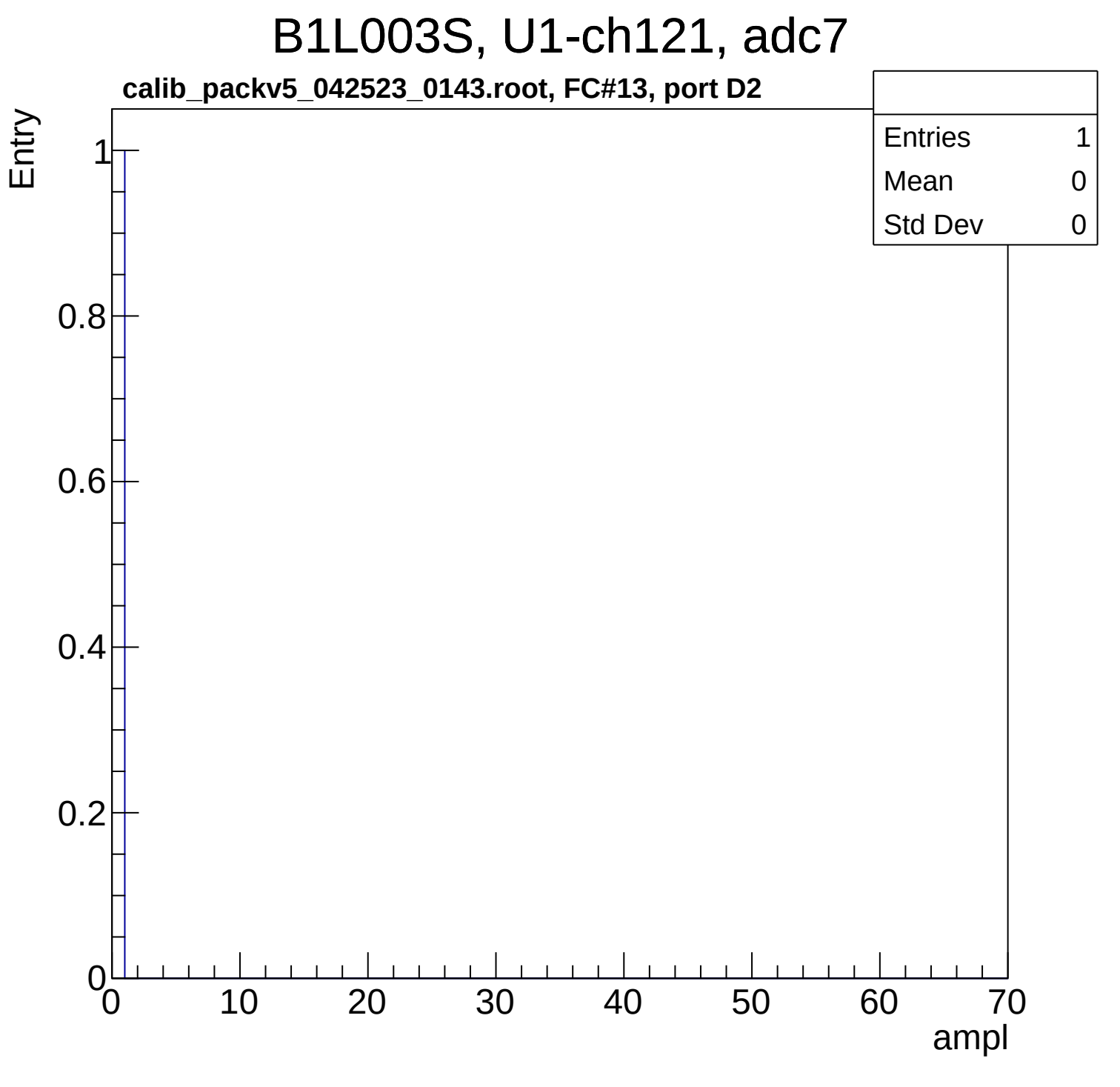

B1L003S, U1-ch121, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 44.3
Std Dev 3.148
Gaus mean : 44.9659
Gaus Width: 3.2286
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

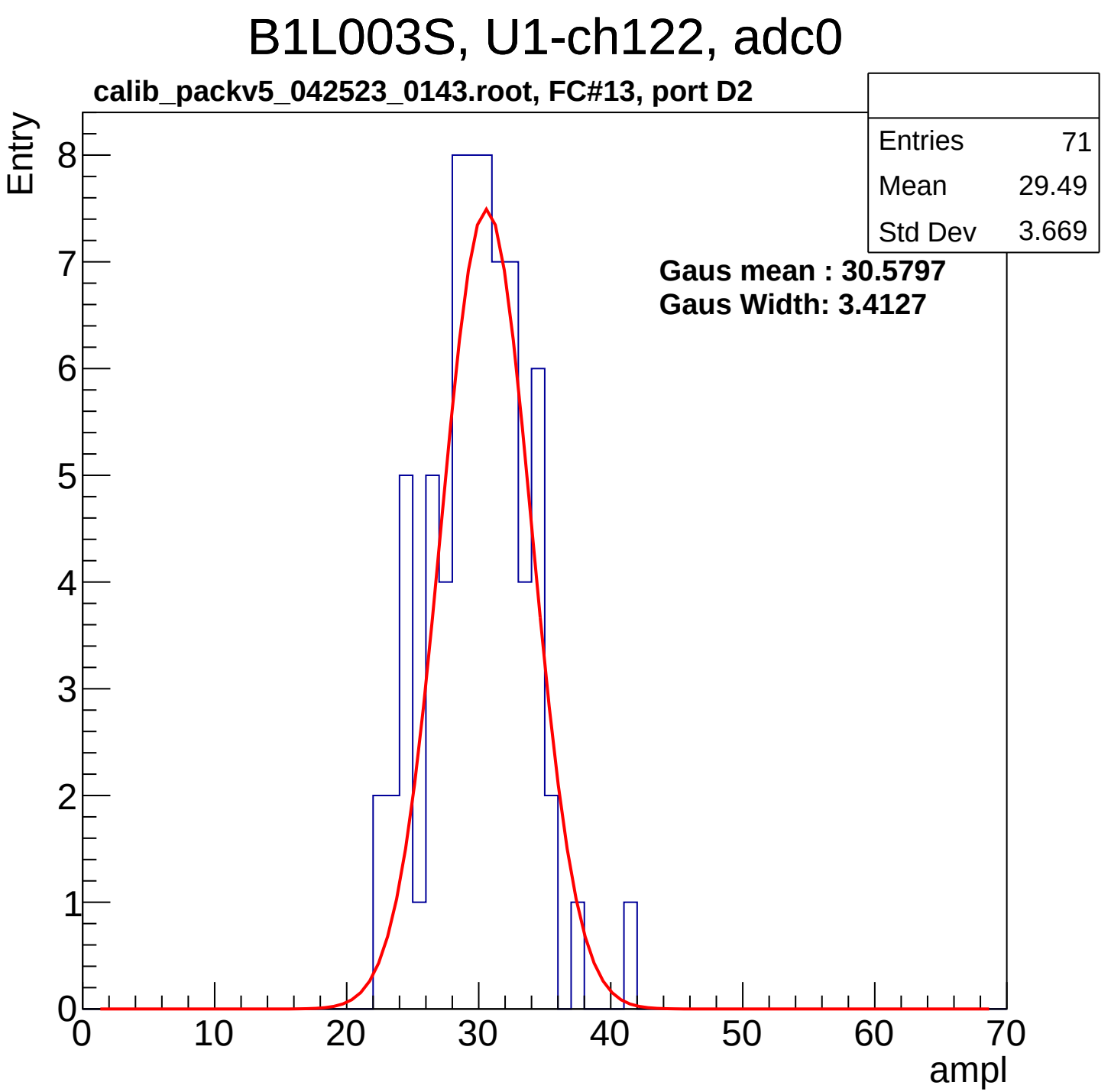

B1L003S, U1-ch121, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 49.87
Std Dev 3.64
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

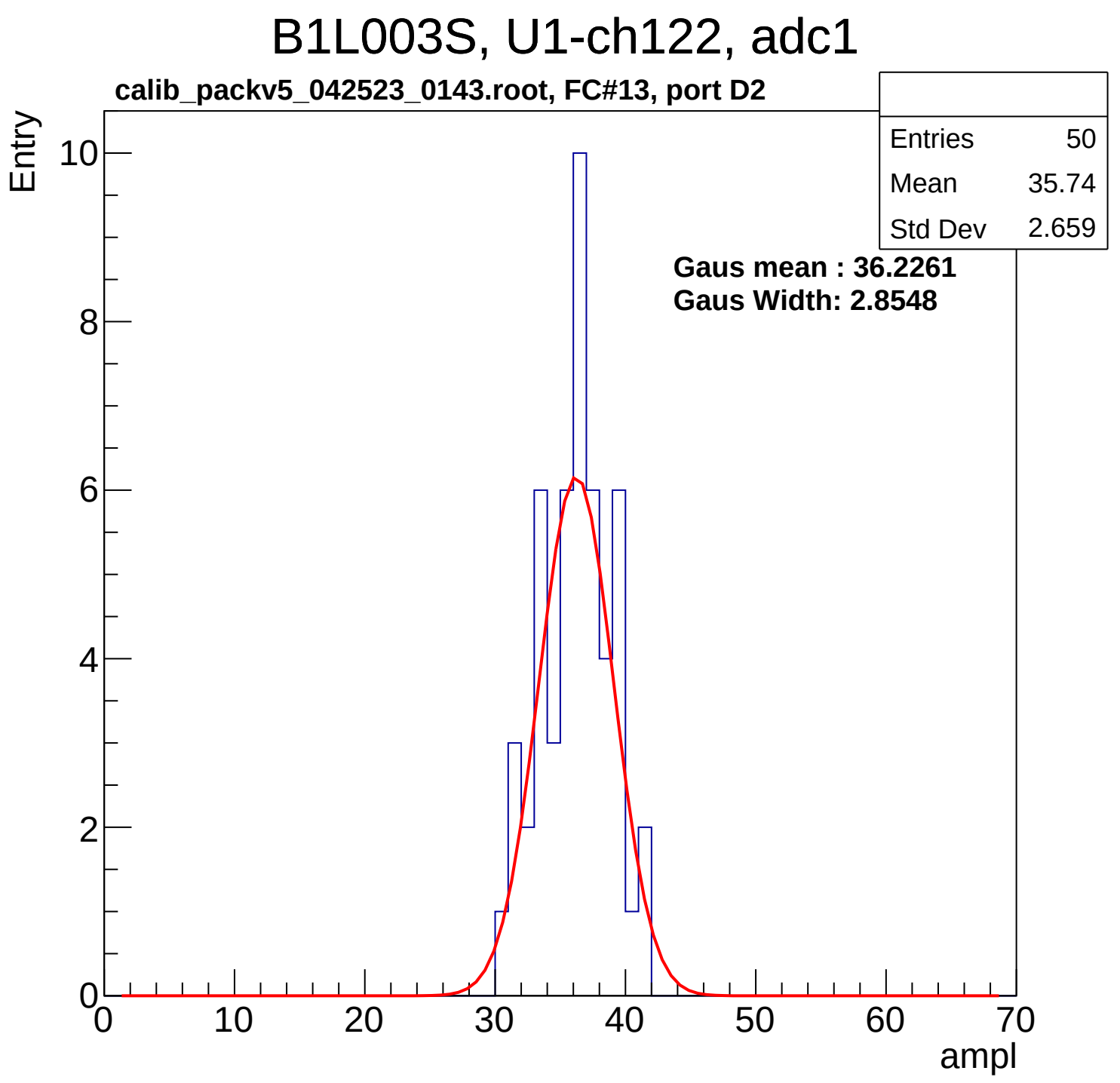

B1L003S, U1-ch121, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 68
Mean 56.25
Std Dev 3.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

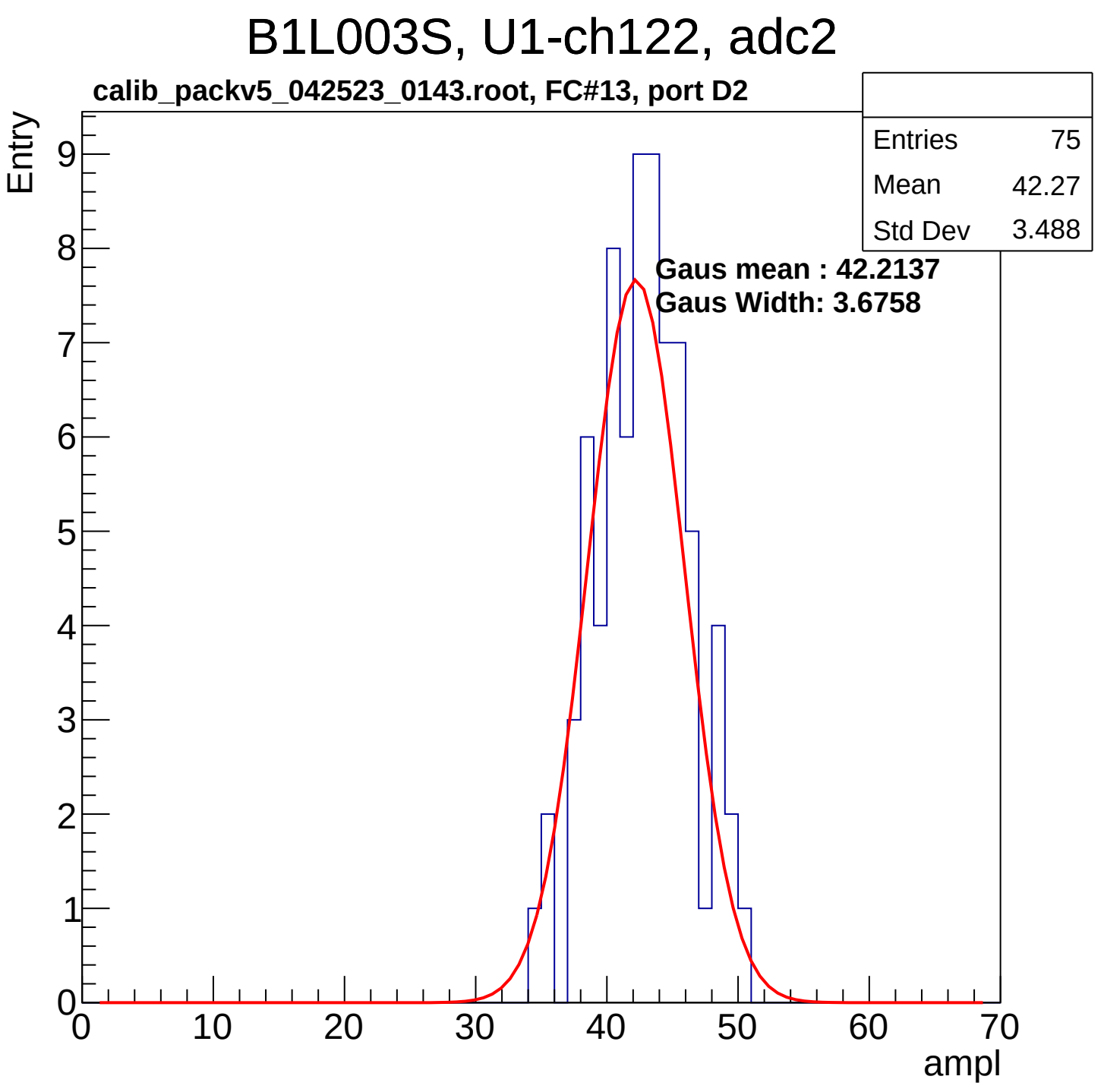

B1L003S, U1-ch121, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 58.64
Std Dev 10.51
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

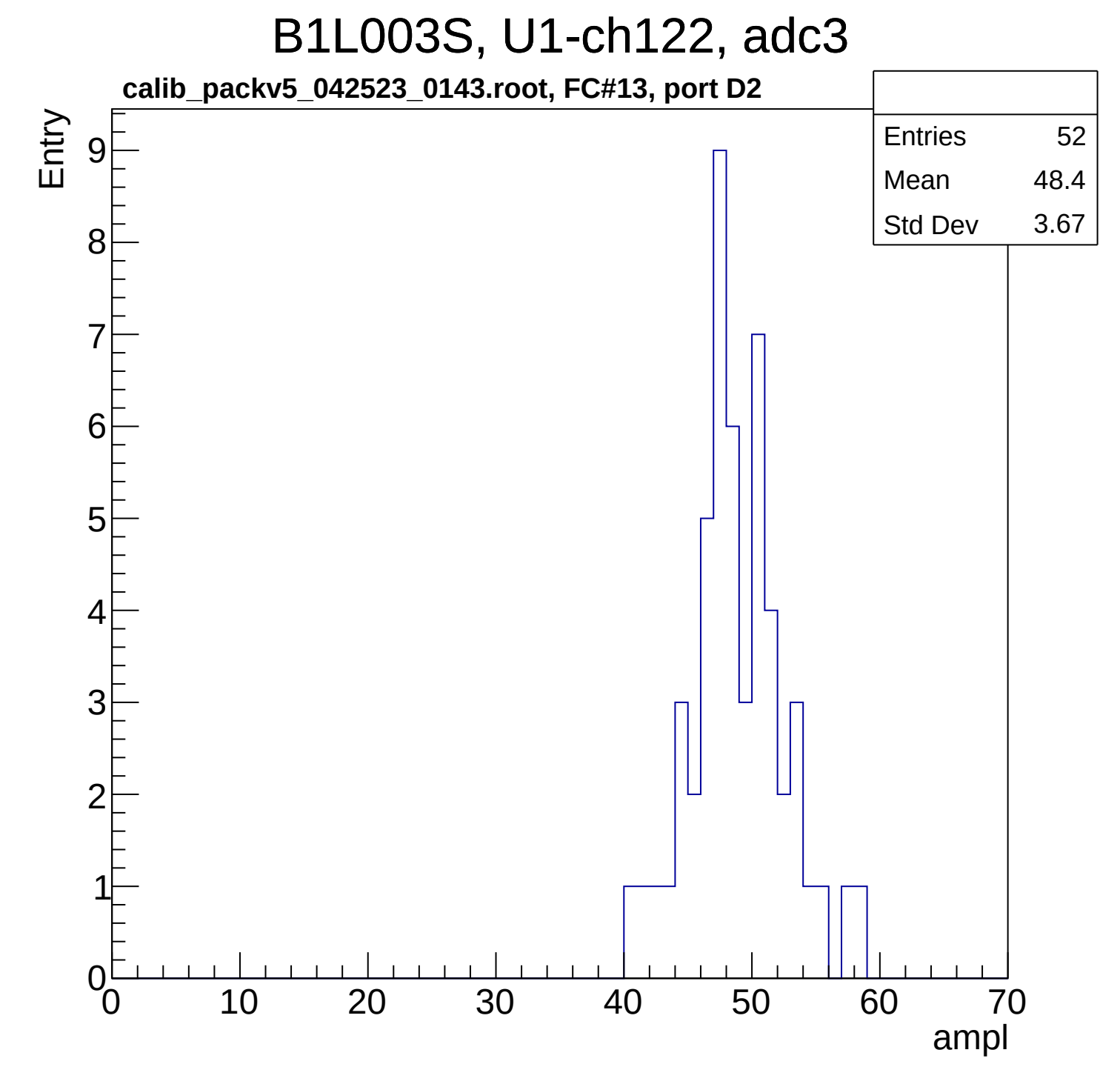

B1L003S, U1-ch121, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 63
Std Dev 0
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

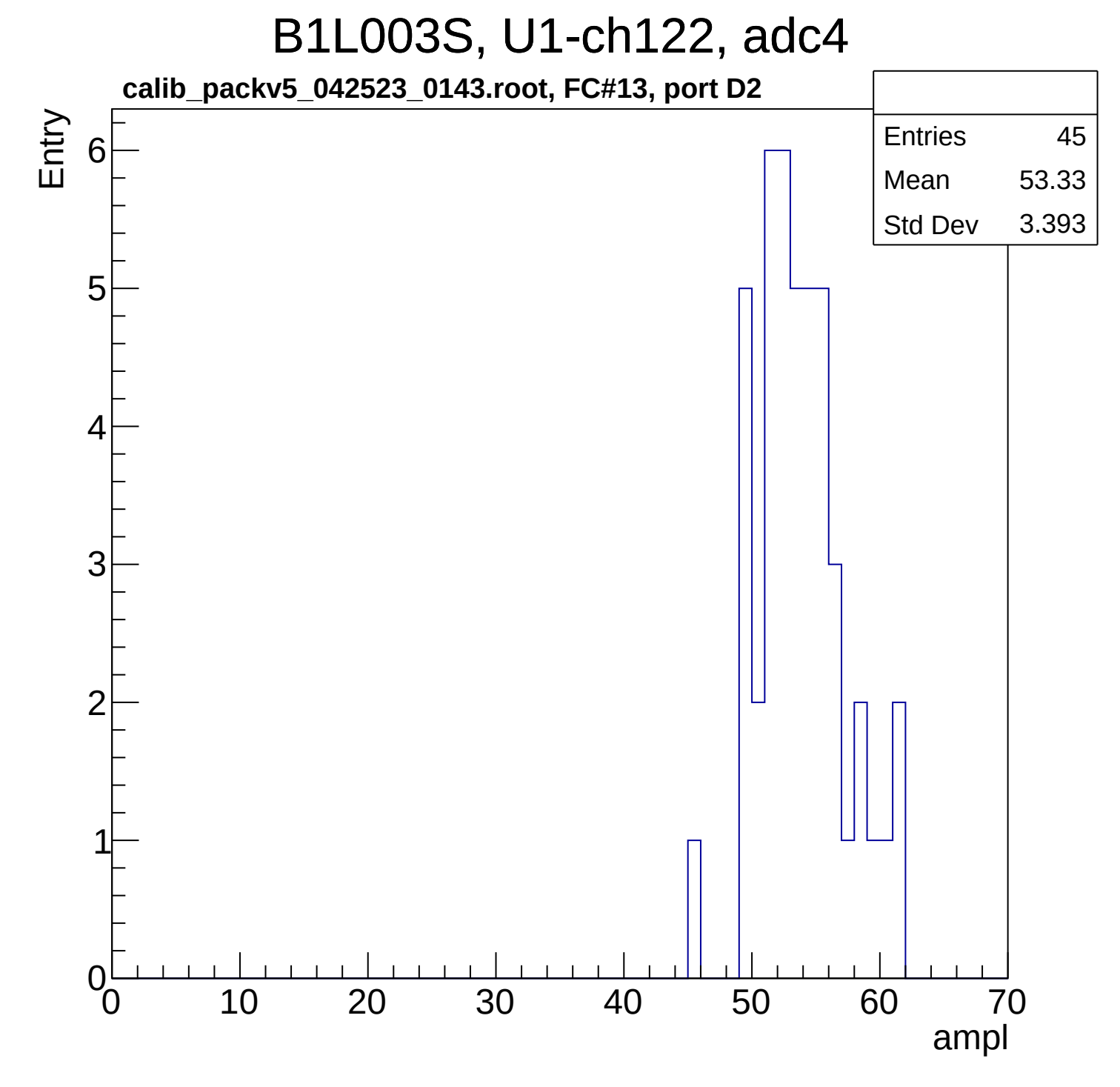

B1L003S, U1-ch121, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

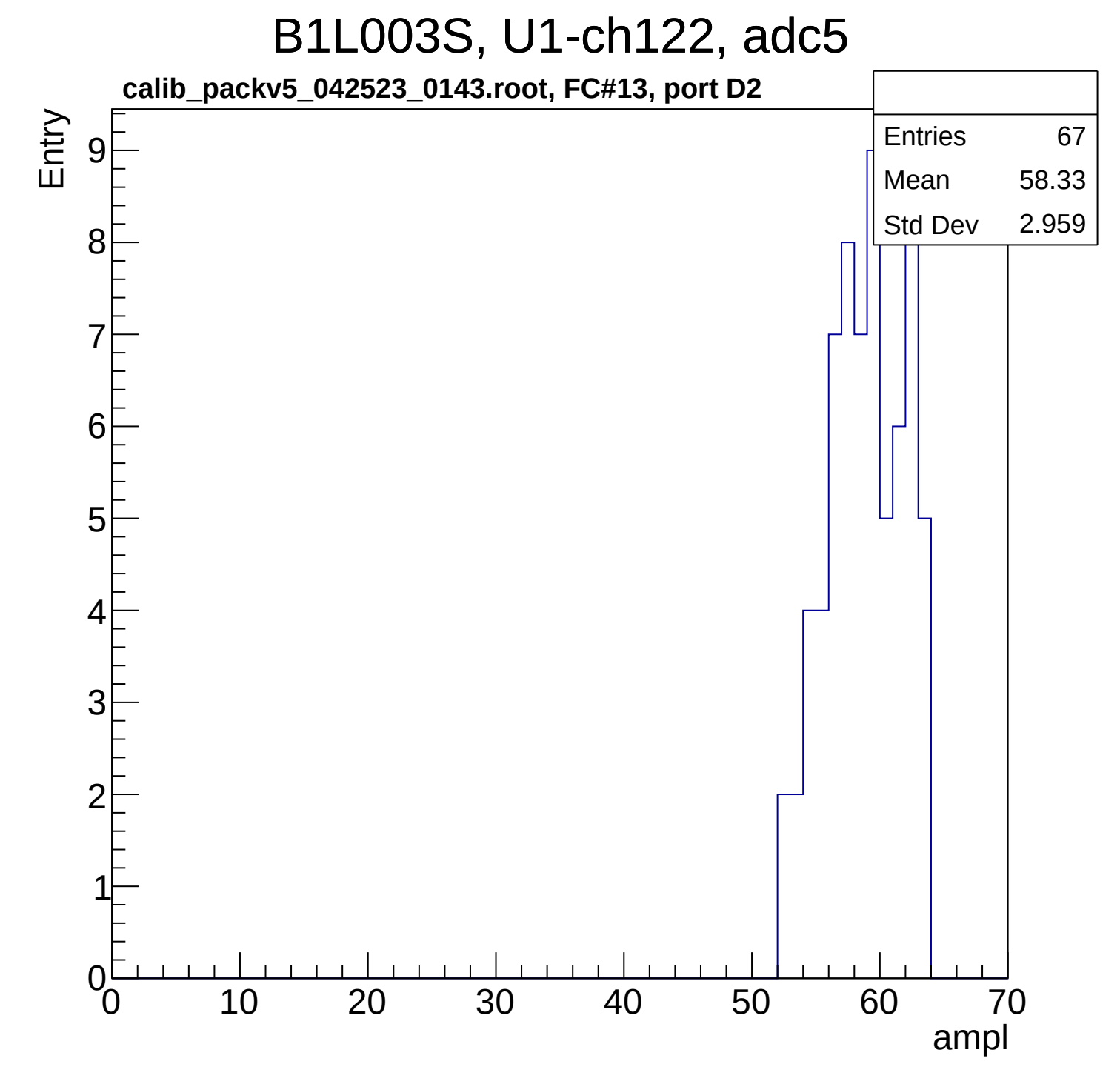

B1L003S, U1-ch122, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 29.49
Std Dev 3.669
Gaus mean : 30.5797
Gaus Width: 3.4127
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch122, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 50
Mean 35.74
Std Dev 2.659
Gaus mean : 36.2261
Gaus Width: 2.8548
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L003S, U1-ch122, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 42.27
Std Dev 3.488
Gaus mean : 42.2137
Gaus Width: 3.6758
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch122, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 48.4
Std Dev 3.67
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch122, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 45
Mean 53.33
Std Dev 3.393
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L003S, U1-ch122, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 58.33
Std Dev 2.959
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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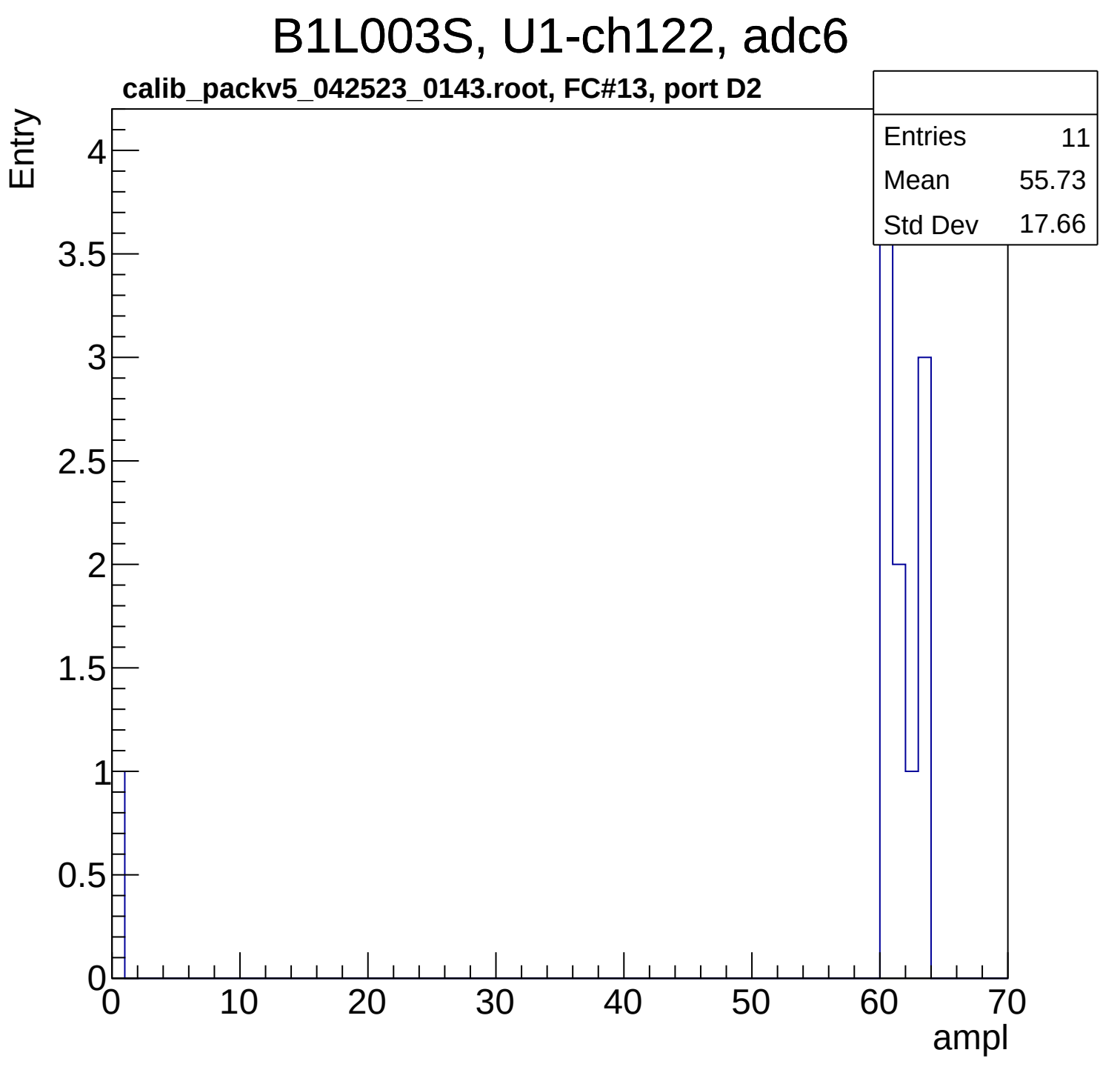
B1L003S, U1-ch122, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 55.73
Std Dev 17.66
Entry
ampl
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1
1.5
2
2.5
3
3.5
4
0
10
20
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40
50
60
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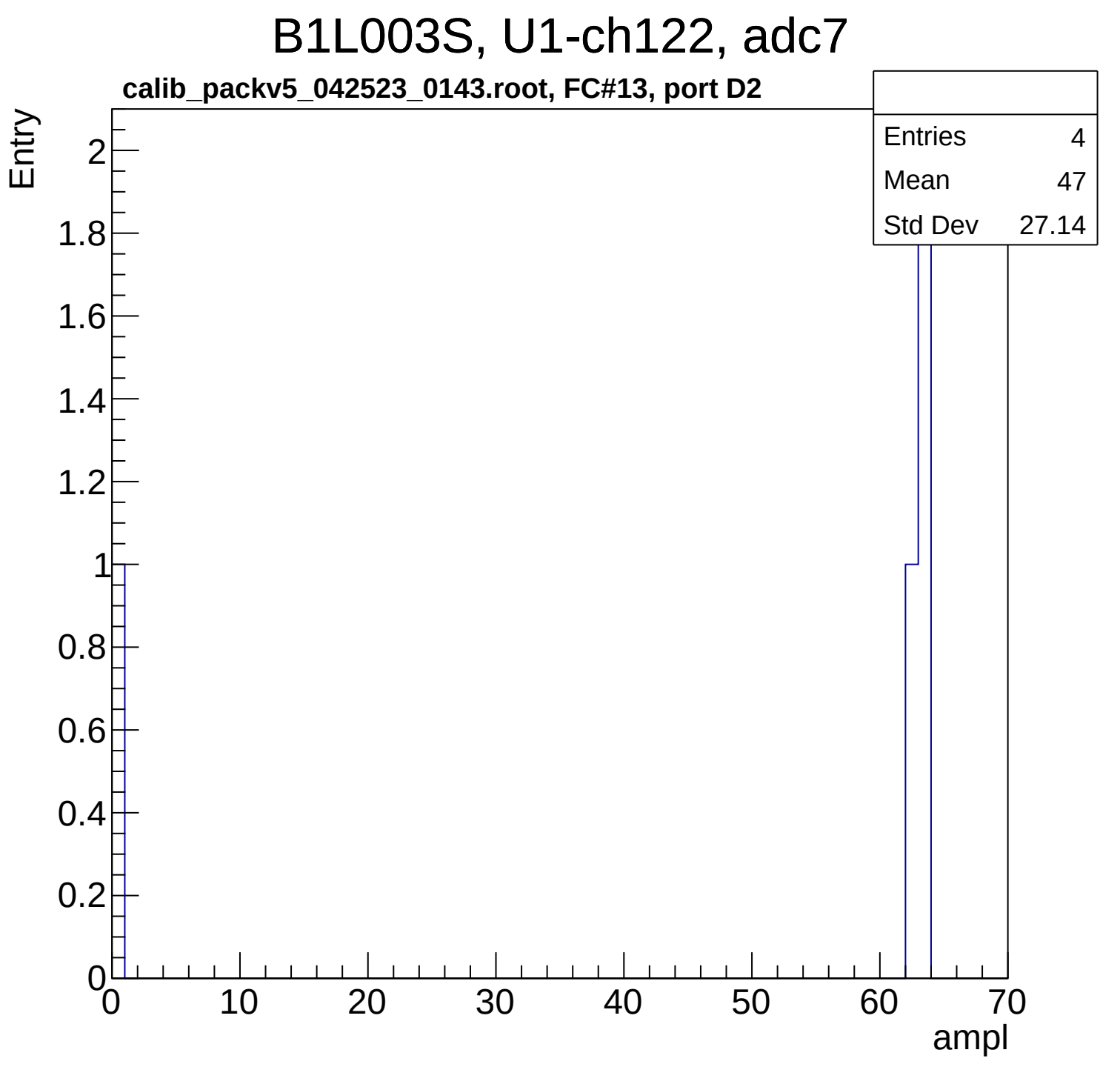
B1L003S, U1-ch122, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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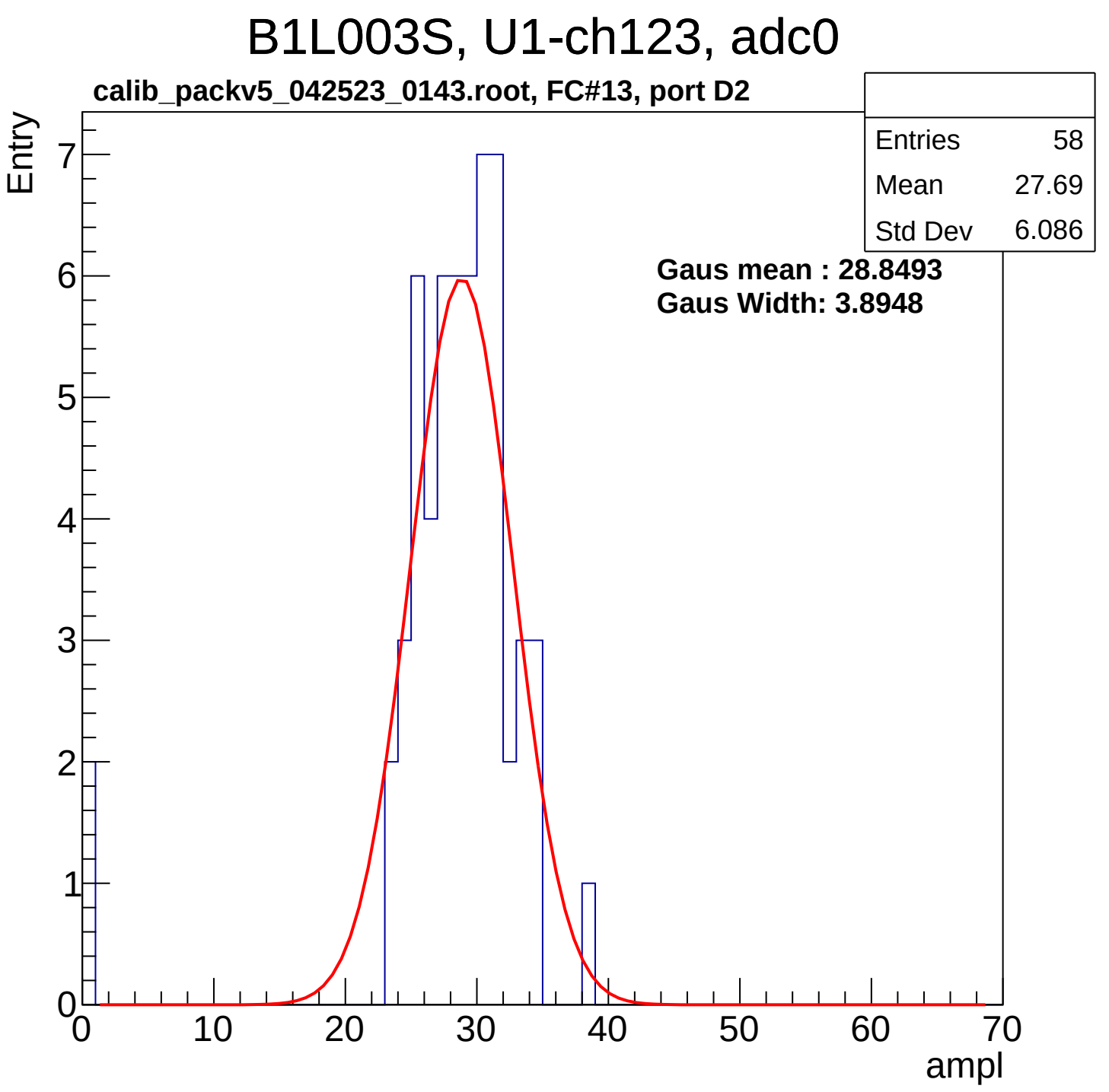
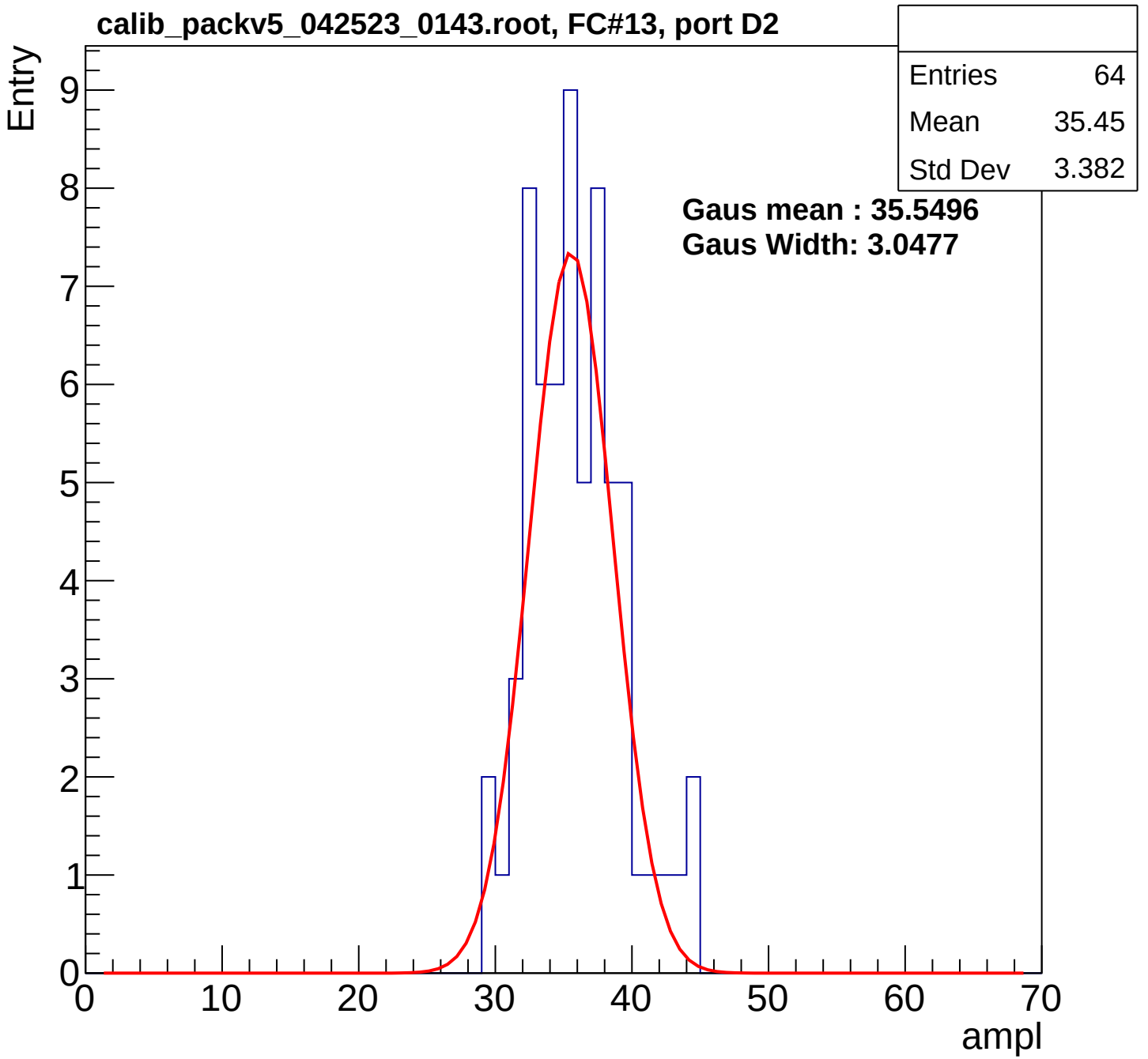
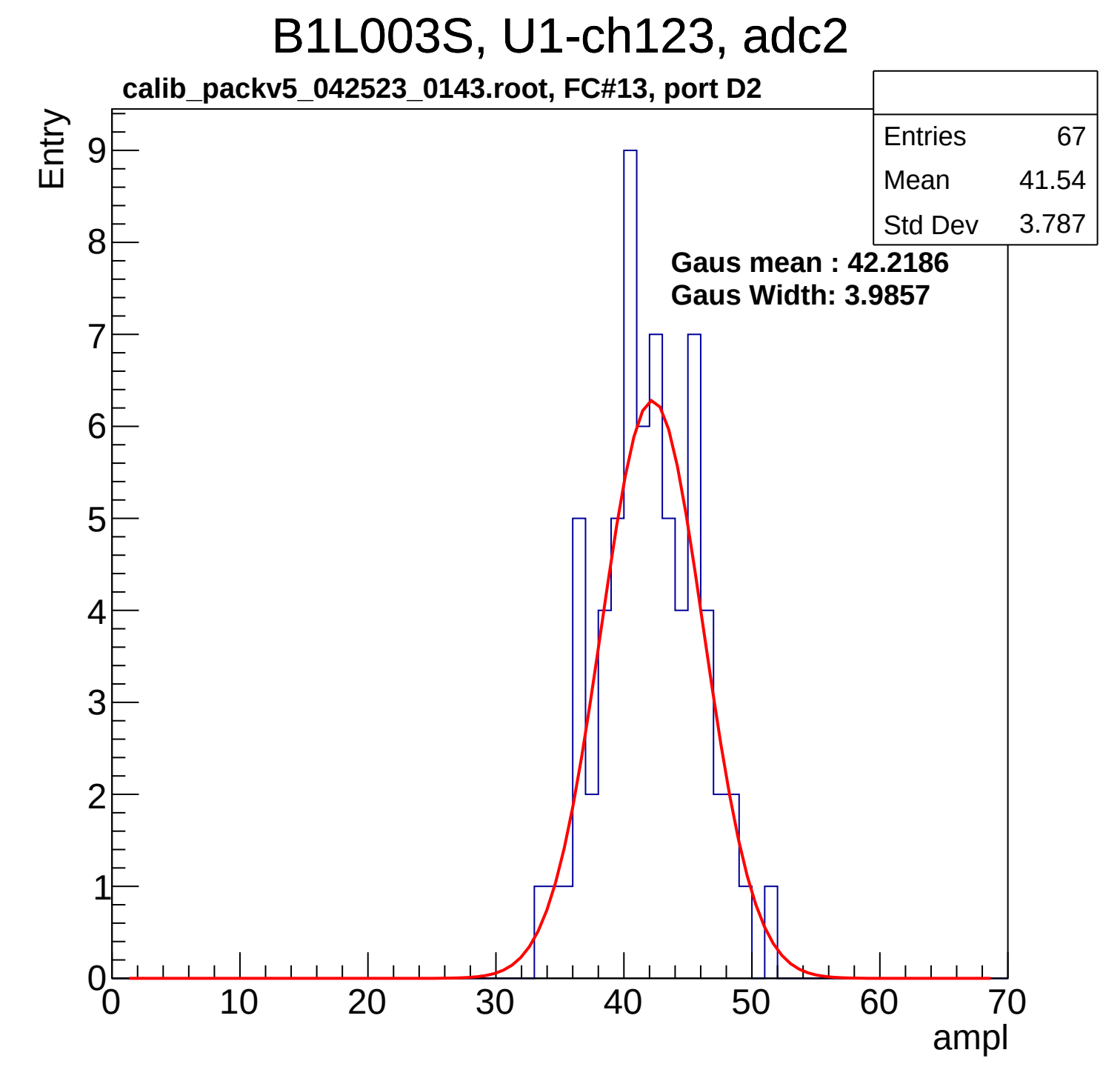
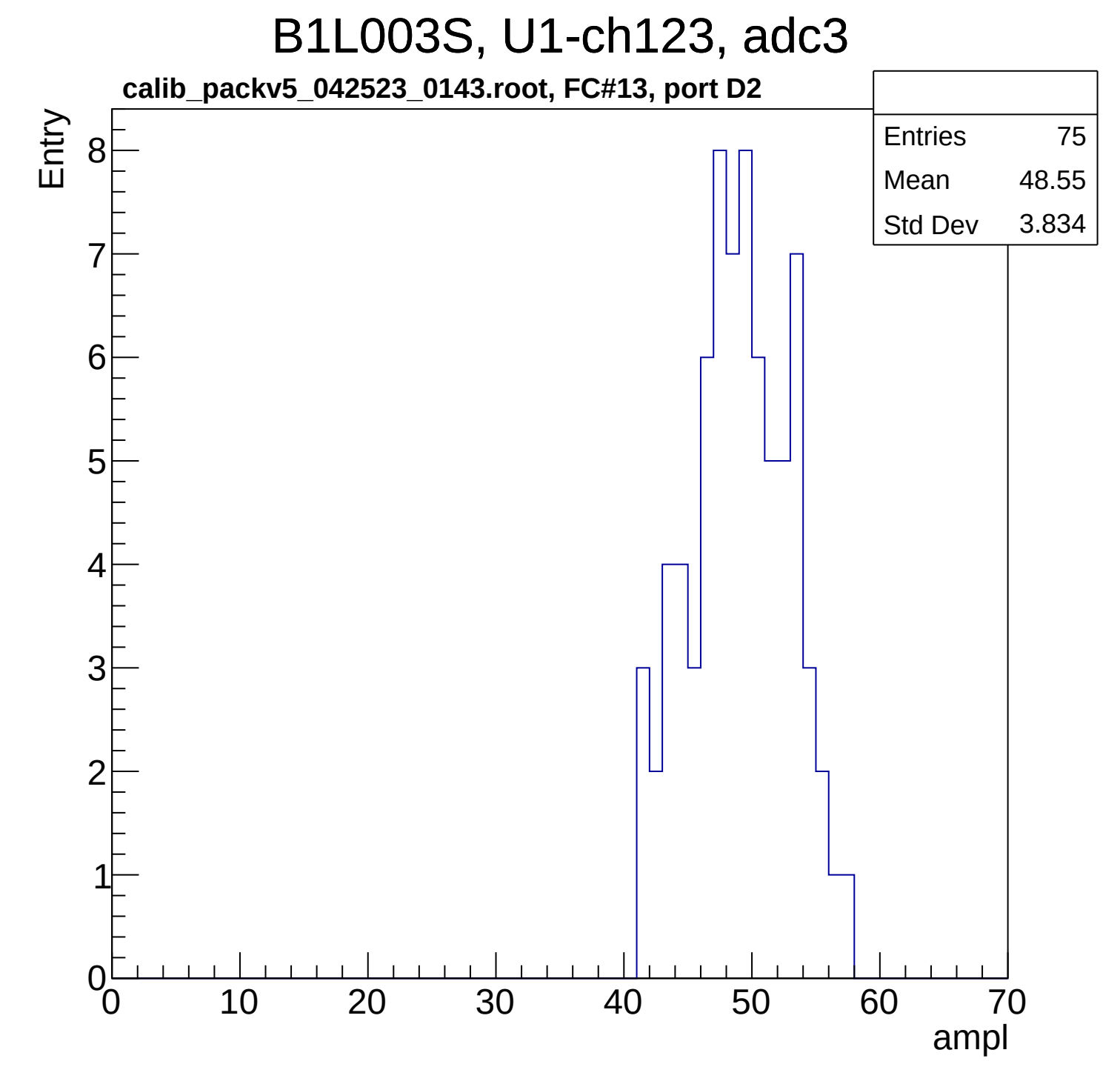

B1L003S, U1-ch123, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 27.69
Std Dev 6.086
Gaus mean : 28.8493
Gaus Width: 3.8948
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch123, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 35.45
Std Dev 3.382
Gaus mean : 35.5496
Gaus Width: 3.0477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch123, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 67
Mean 41.54
Std Dev 3.787
Gaus mean : 42.2186
Gaus Width: 3.9857
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch123, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 48.55
Std Dev 3.834
Entry
ampl
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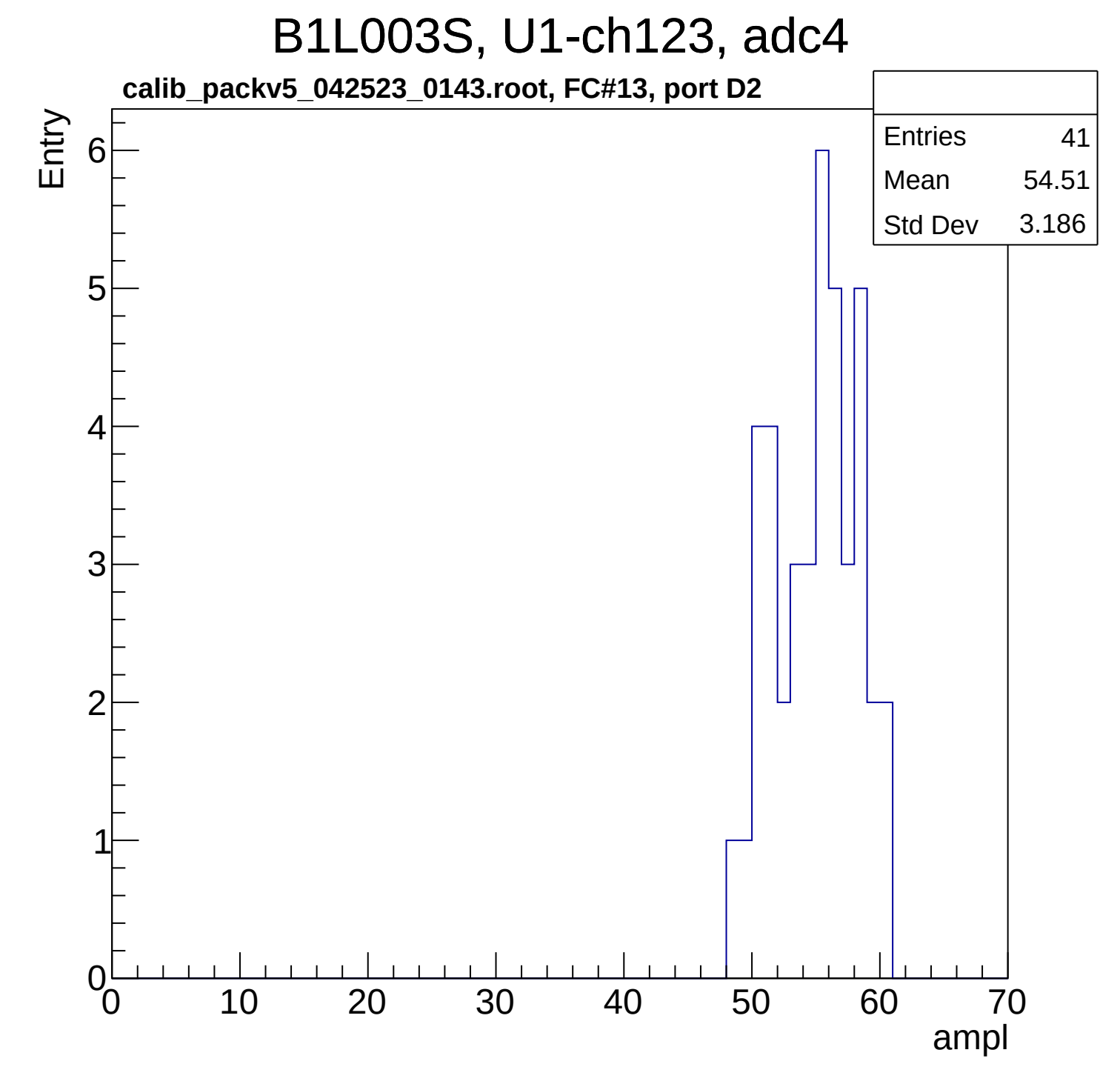
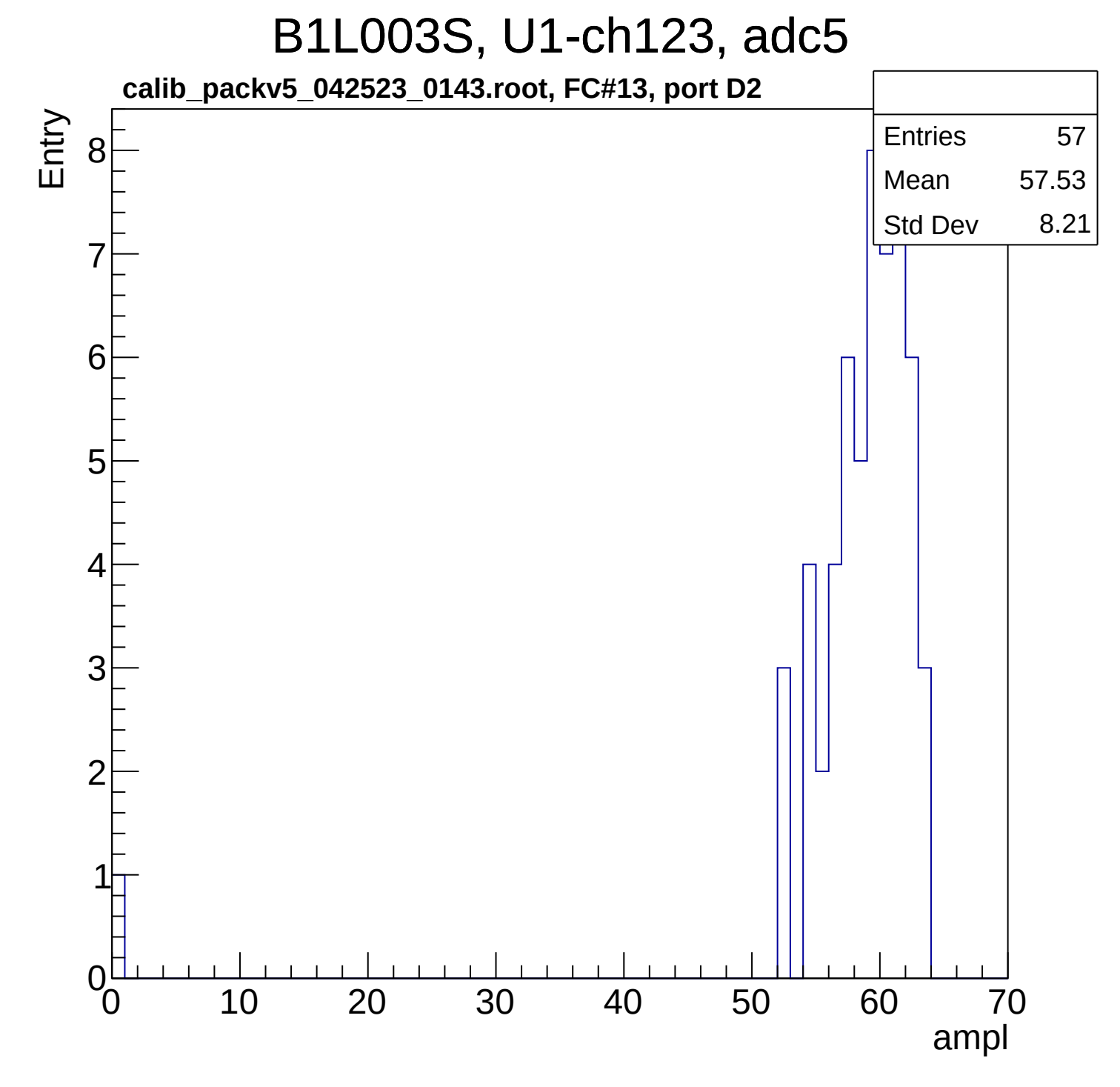
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch123, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 41
Mean 54.51
Std Dev 3.186
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch123, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 57
Mean 57.53
Std Dev 8.21
Entry
ampl
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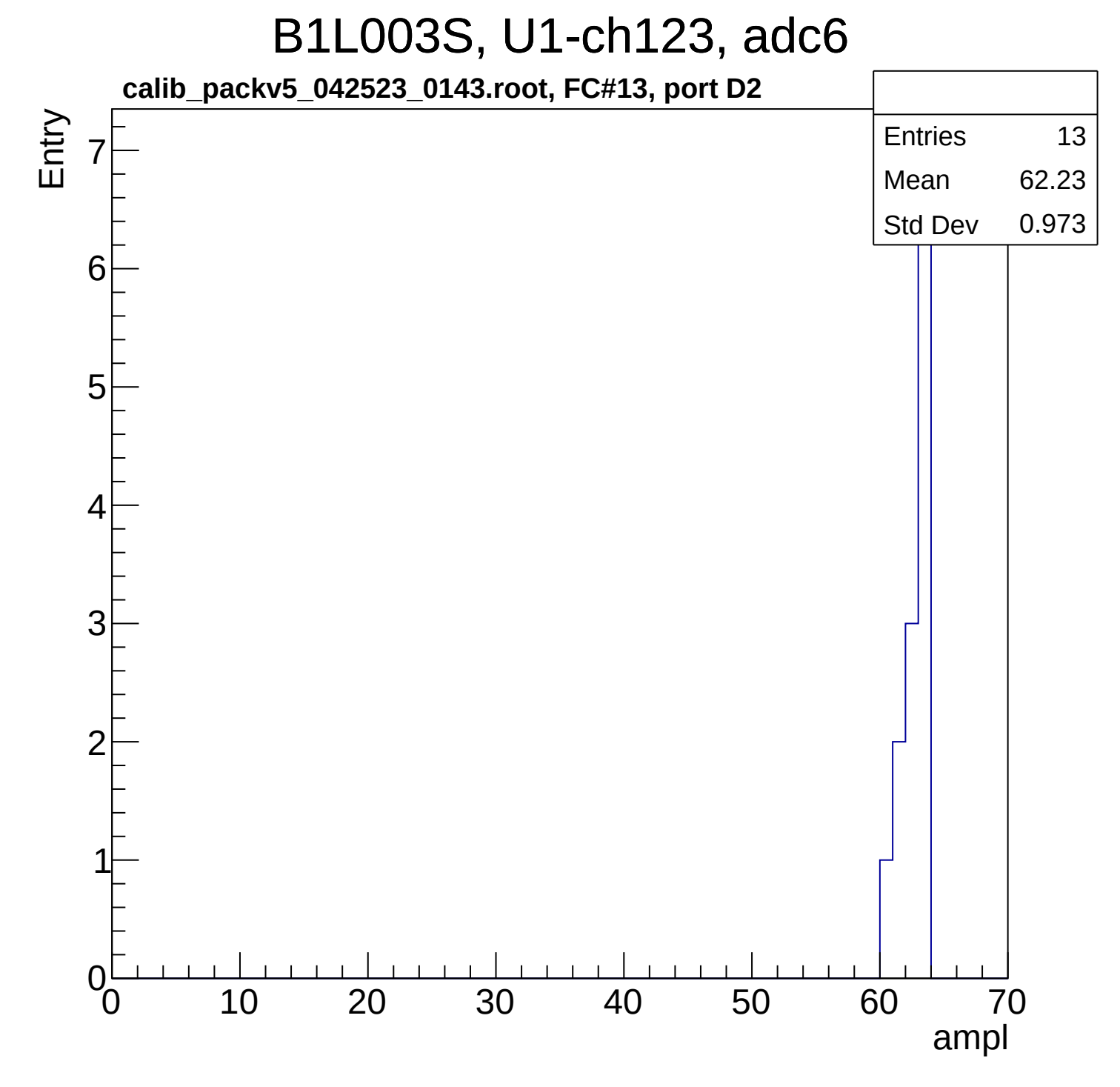
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch123, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 13
Mean 62.23
Std Dev 0.973
Entry
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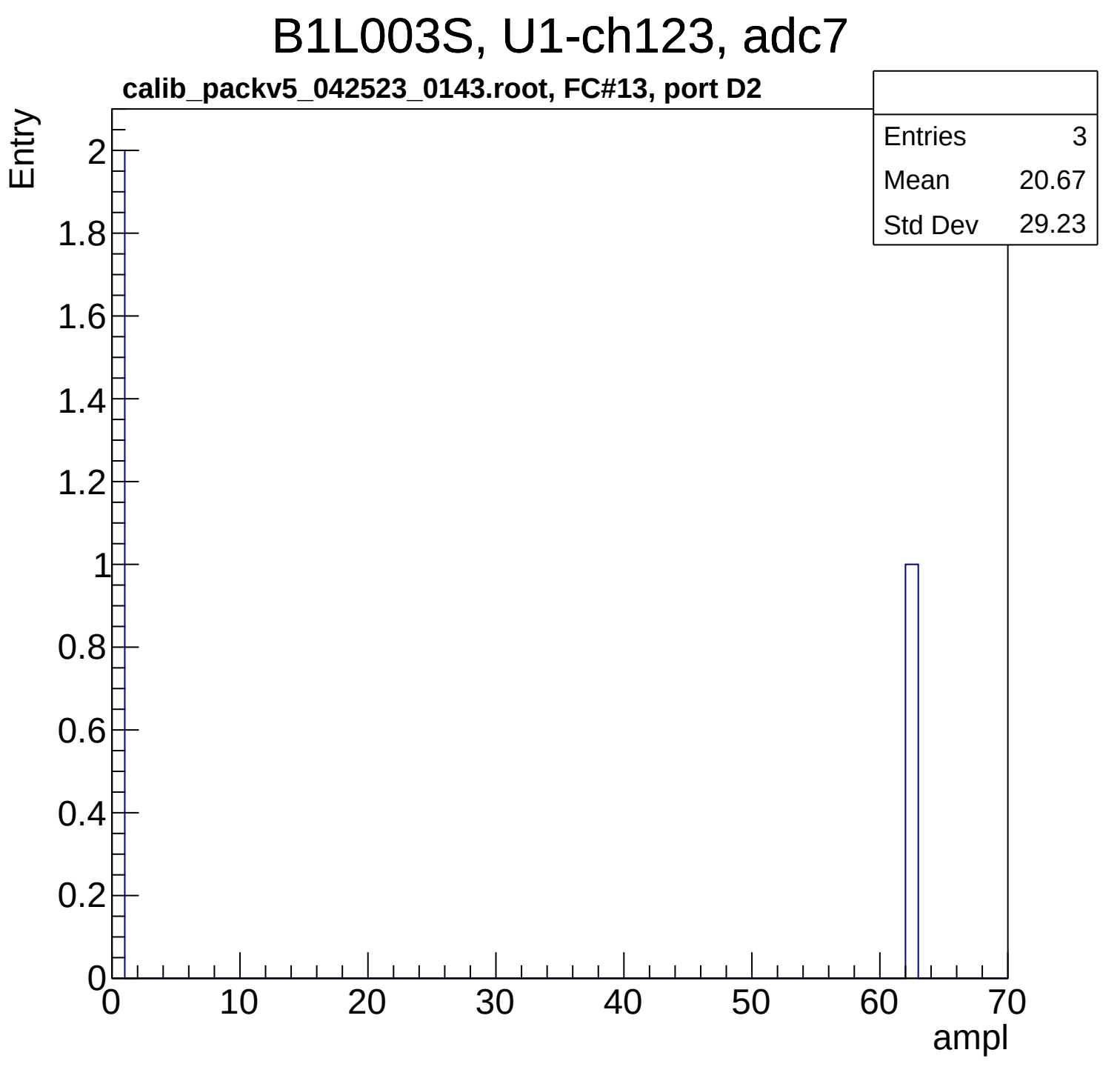
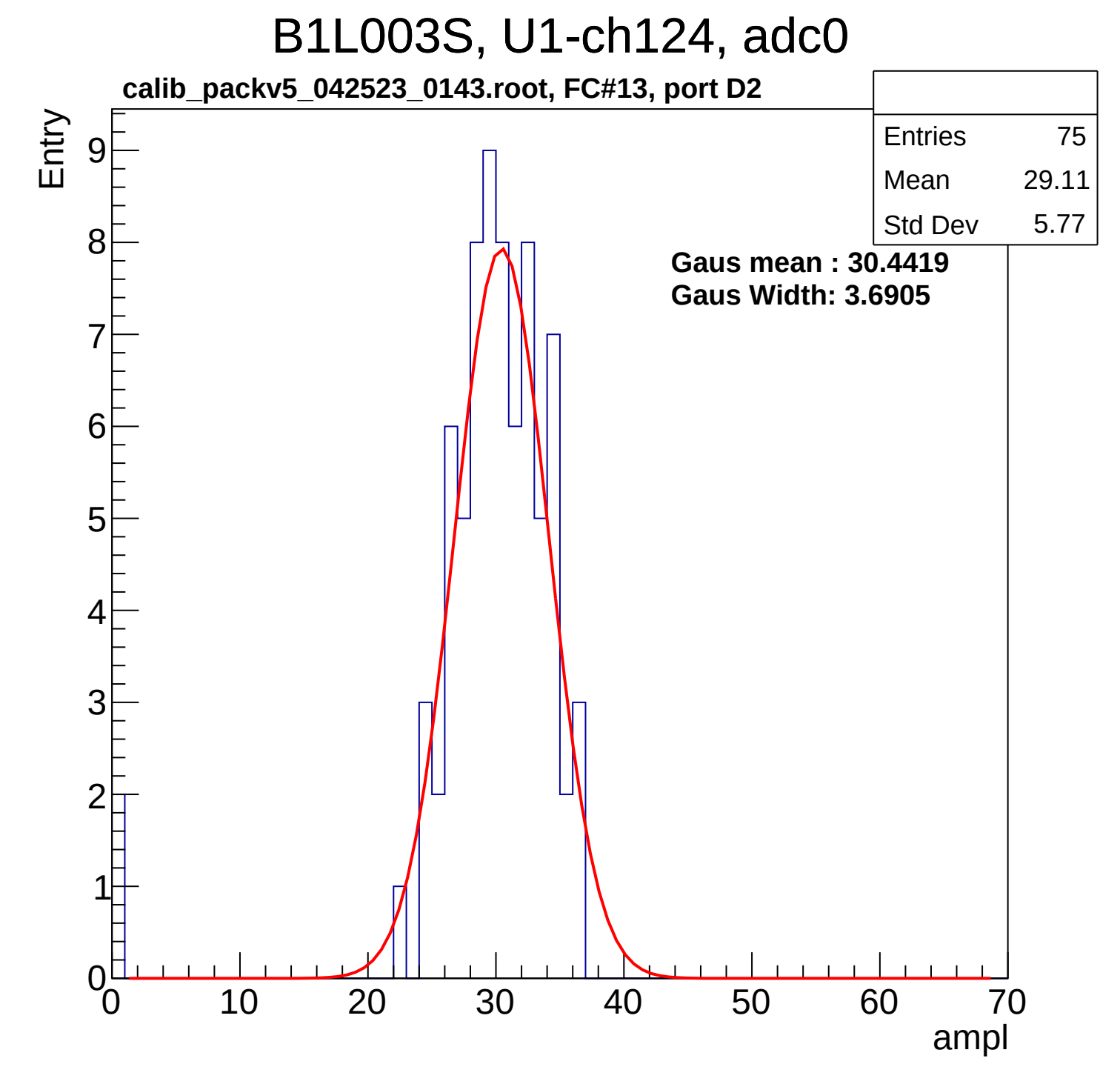
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch123, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch124, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 75
Mean 29.11
Std Dev 5.77
Gaus mean : 30.4419
Gaus Width: 3.6905
Entry
ampl
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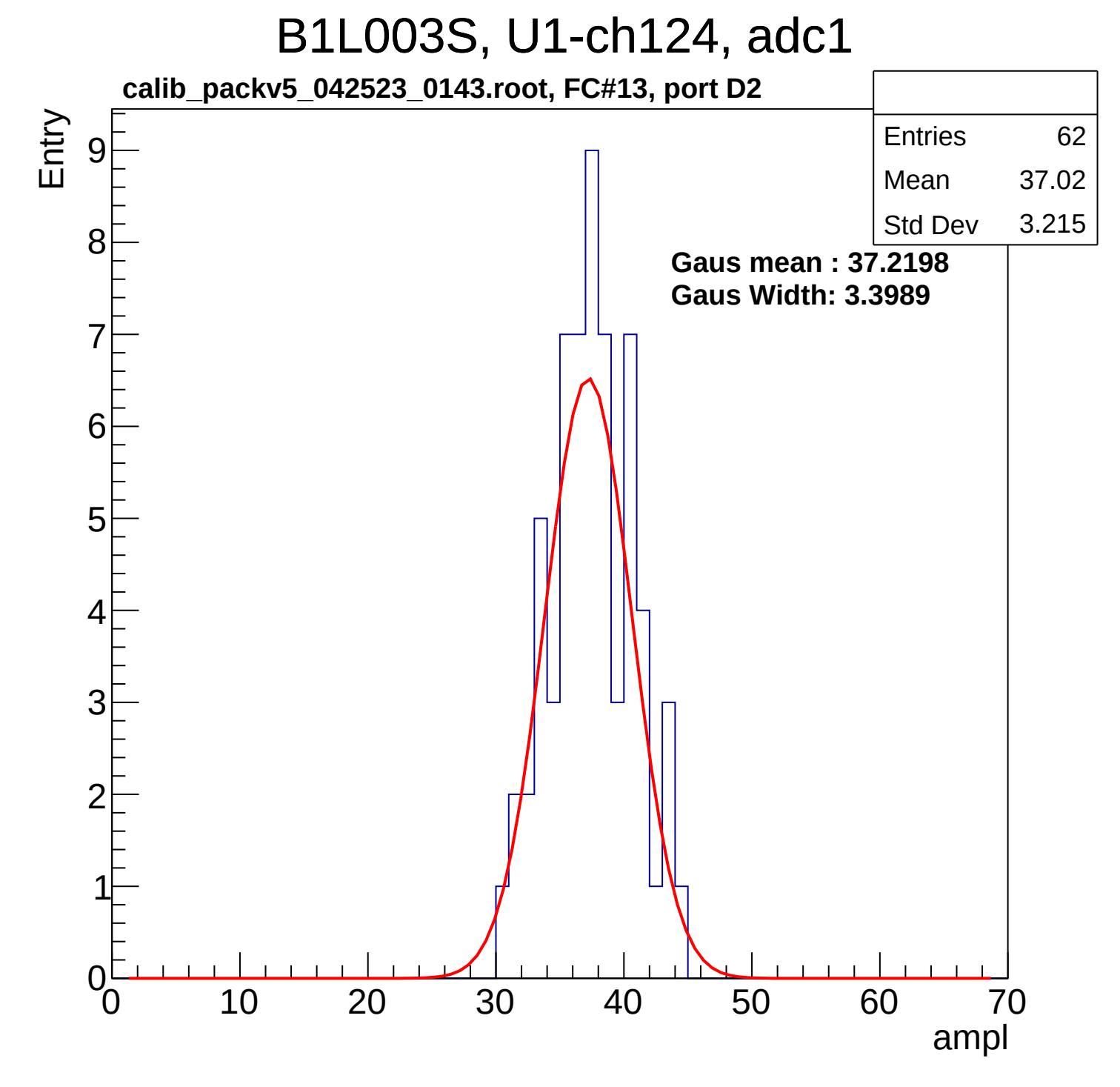
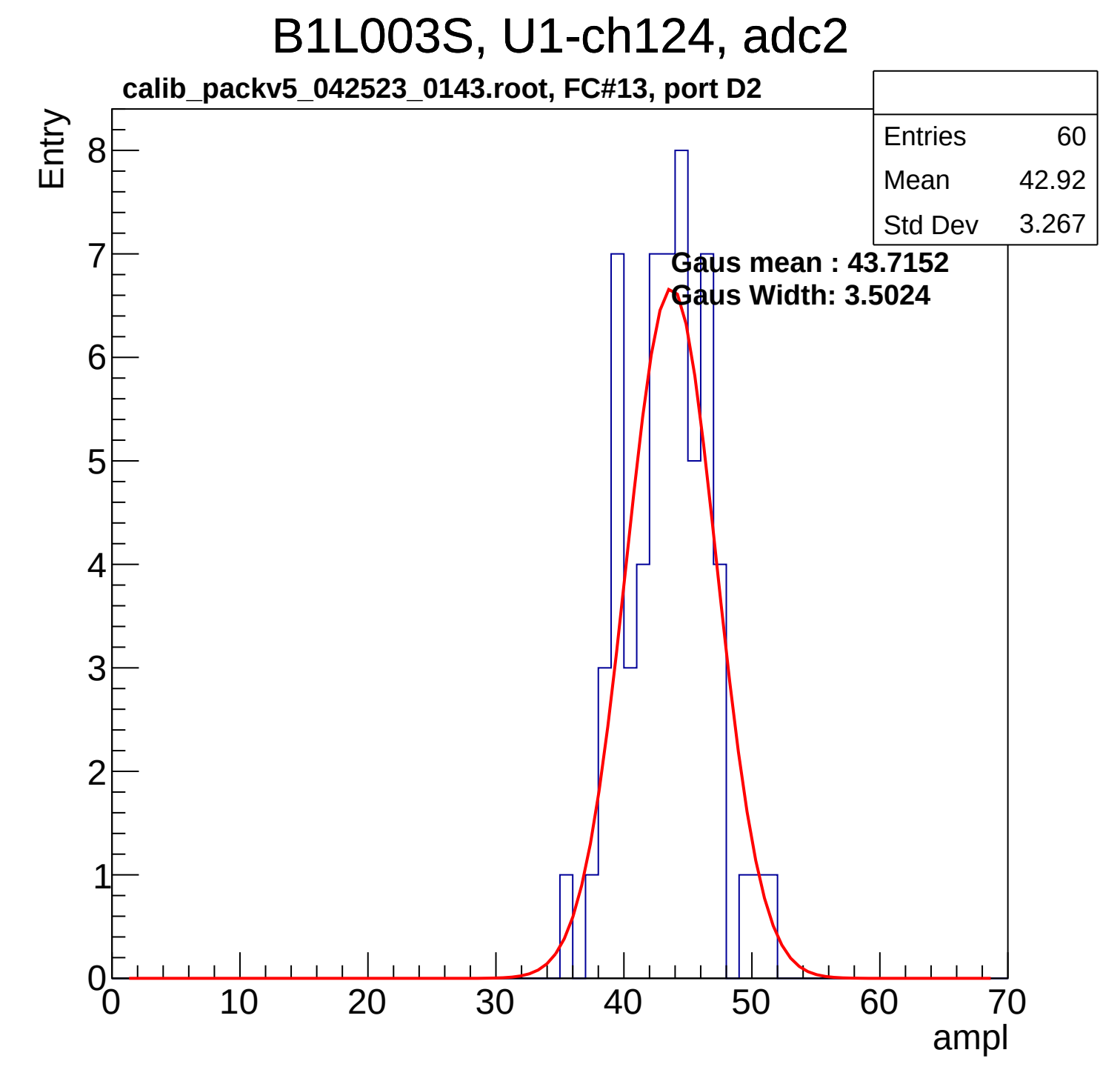
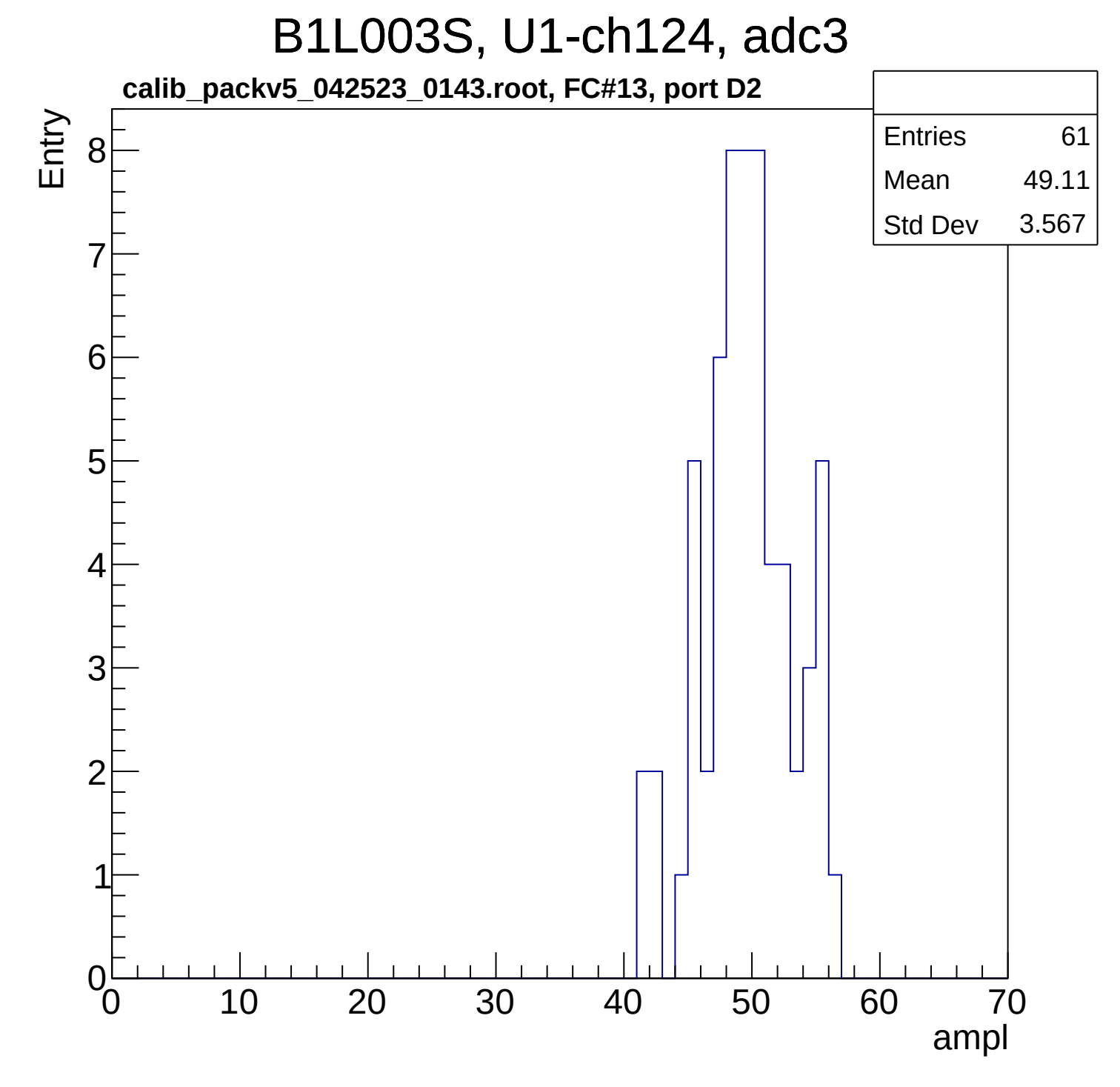
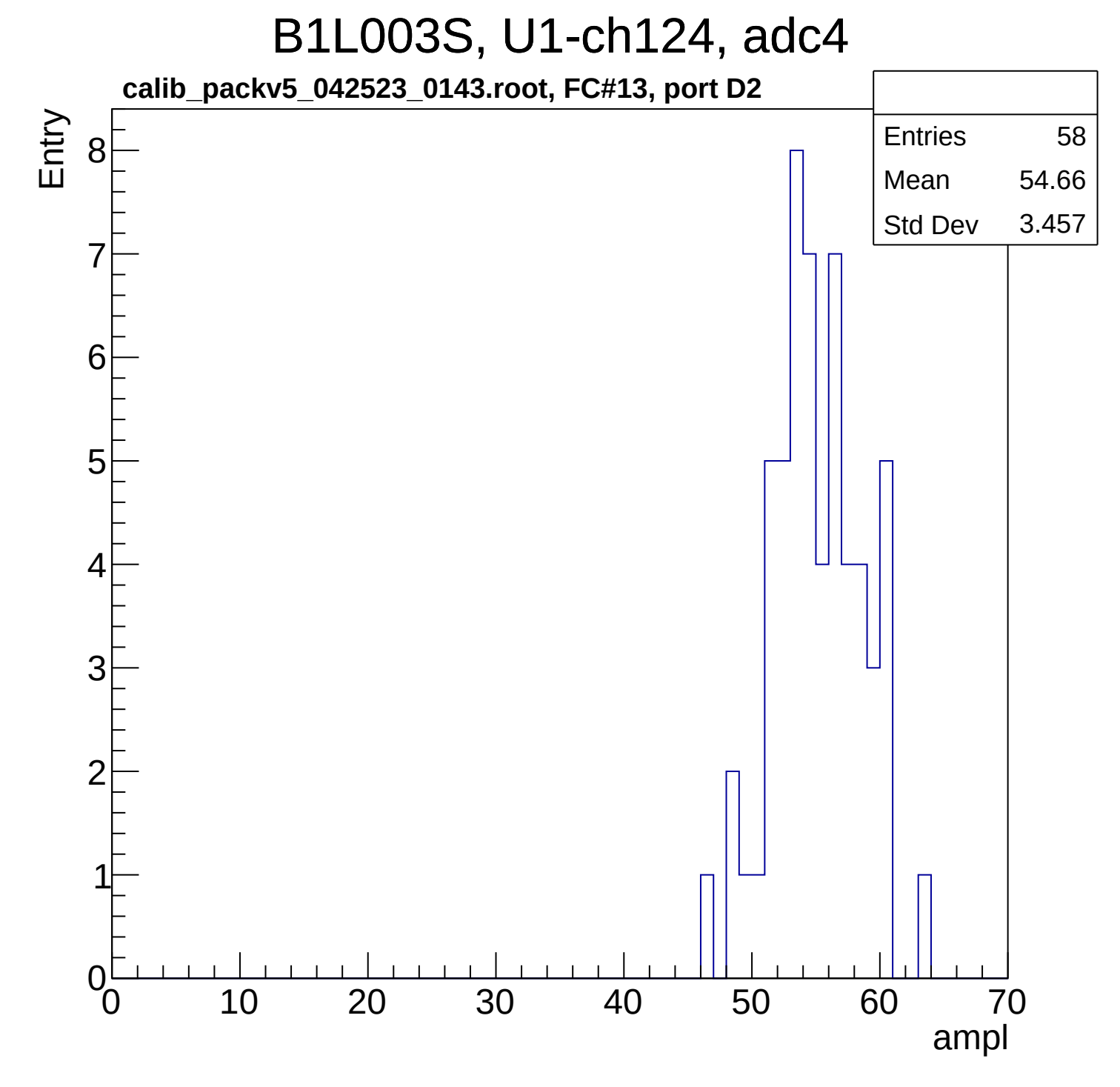
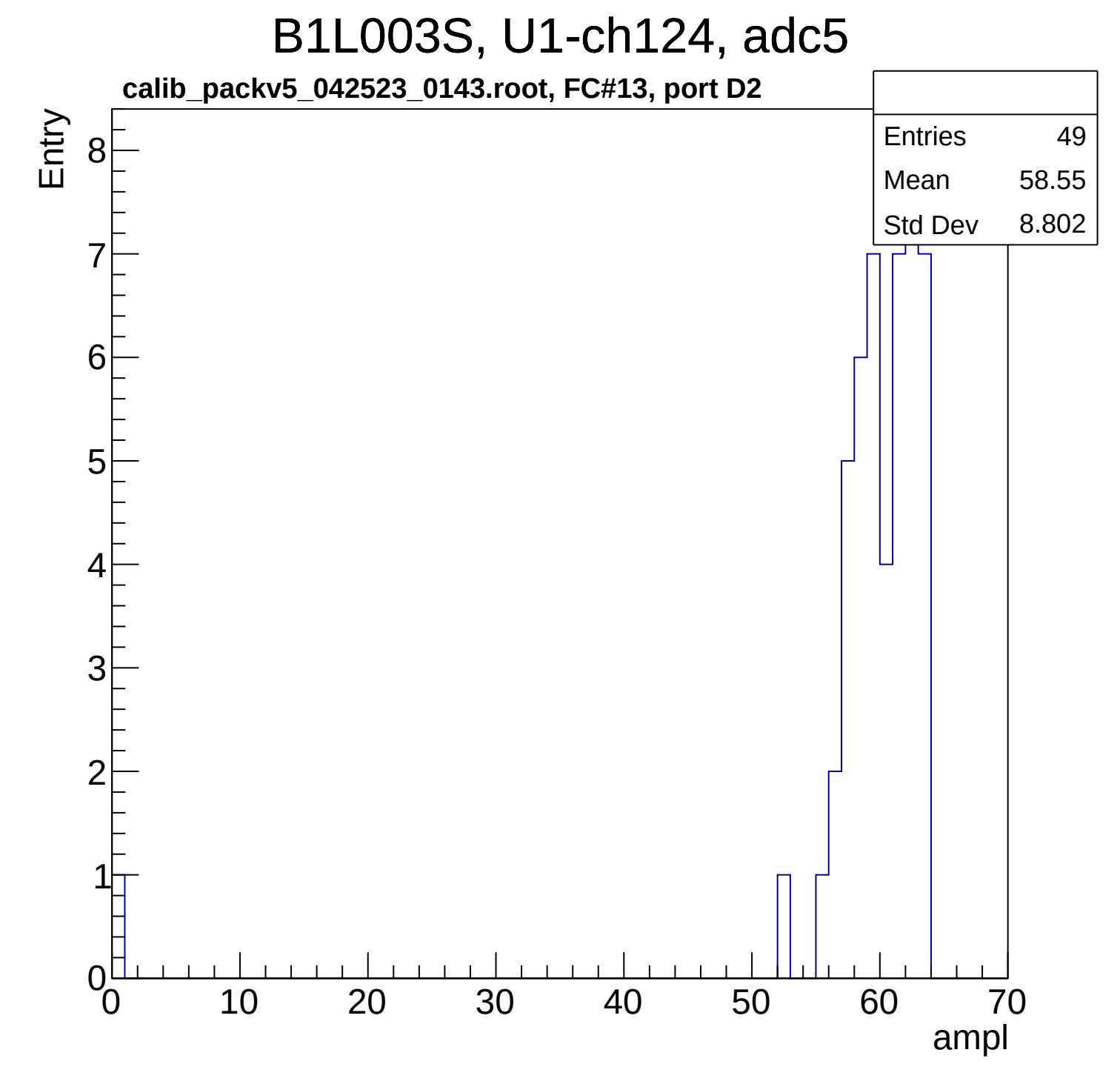
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

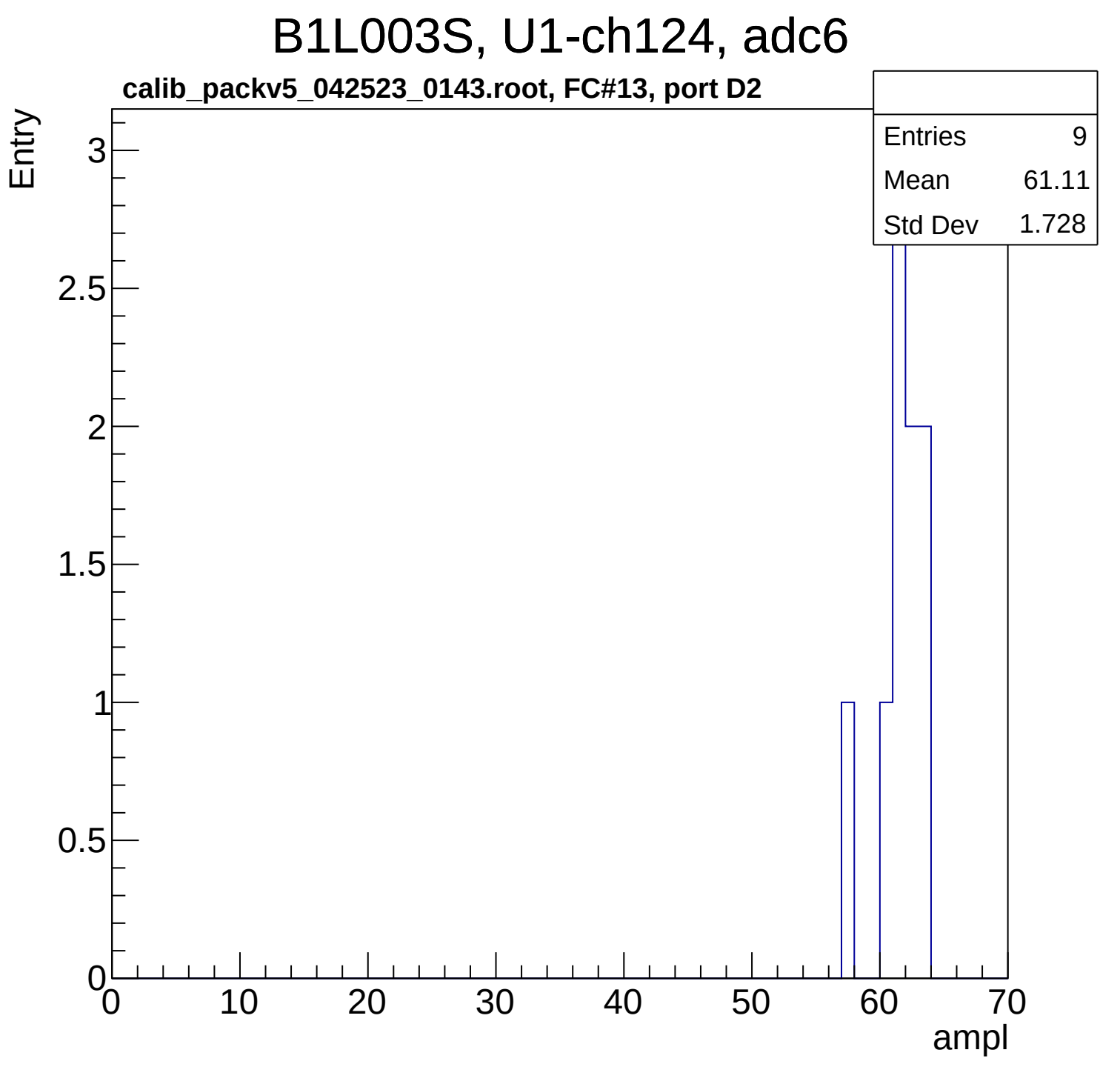

B1L003S, U1-ch124, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 37.02
Std Dev 3.215
Gaus mean : 37.2198
Gaus Width: 3.3989
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

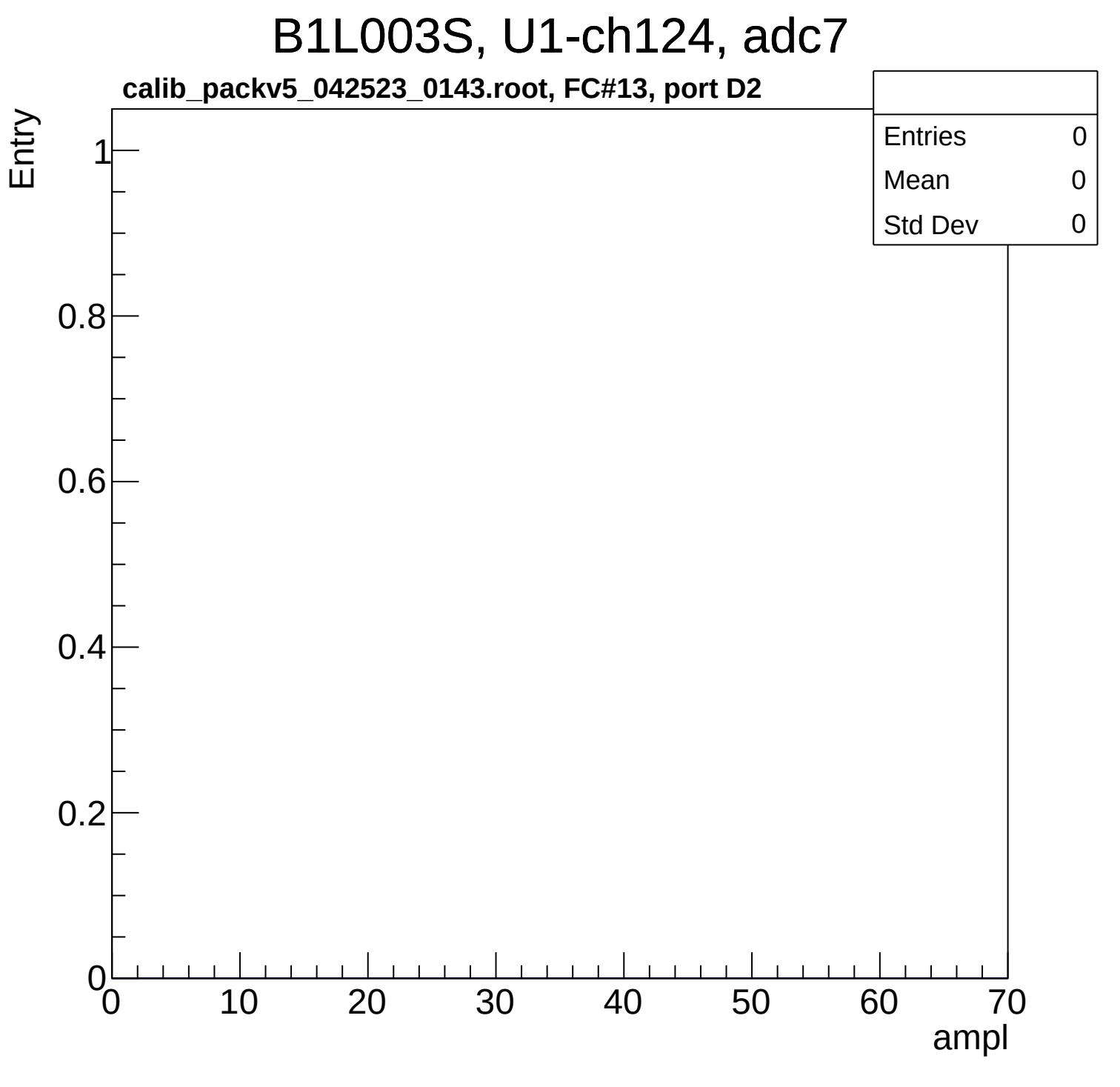

B1L003S, U1-ch124, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 60
Mean 42.92
Std Dev 3.267
Gaus mean : 43.7152
Gaus Width: 3.5024
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

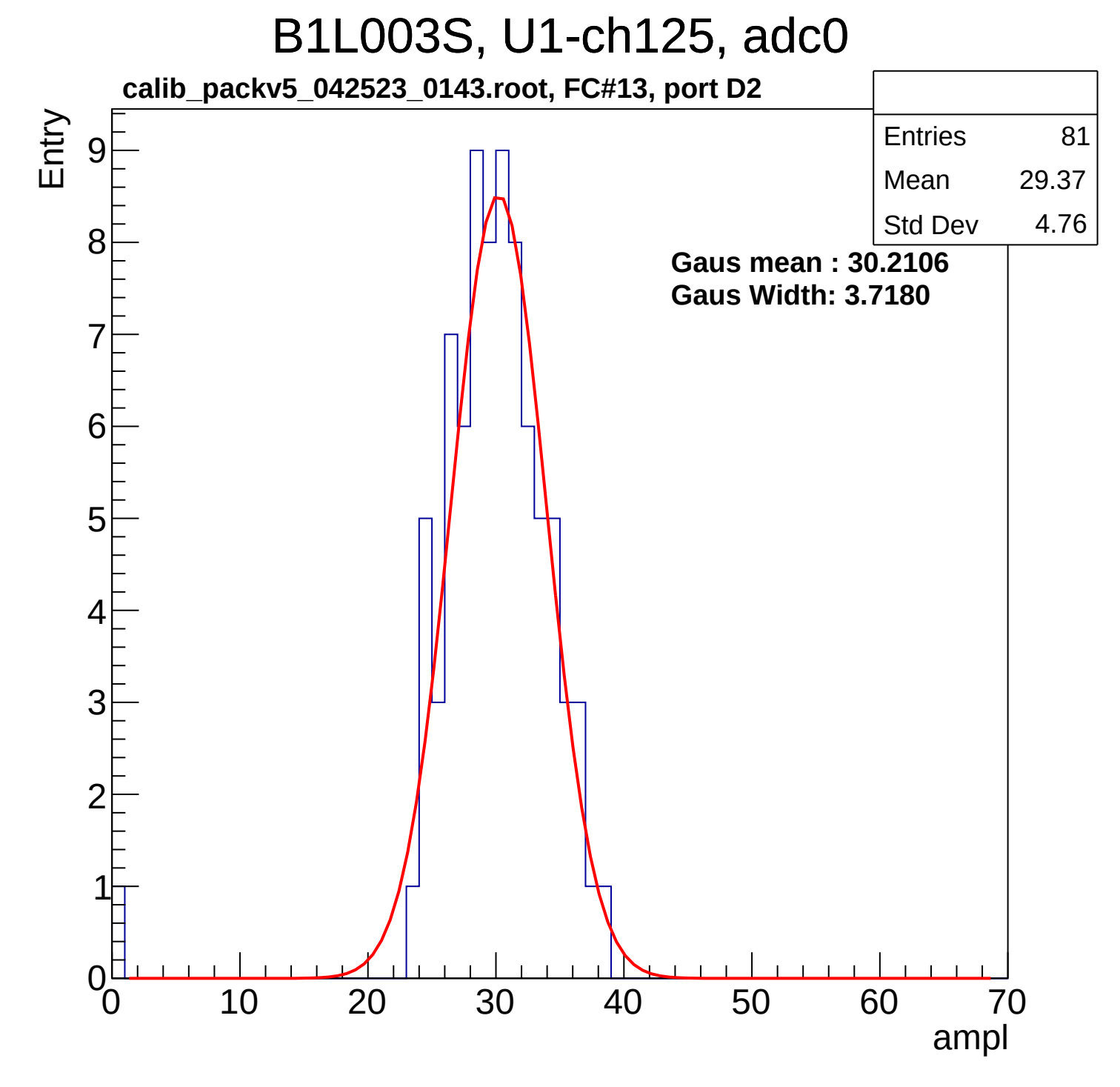

B1L003S, U1-ch124, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 61
Mean 49.11
Std Dev 3.567
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

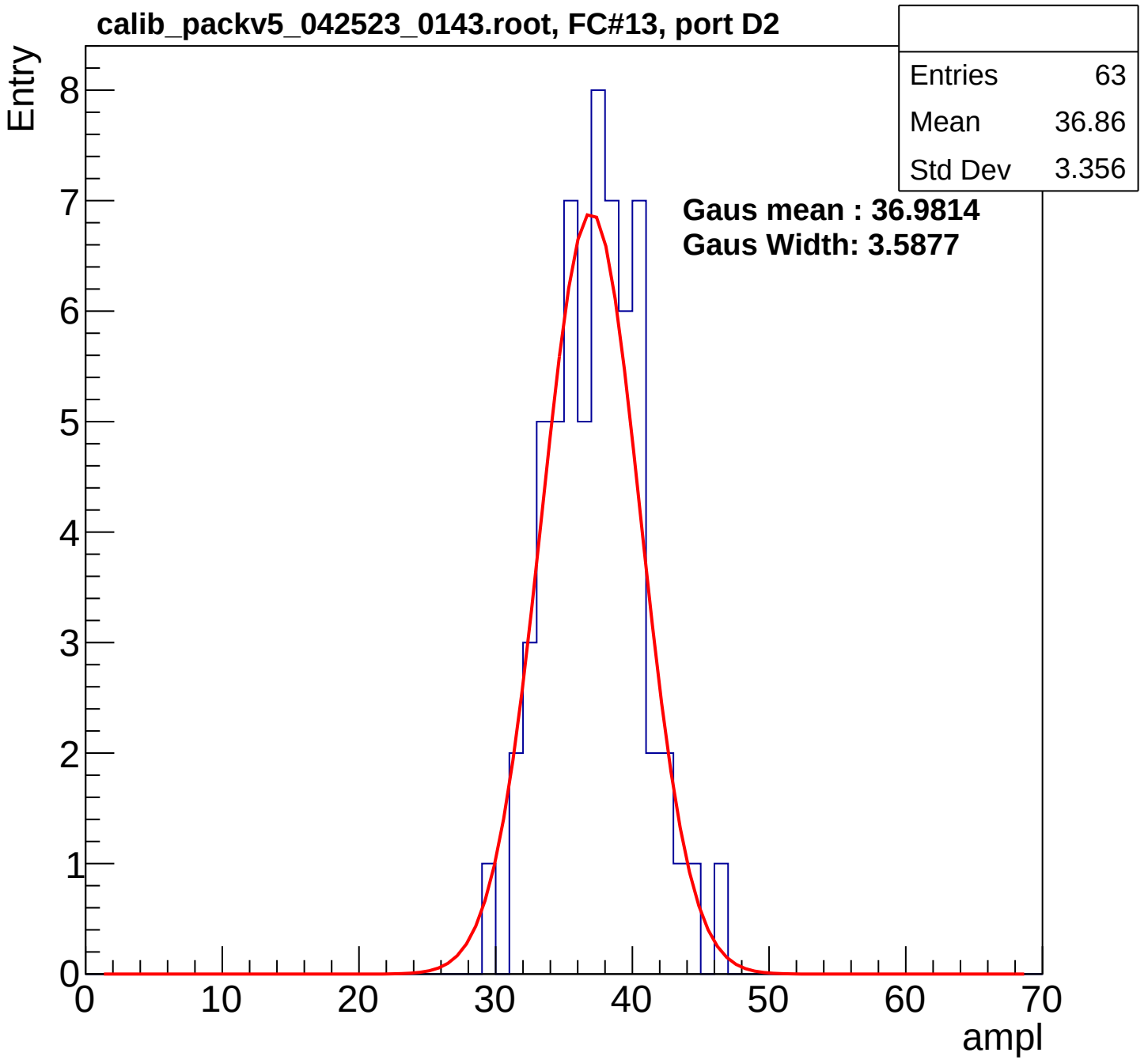

B1L003S, U1-ch124, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.66
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch124, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 49
Mean 58.55
Std Dev 8.802
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch124, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 9
Mean 61.11
Std Dev 1.728
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L003S, U1-ch124, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch125, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 81
Mean 29.37
Std Dev 4.76
Gaus mean : 30.2106
Gaus Width: 3.7180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch125, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 63
Mean 36.86
Std Dev 3.356
Gaus mean : 36.9814
Gaus Width: 3.5877
Entry
ampl
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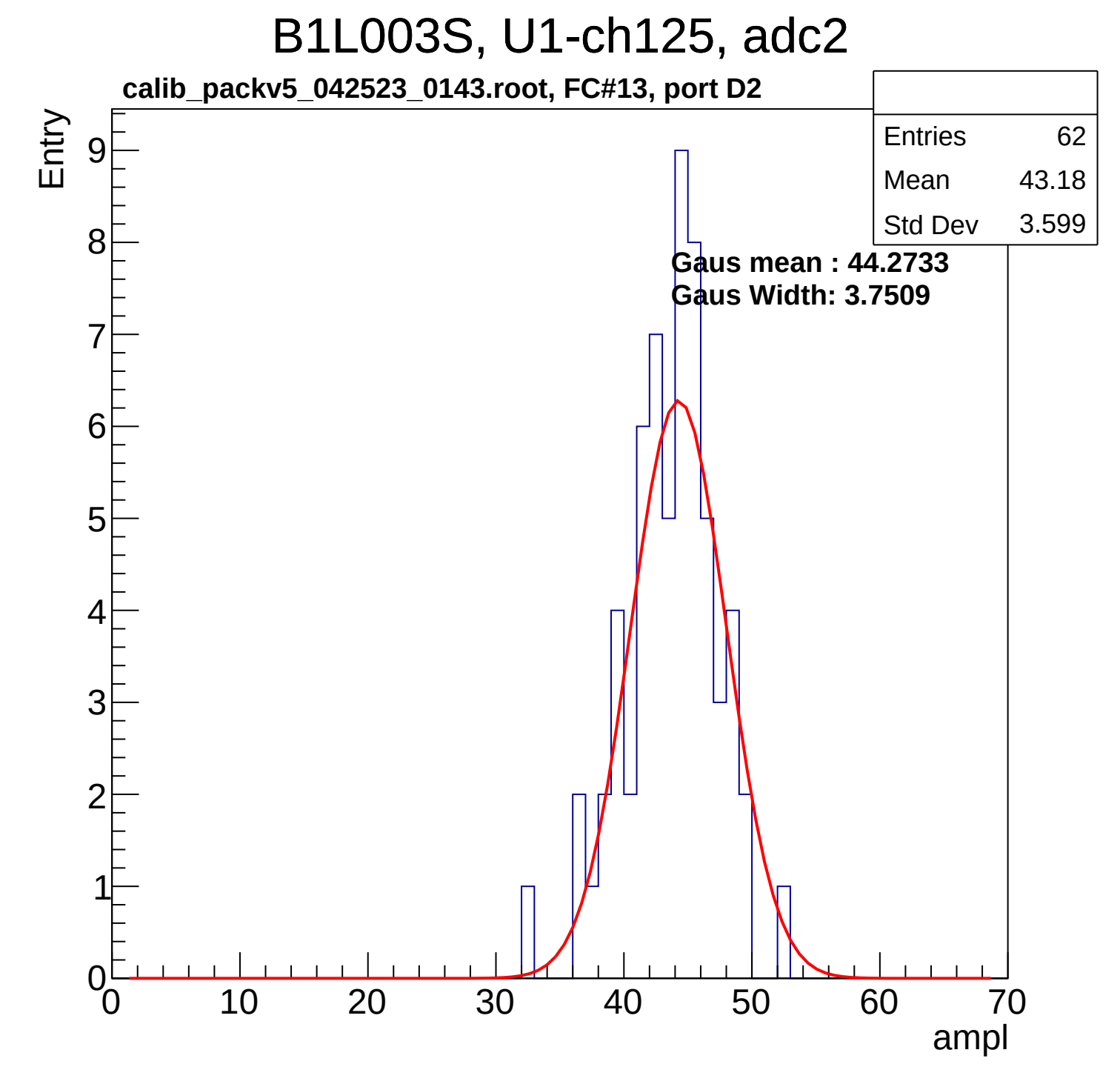
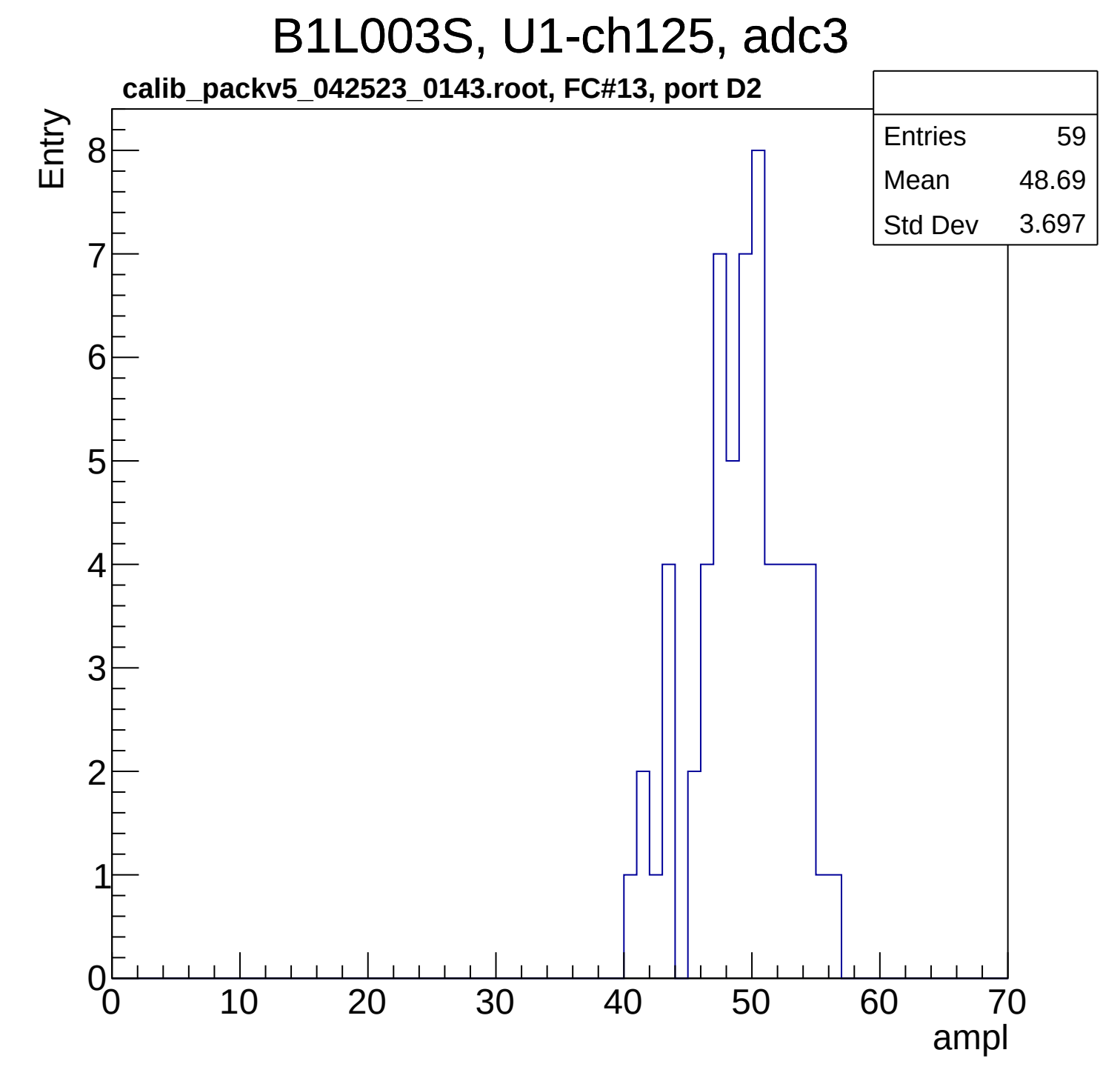
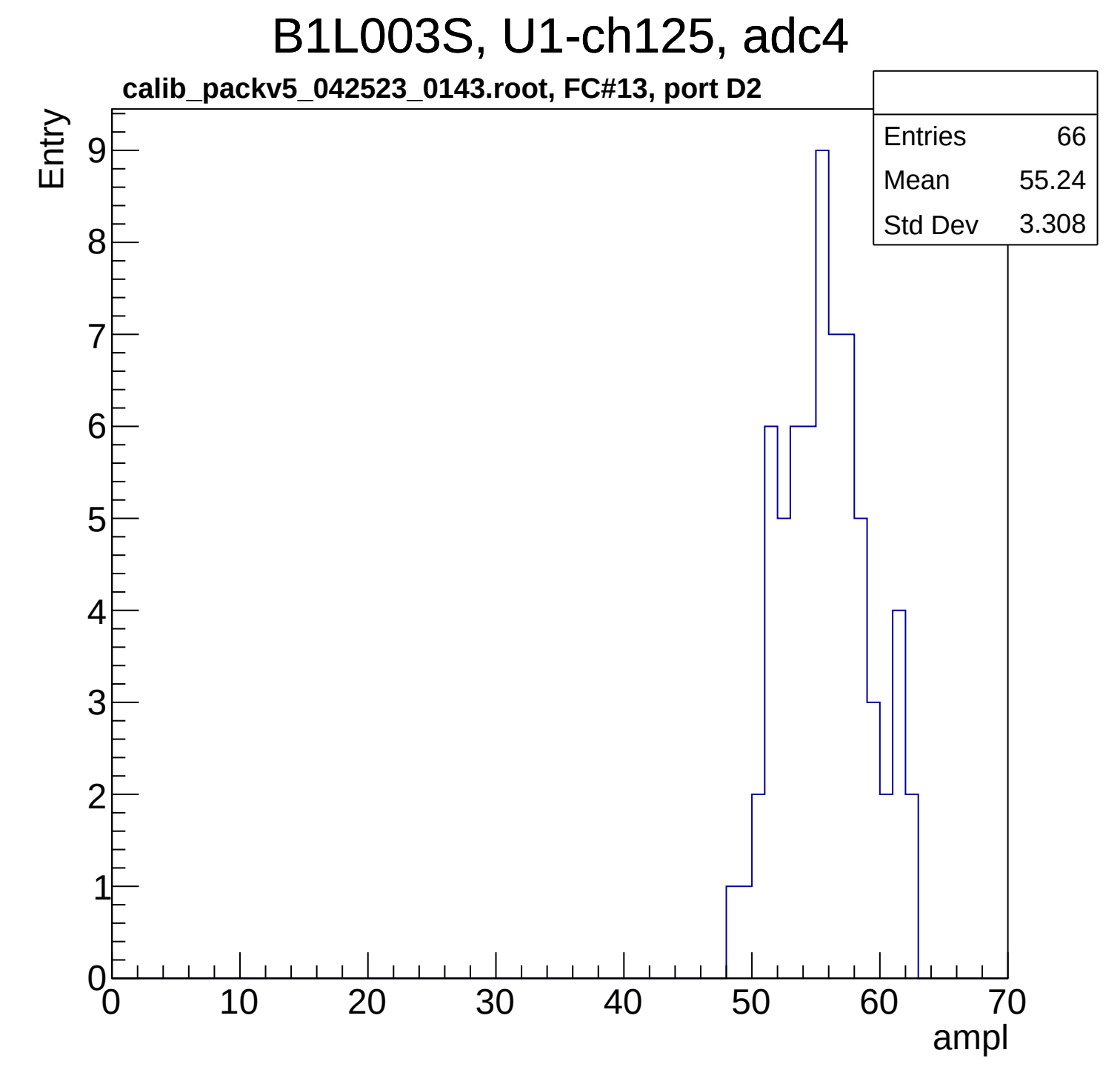
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch125, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 62
Mean 43.18
Std Dev 3.599
Gaus mean : 44.2733
Gaus Width: 3.7509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch125, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 48.69
Std Dev 3.697
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch125, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.24
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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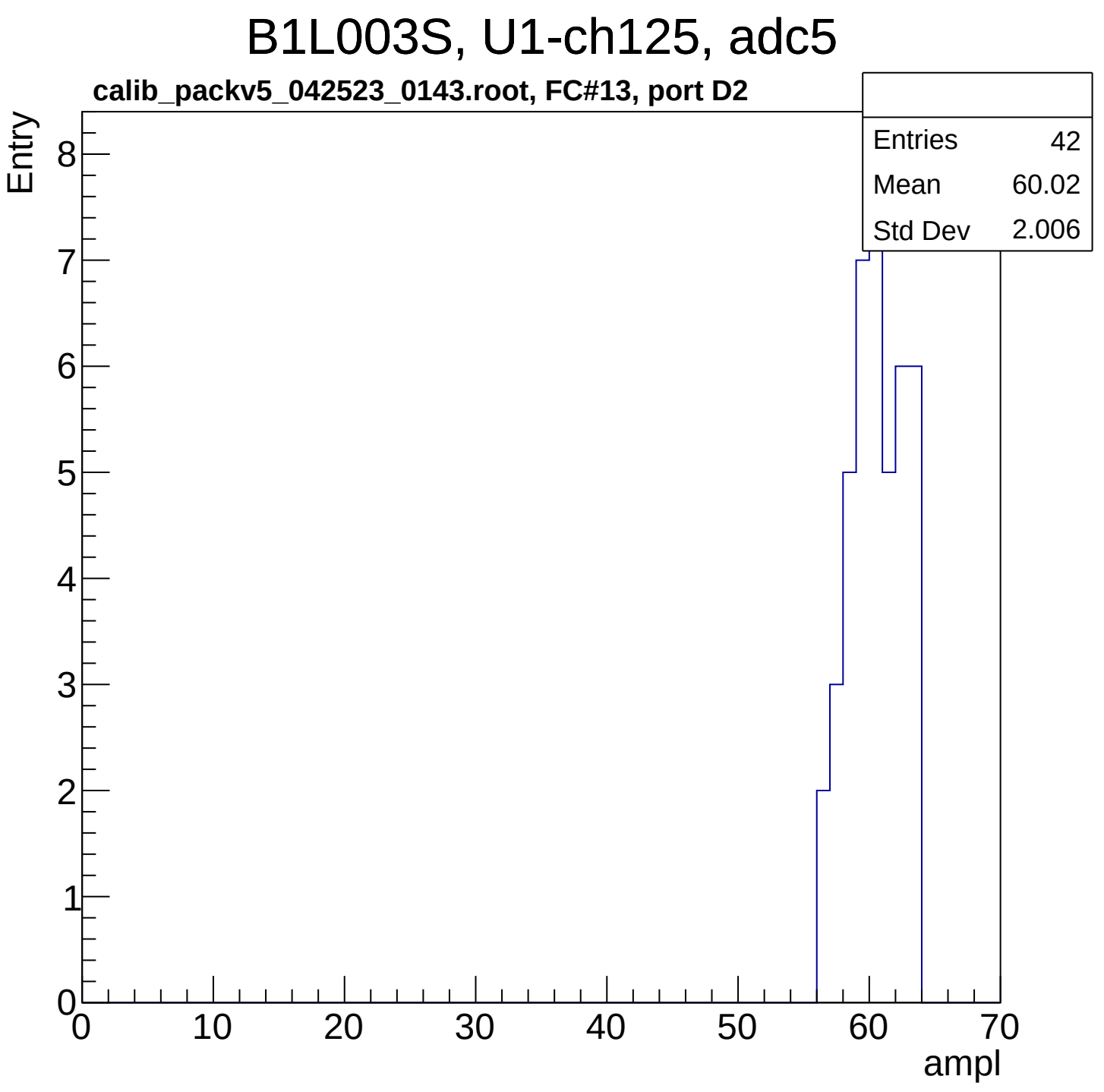
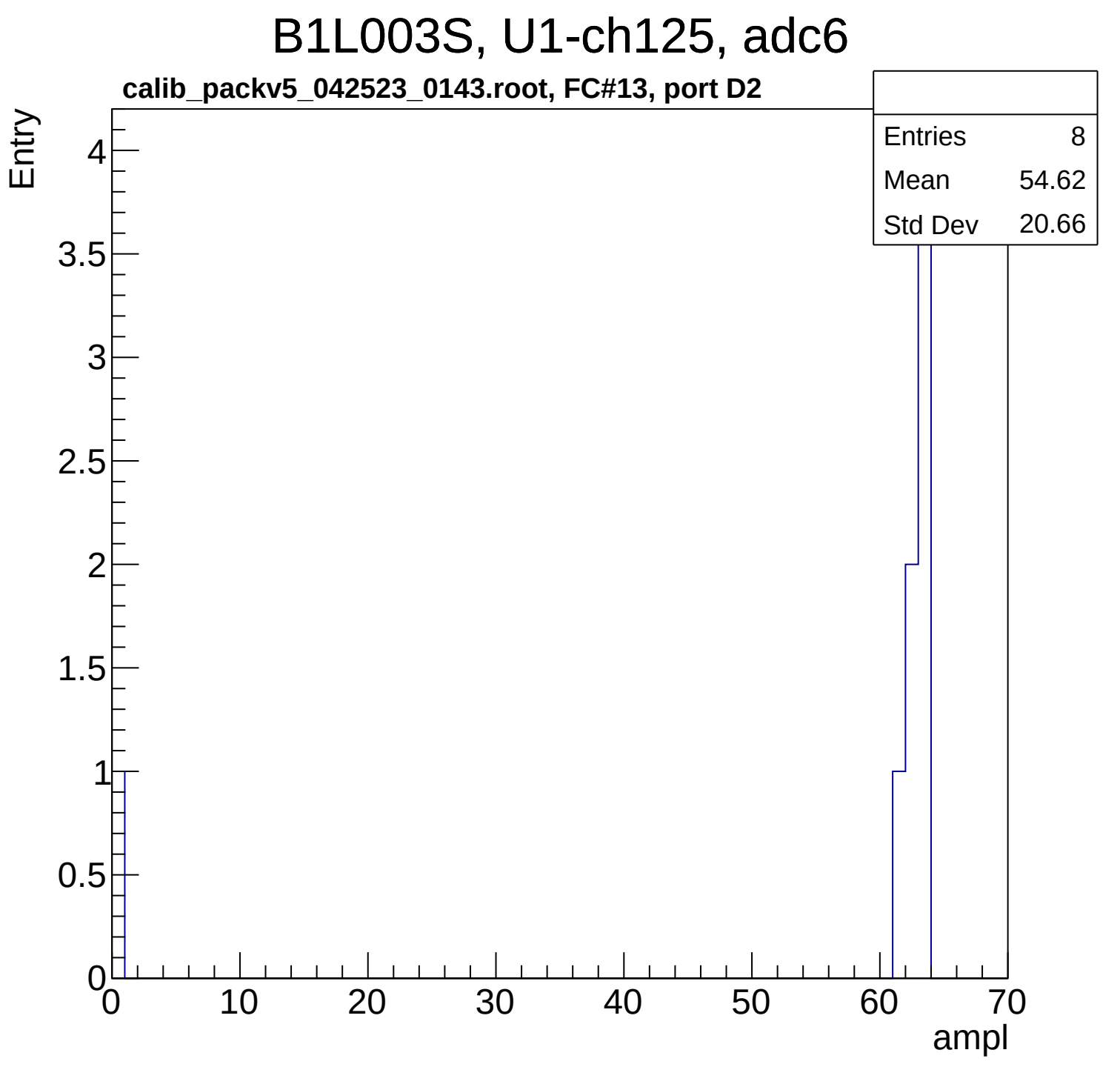
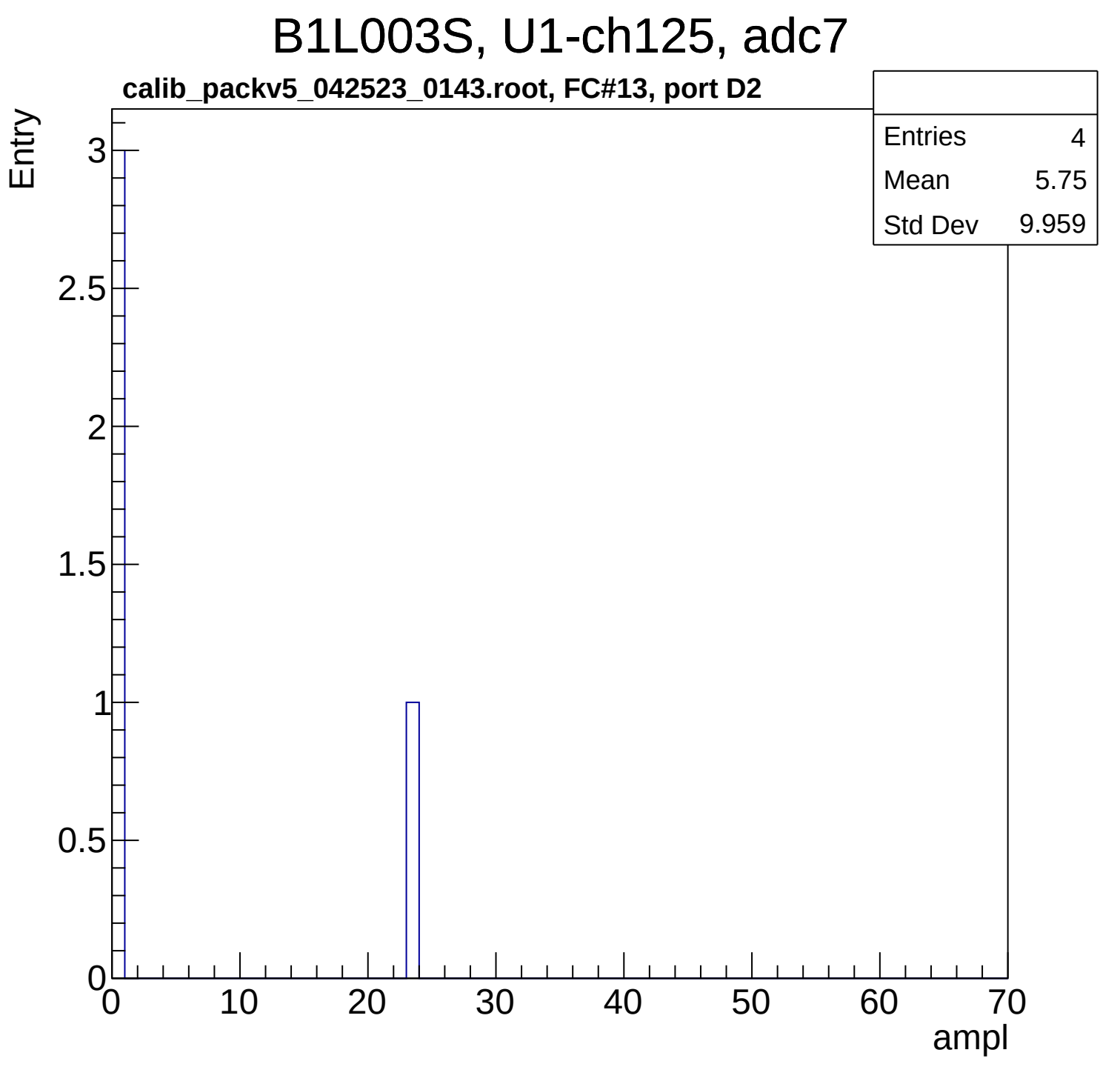
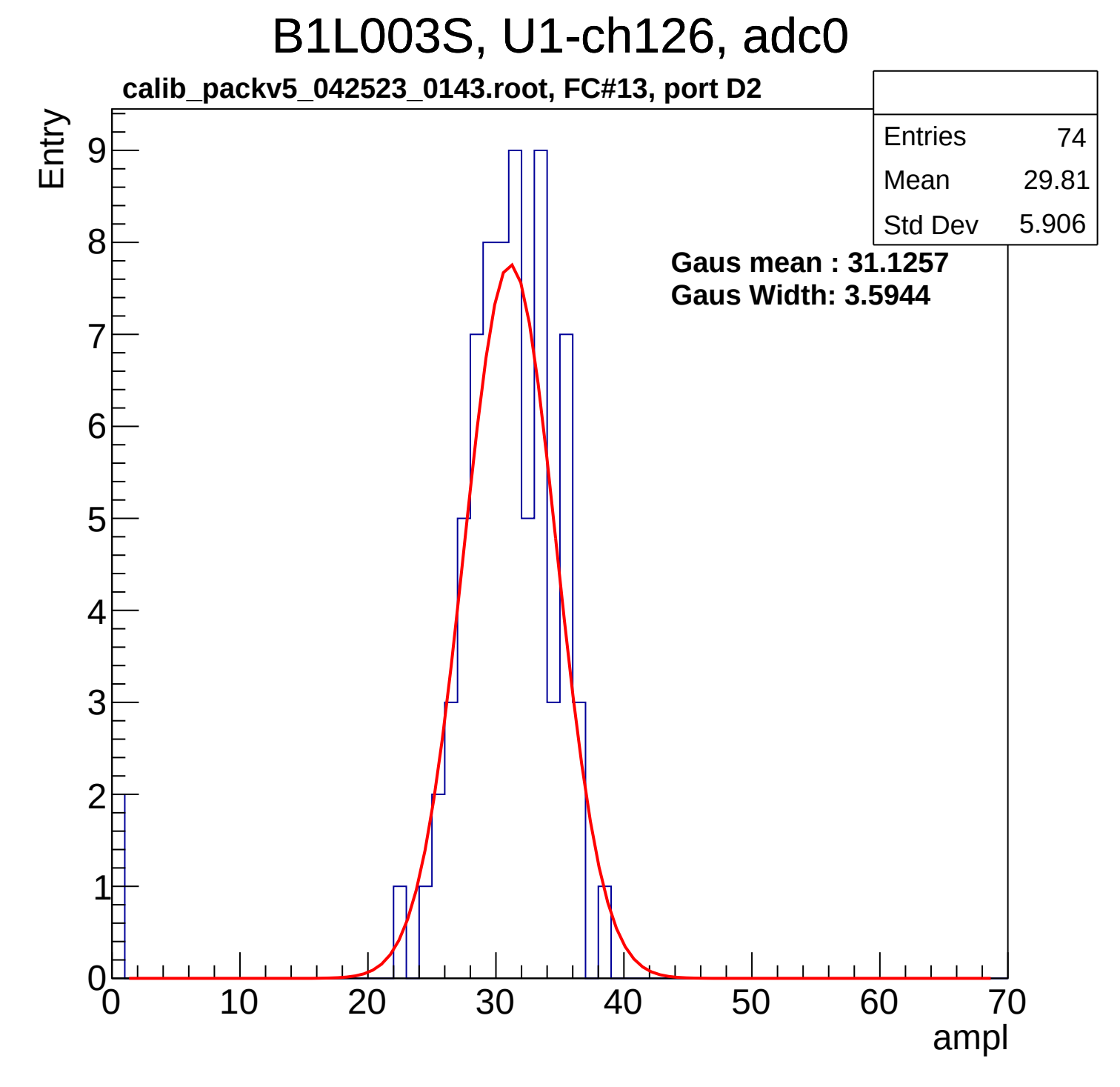
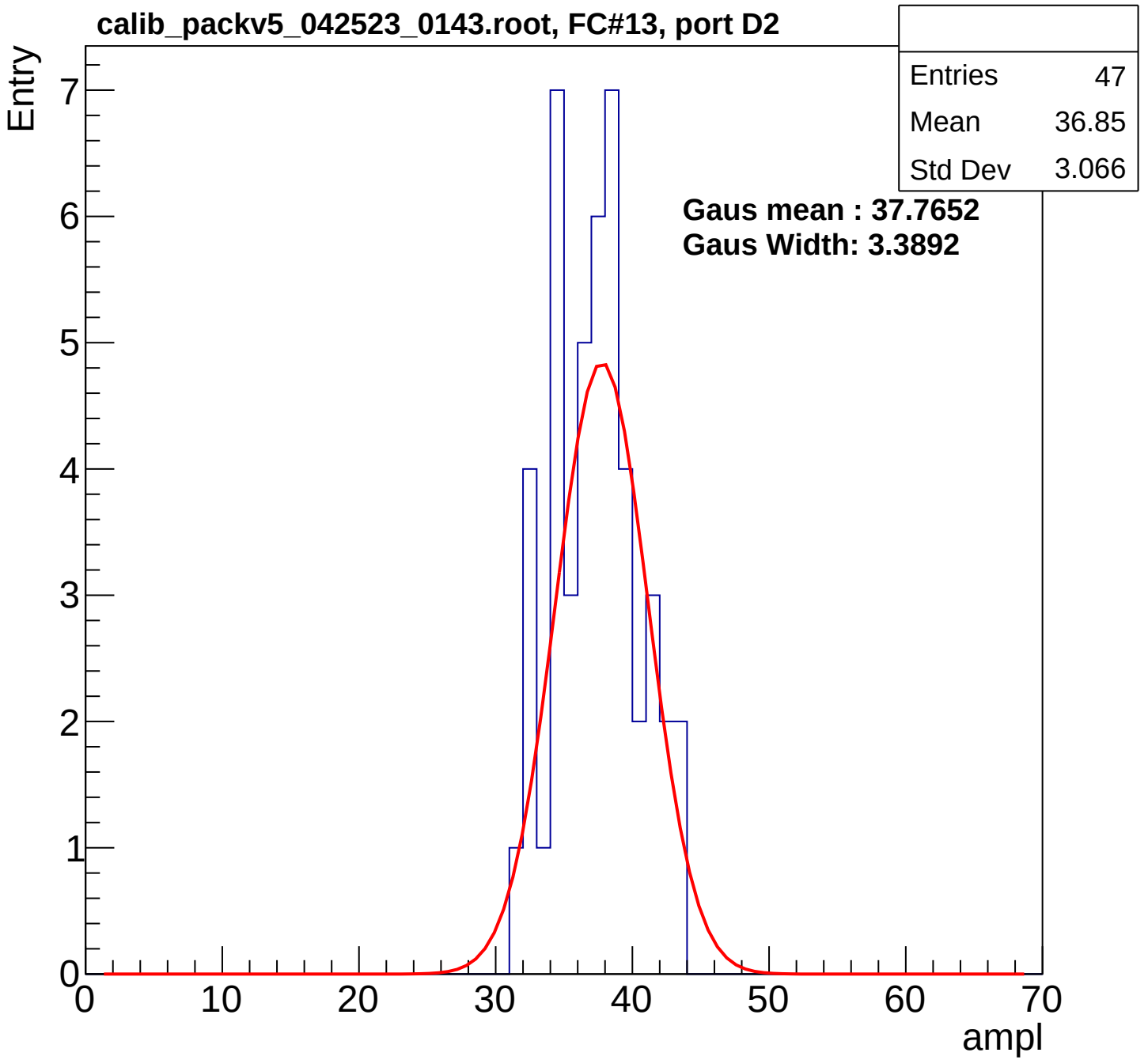
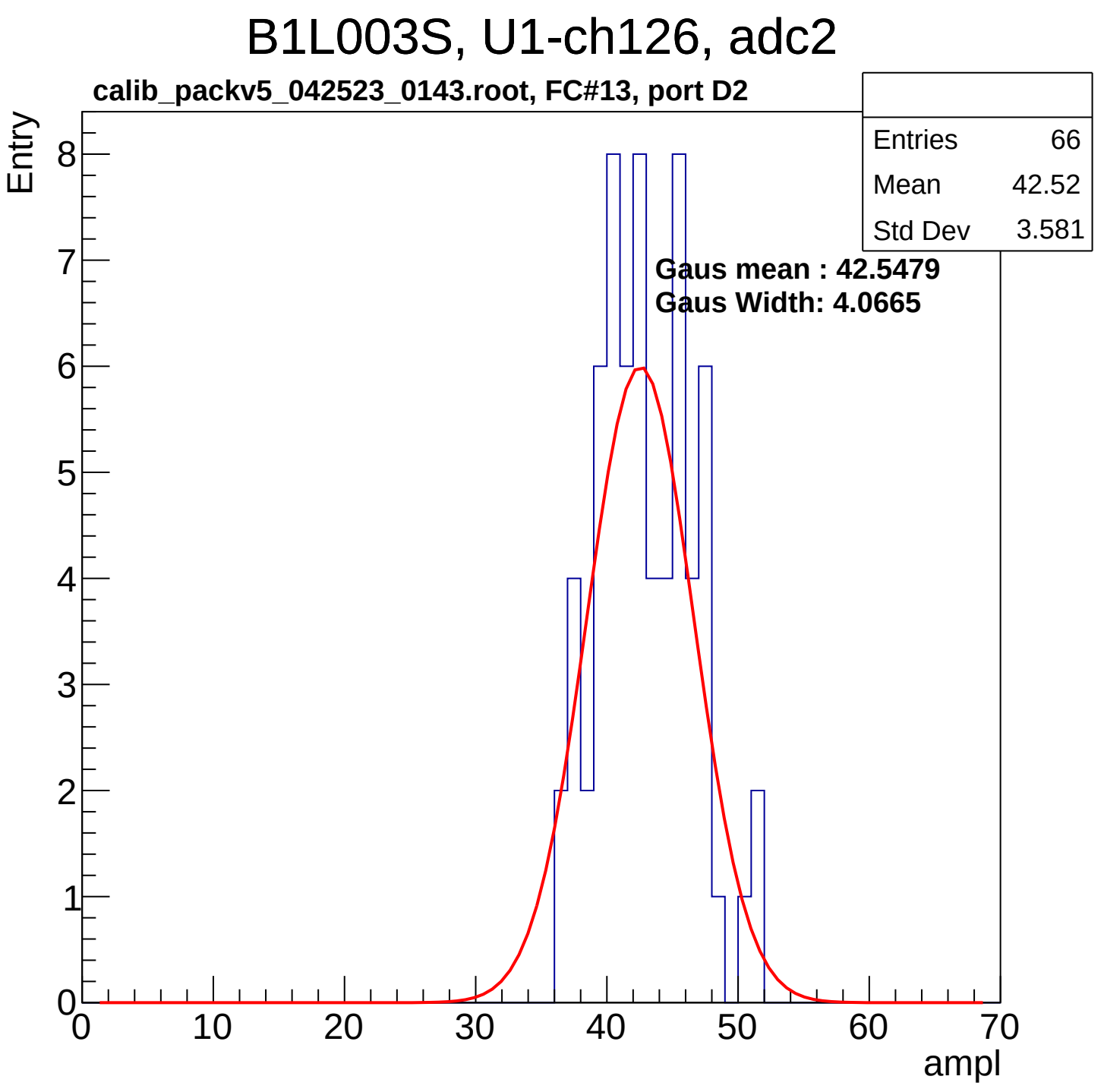

B1L003S, U1-ch125, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 42
Mean 60.02
Std Dev 2.006
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

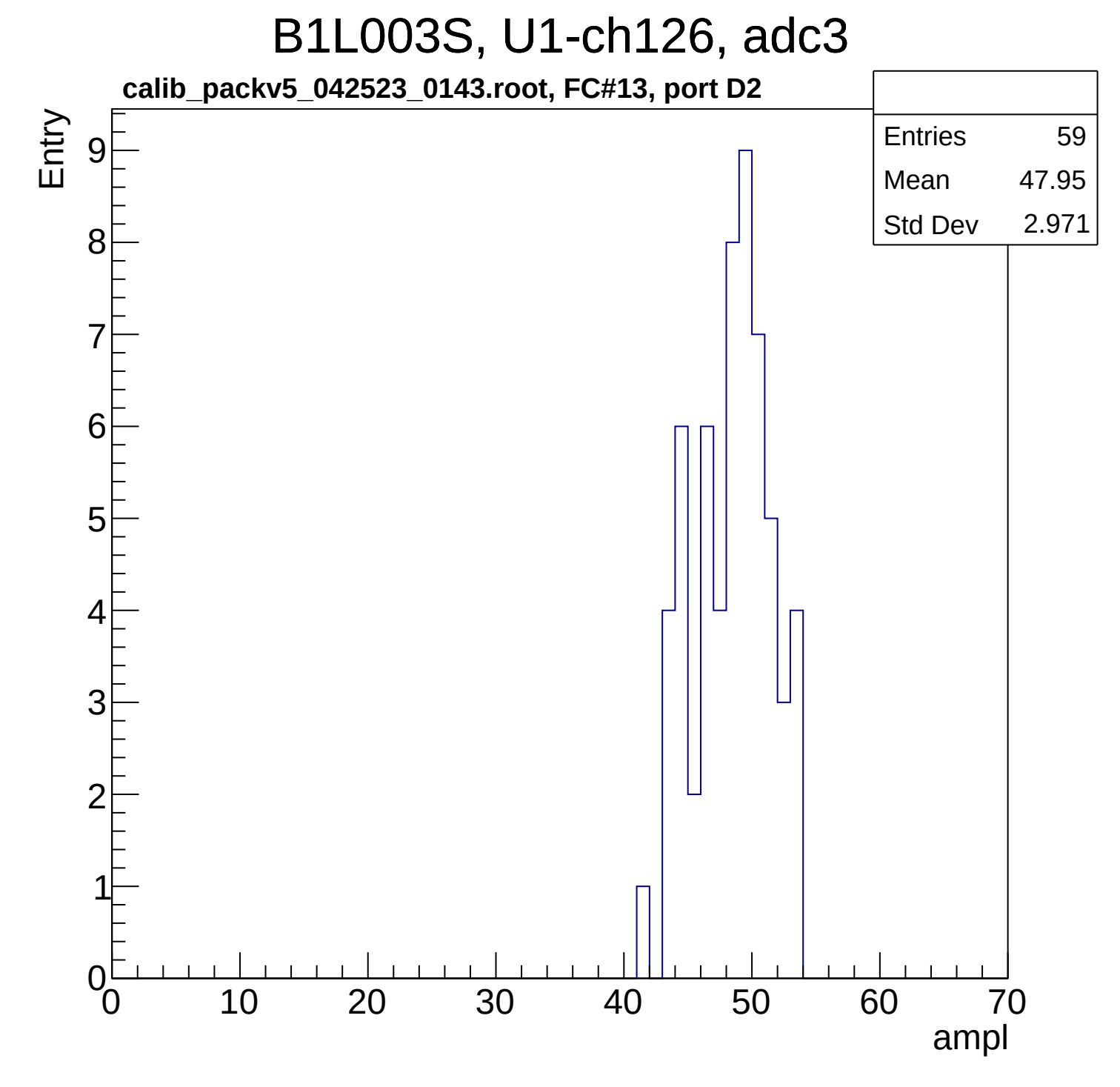

B1L003S, U1-ch125, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 8
Mean 54.62
Std Dev 20.66
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

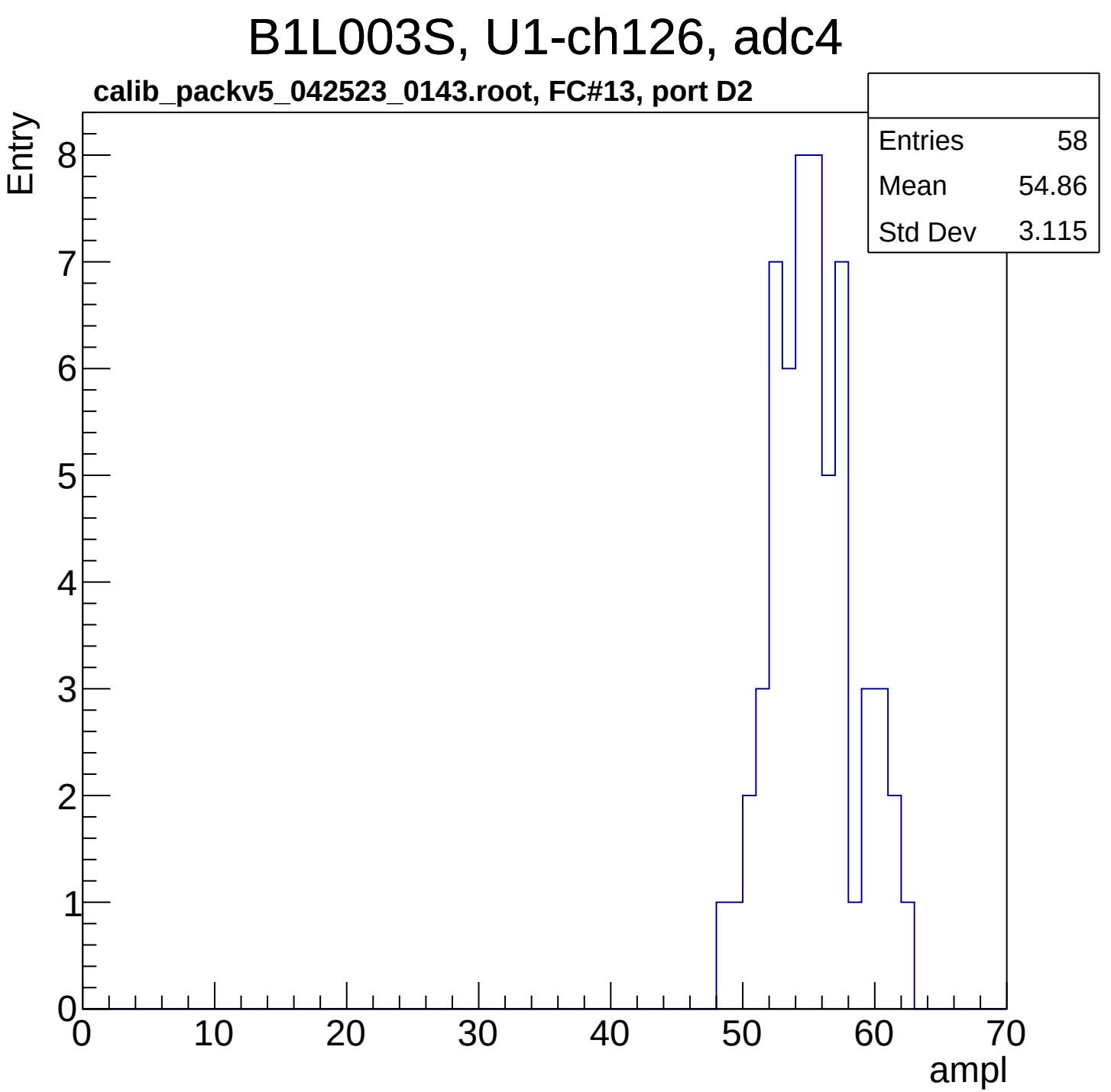

B1L003S, U1-ch125, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 4
Mean 5.75
Std Dev 9.959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

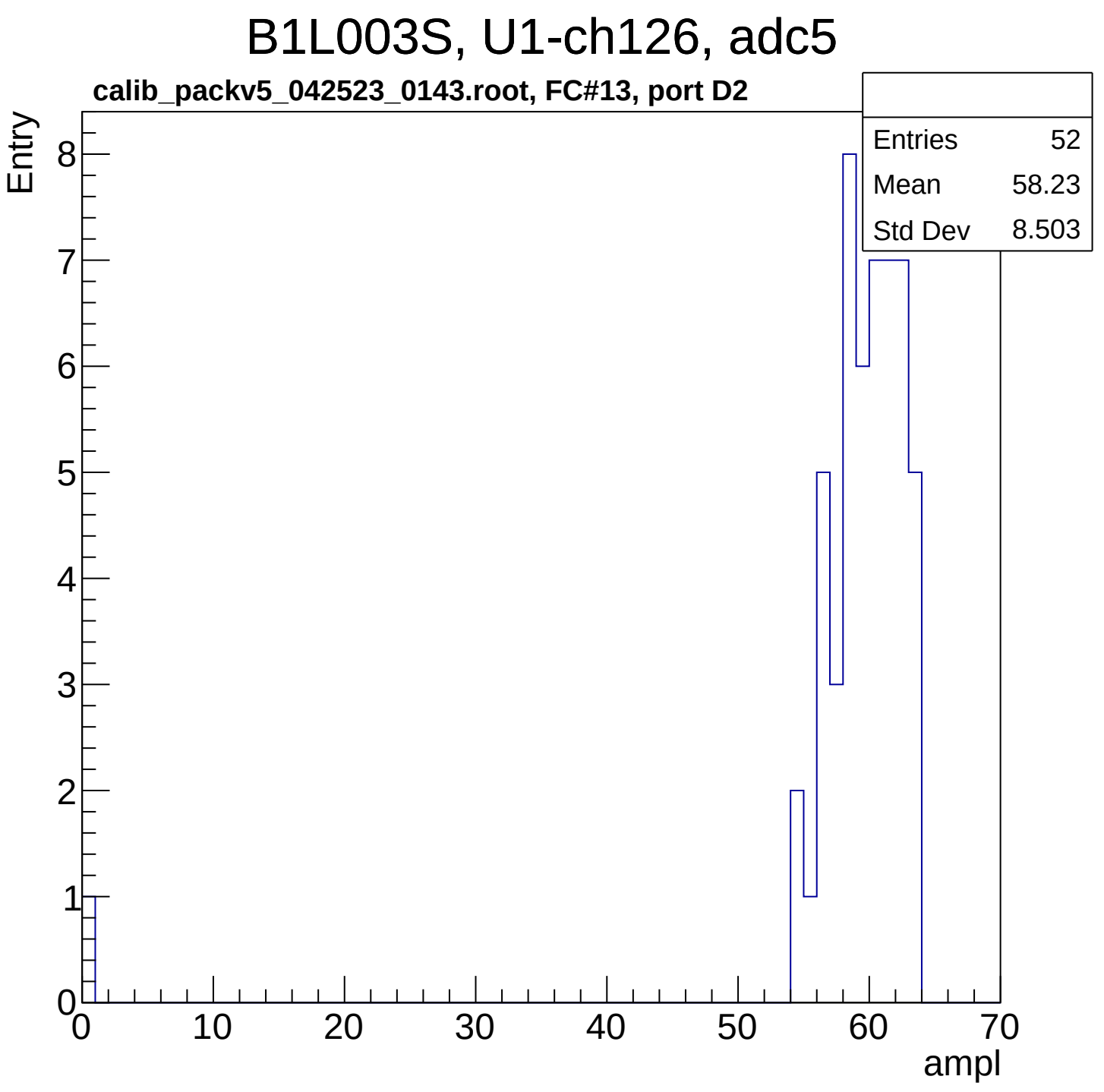

B1L003S, U1-ch126, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 74
Mean 29.81
Std Dev 5.906
Gaus mean : 31.1257
Gaus Width: 3.5944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch126, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 47
Mean 36.85
Std Dev 3.066
Gaus mean : 37.7652
Gaus Width: 3.3892
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch126, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 42.52
Std Dev 3.581
Gaus mean : 42.5479
Gaus Width: 4.0665
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L003S, U1-ch126, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 59
Mean 47.95
Std Dev 2.971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch126, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 58
Mean 54.86
Std Dev 3.115
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch126, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 52
Mean 58.23
Std Dev 8.503
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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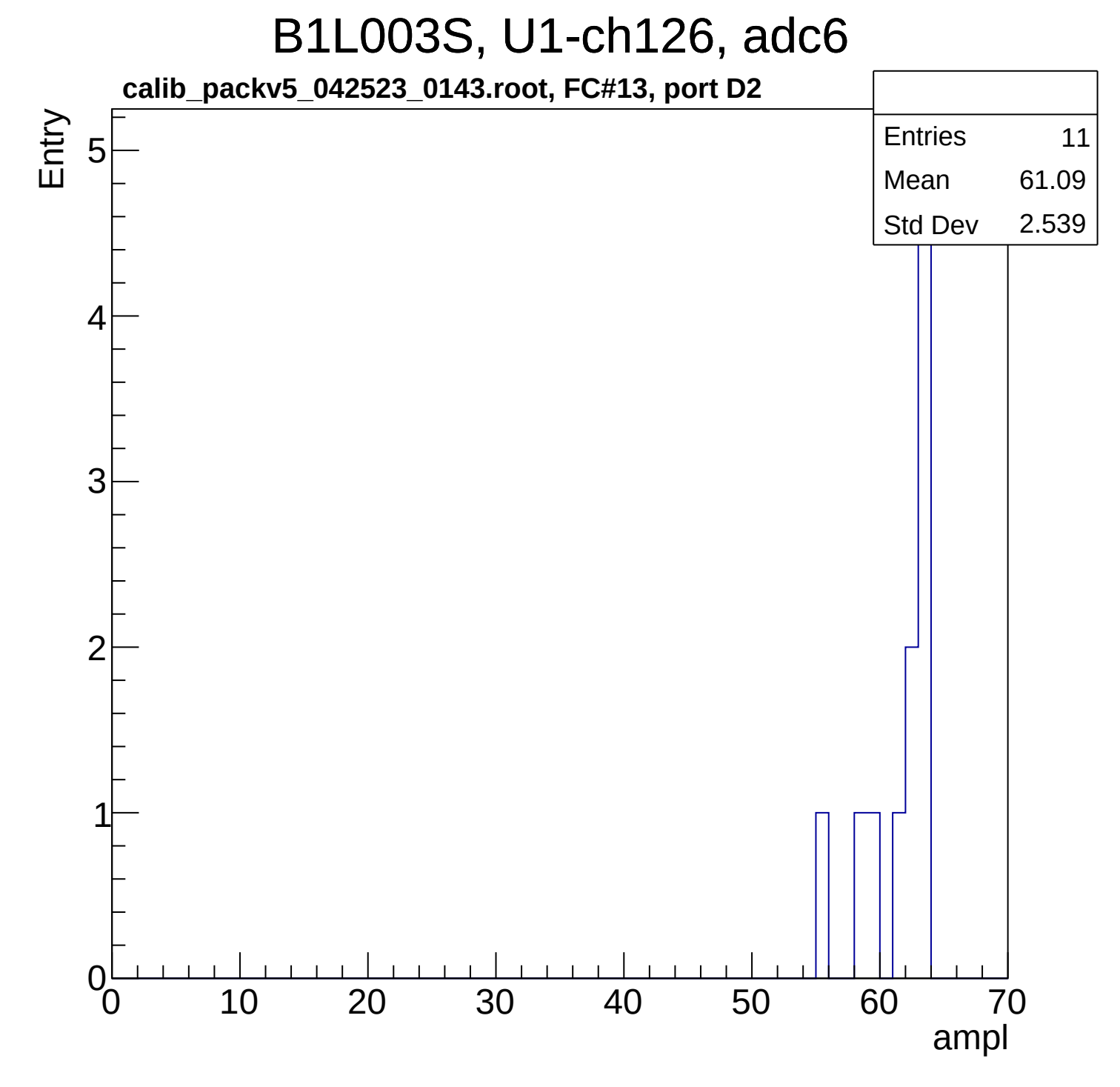
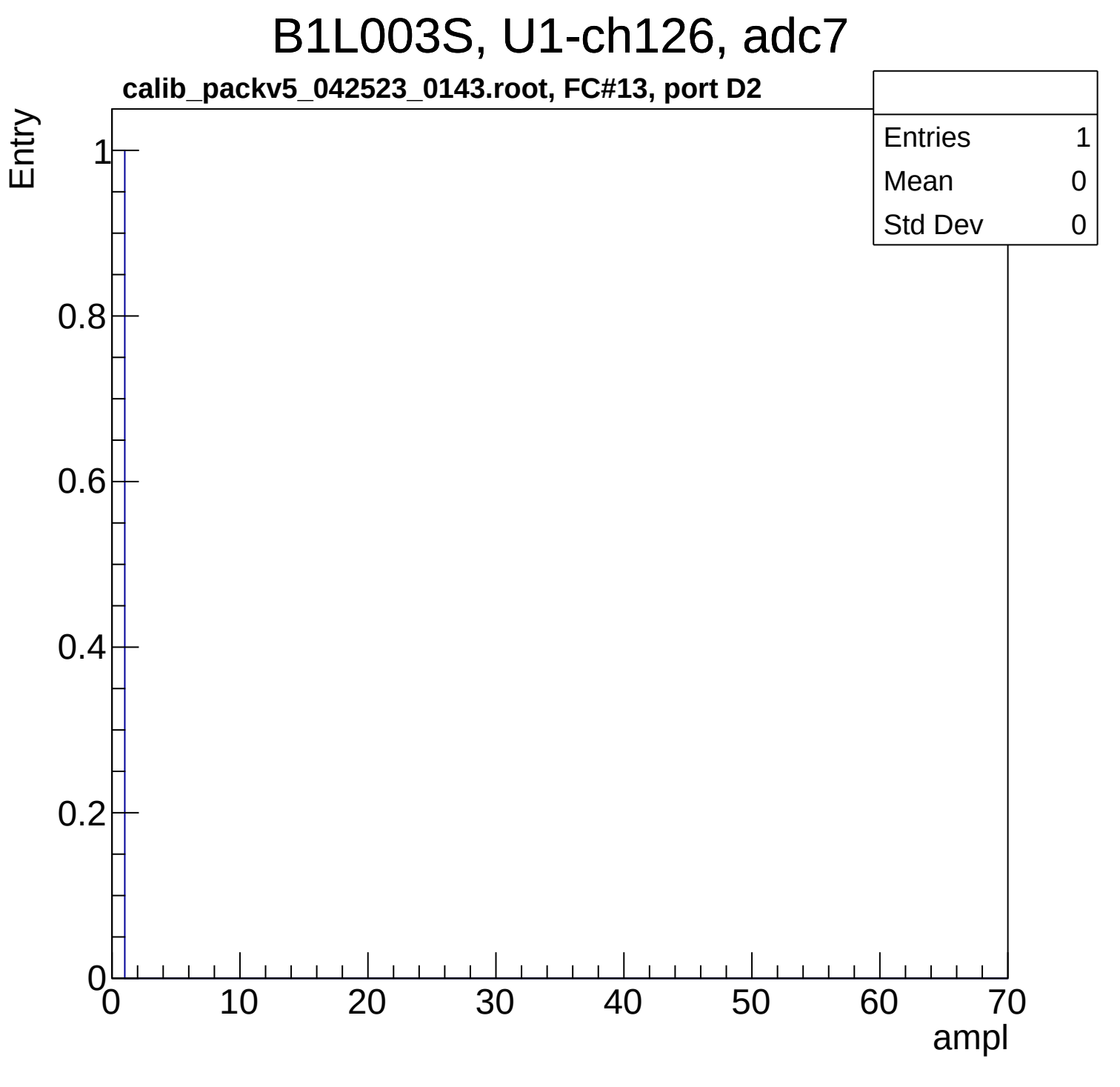
60
70


B1L003S, U1-ch126, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 11
Mean 61.09
Std Dev 2.539
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch126, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 1
Mean 0
Std Dev 0
Entry
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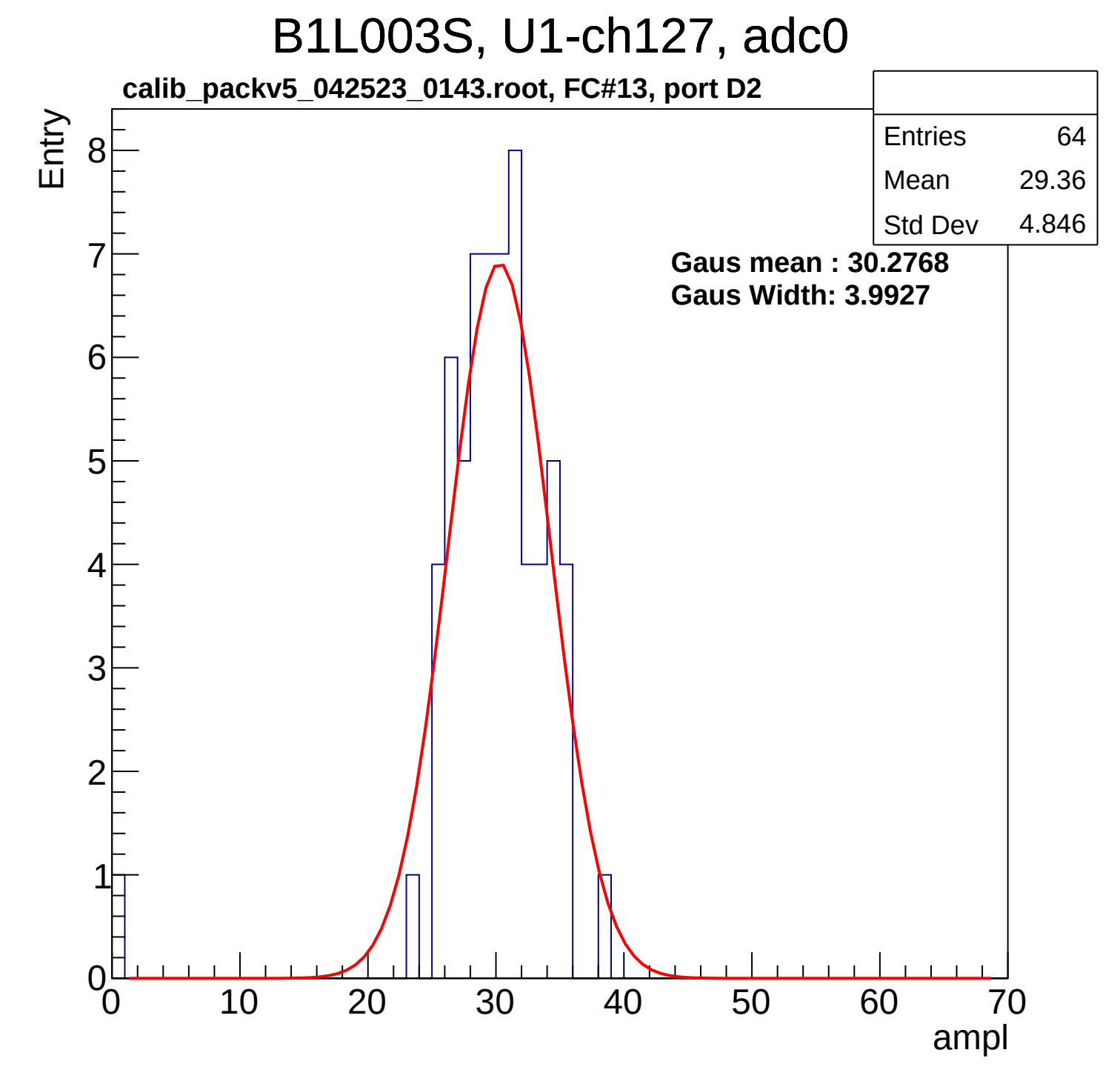
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch127, adc0
calib_packv5_042523_0143.root, FC#13, port D2
Entries 64
Mean 29.36
Std Dev 4.846
Gaus mean : 30.2768
Gaus Width: 3.9927
Entry
ampl
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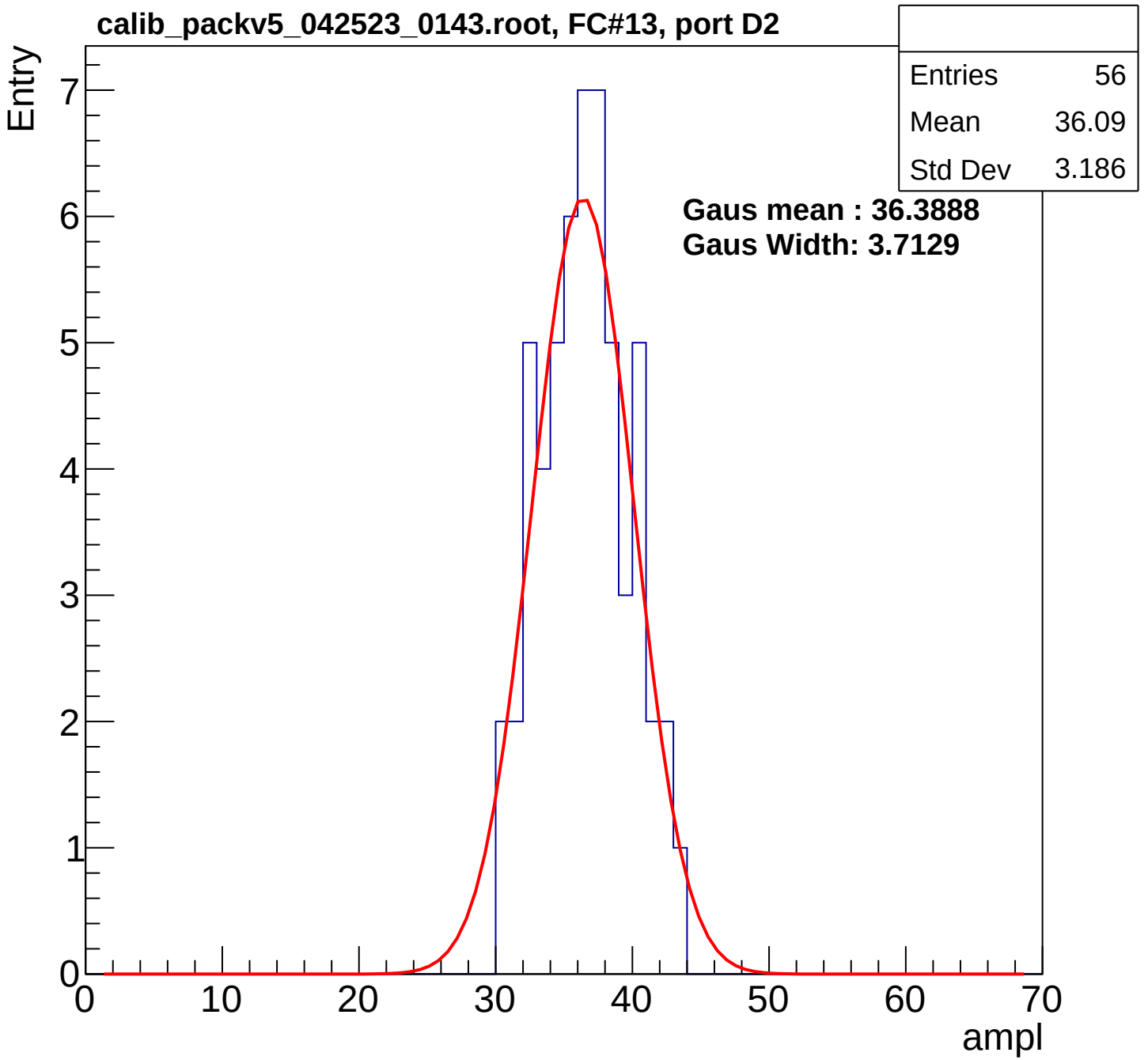
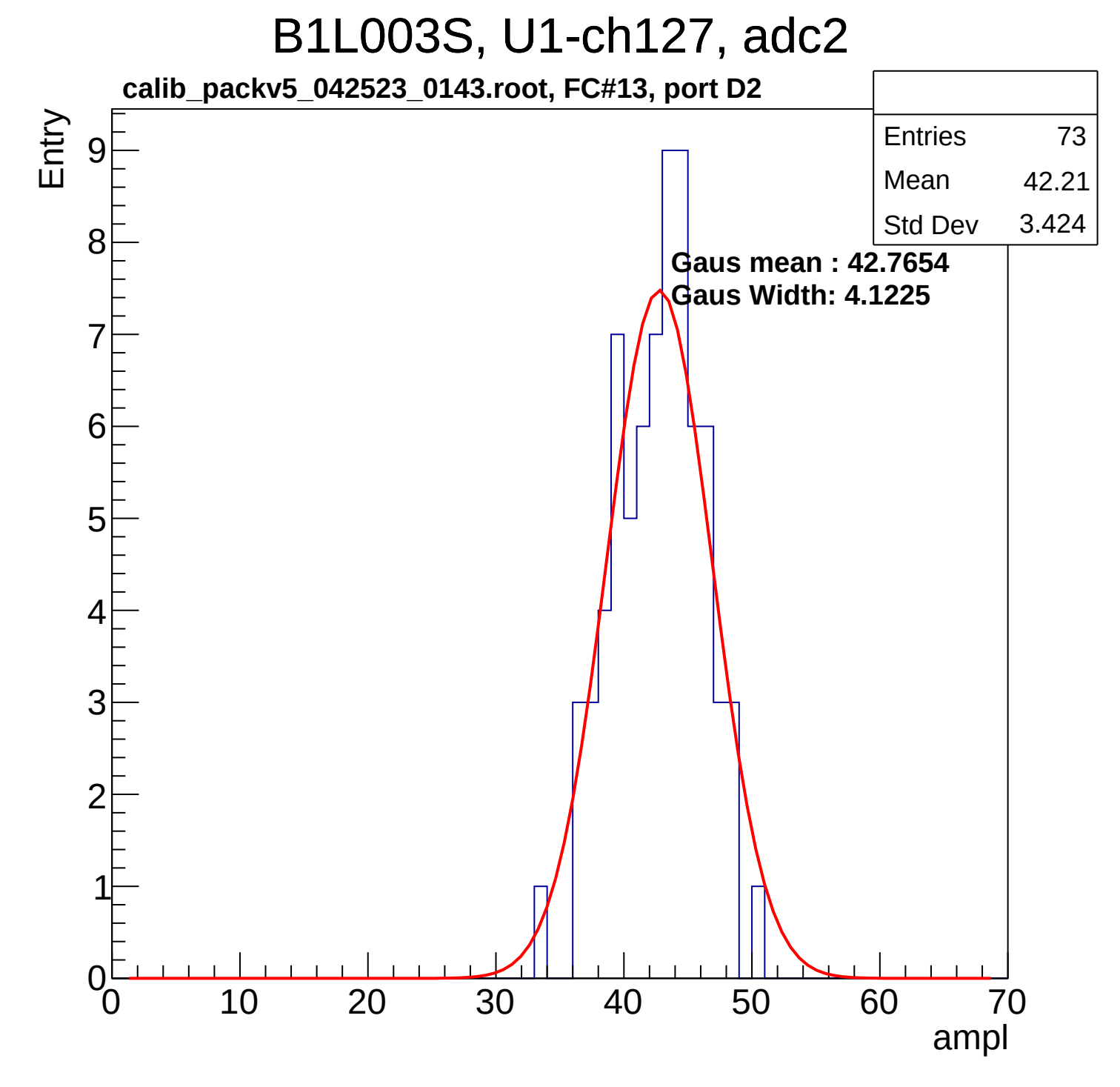
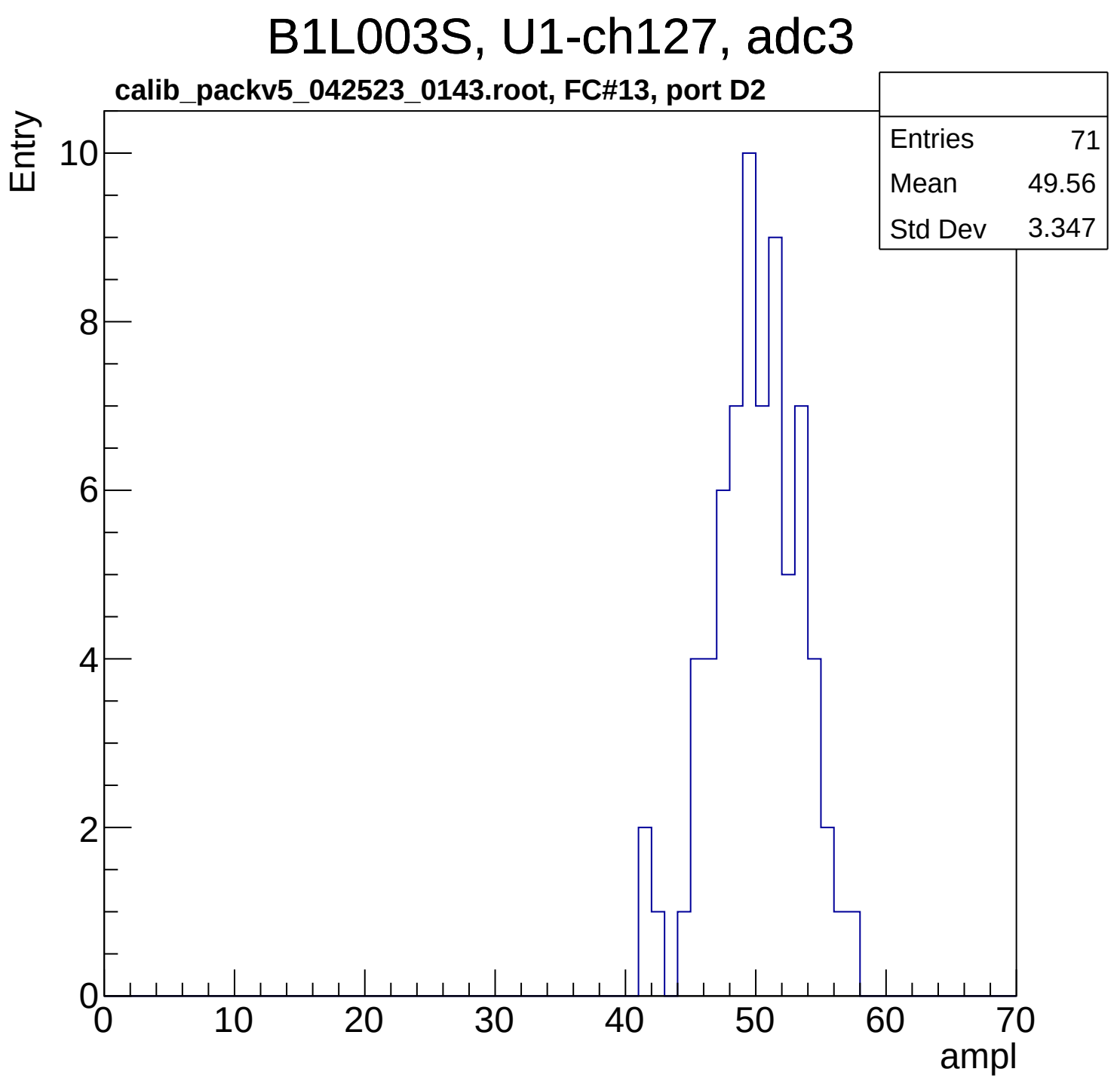
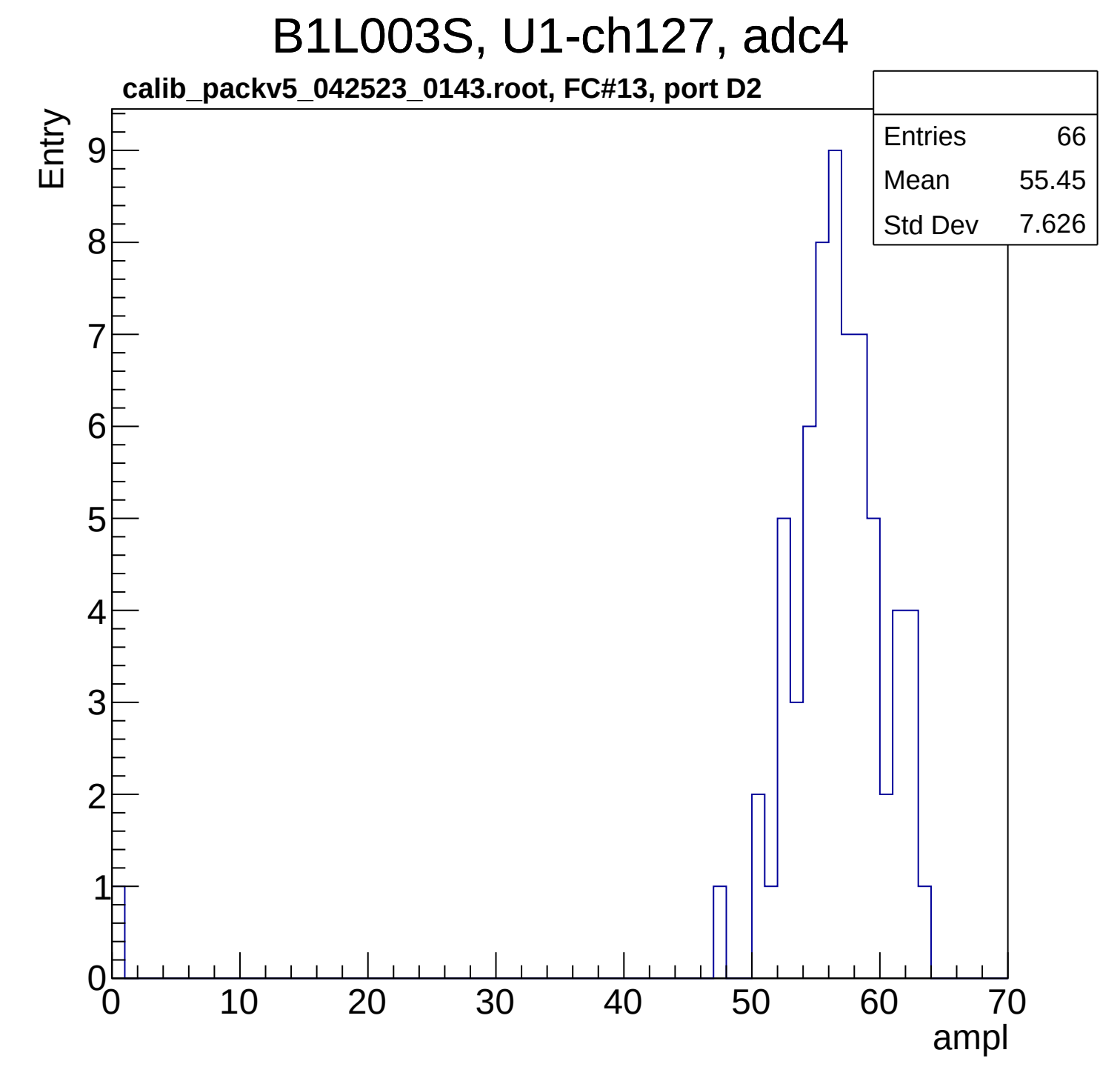
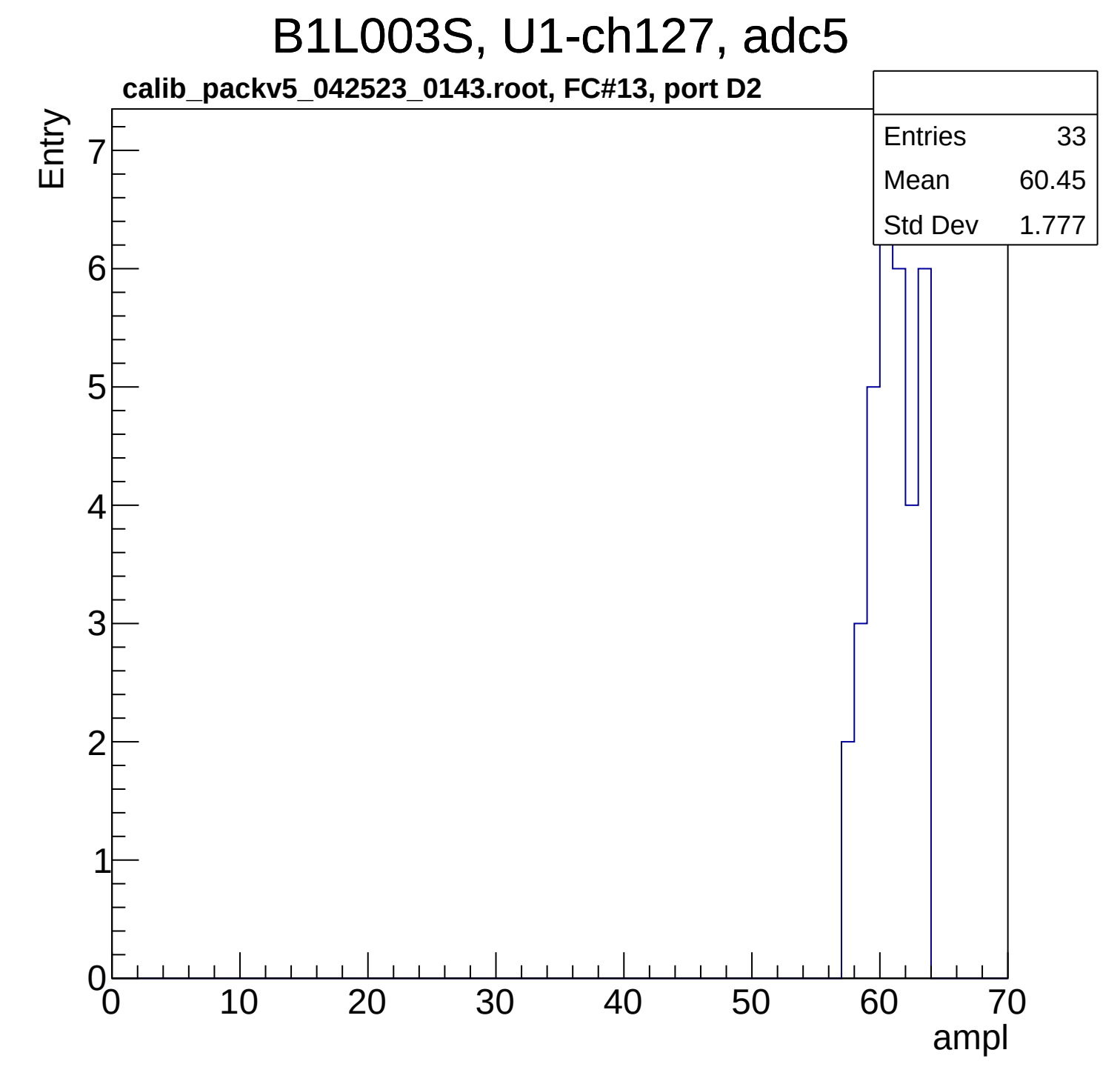
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

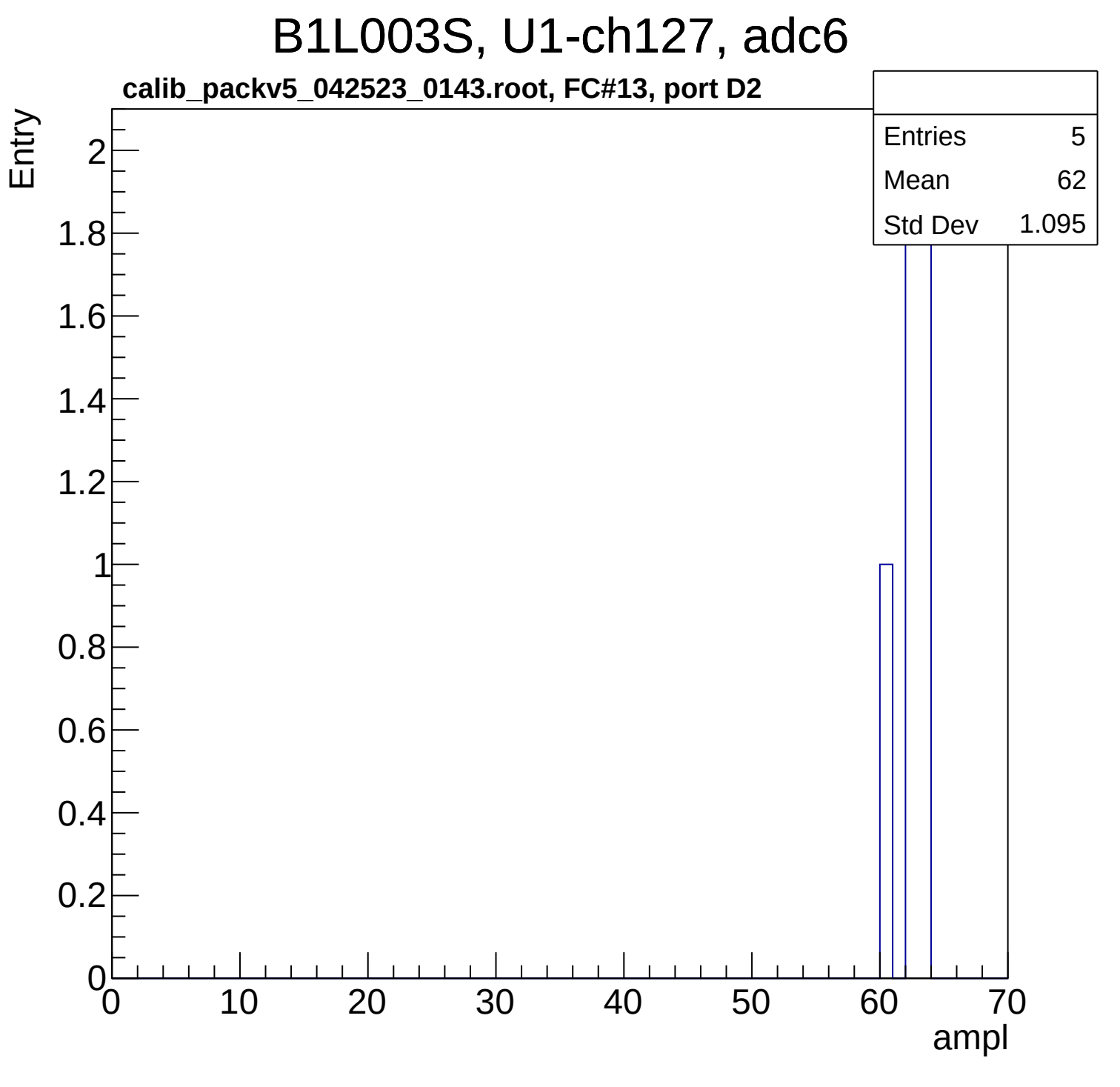

B1L003S, U1-ch127, adc1
calib_packv5_042523_0143.root, FC#13, port D2
Entries 56
Mean 36.09
Std Dev 3.186
Gaus mean : 36.3888
Gaus Width: 3.7129
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

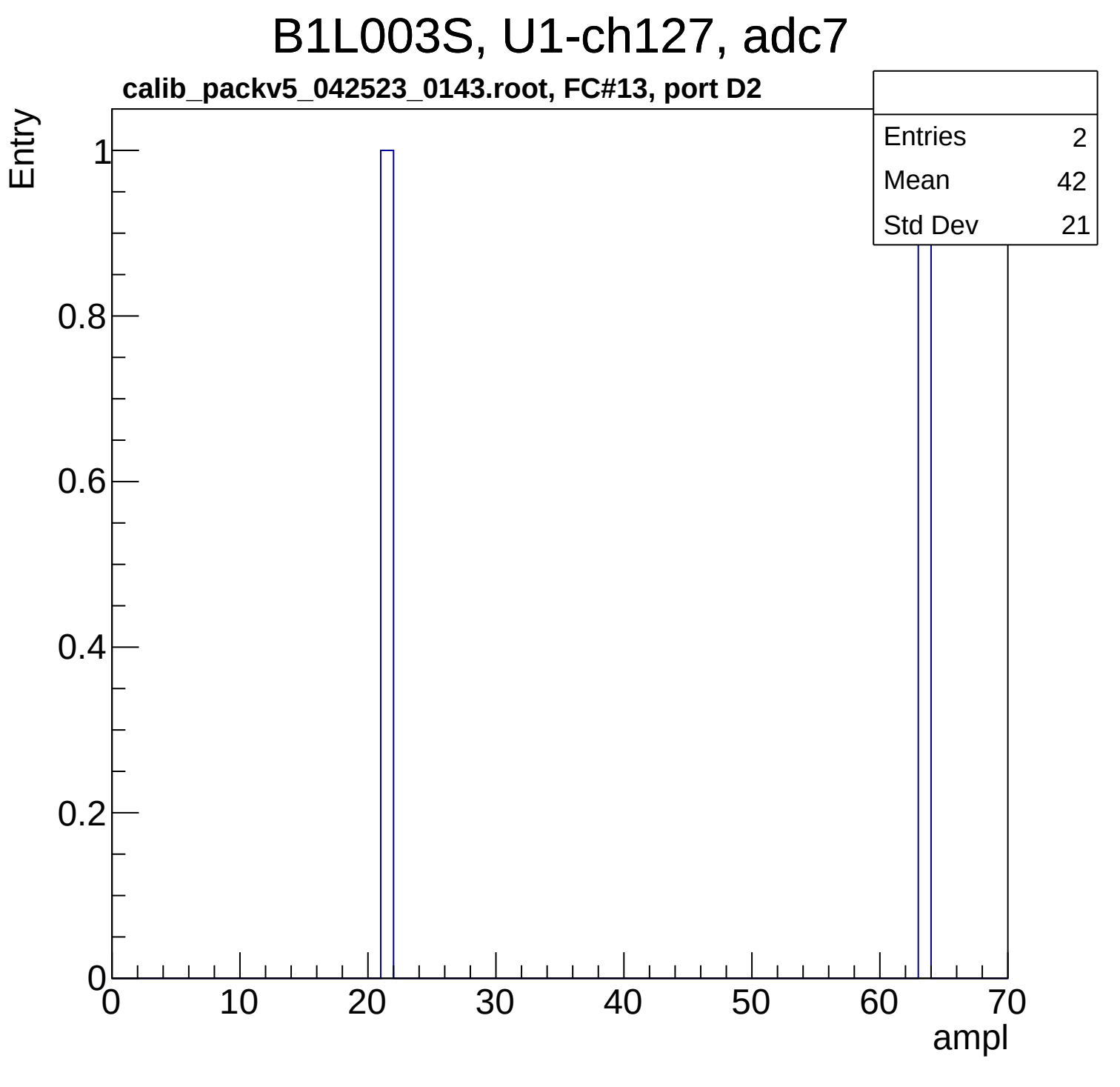

B1L003S, U1-ch127, adc2
calib_packv5_042523_0143.root, FC#13, port D2
Entries 73
Mean 42.21
Std Dev 3.424
Gaus mean : 42.7654
Gaus Width: 4.1225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch127, adc3
calib_packv5_042523_0143.root, FC#13, port D2
Entries 71
Mean 49.56
Std Dev 3.347
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch127, adc4
calib_packv5_042523_0143.root, FC#13, port D2
Entries 66
Mean 55.45
Std Dev 7.626
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L003S, U1-ch127, adc5
calib_packv5_042523_0143.root, FC#13, port D2
Entries 33
Mean 60.45
Std Dev 1.777
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L003S, U1-ch127, adc6
calib_packv5_042523_0143.root, FC#13, port D2
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L003S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 21
Entry
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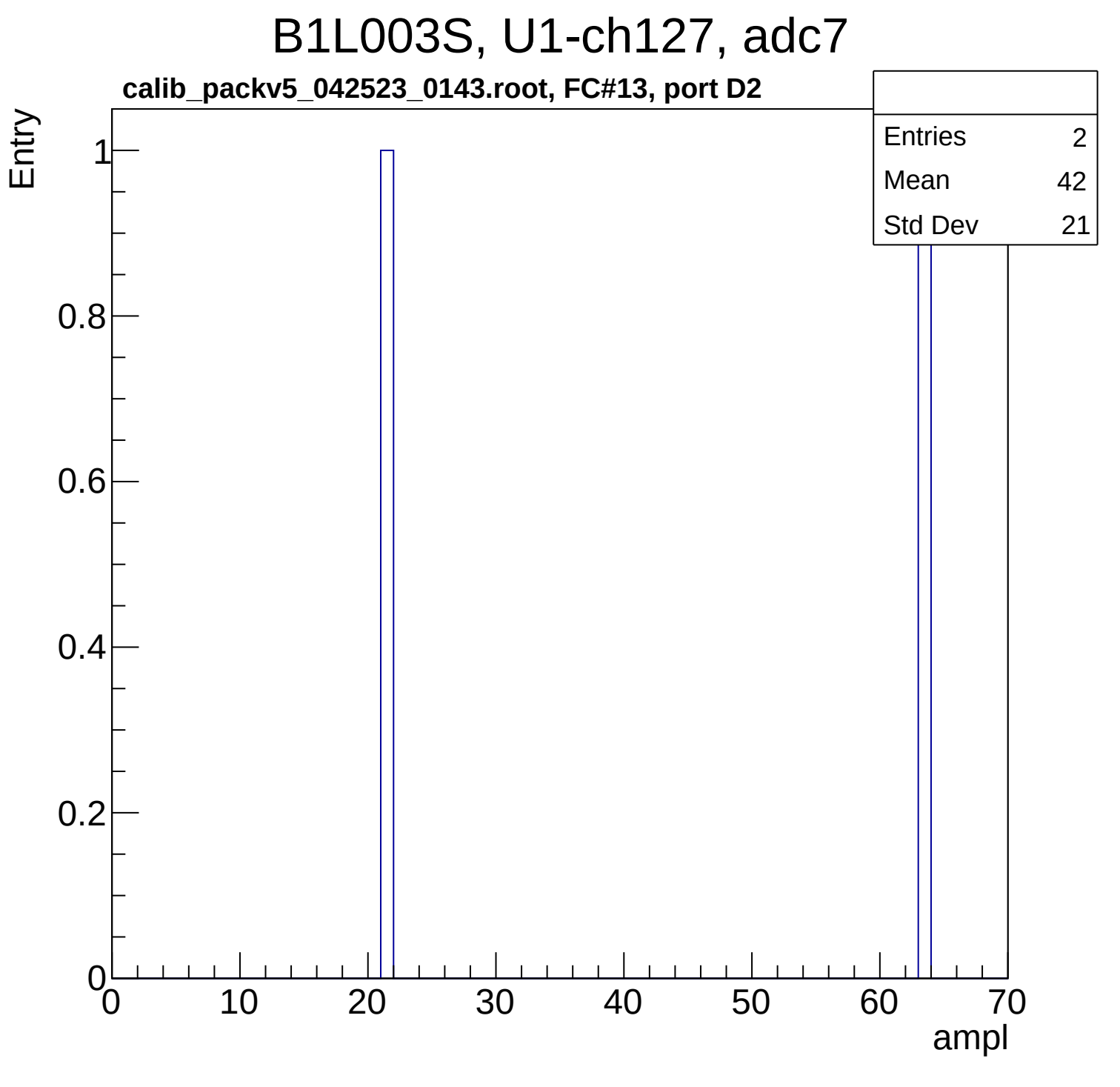
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L003S, U1-ch127, adc7
calib_packv5_042523_0143.root, FC#13, port D2
Entries 2
Mean 42
Std Dev 21
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl