

B0L002S, U20-ch0

calib_packv5_042523_0143.root, FC#8, port C1

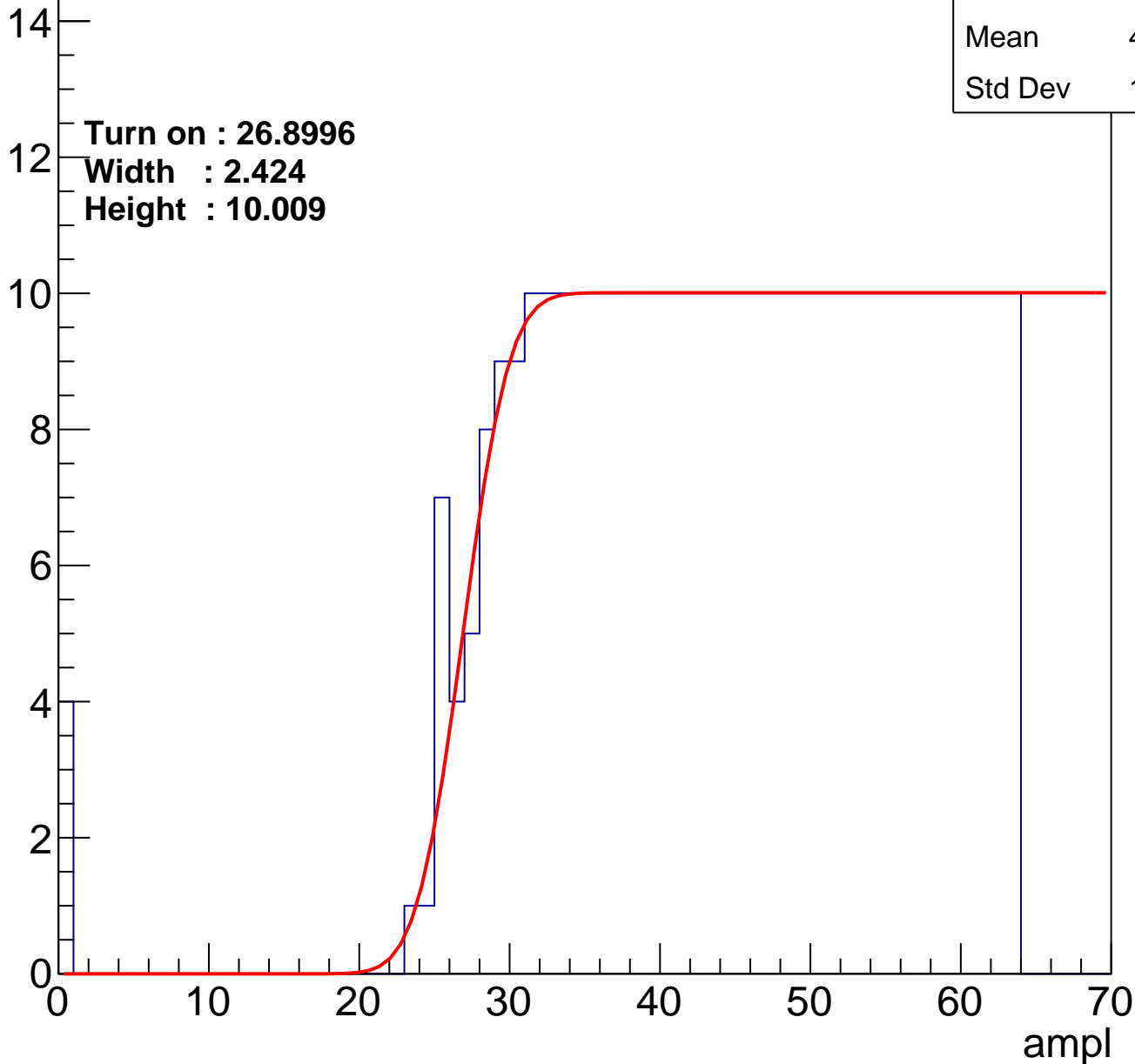
Entry

Entries	378
Mean	44.25
Std Dev	11.79

Turn on : 26.8996

Width : 2.424

Height : 10.009



B0L002S, U20-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.03
Std Dev	10.94

Turn on : 27.7053

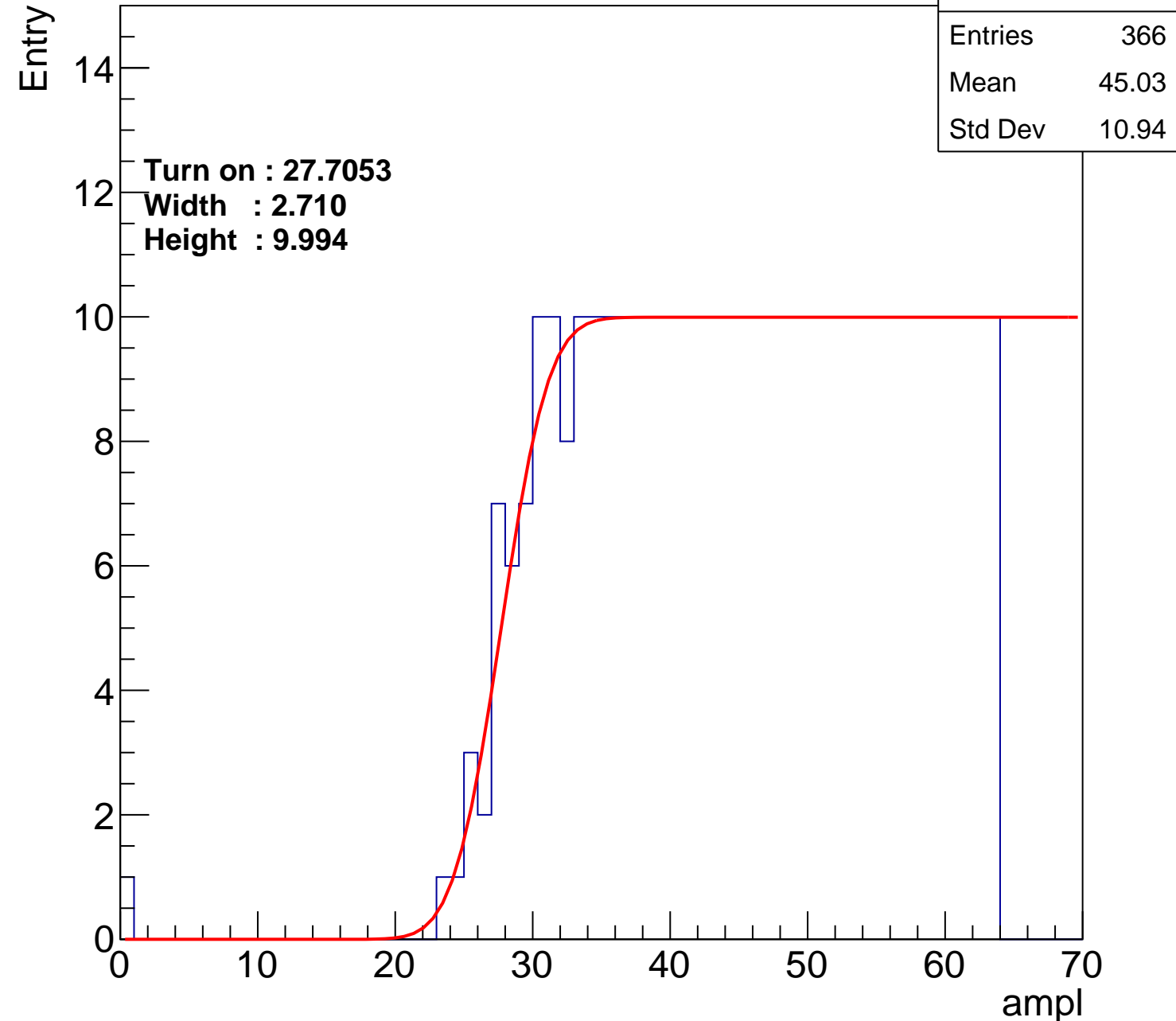
Width : 2.710

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.94
Std Dev	11.12

Turn on : 28.0406

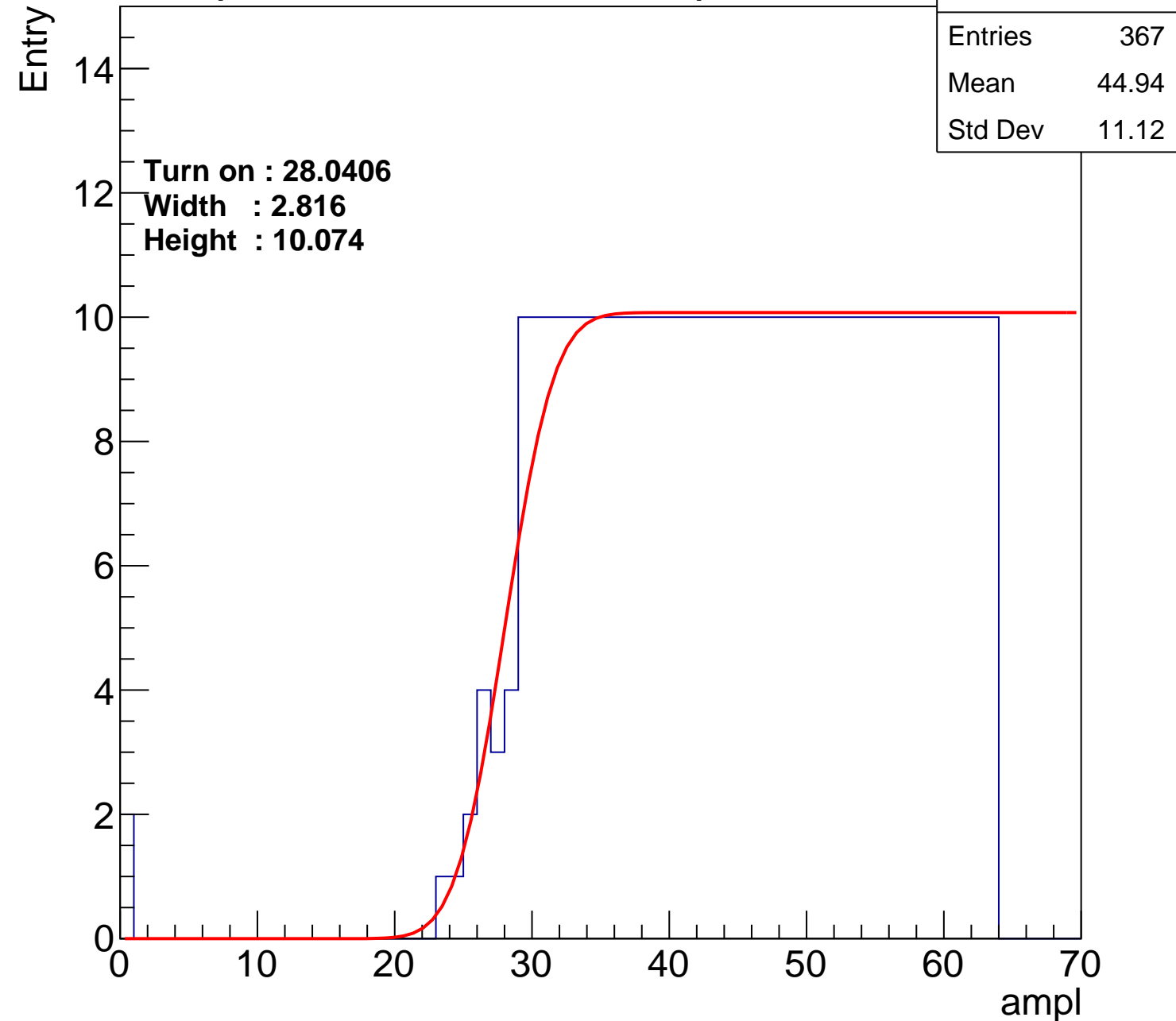
Width : 2.816

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch3

calib_packv5_042523_0143.root, FC#8, port C1

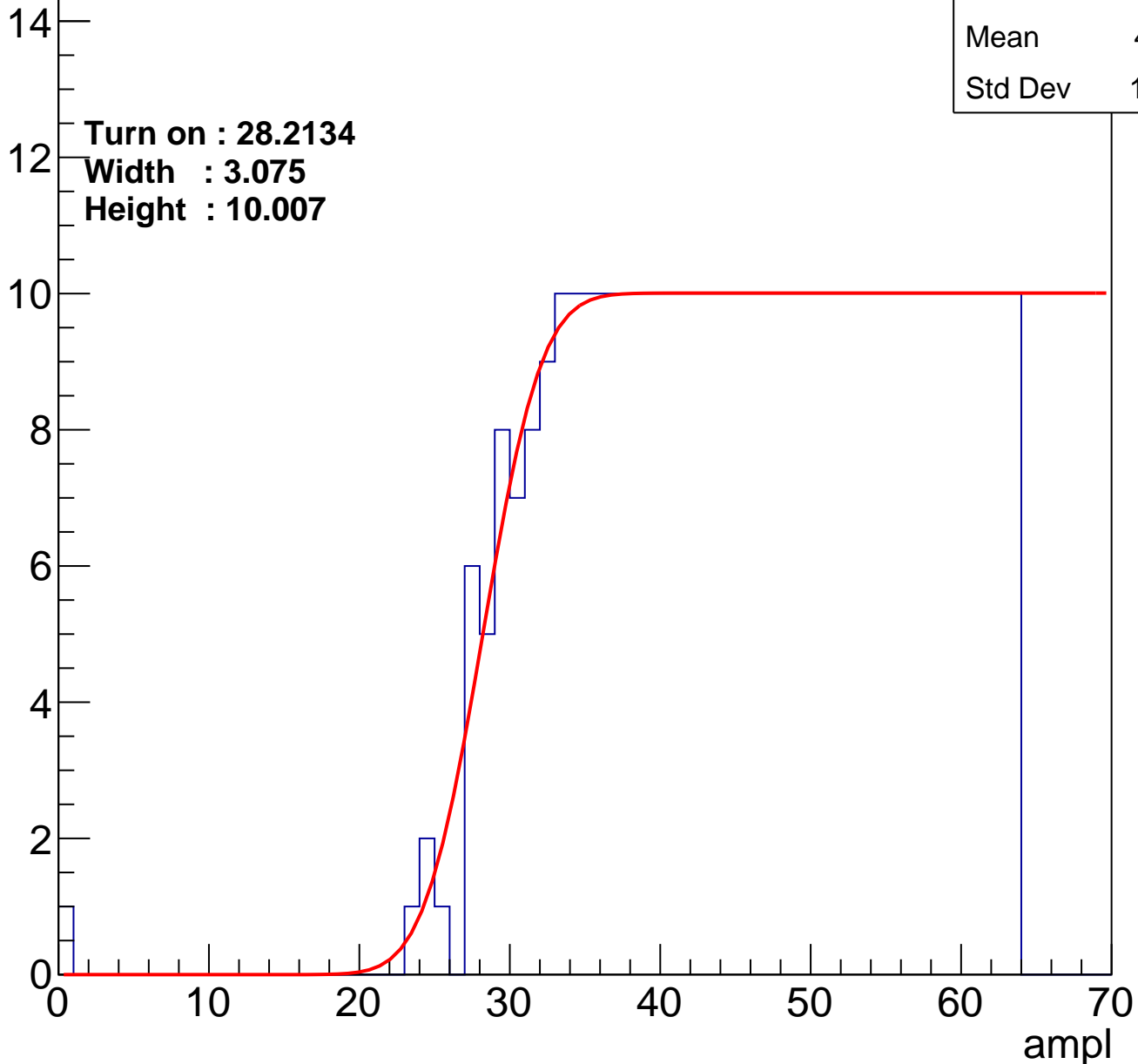
Entries	358
Mean	45.41
Std Dev	10.76

Turn on : 28.2134

Width : 3.075

Height : 10.007

Entry



B0L002S, U20-ch4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.22
Std Dev	11.54

Turn on : 26.5247

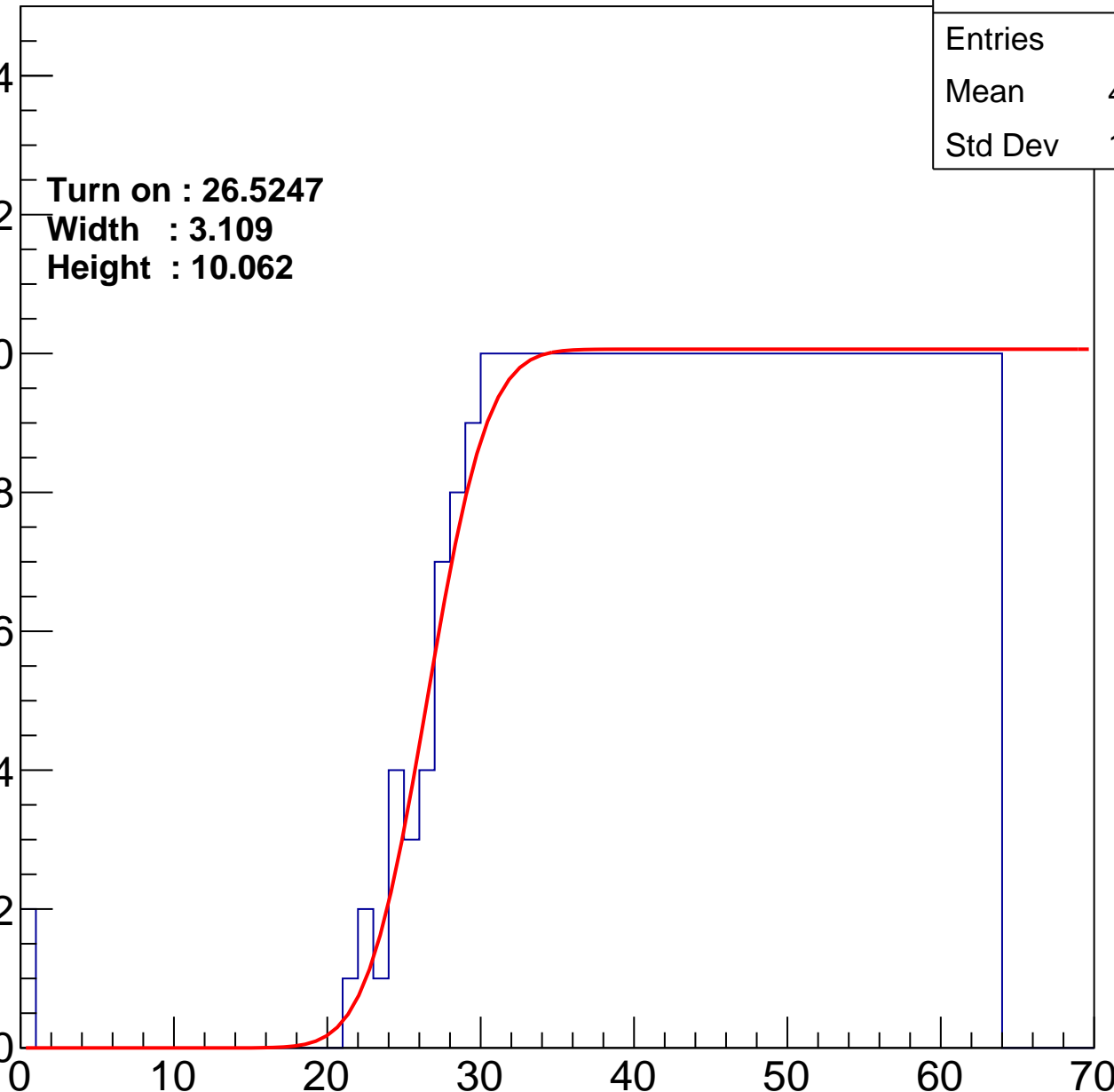
Width : 3.109

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.25
Std Dev	10.8

Turn on : 27.5883

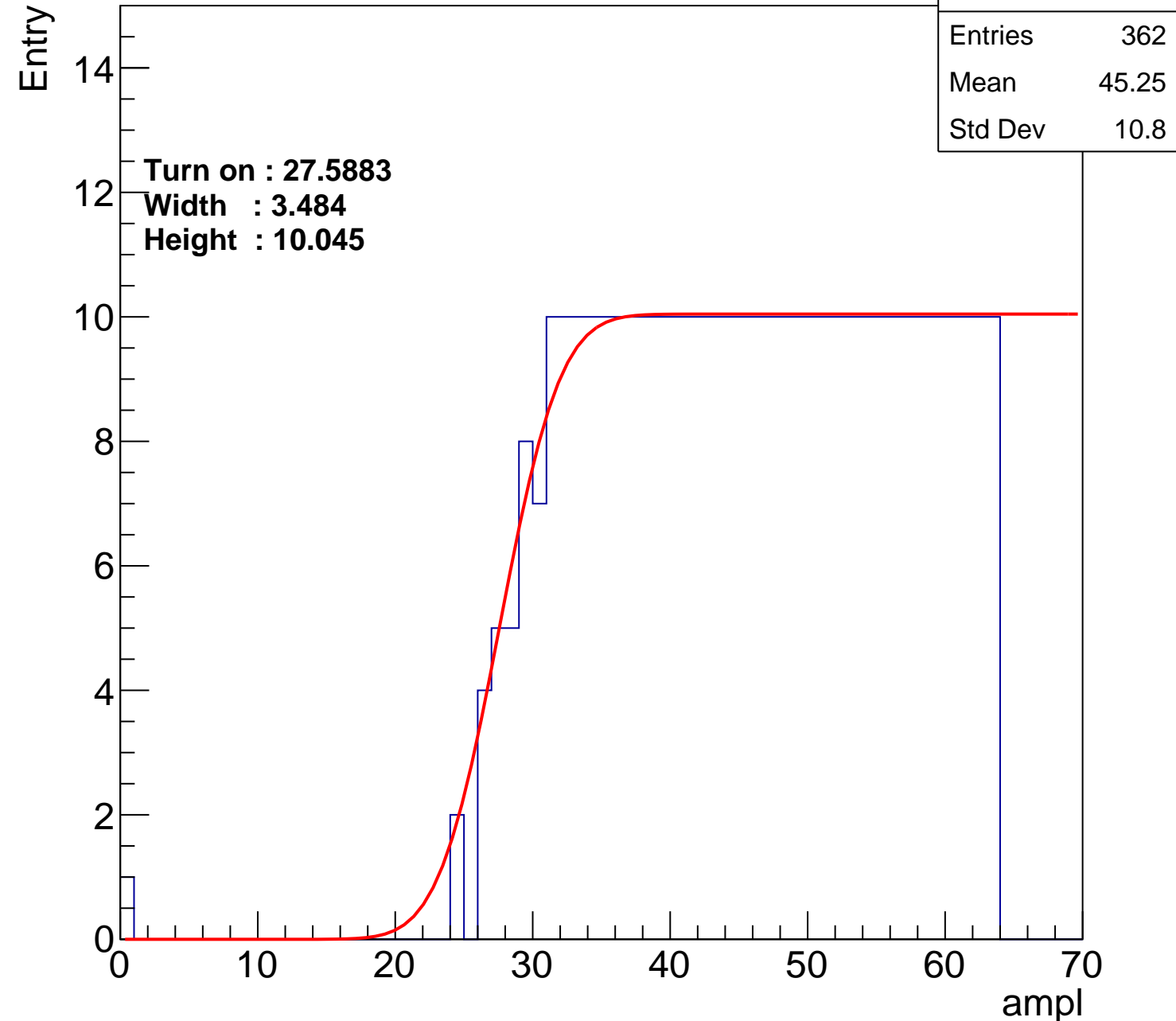
Width : 3.484

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch6

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.05
Std Dev	11.71

Turn on : 25.3725

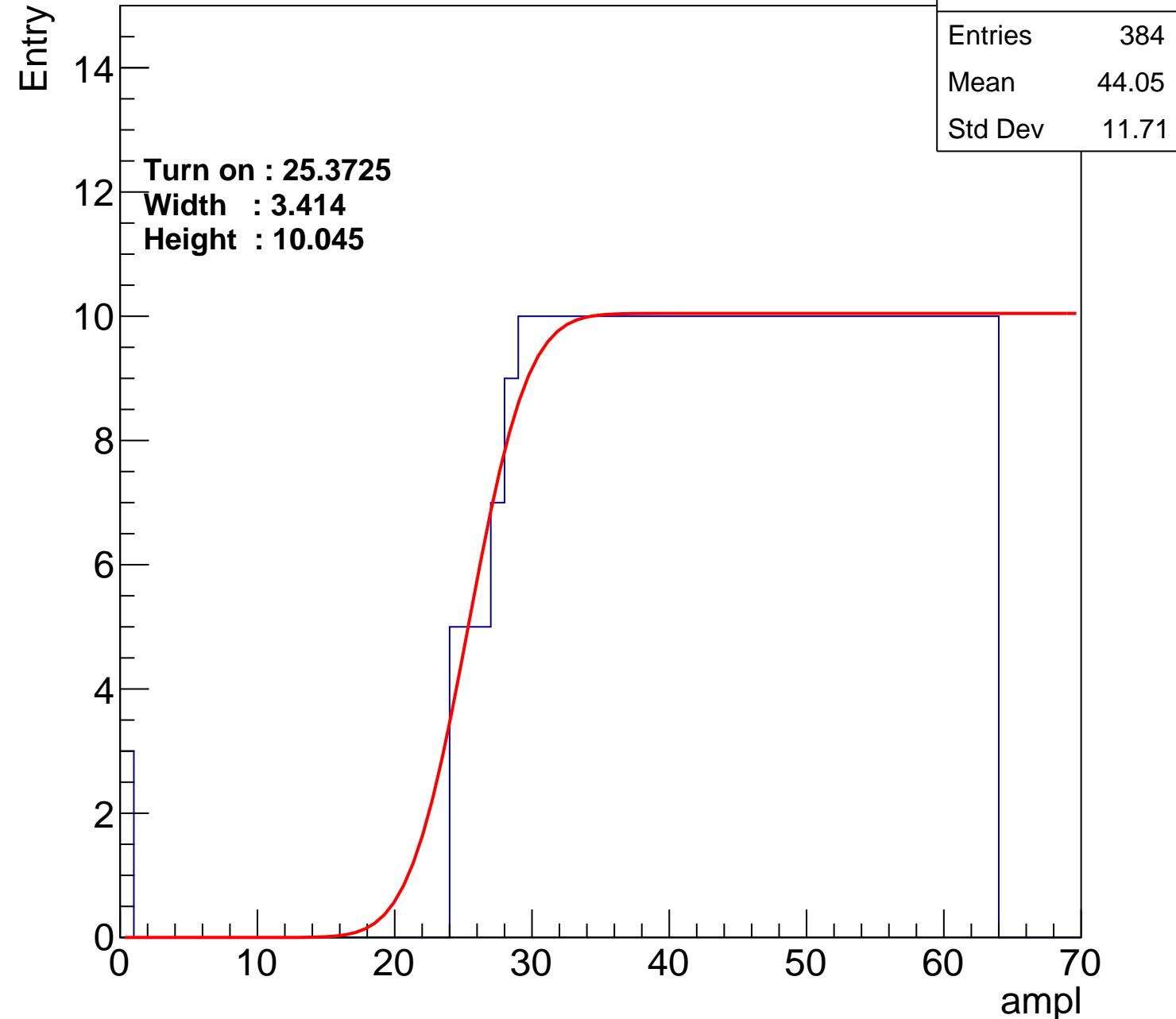
Width : 3.414

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch7

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.8220

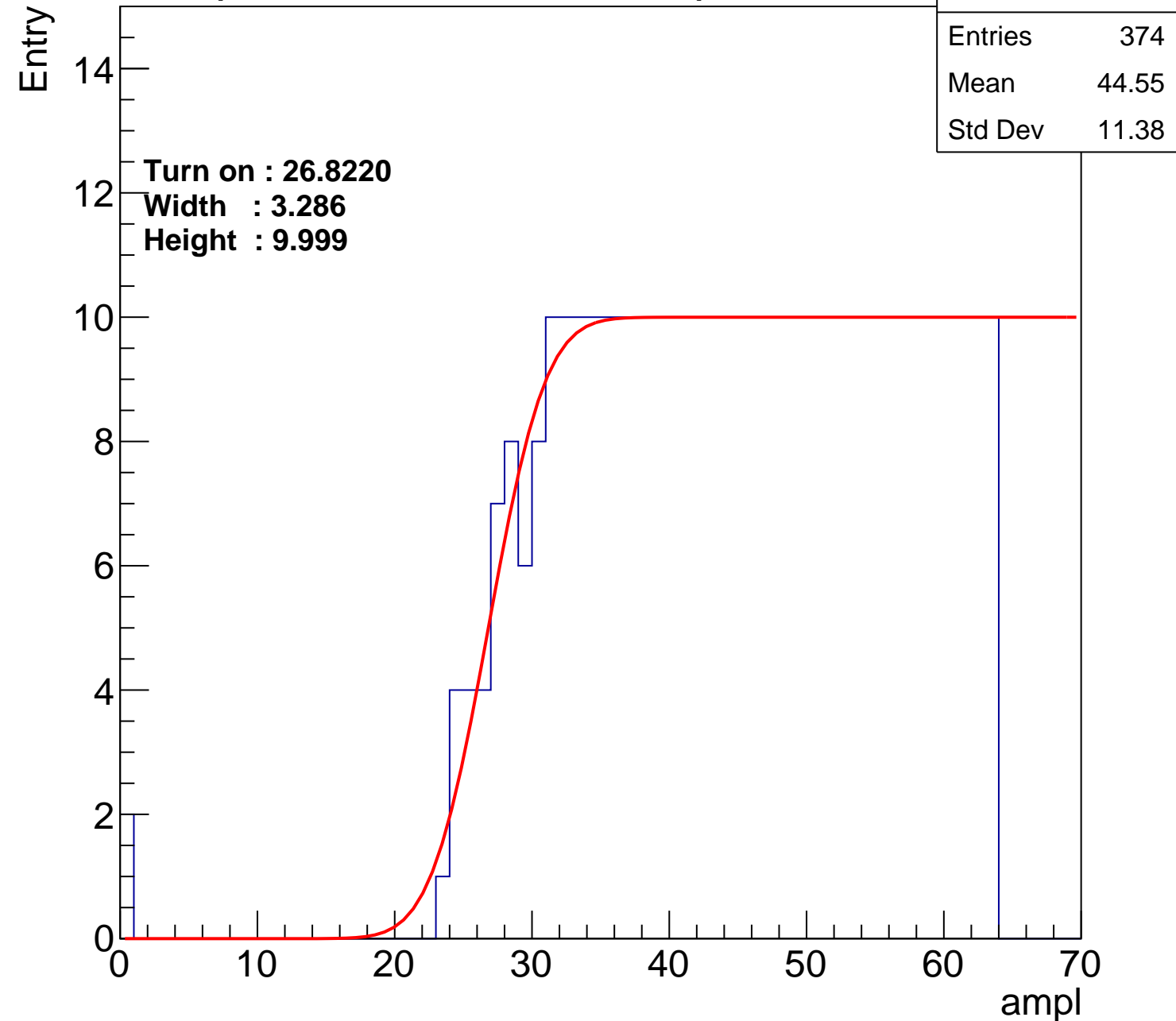
Width : 3.286

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch8

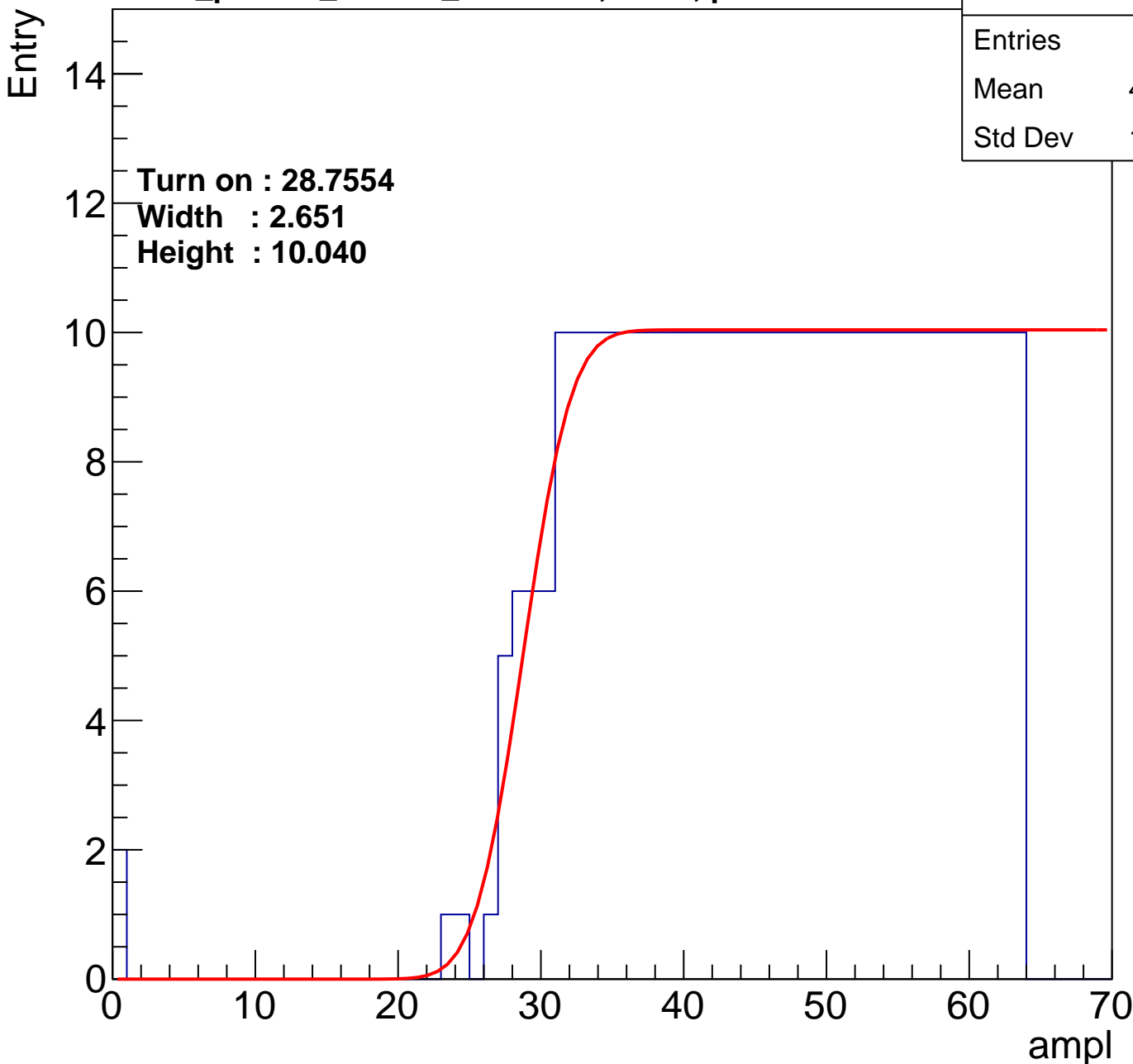
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.7554

Width : 2.651

Height : 10.040

Entries	358
Mean	45.36
Std Dev	10.93



B0L002S, U20-ch9

calib_packv5_042523_0143.root, FC#8, port C1

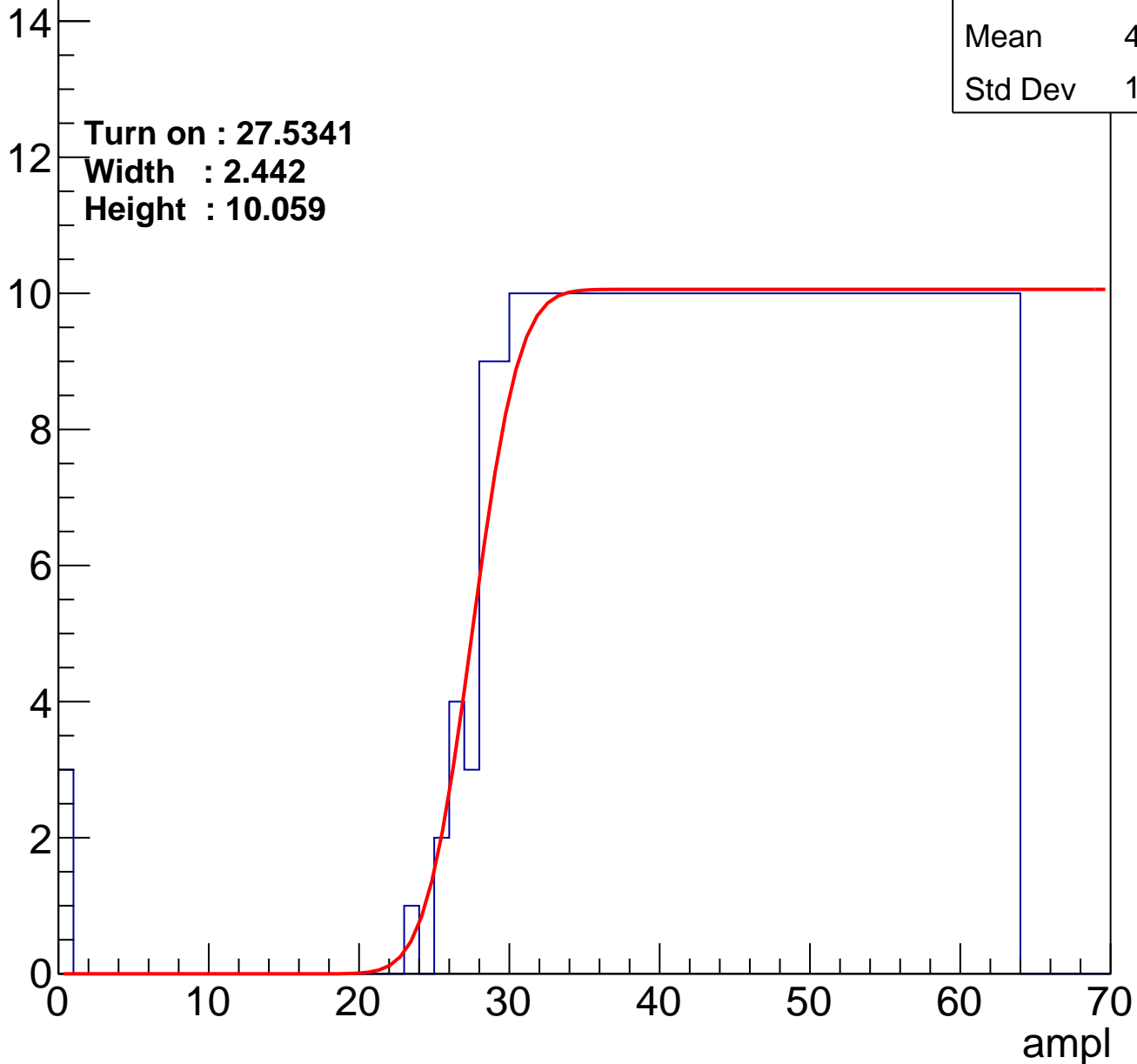
Entry

Entries	371
Mean	44.69
Std Dev	11.39

Turn on : 27.5341

Width : 2.442

Height : 10.059



B0L002S, U20-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.29
Std Dev	11.53

Turn on : 26.2646

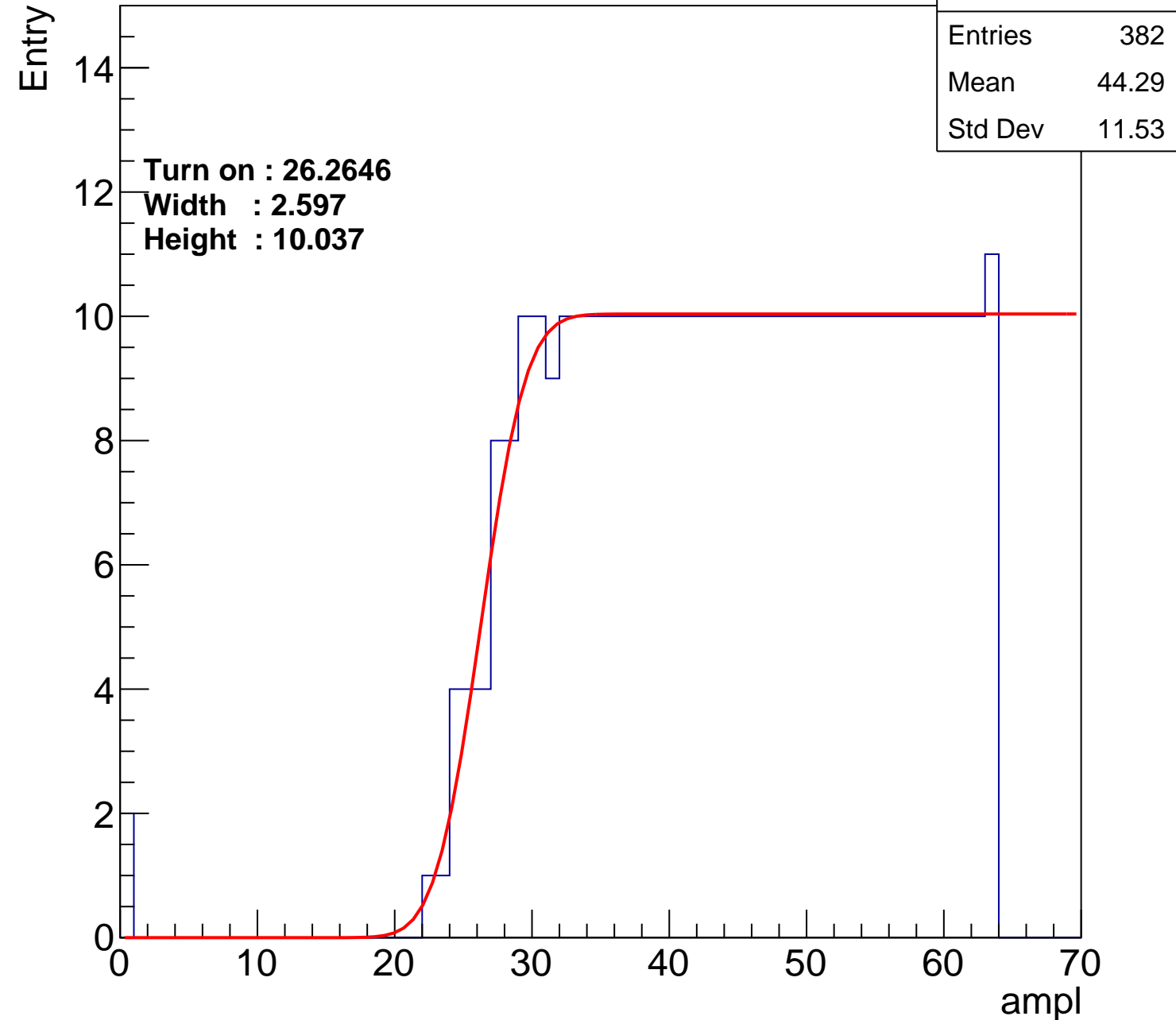
Width : 2.597

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
---------	-----

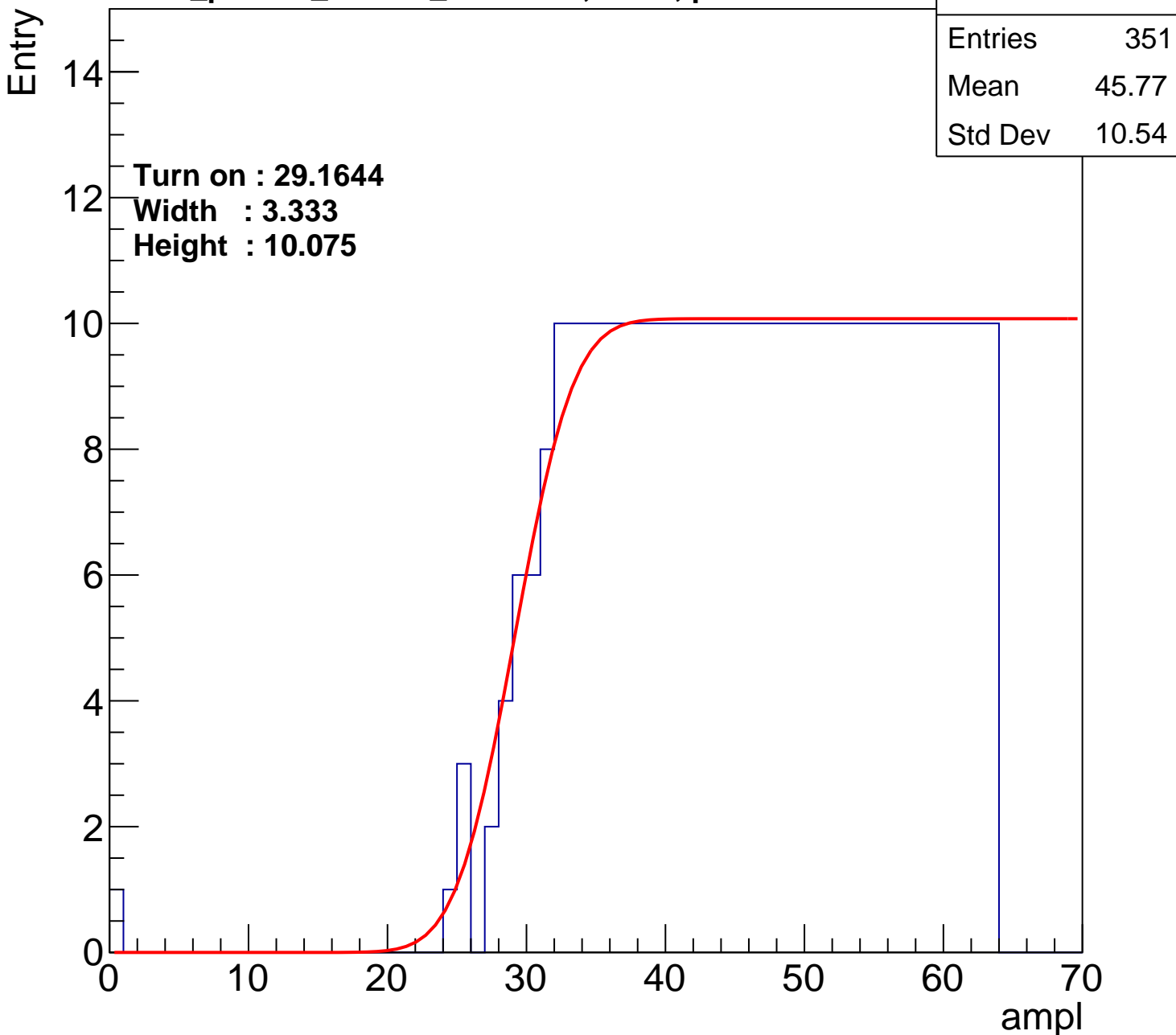
Mean	45.77
------	-------

Std Dev	10.54
---------	-------

Turn on : 29.1644

Width : 3.333

Height : 10.075



B0L002S, U20-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.78
Std Dev	11.42

Turn on : 27.8381

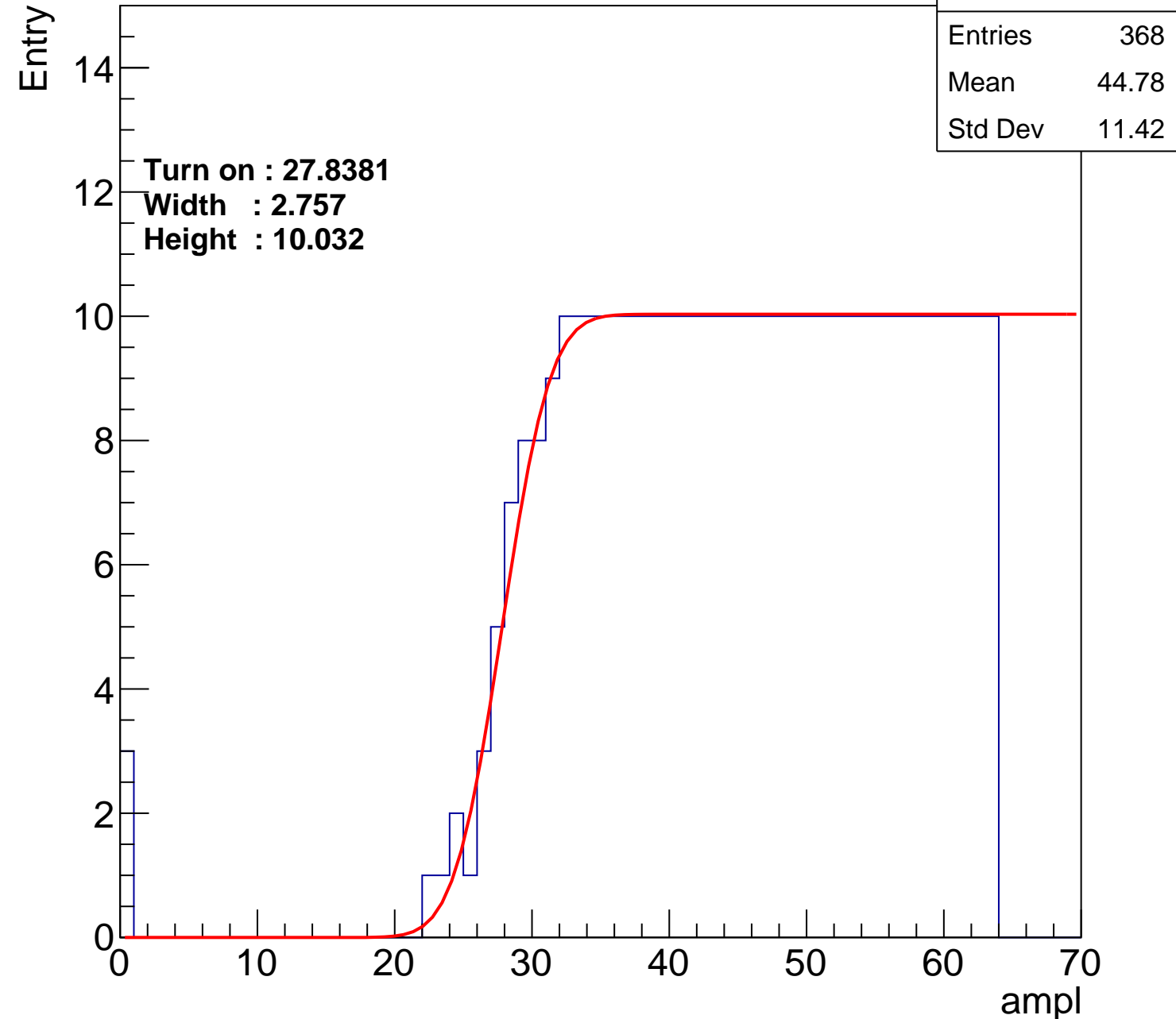
Width : 2.757

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.15

Turn on : 27.5597

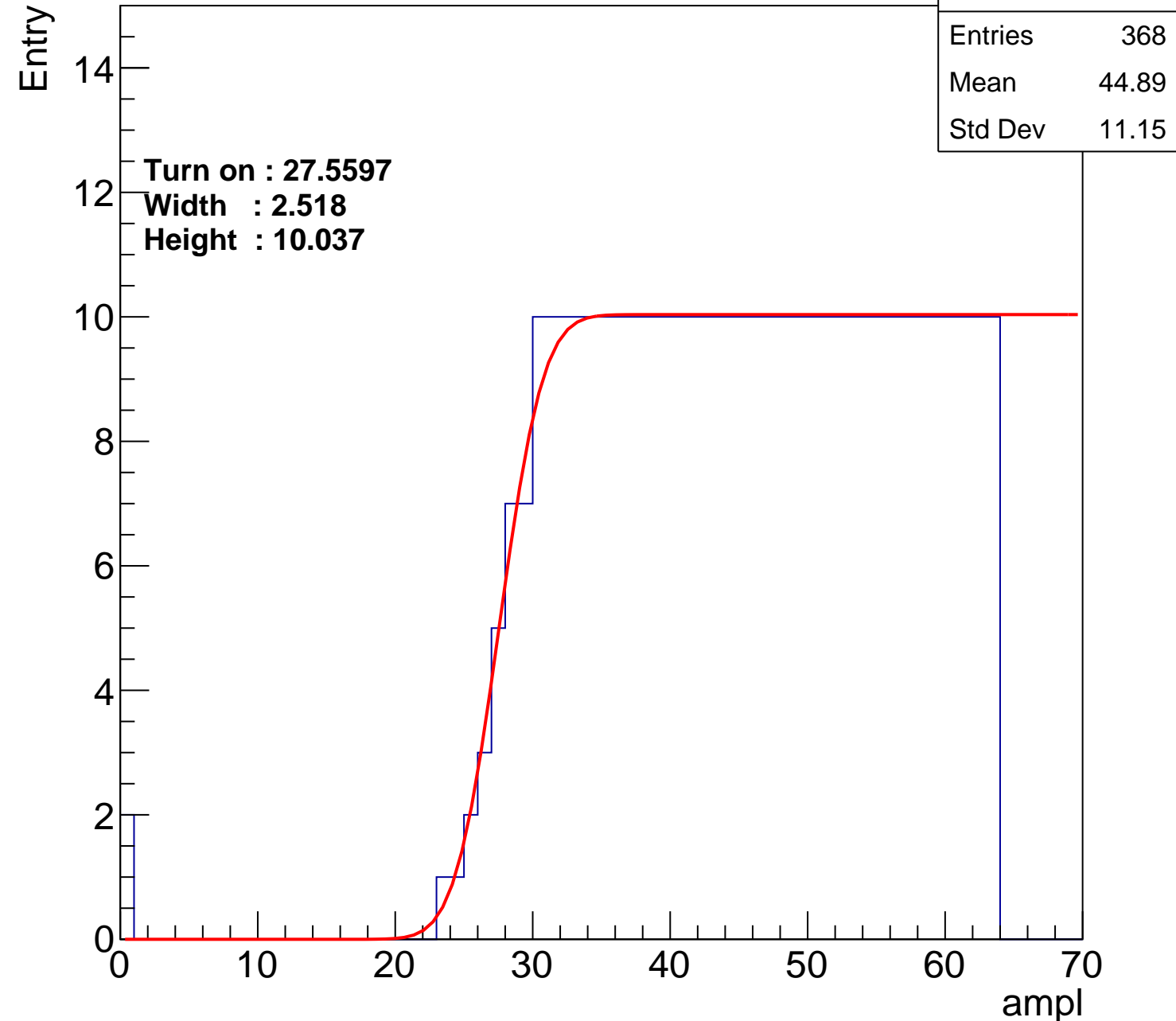
Width : 2.518

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.71
Std Dev	11.41

Turn on : 27.7011

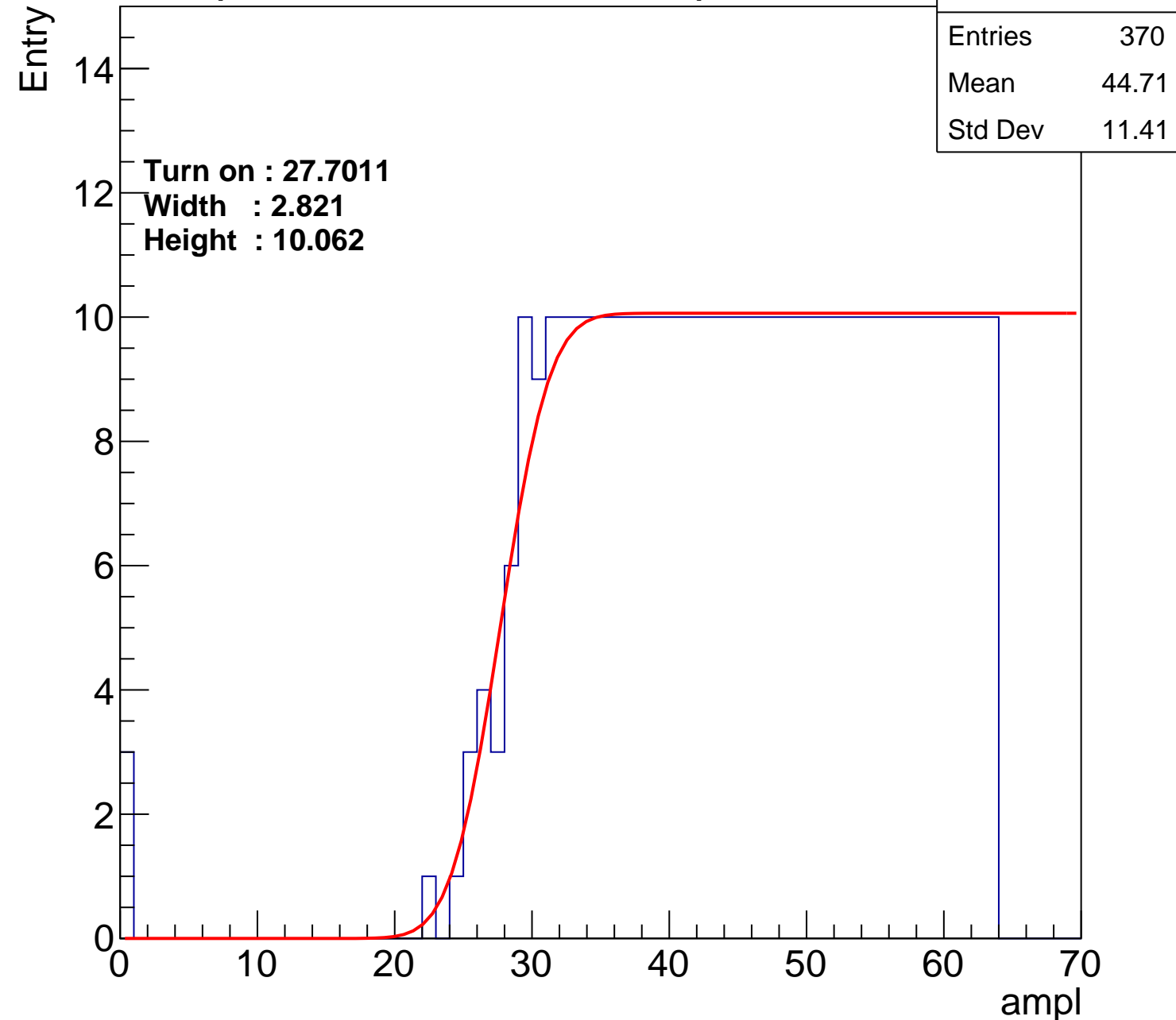
Width : 2.821

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.9189

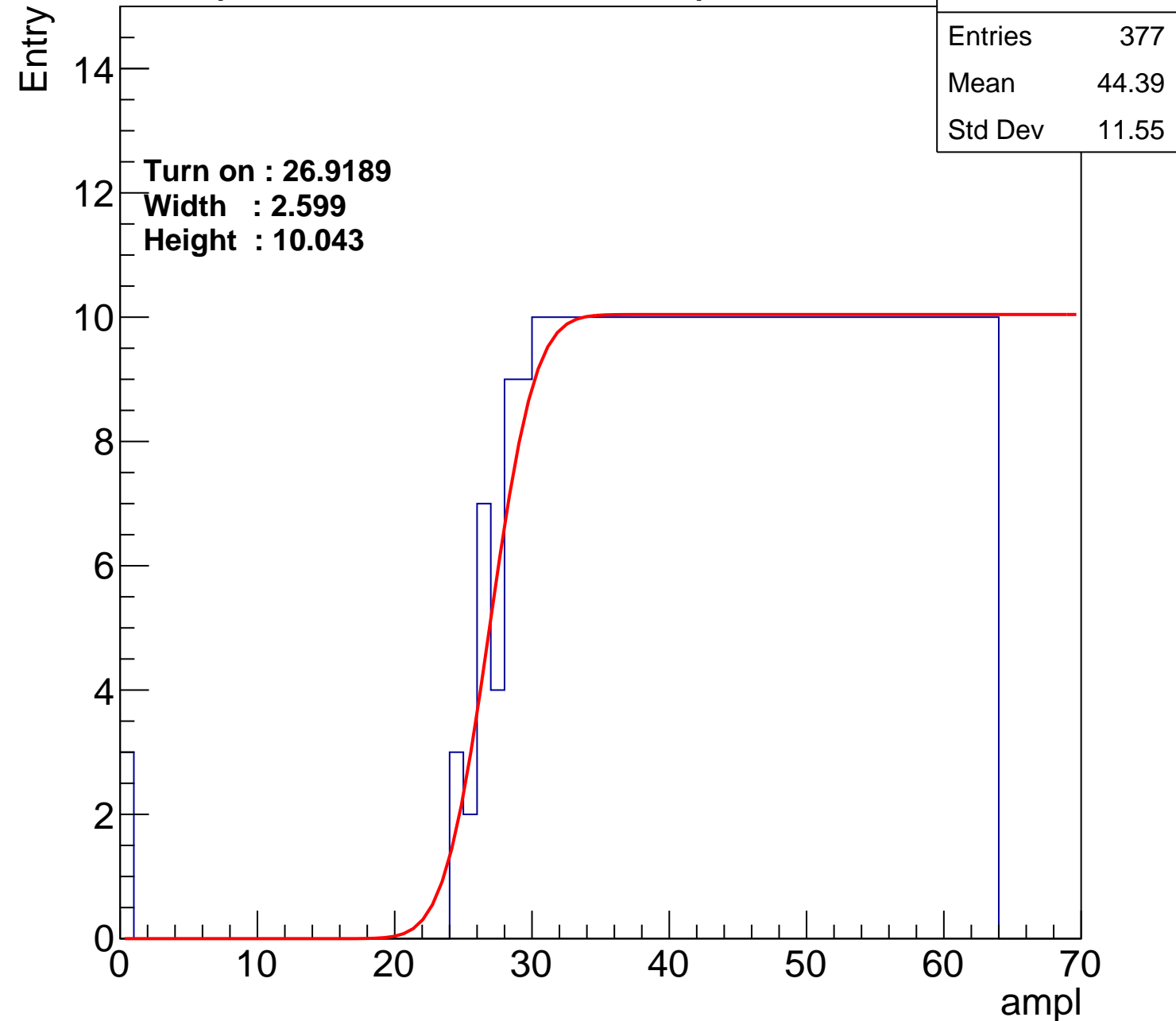
Width : 2.599

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.56
Std Dev	10.82

Turn on : 28.8895

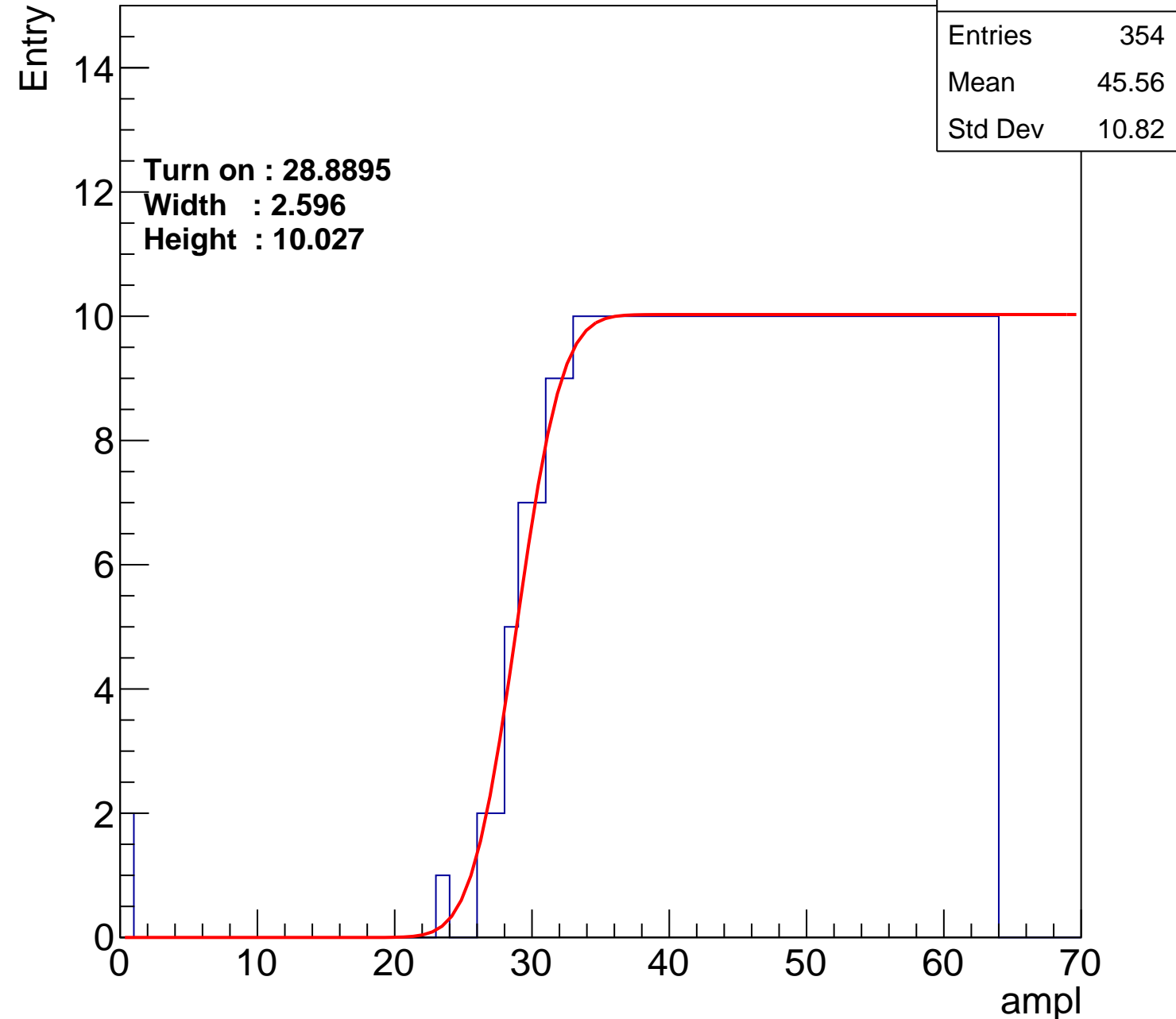
Width : 2.596

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch17

calib_packv5_042523_0143.root, FC#8, port C1

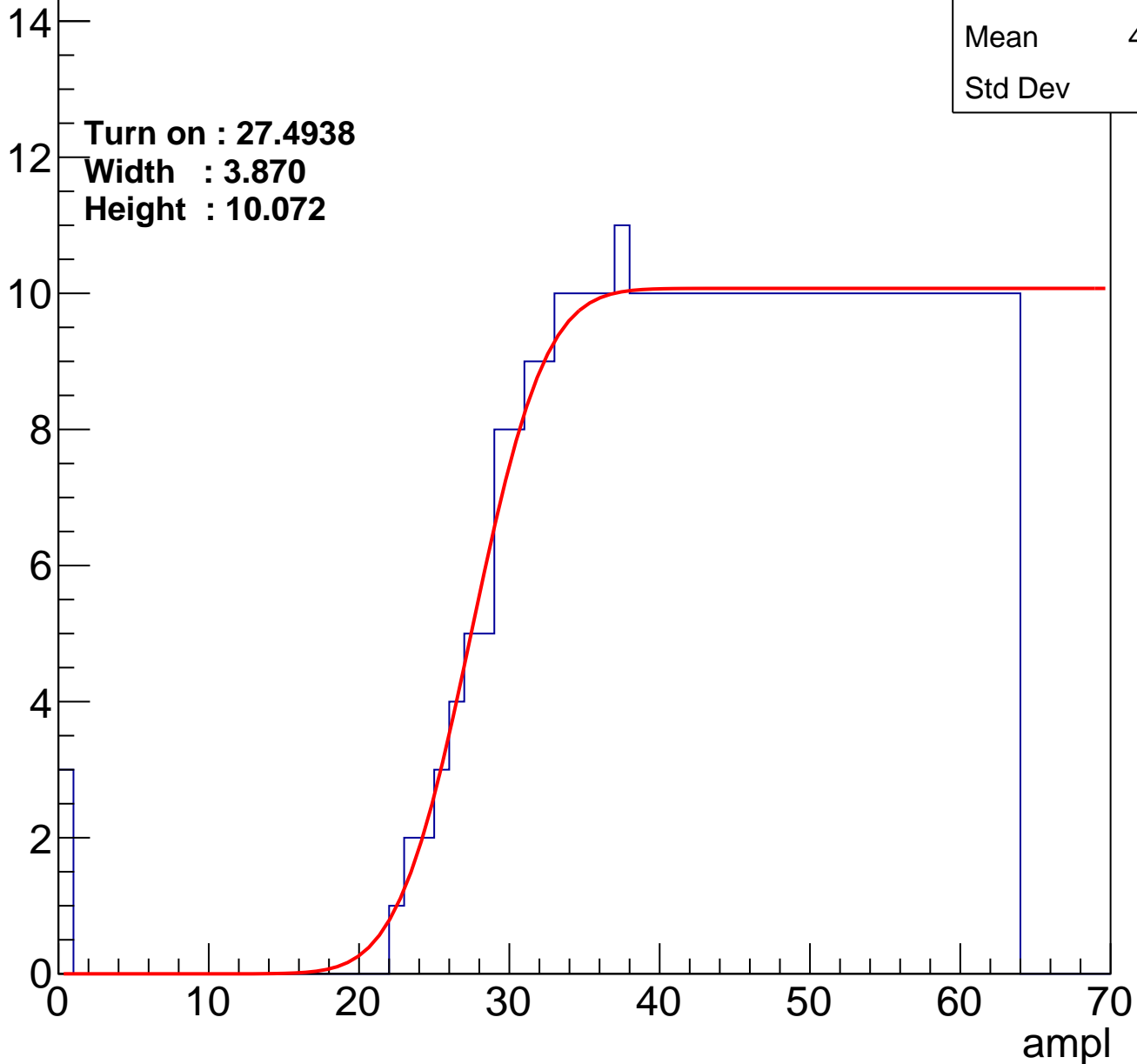
Entries	370
Mean	44.66
Std Dev	11.5

Turn on : 27.4938

Width : 3.870

Height : 10.072

Entry



B0L002S, U20-ch18

calib_packv5_042523_0143.root, FC#8, port C1

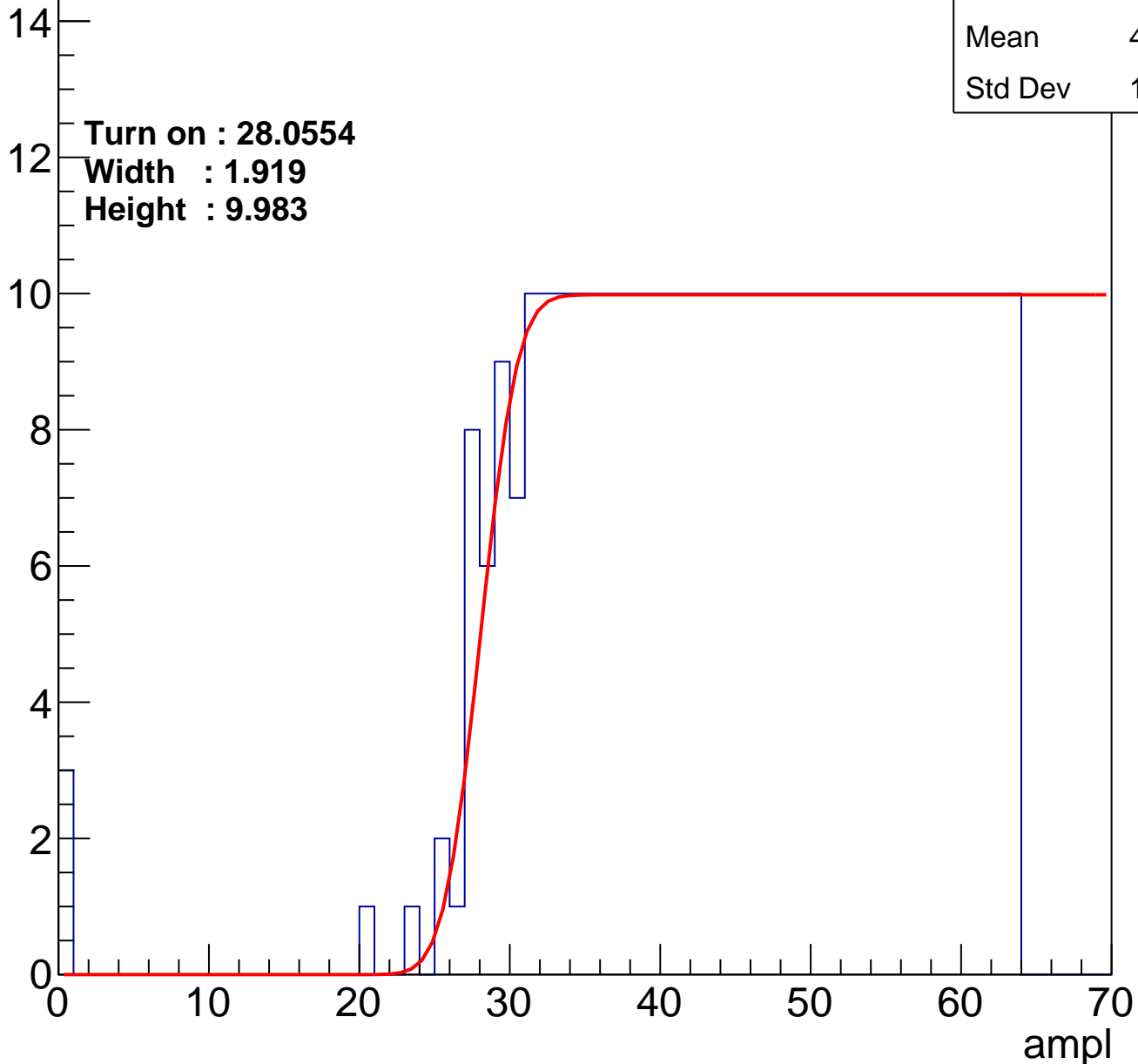
Entries	368
Mean	44.79
Std Dev	11.39

Turn on : 28.0554

Width : 1.919

Height : 9.983

Entry



B0L002S, U20-ch19

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.82
Std Dev	10.51

Turn on : 29.1159

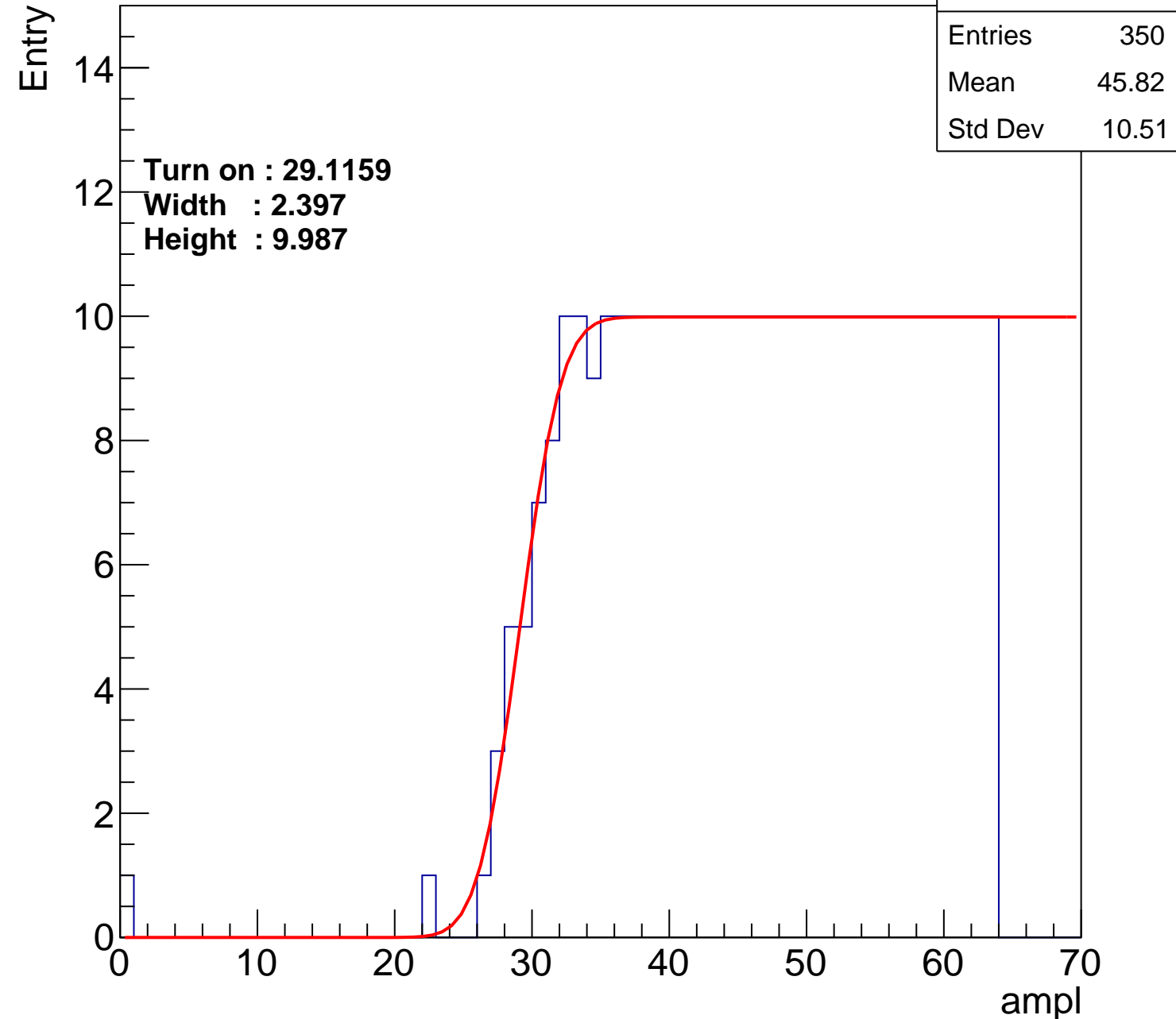
Width : 2.397

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.58
Std Dev	10.86

Turn on : 29.3301

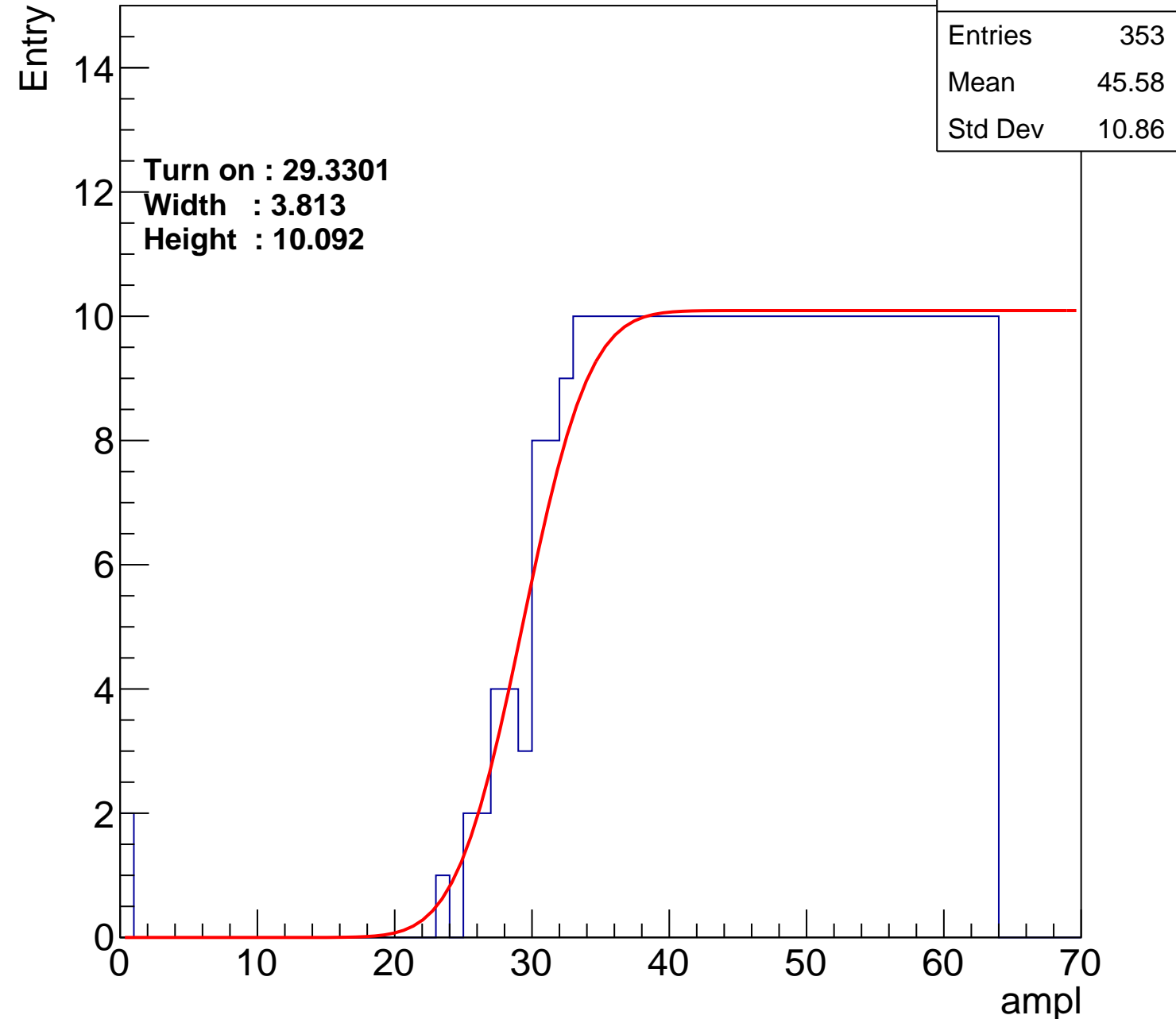
Width : 3.813

Height : 10.092

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.42
Std Dev	11.74

Turn on : 27.1884

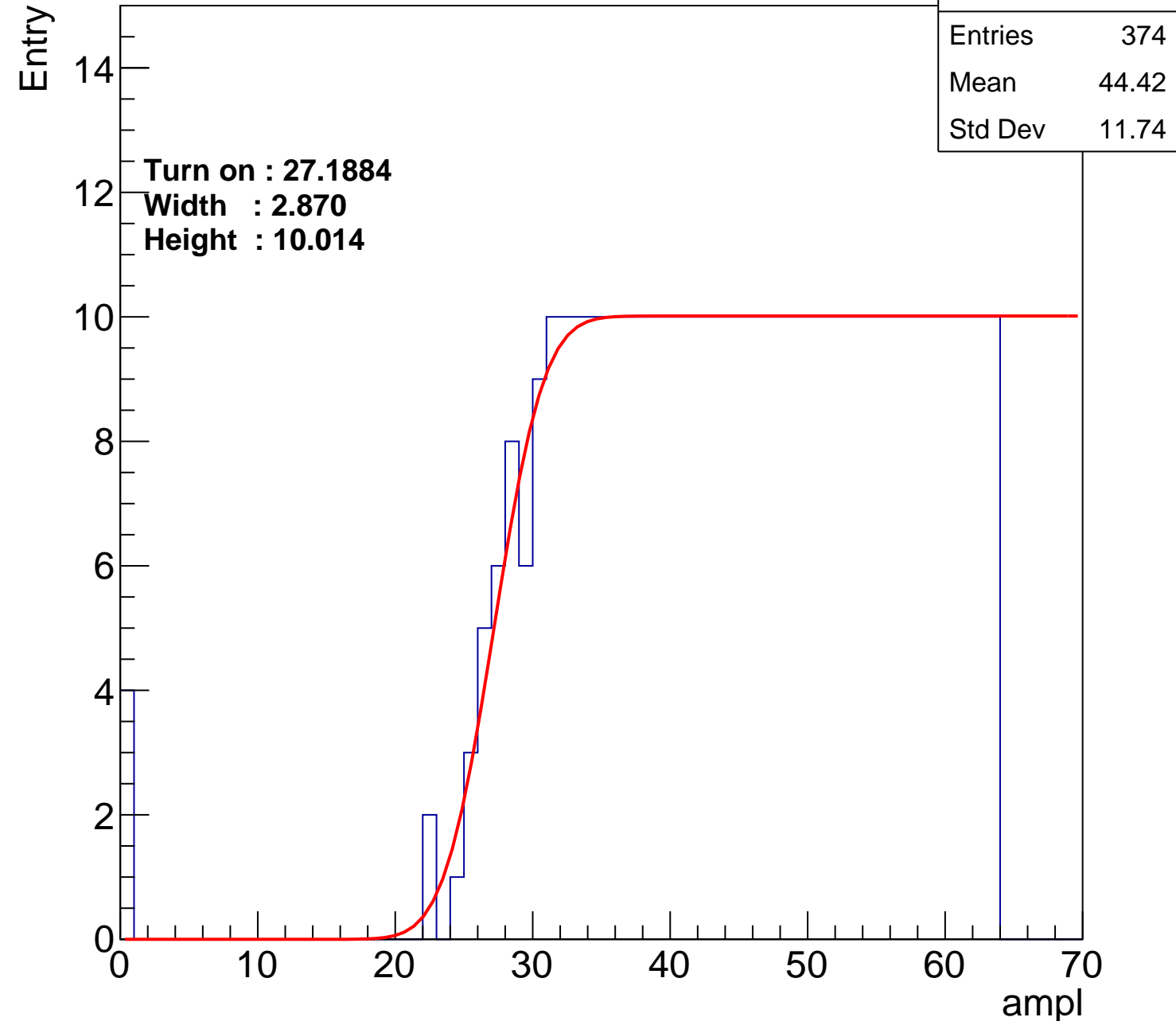
Width : 2.870

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch22

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.53
Std Dev	10.82

Turn on : 29.2058

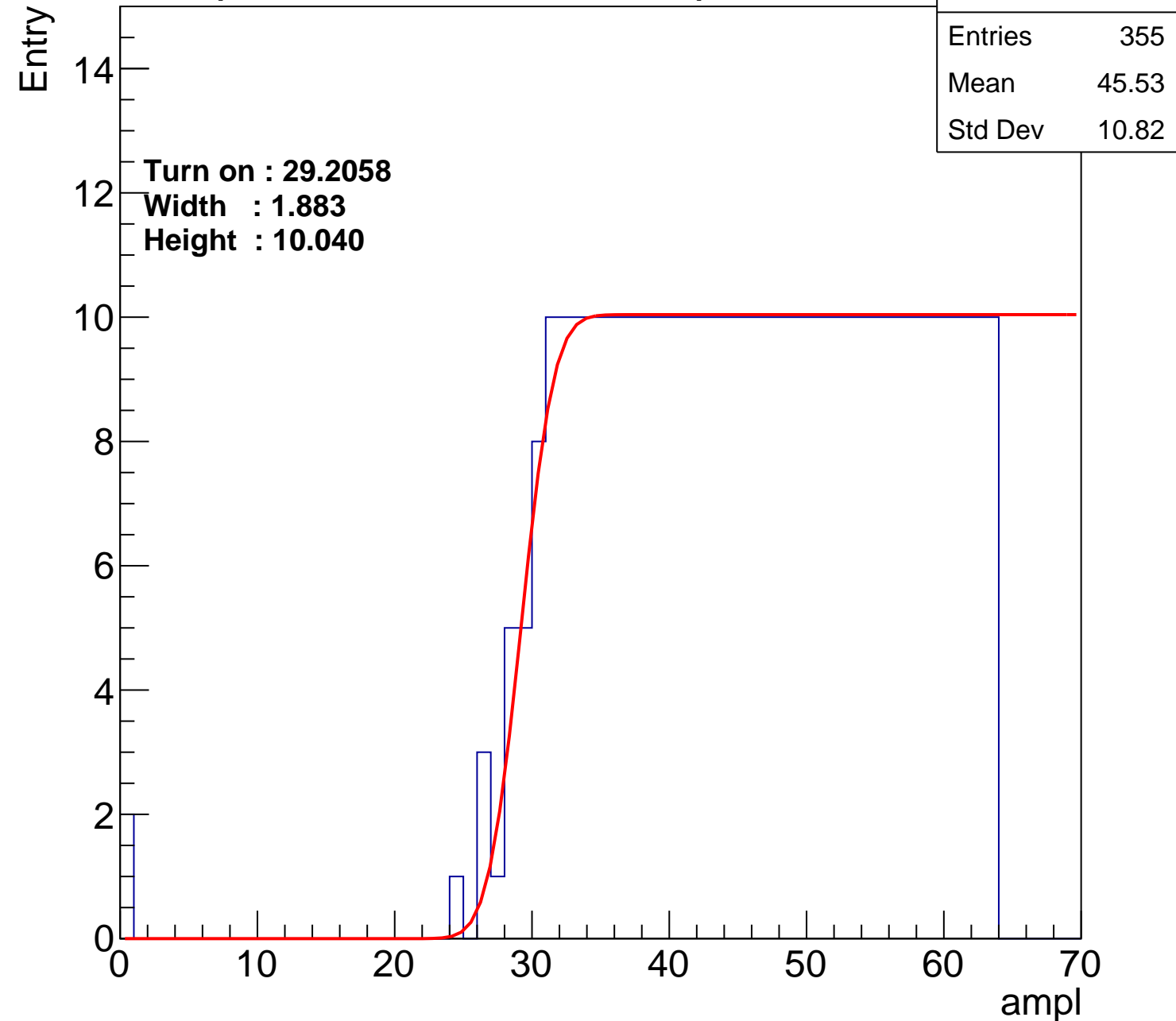
Width : 1.883

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch23

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.39
Std Dev	11.29

Turn on : 29.4742

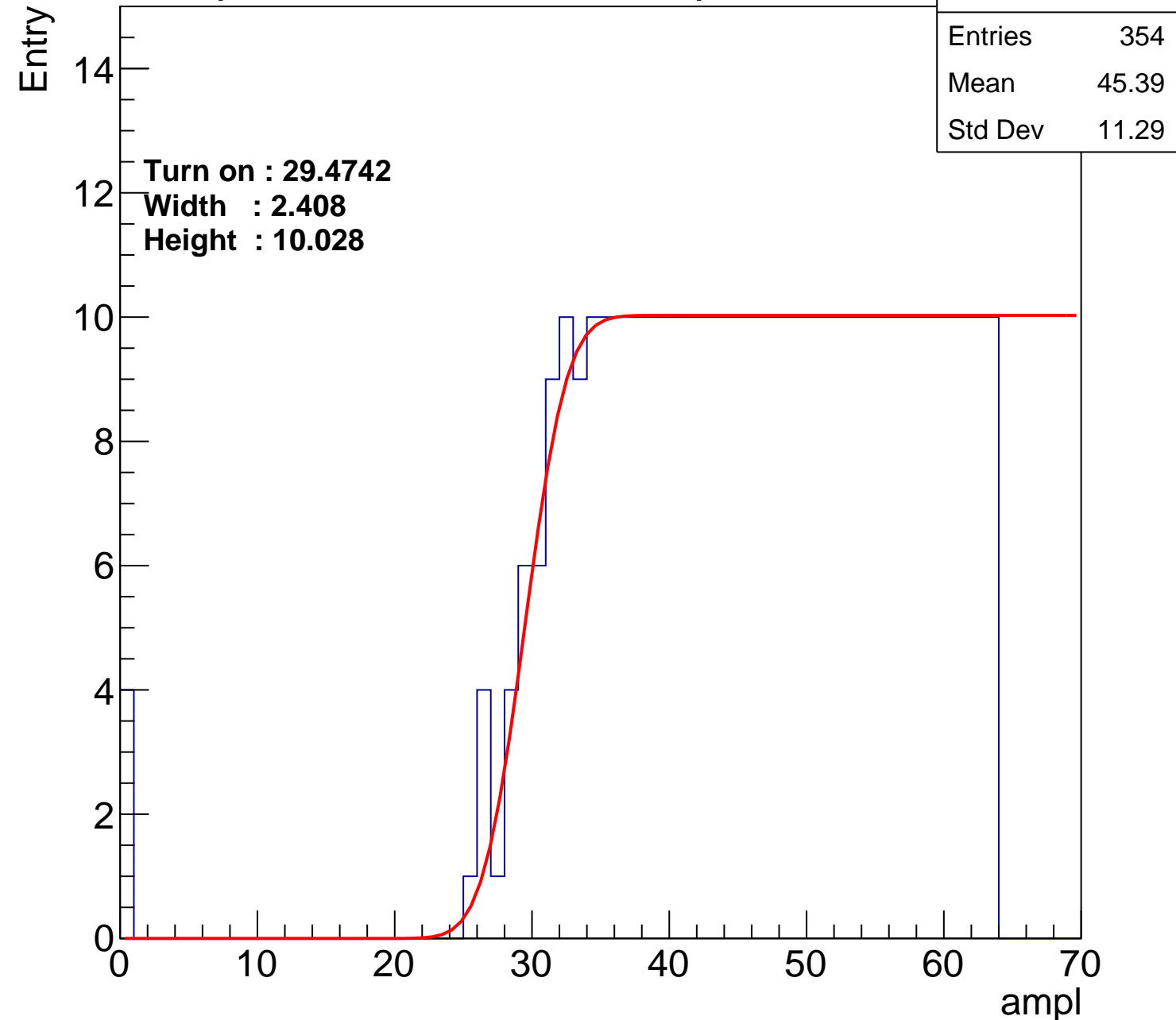
Width : 2.408

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch24

calib_packv5_042523_0143.root, FC#8, port C1

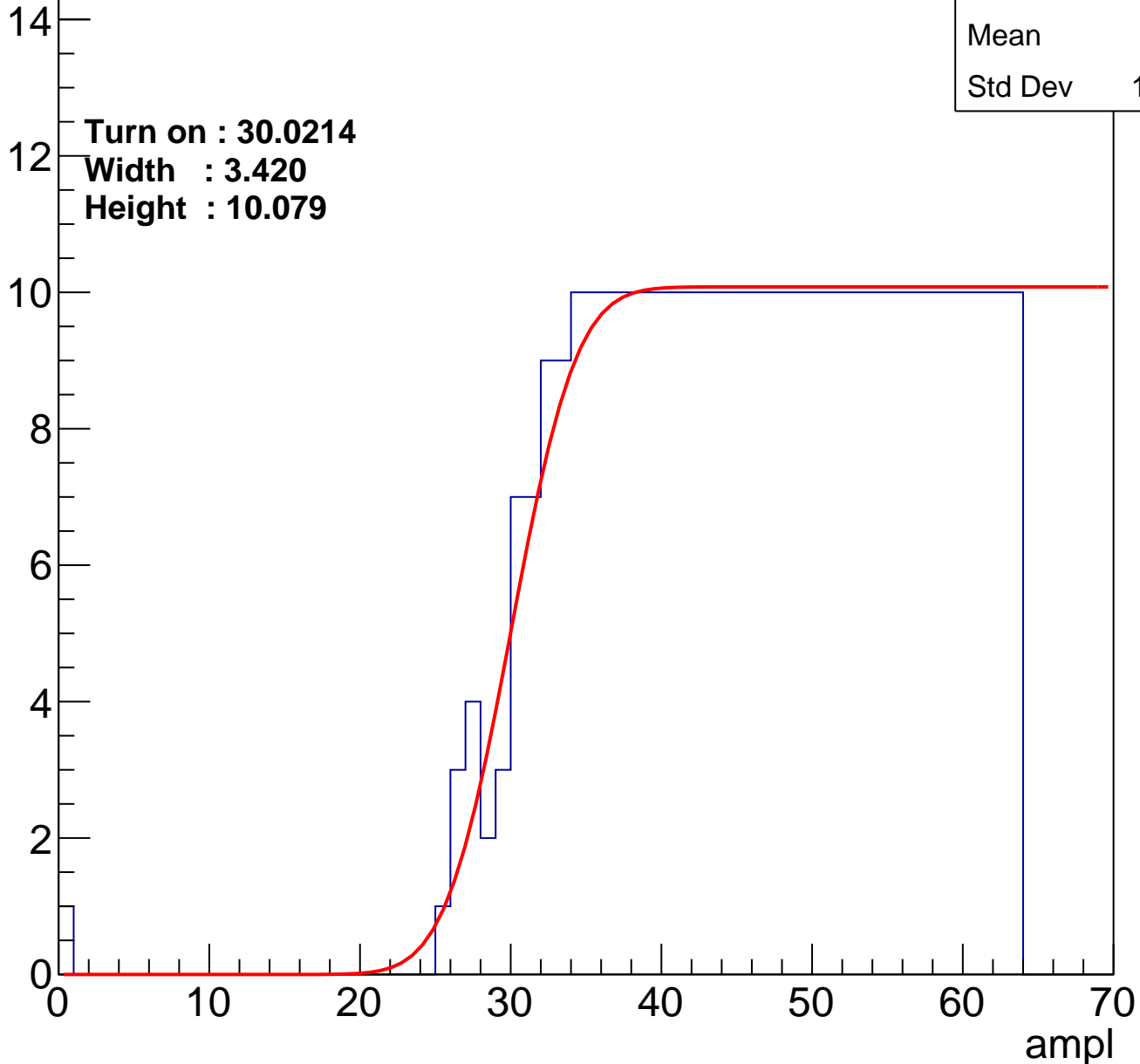
Entries	346
Mean	46
Std Dev	10.44

Turn on : 30.0214

Width : 3.420

Height : 10.079

Entry



B0L002S, U20-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.75
Std Dev	11.26

Turn on : 27.0545

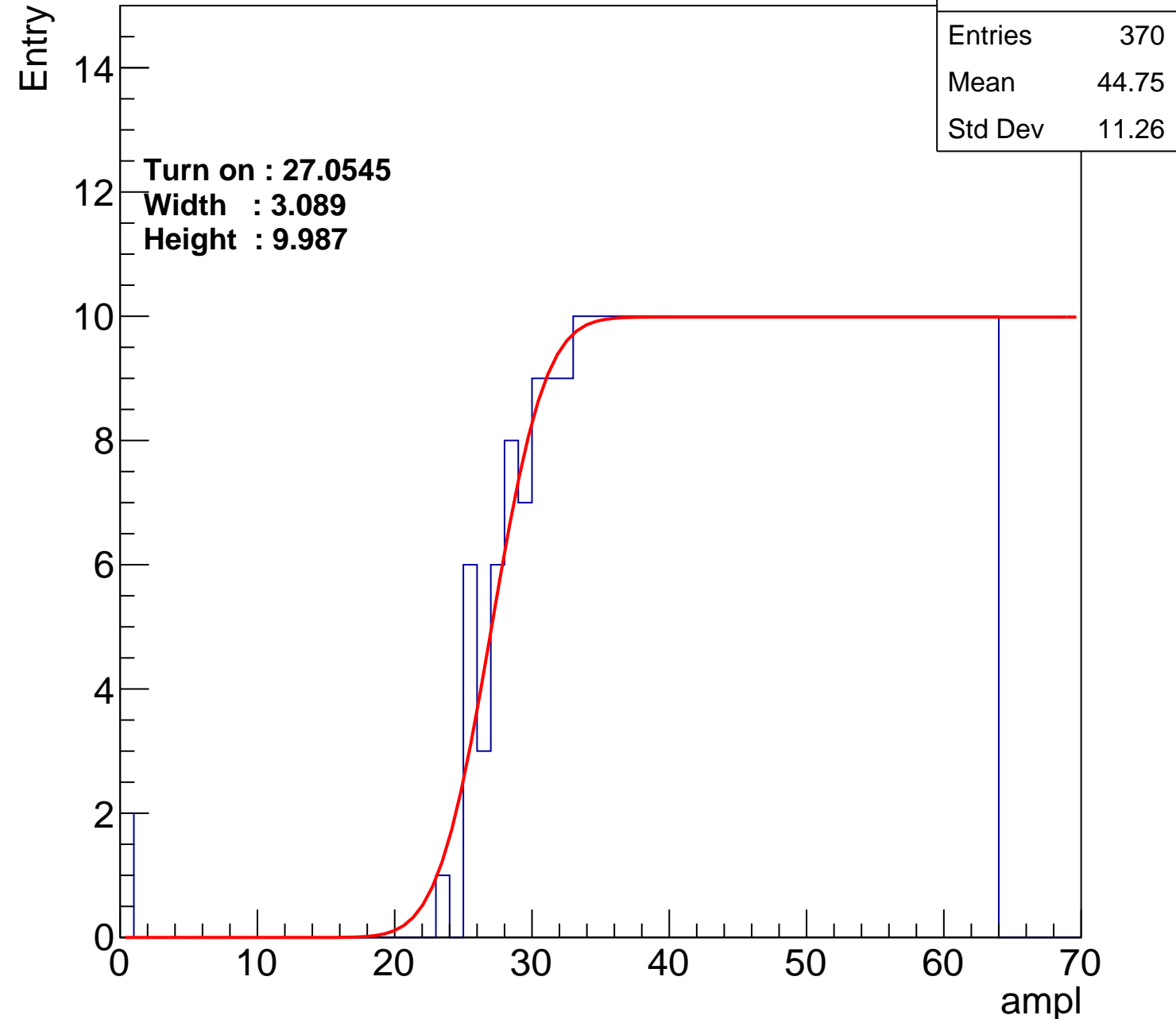
Width : 3.089

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch26

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.65
Std Dev	11.14

Turn on : 26.5803

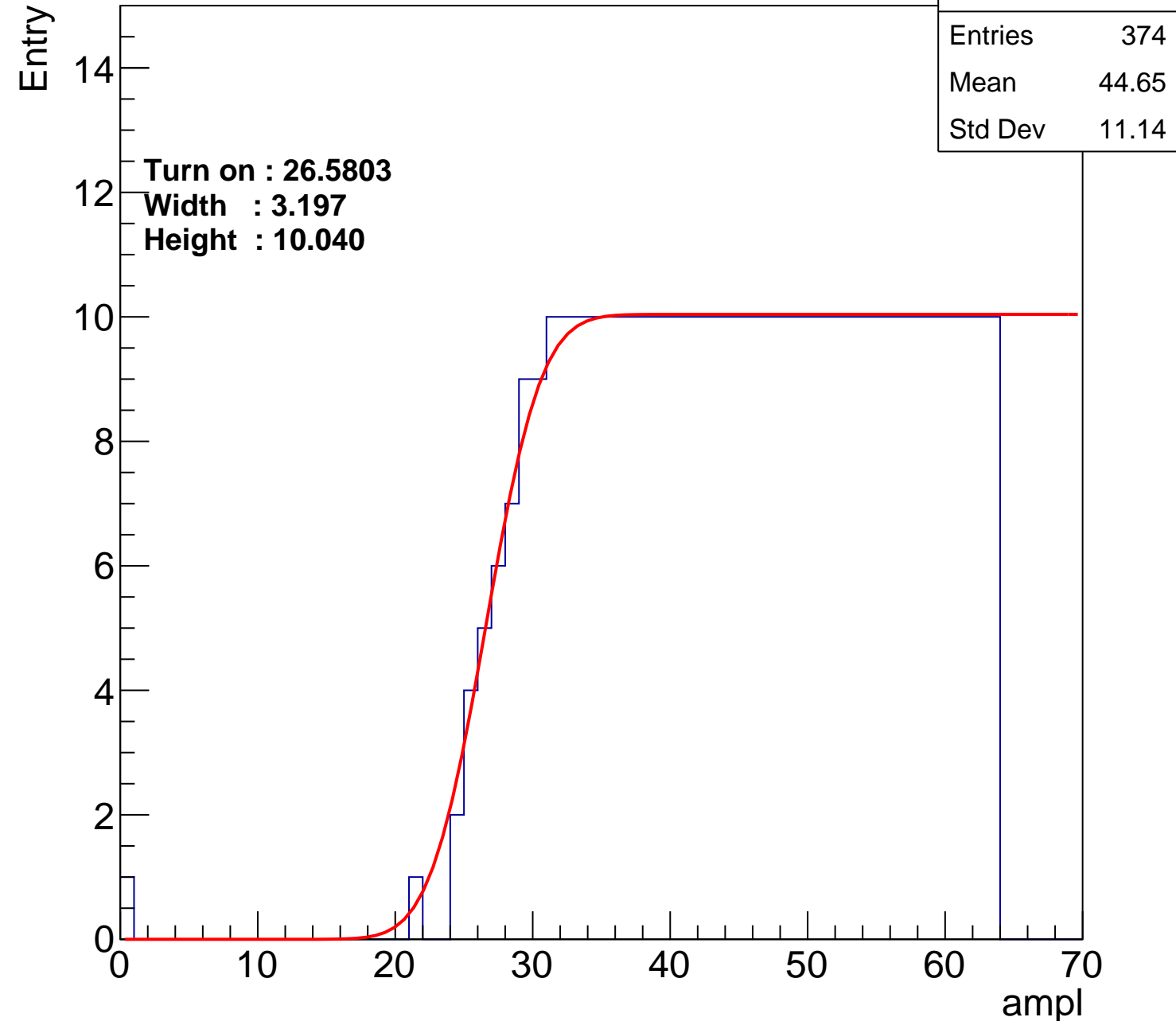
Width : 3.197

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch27

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.28
Std Dev	11.37

Turn on : 26.5423

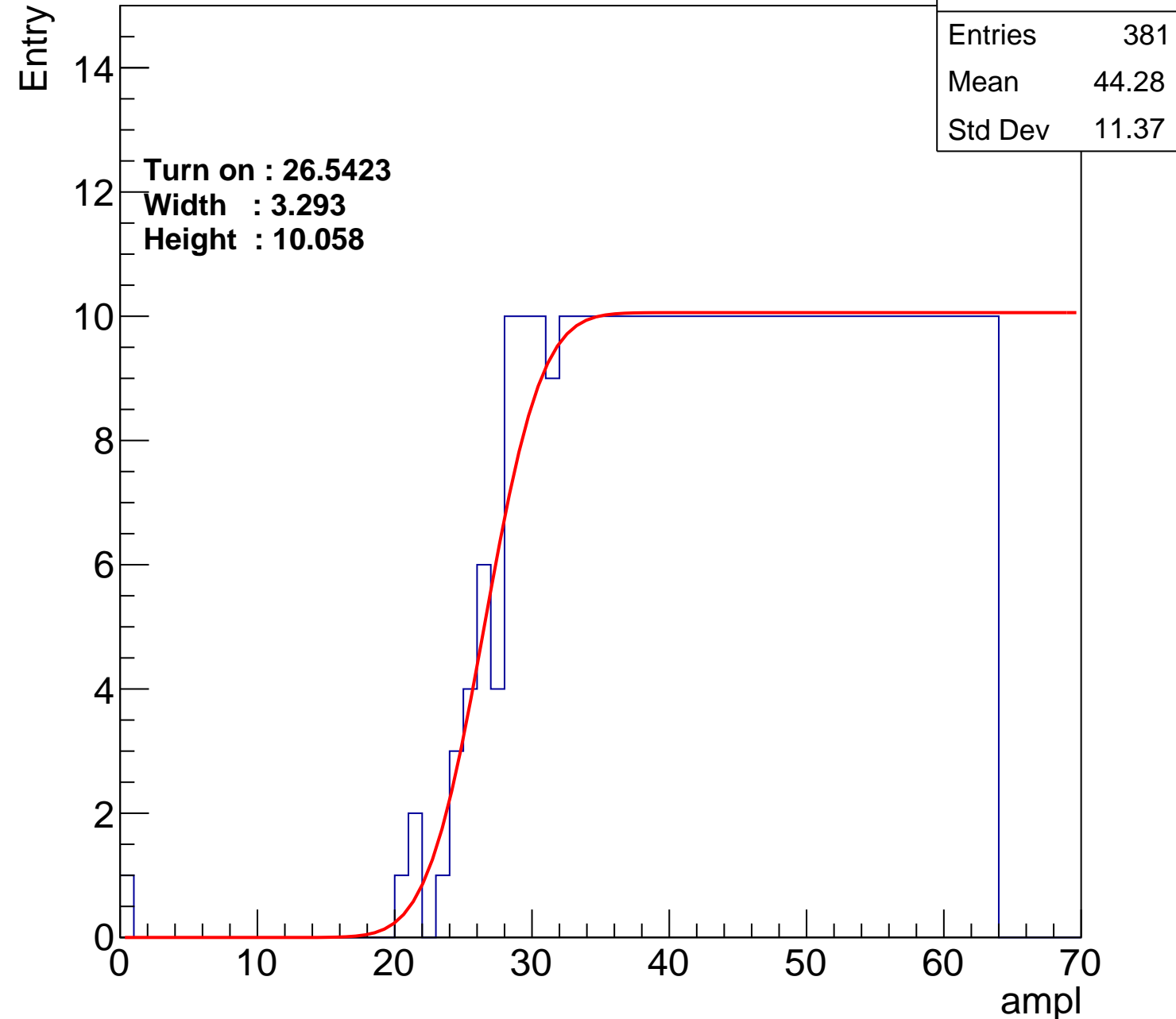
Width : 3.293

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch28

calib_packv5_042523_0143.root, FC#8, port C1

Entries	381
Mean	44.01
Std Dev	12.08

Turn on : 27.3333

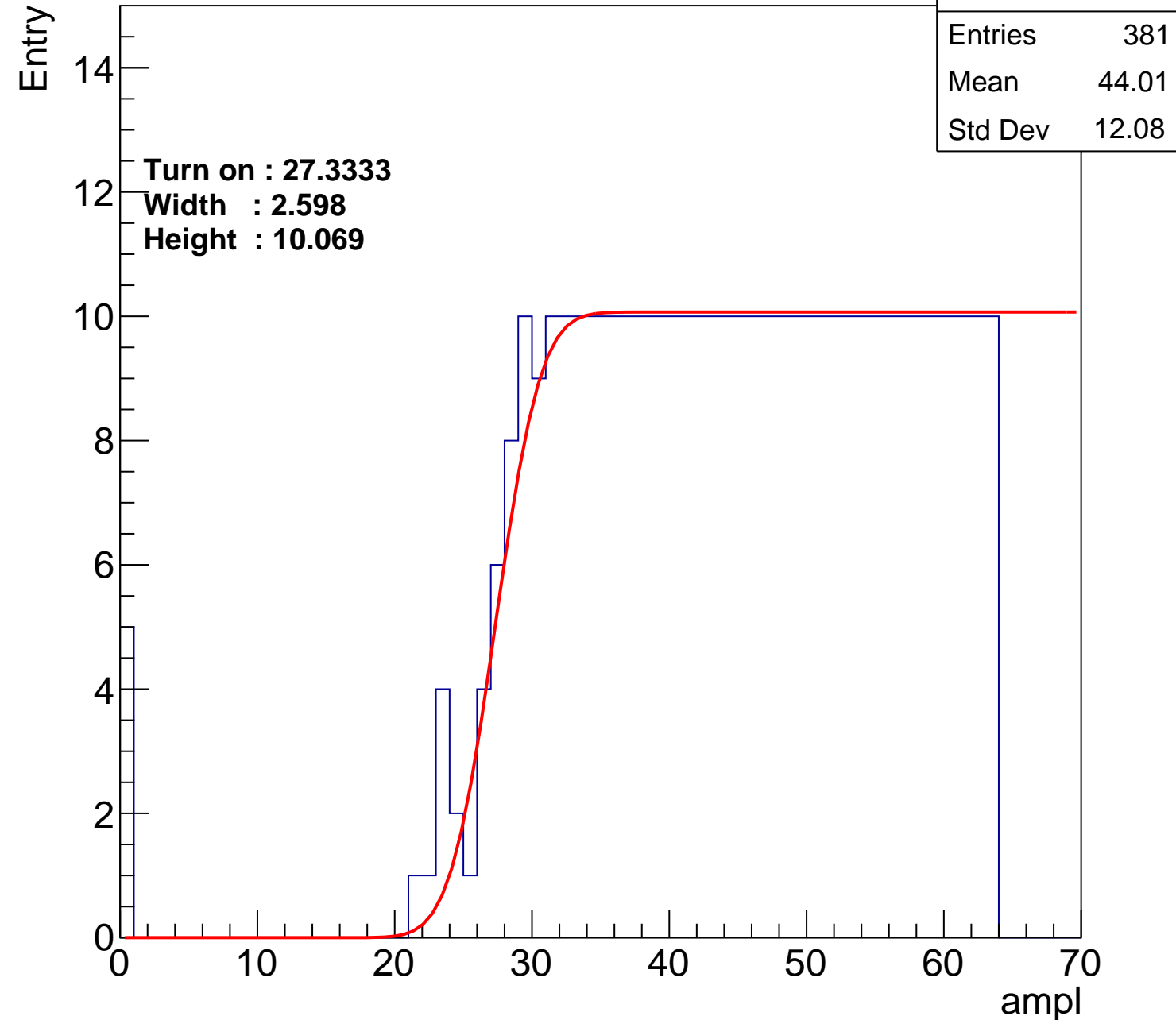
Width : 2.598

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.2
Std Dev	11.22

Turn on : 28.3954

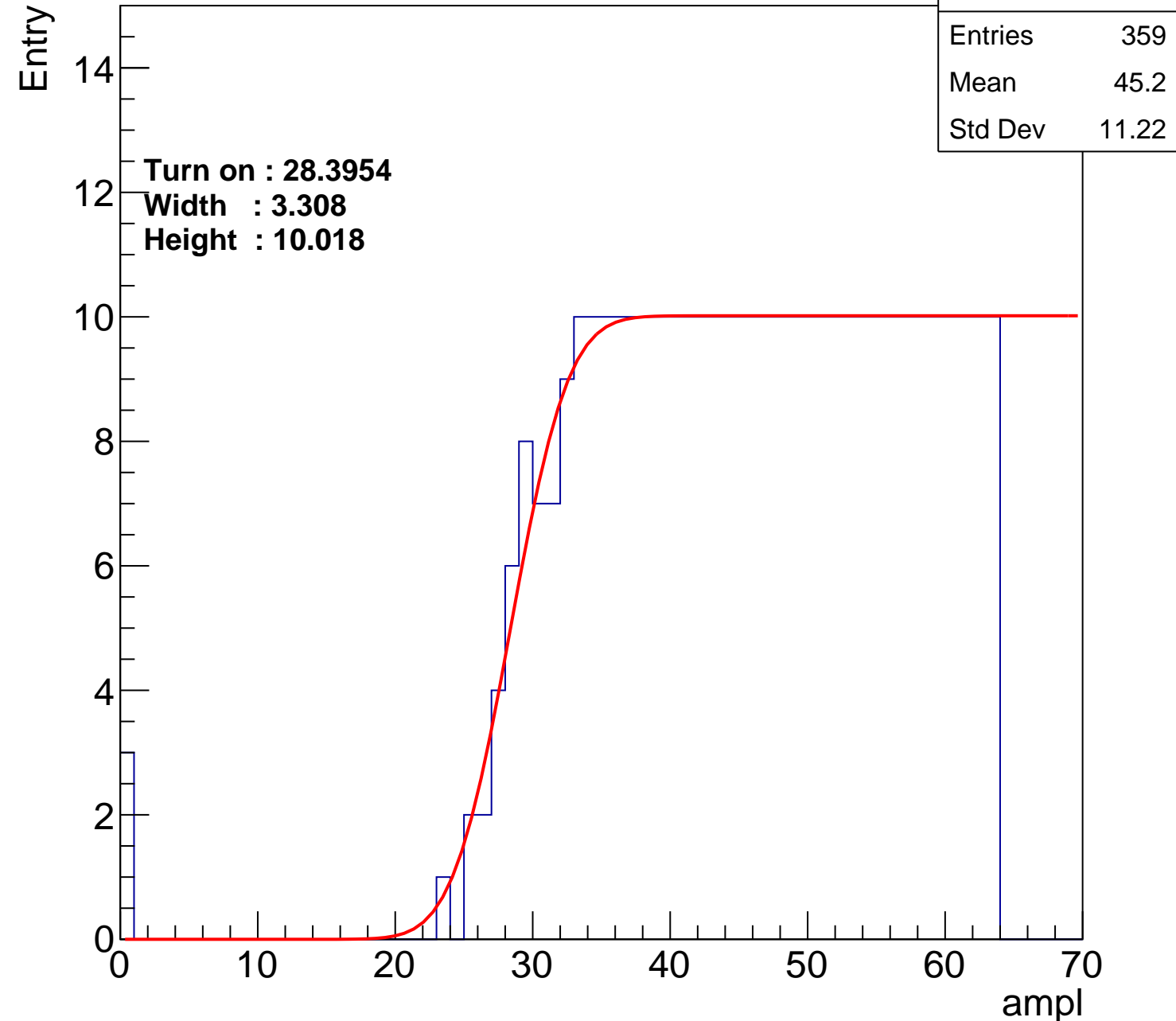
Width : 3.308

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch30

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.34
Std Dev	10.92

Turn on : 28.1126

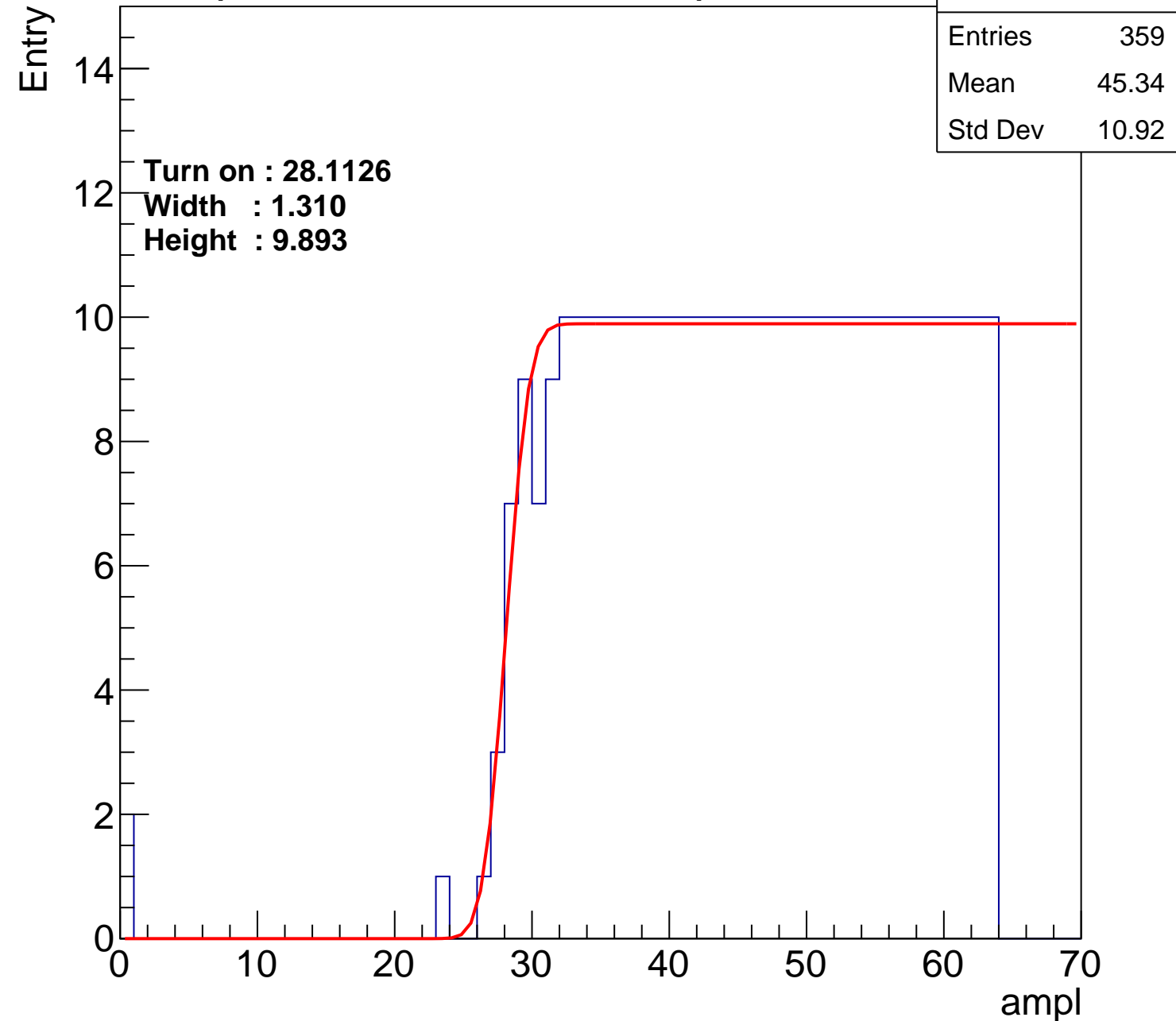
Width : 1.310

Height : 9.893

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch31

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.61
Std Dev	11.57

Turn on : 28.5330

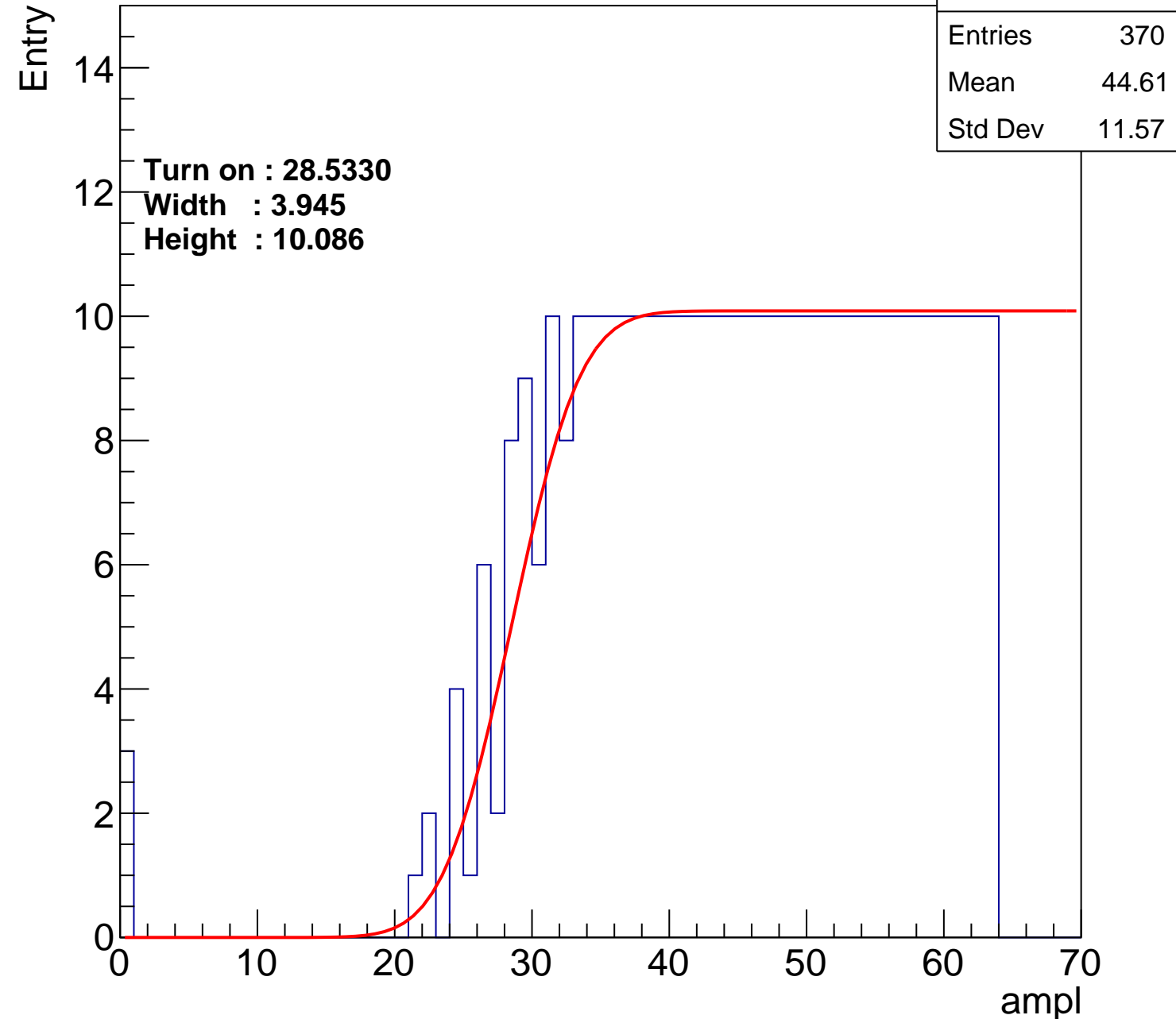
Width : 3.945

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.97
Std Dev	11.14

Turn on : 27.6972

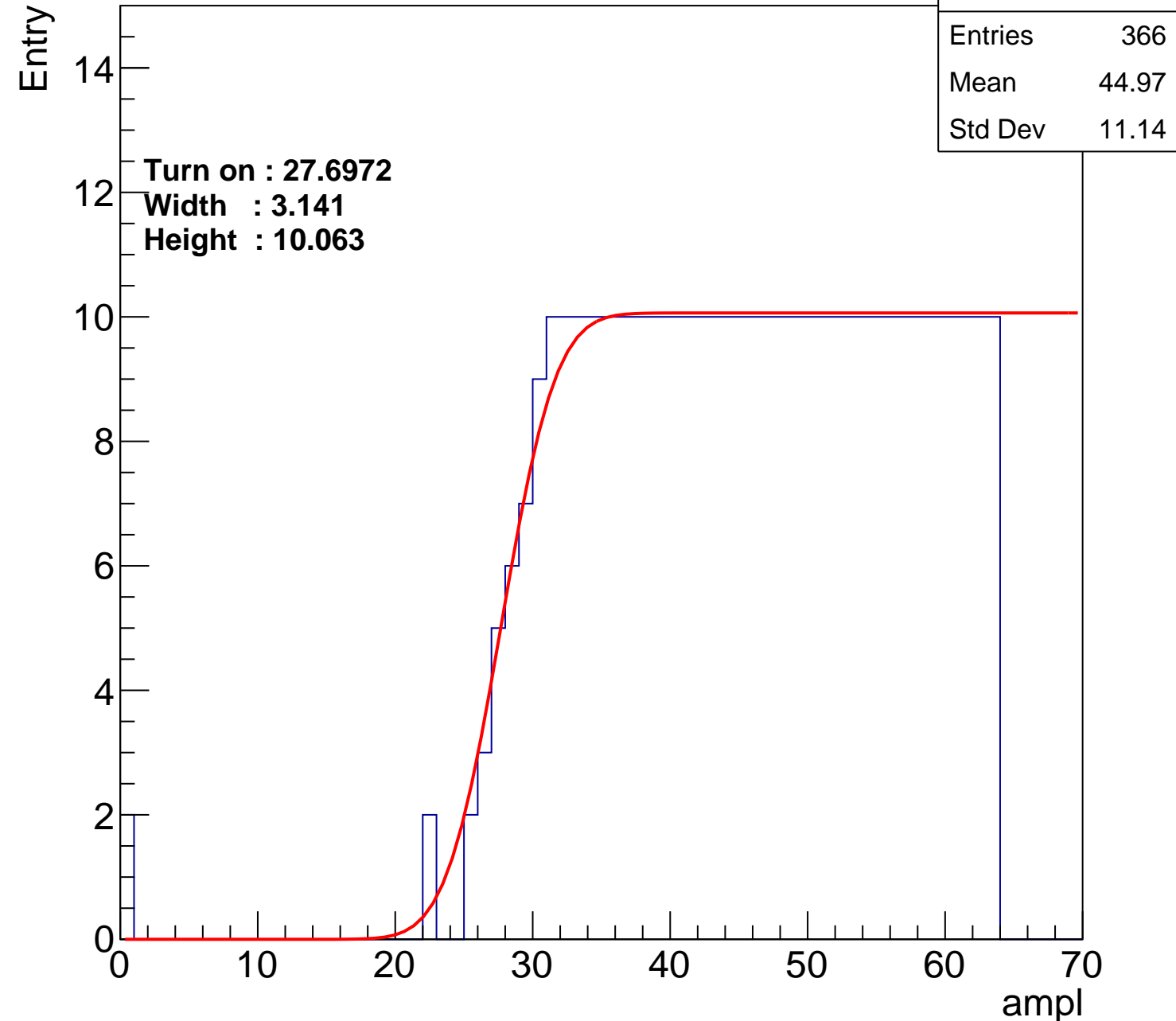
Width : 3.141

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.68
Std Dev	12.1

Turn on : 25.6993

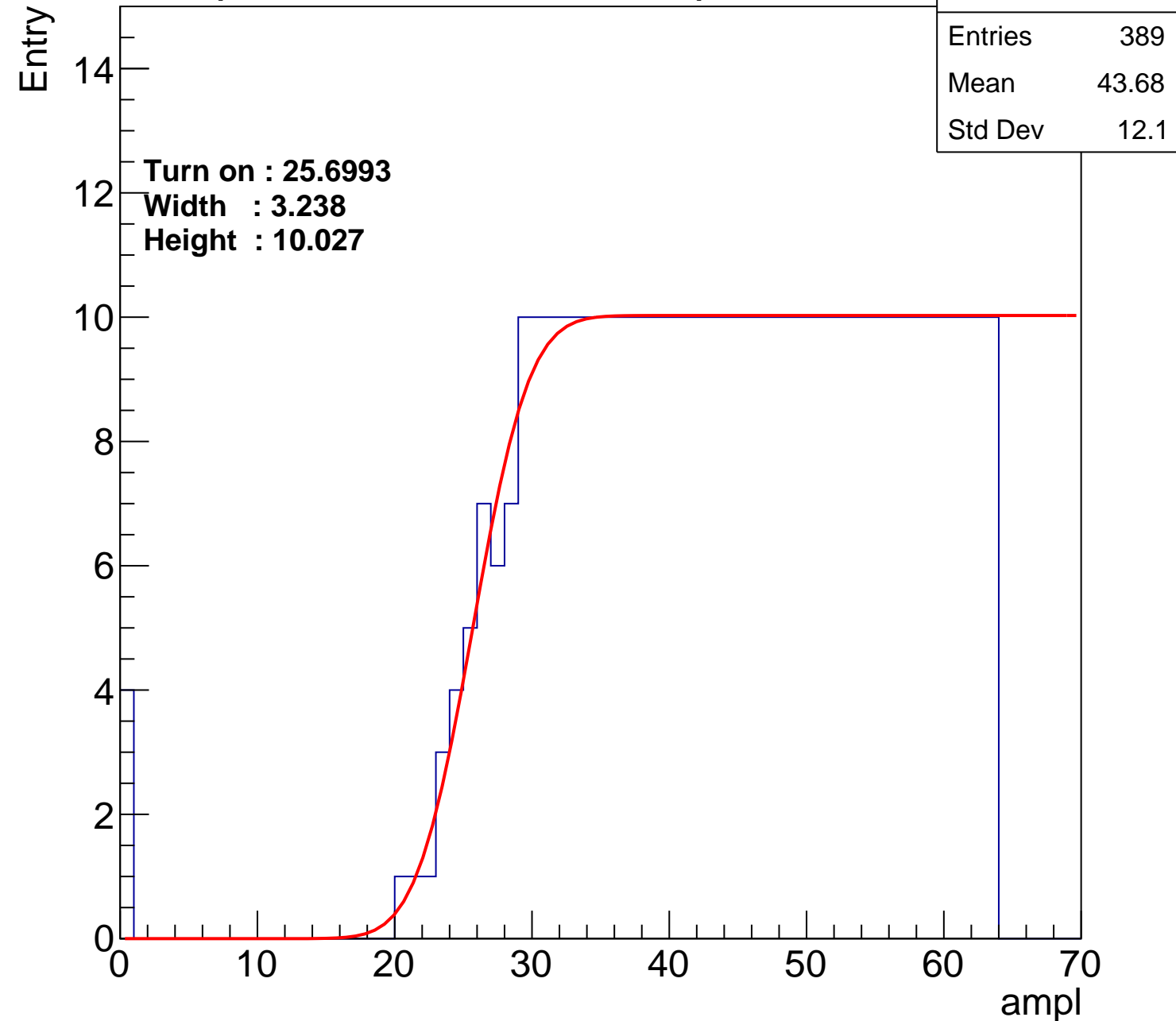
Width : 3.238

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.9727

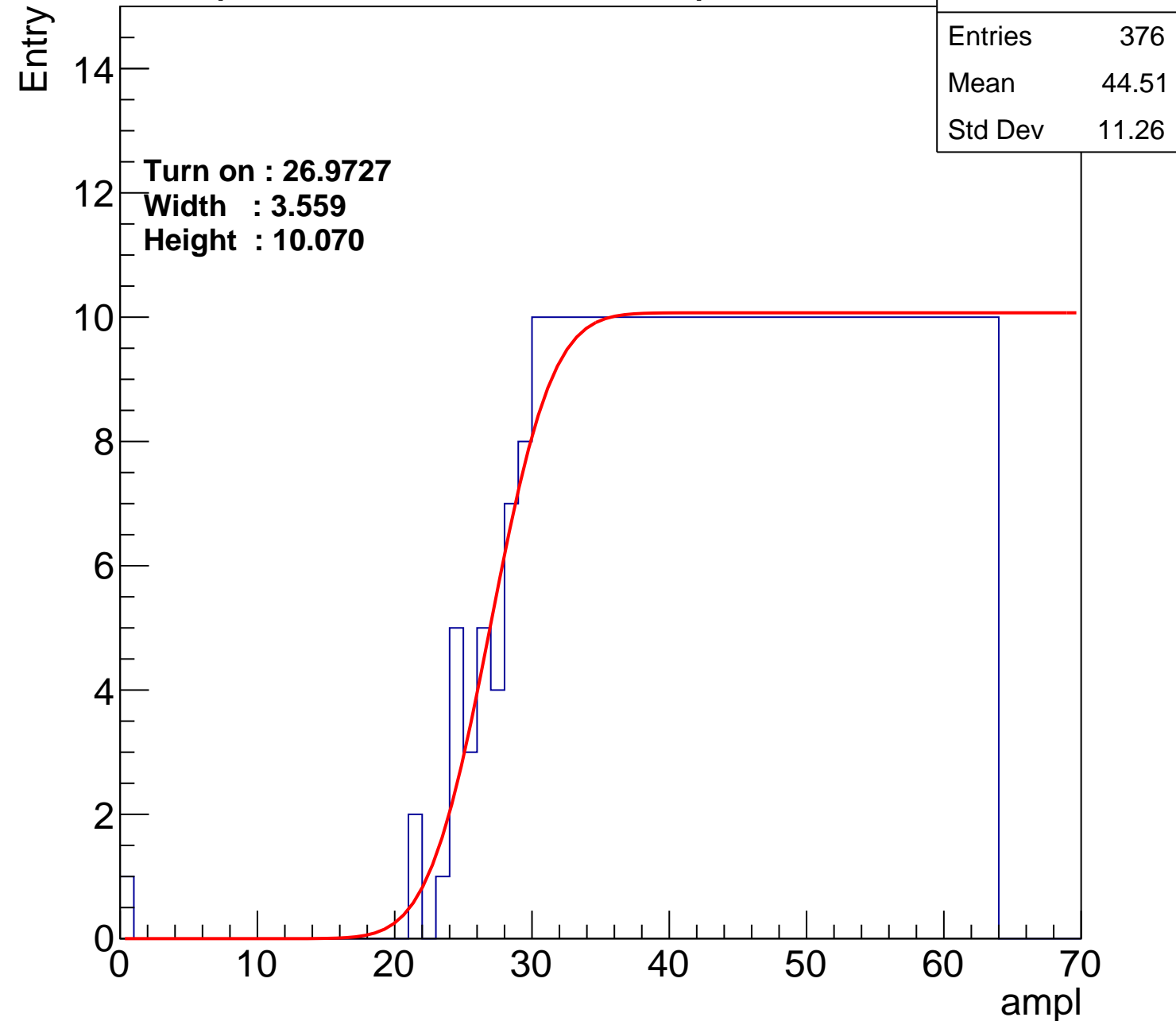
Width : 3.559

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#8, port C1

calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 26.1756
Width : 2.405
Height : 10.007



B0L002S, U20-ch36

calib_packv5_042523_0143.root, FC#8, port C1

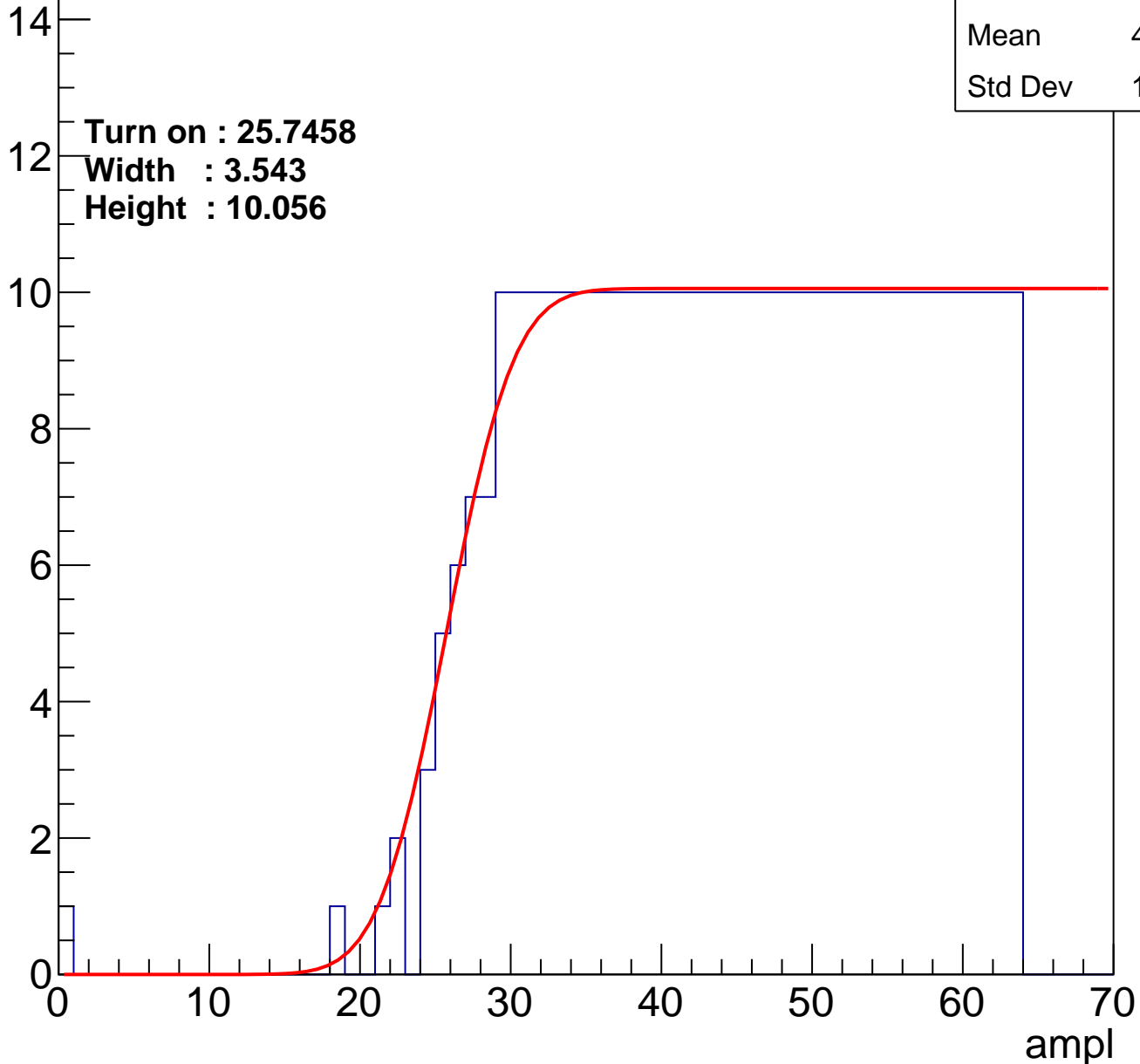
Entries	383
Mean	44.18
Std Dev	11.42

Turn on : 25.7458

Width : 3.543

Height : 10.056

Entry



B0L002S, U20-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.24
Std Dev	11.16

Turn on : 27.9930

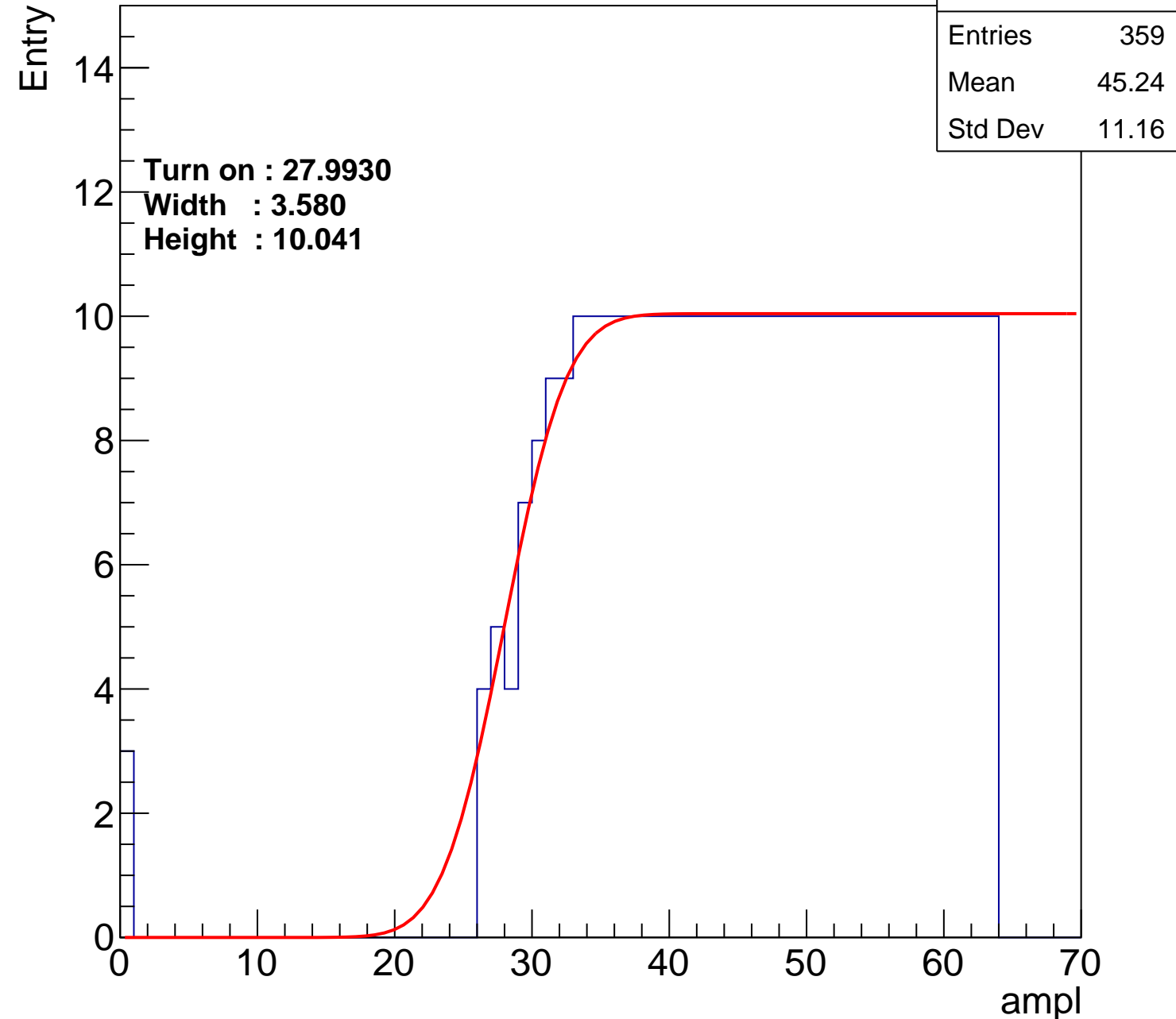
Width : 3.580

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch38

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.53
Std Dev	10.88

Turn on : 29.3884

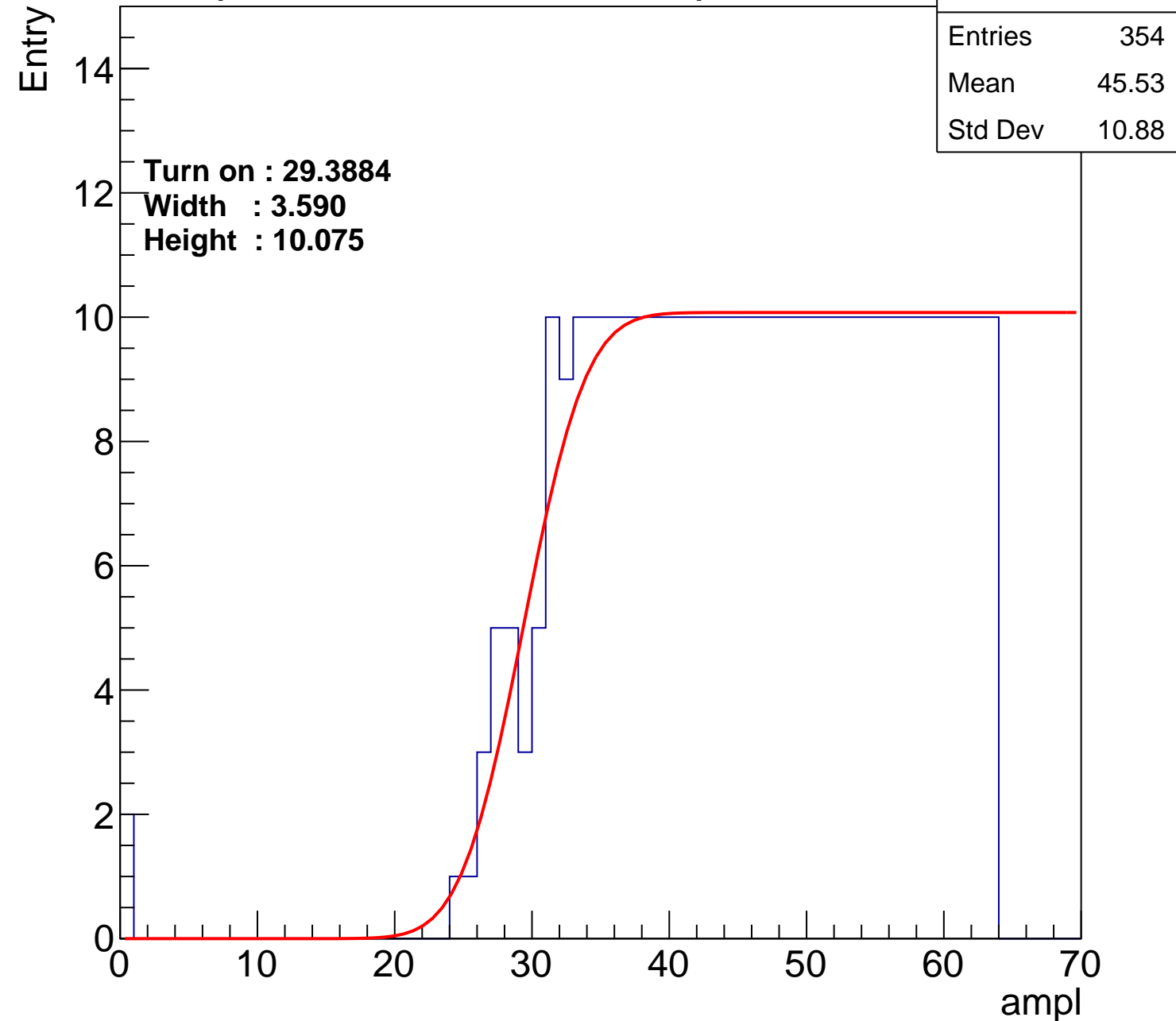
Width : 3.590

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.34
Std Dev	11.76

Turn on : 26.9405

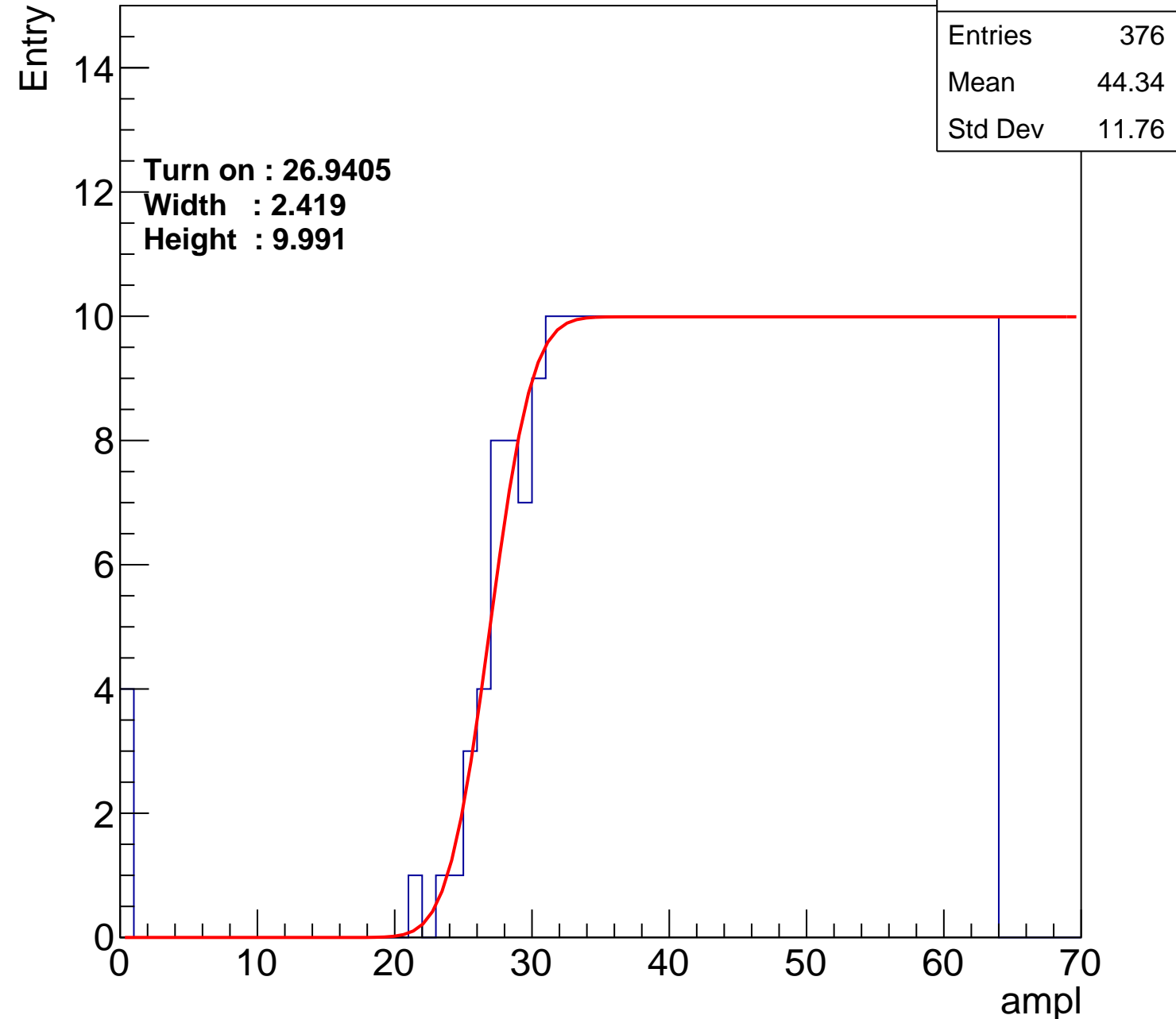
Width : 2.419

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.91
Std Dev	11.36

Turn on : 27.9578

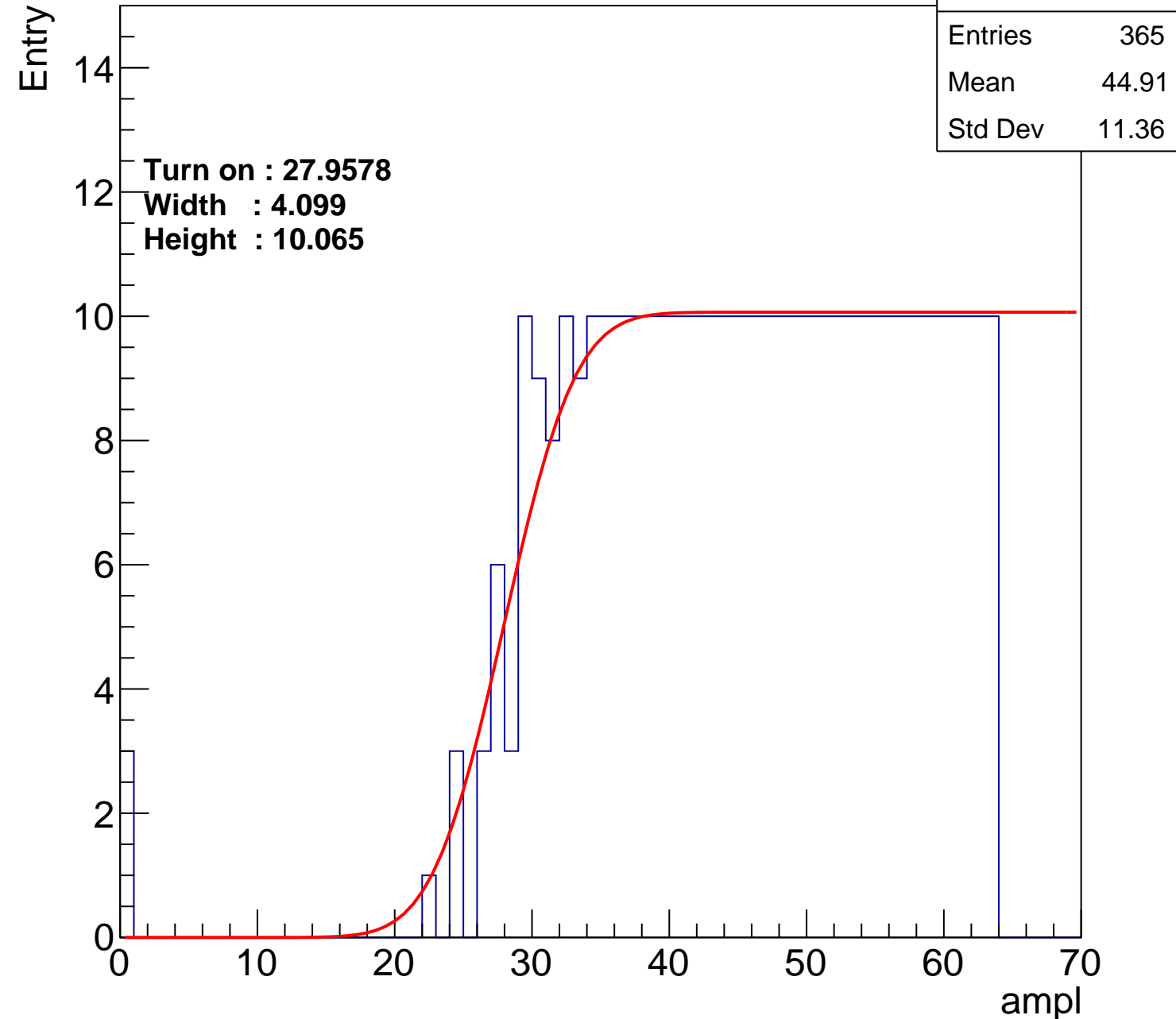
Width : 4.099

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch41

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.6912

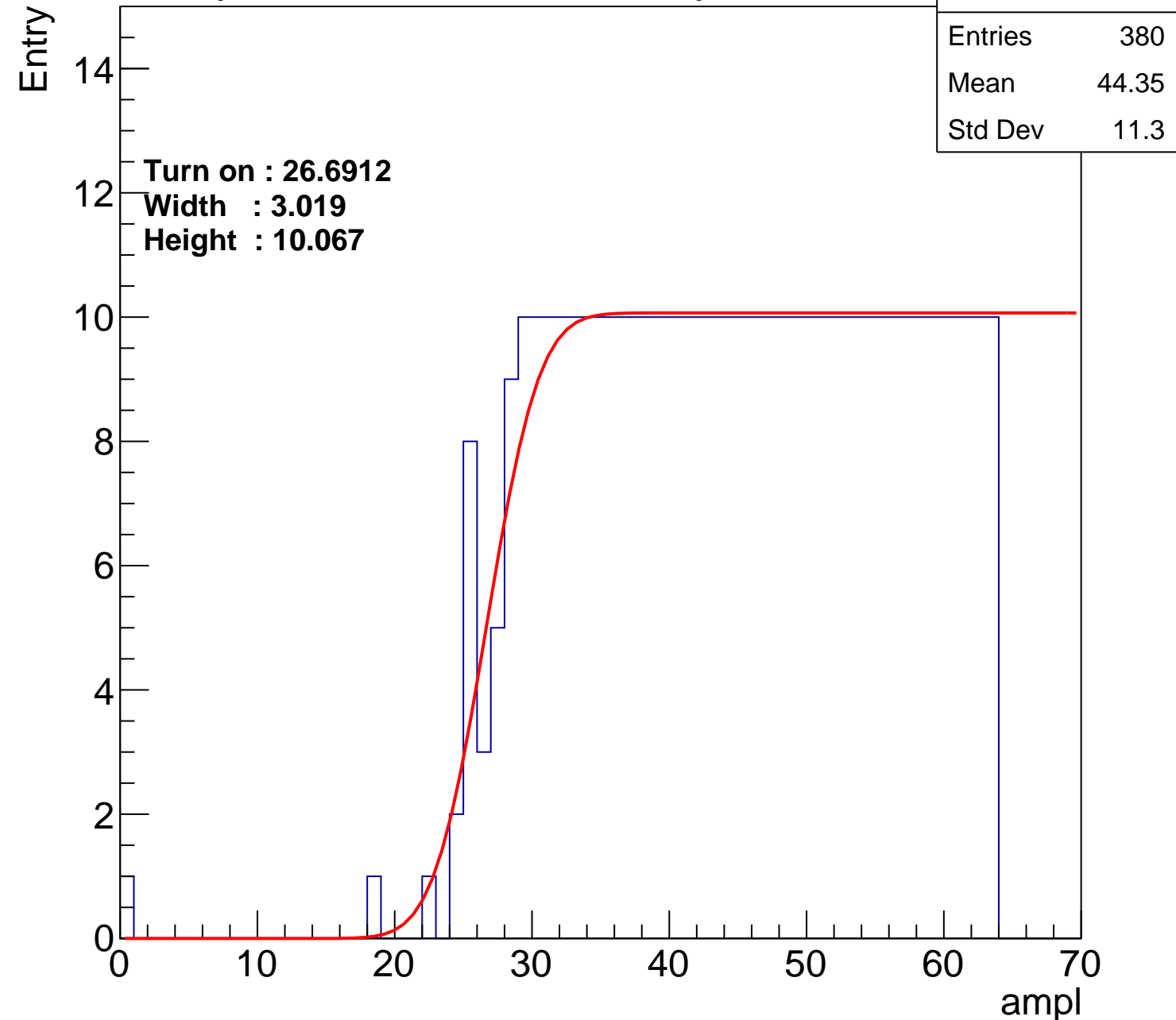
Width : 3.019

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.18
Std Dev	11.2

Turn on : 28.5402

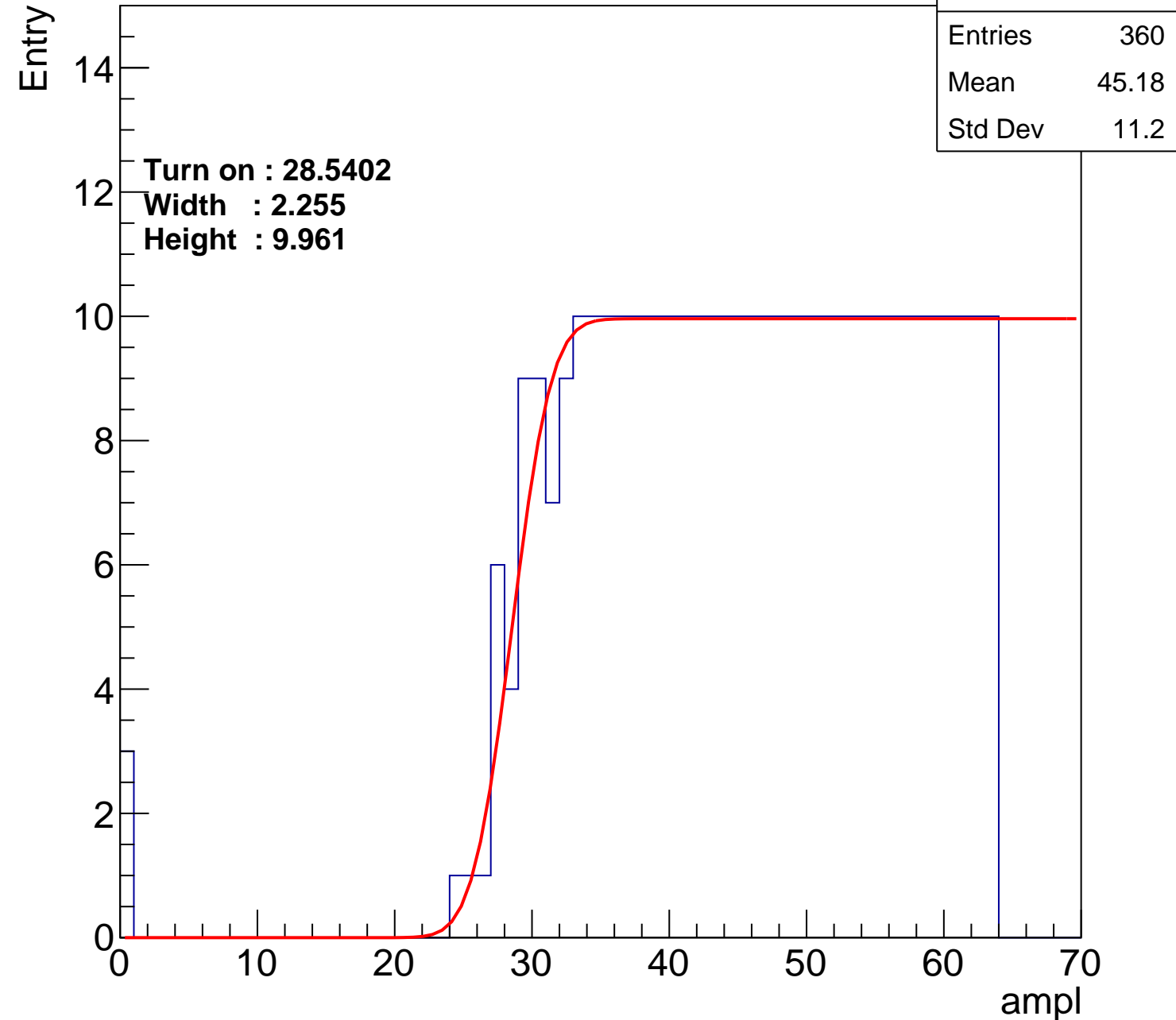
Width : 2.255

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	394
Mean	43.66
Std Dev	11.8

Turn on : 25.4754

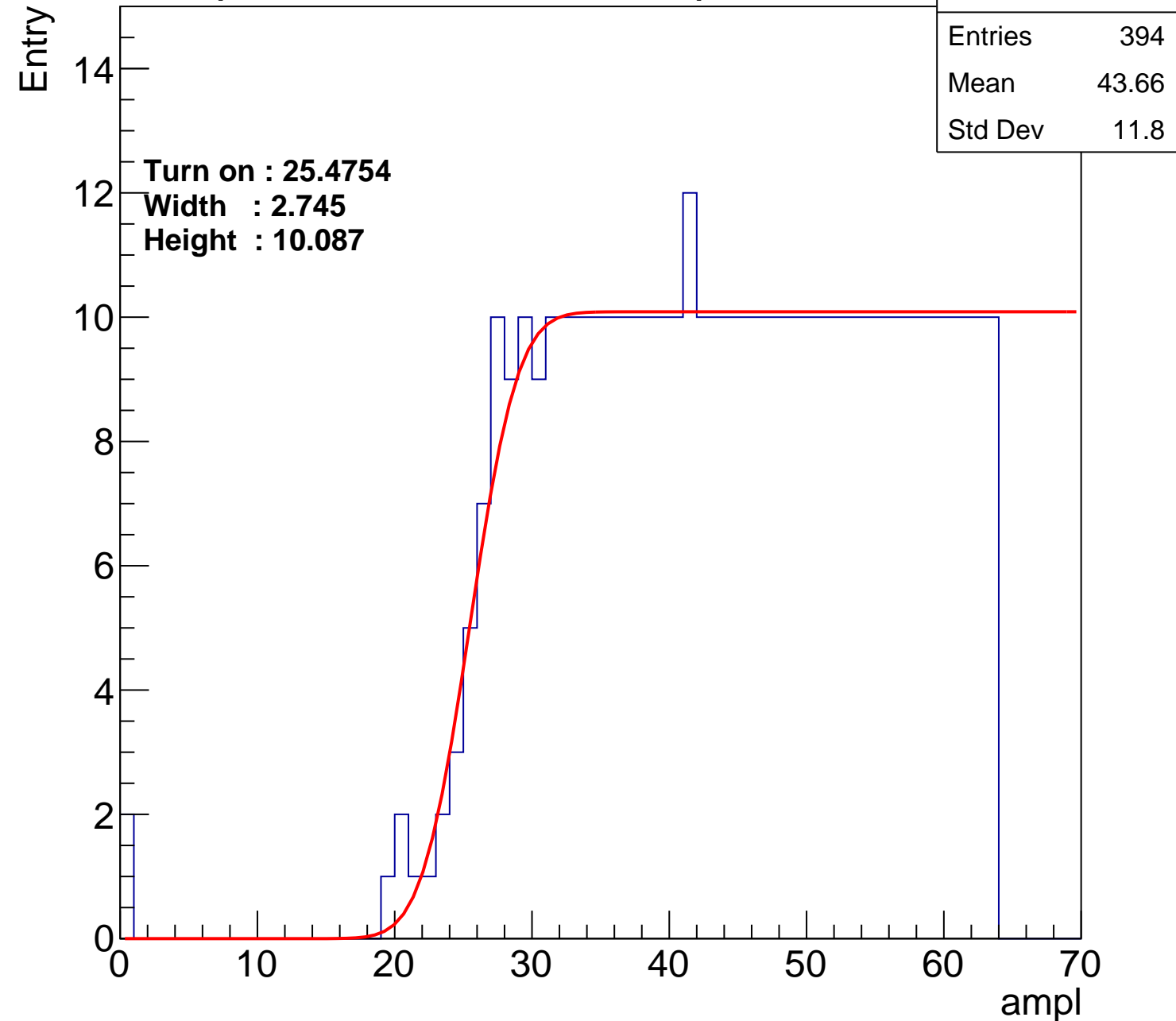
Width : 2.745

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch44

calib_packv5_042523_0143.root, FC#8, port C1

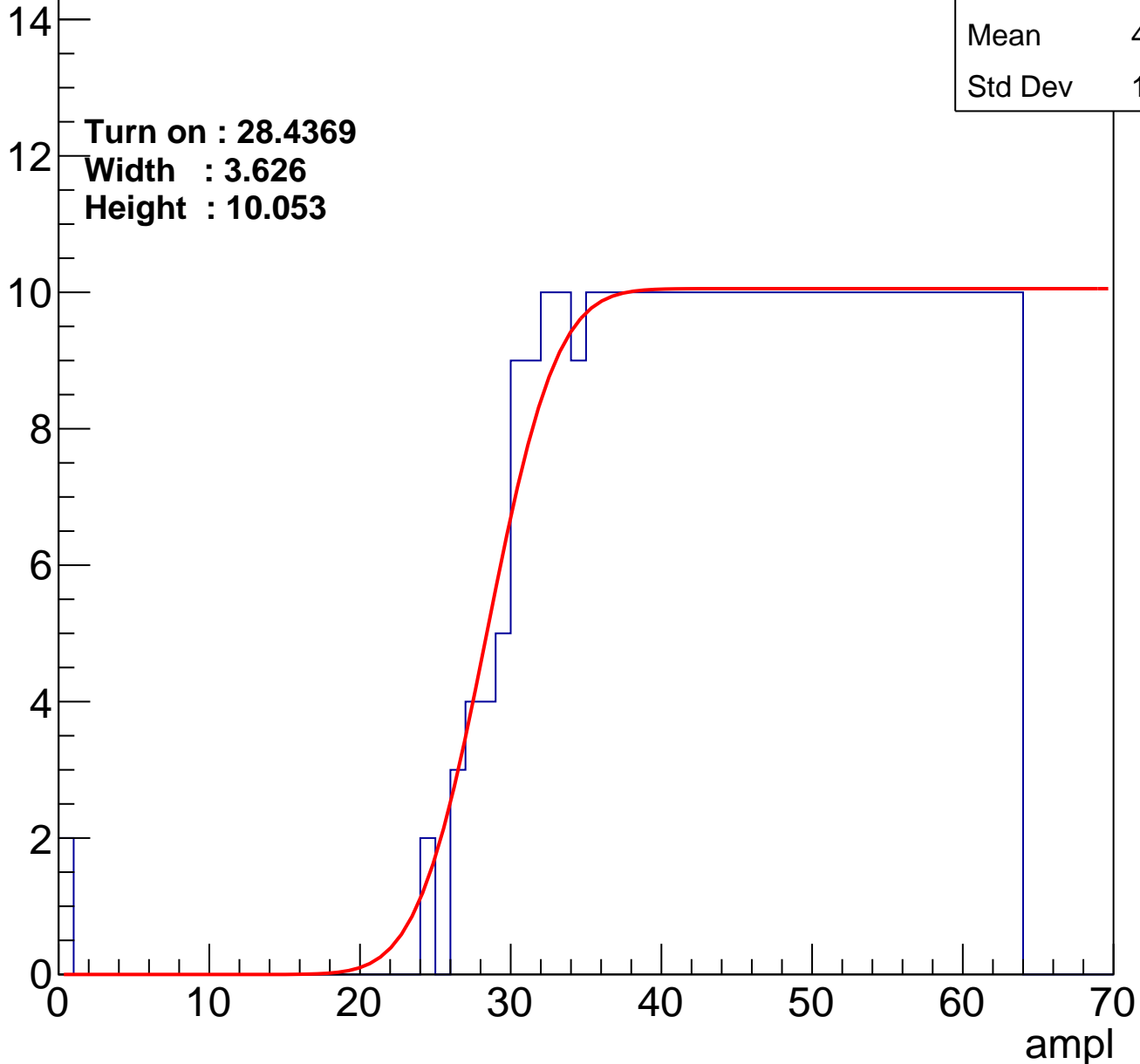
Entries	357
Mean	45.39
Std Dev	10.93

Turn on : 28.4369

Width : 3.626

Height : 10.053

Entry



B0L002S, U20-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.47
Std Dev	10.71

Turn on : 28.3583

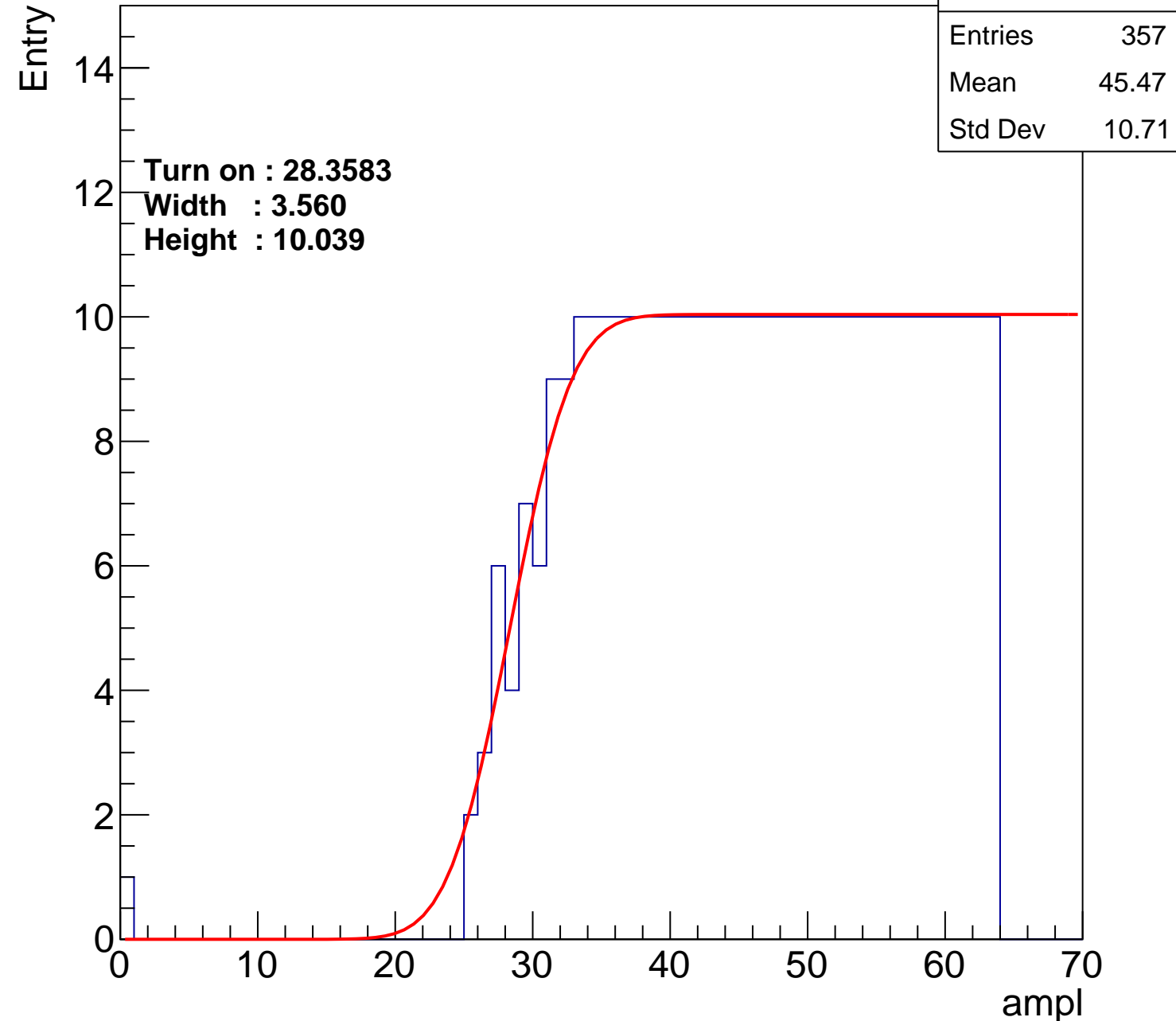
Width : 3.560

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch46

calib_packv5_042523_0143.root, FC#8, port C1

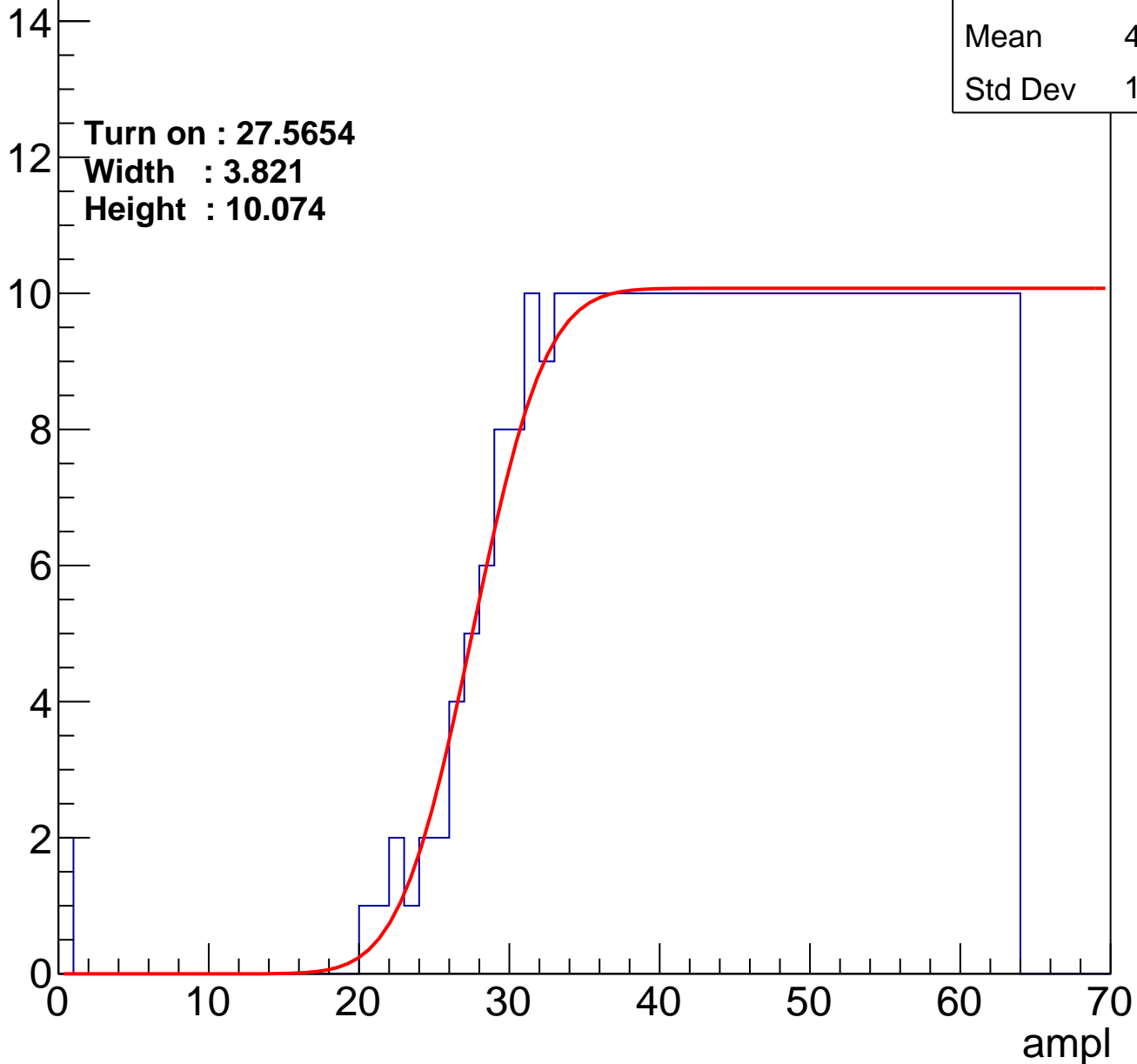
Entries	371
Mean	44.64
Std Dev	11.39

Turn on : 27.5654

Width : 3.821

Height : 10.074

Entry



B0L002S, U20-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.77
Std Dev	11.17

Turn on : 27.8630

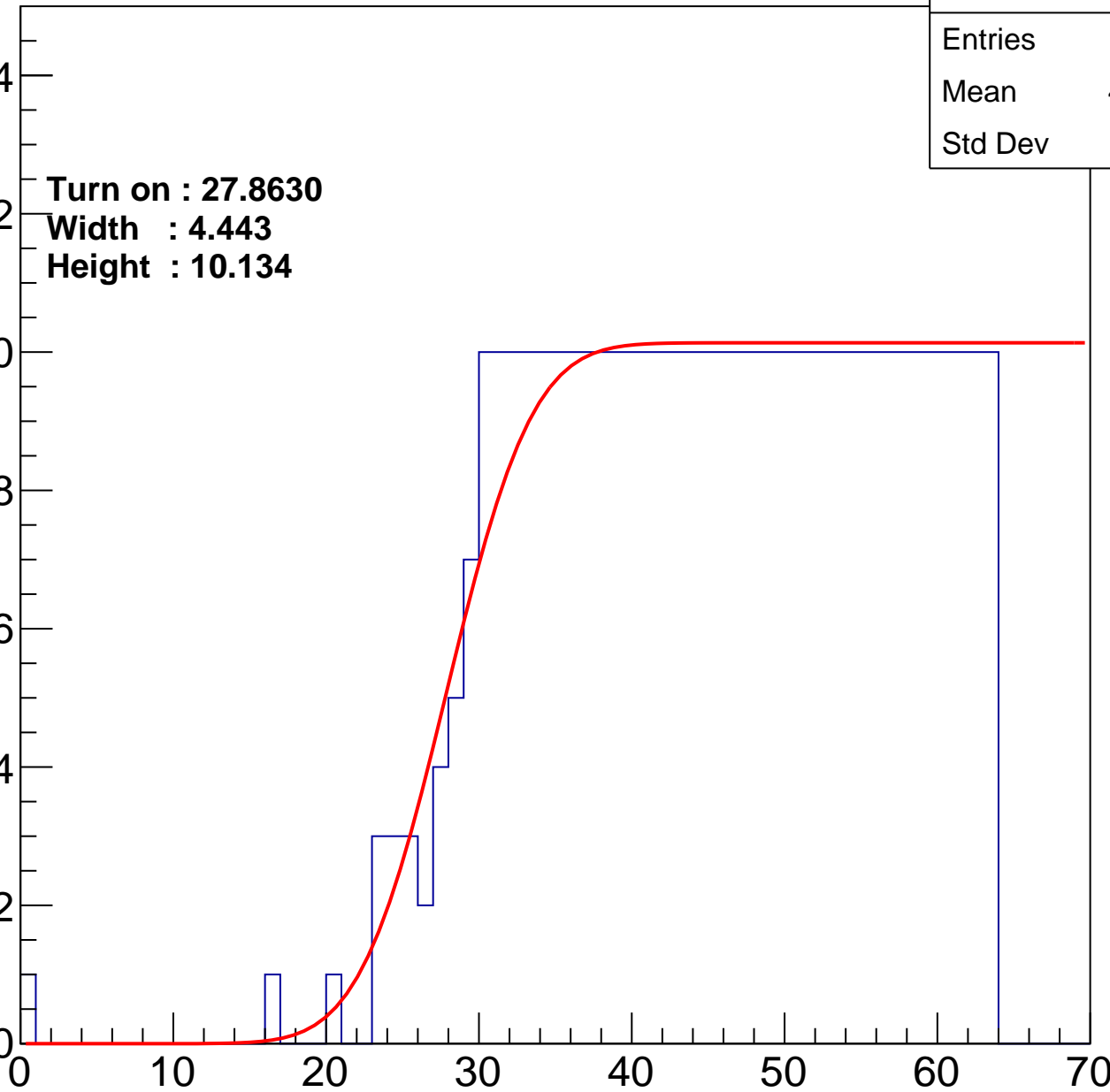
Width : 4.443

Height : 10.134

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch48

calib_packv5_042523_0143.root, FC#8, port C1

Entries	351
Mean	45.59
Std Dev	11.05

Turn on : 29.3319

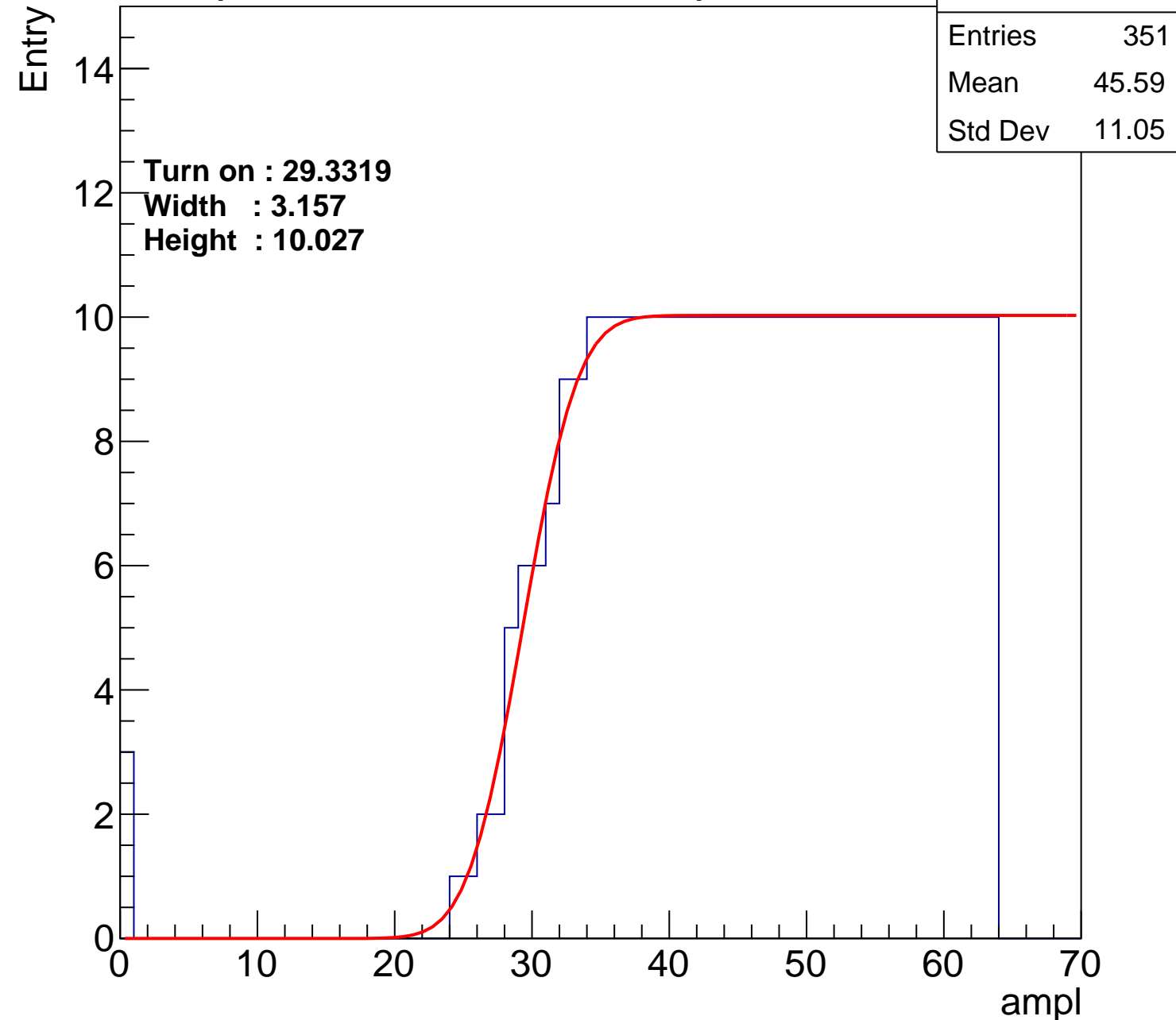
Width : 3.157

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch49

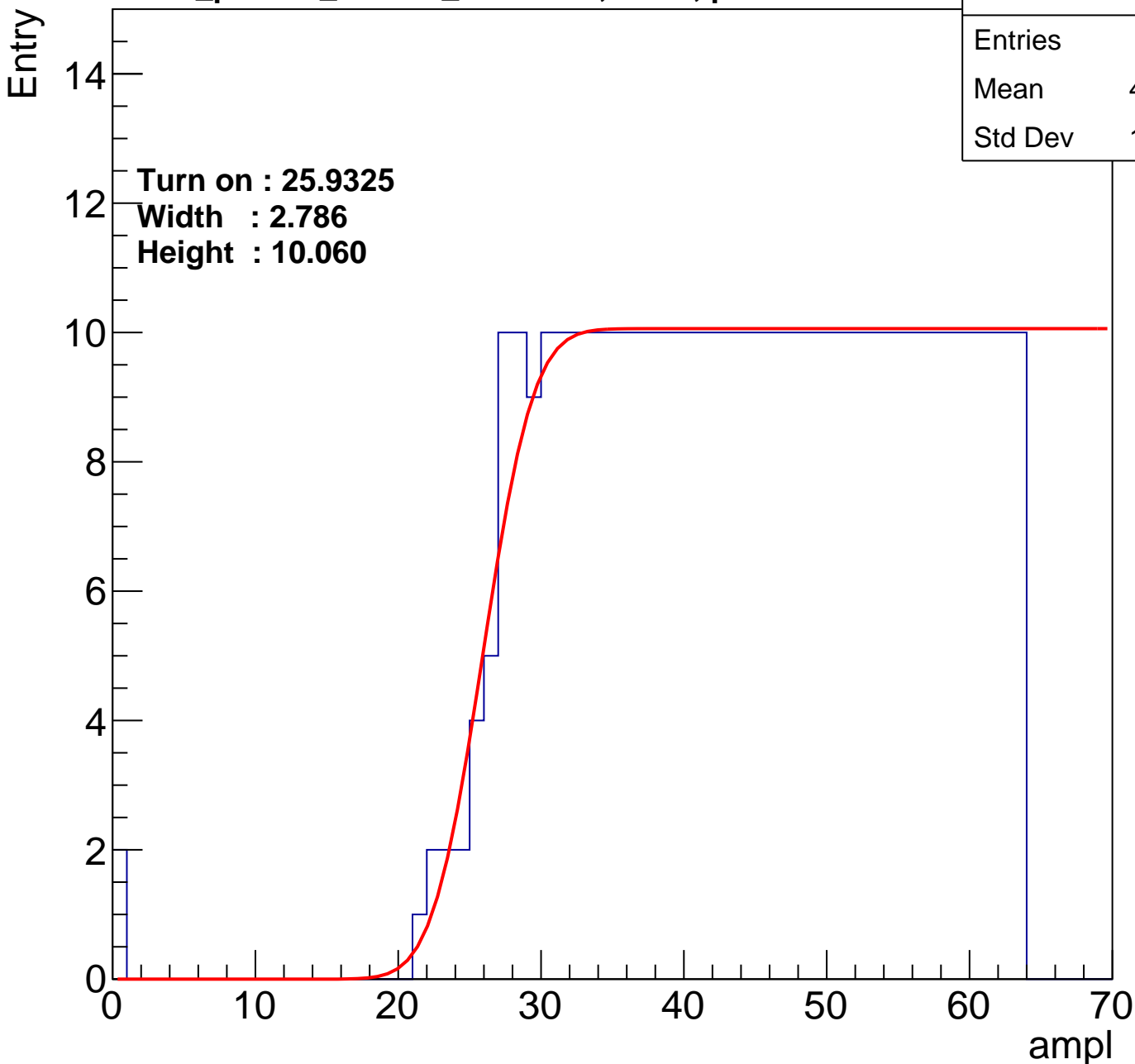
calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.95
Std Dev	11.64

Turn on : 25.9325

Width : 2.786

Height : 10.060



B0L002S, U20-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.84
Std Dev	11.59

Turn on : 28.4656

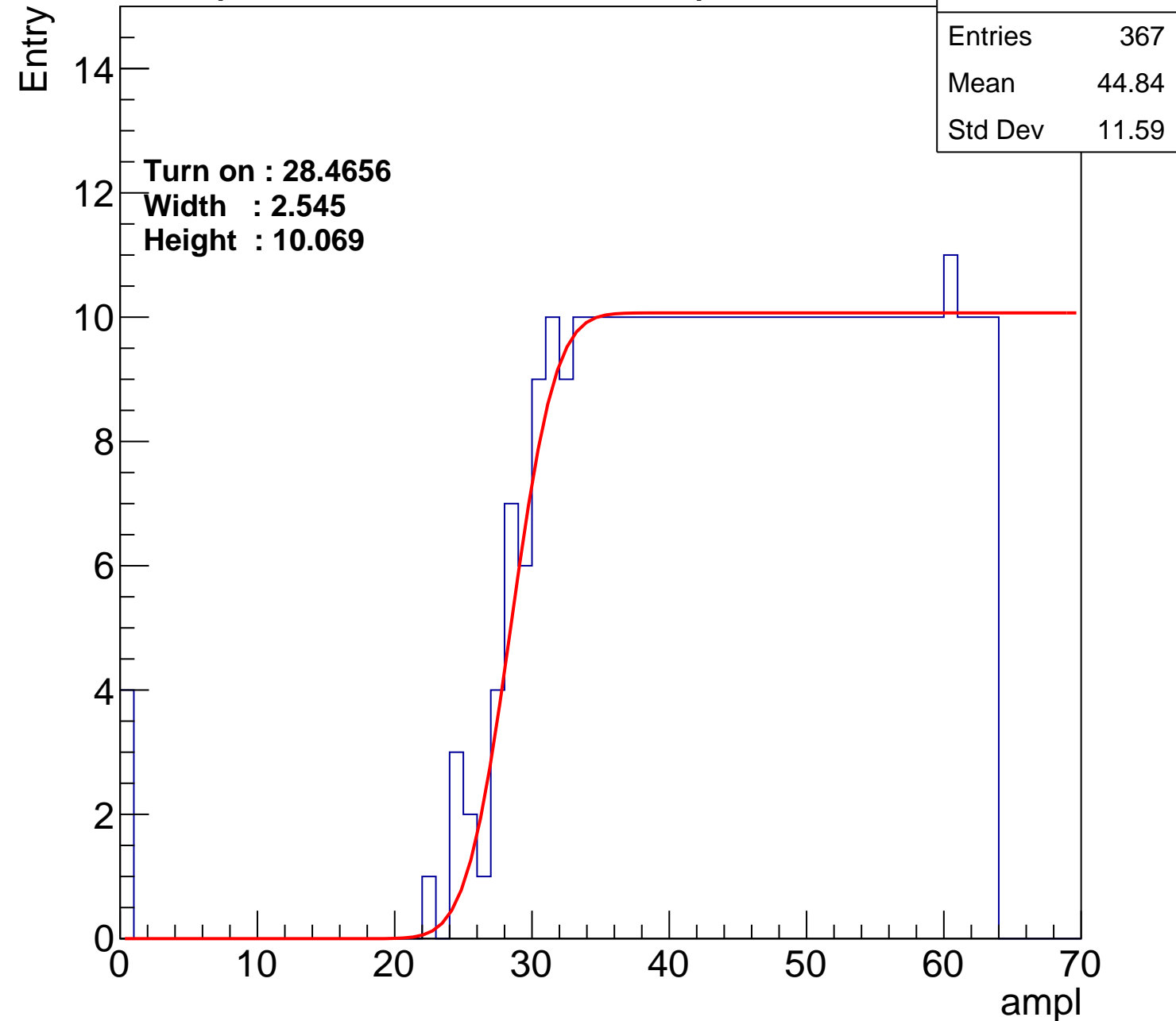
Width : 2.545

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch51

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.38
Std Dev	10.82

Turn on : 28.4584

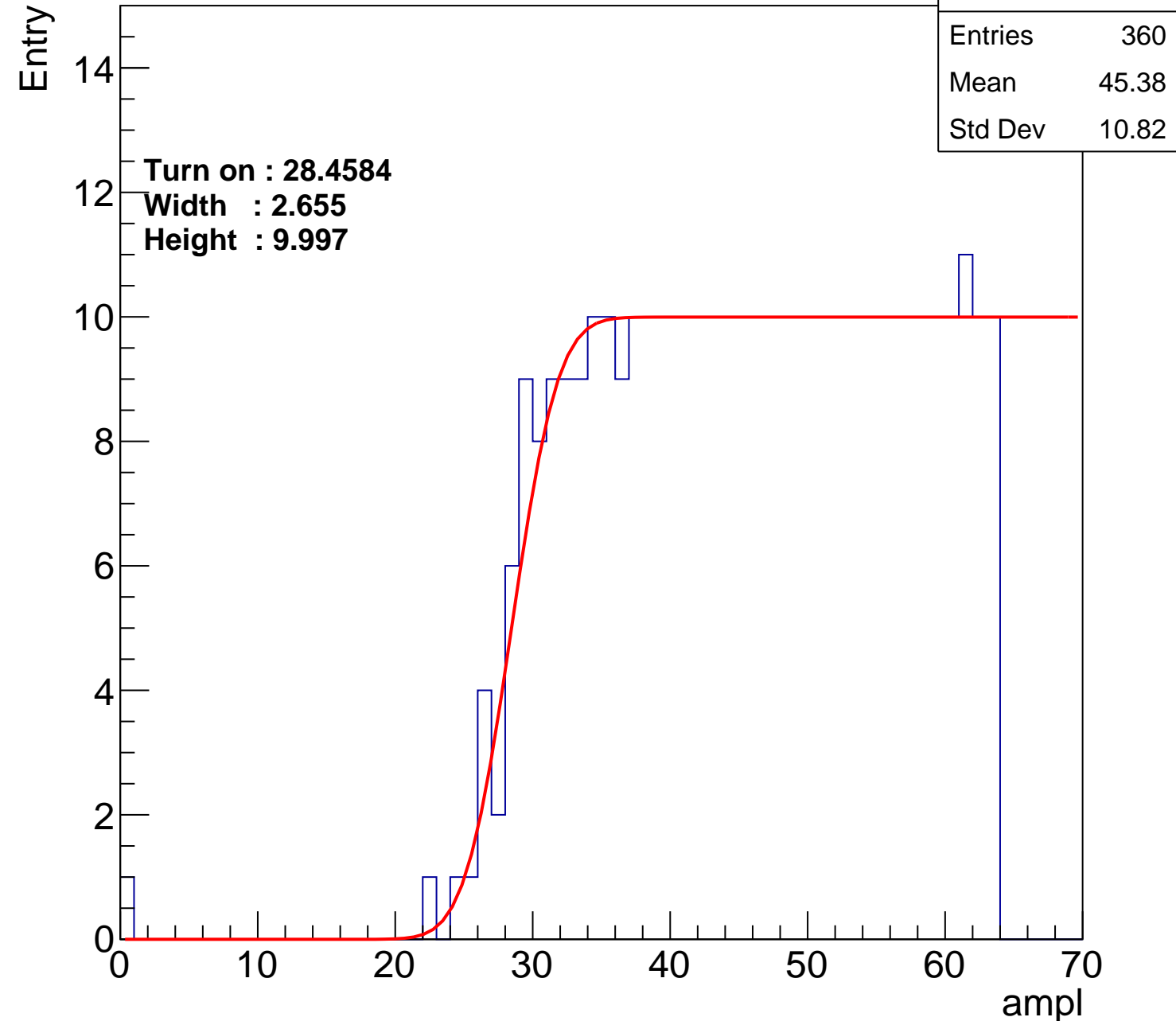
Width : 2.655

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.02
Std Dev	11.3

Turn on : 27.7182

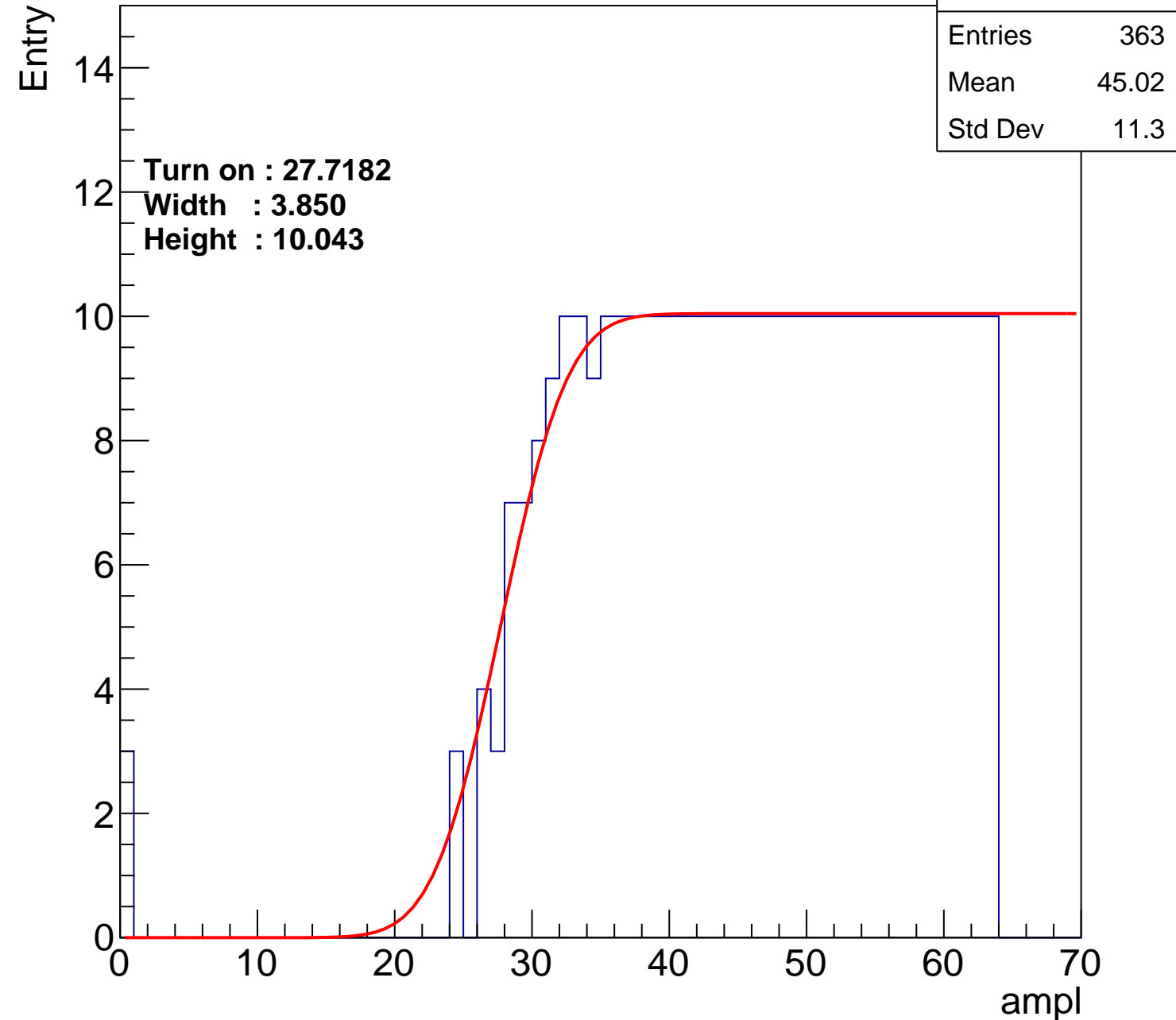
Width : 3.850

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.71
Std Dev	10.54

Turn on : 29.1418

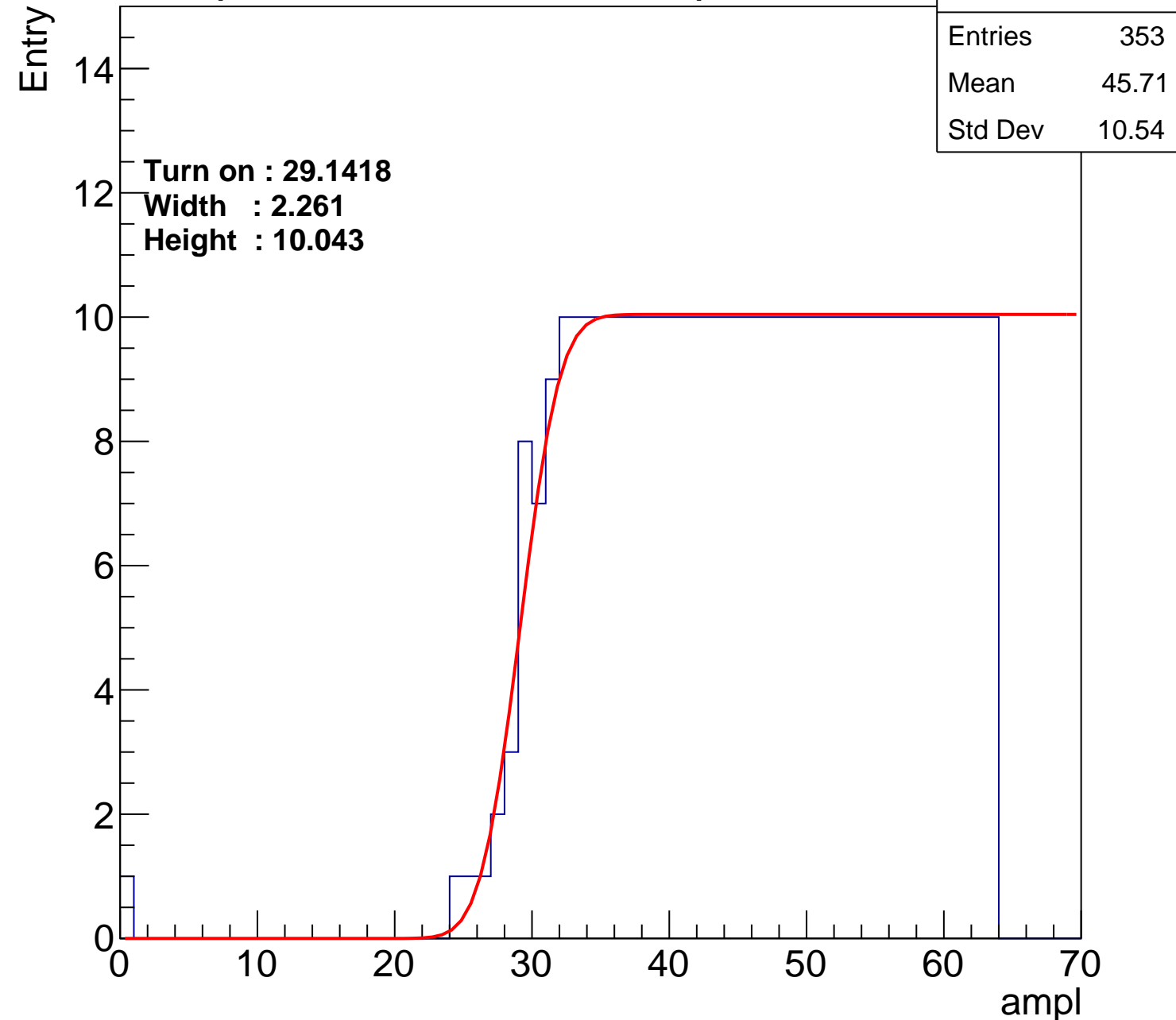
Width : 2.261

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	345
Mean	45.99
Std Dev	10.62

Turn on : 30.0032

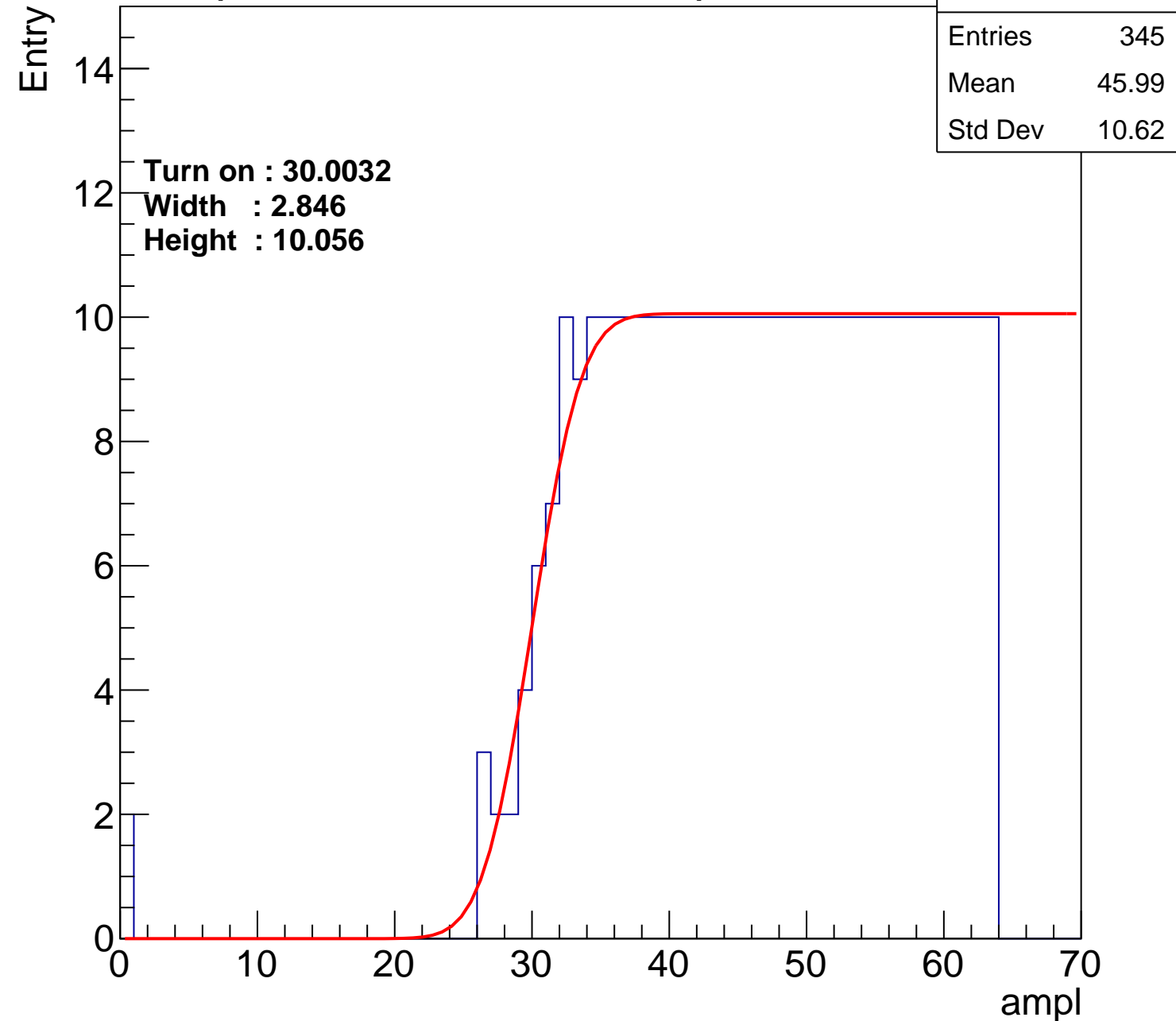
Width : 2.846

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch55

calib_packv5_042523_0143.root, FC#8, port C1

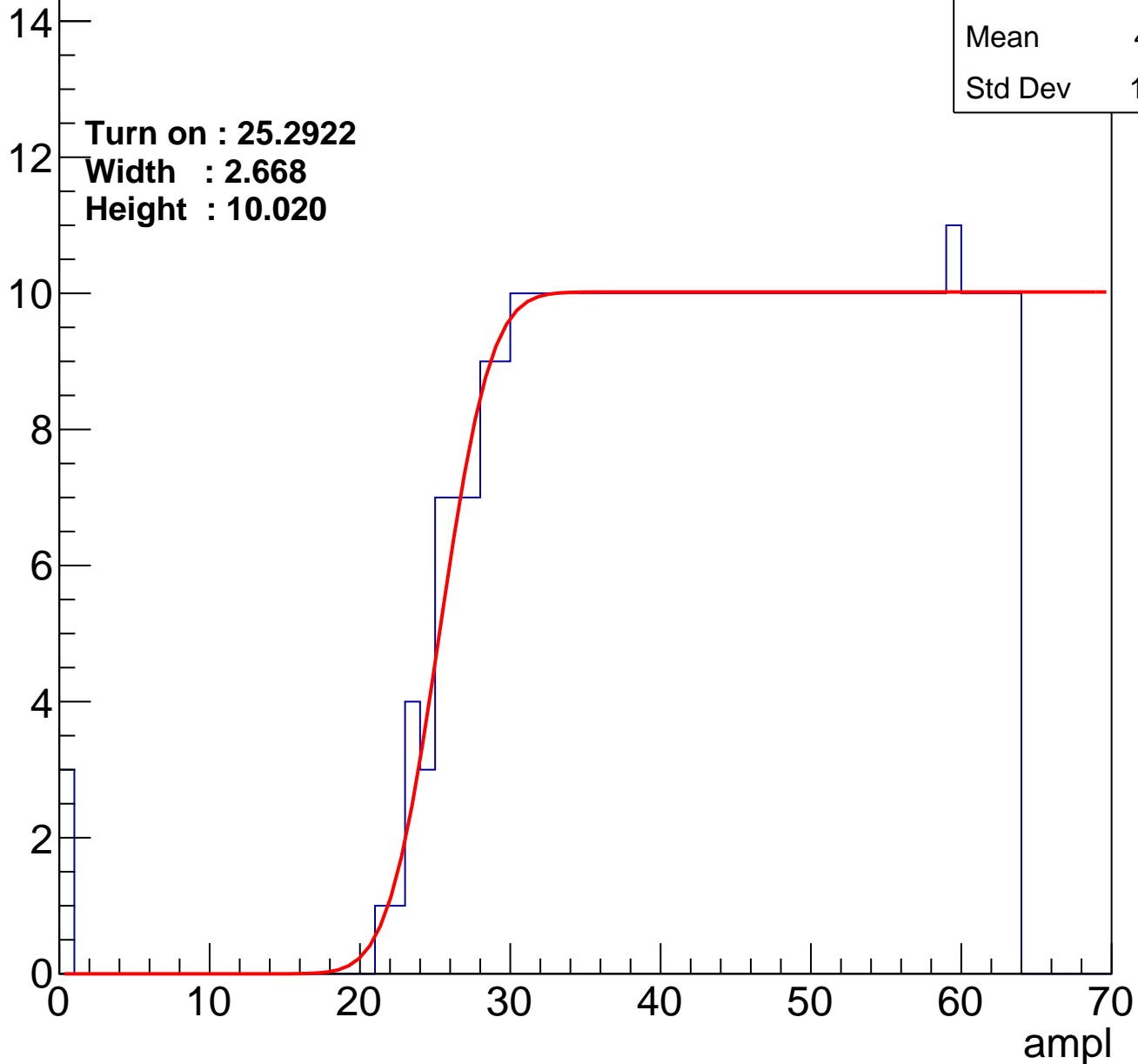
Entries	392
Mean	43.71
Std Dev	11.95

Turn on : 25.2922

Width : 2.668

Height : 10.020

Entry



B0L002S, U20-ch56

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.31
Std Dev	11.76

Turn on : 26.5503

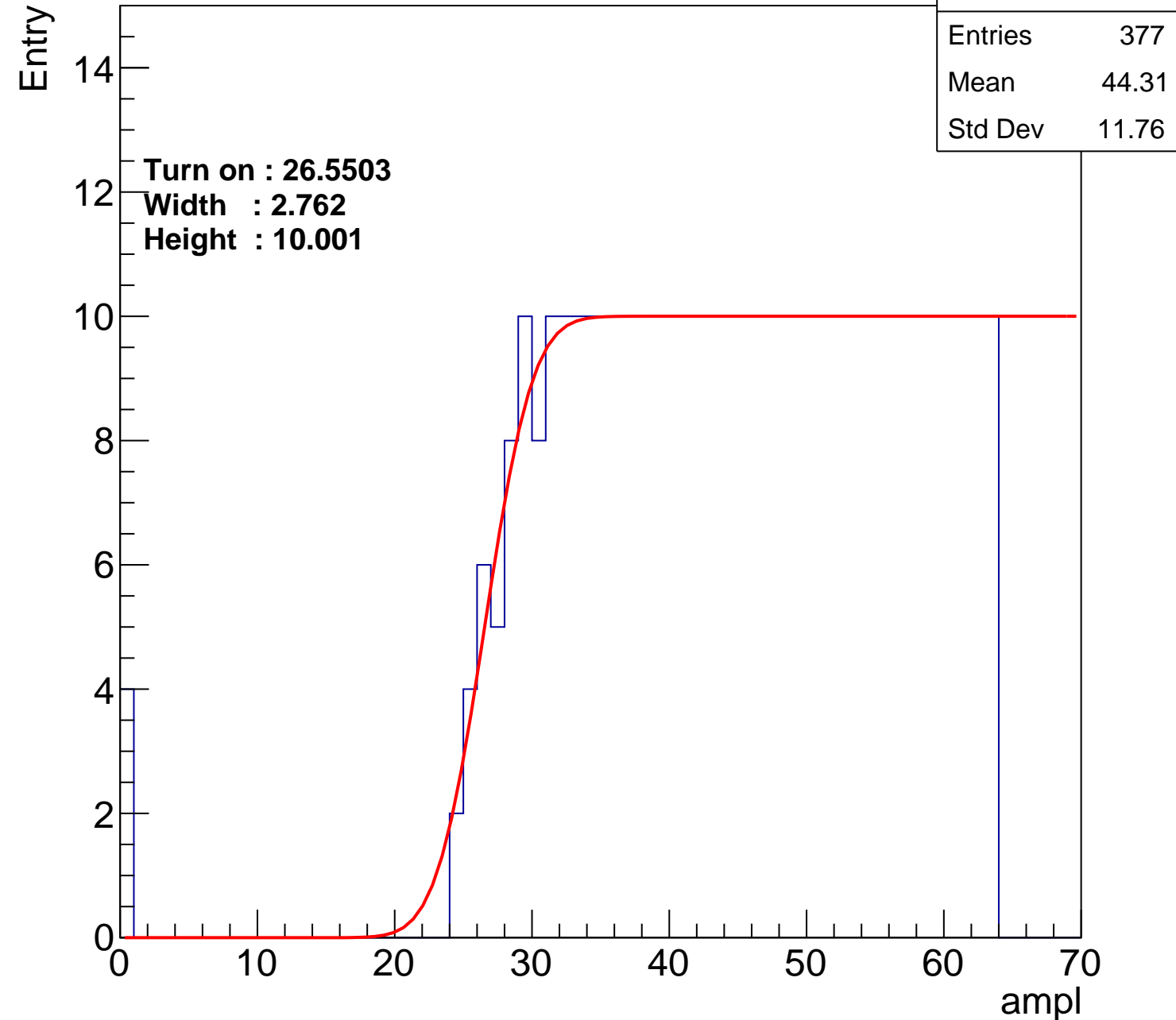
Width : 2.762

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.19
Std Dev	11.71

Turn on : 26.3141

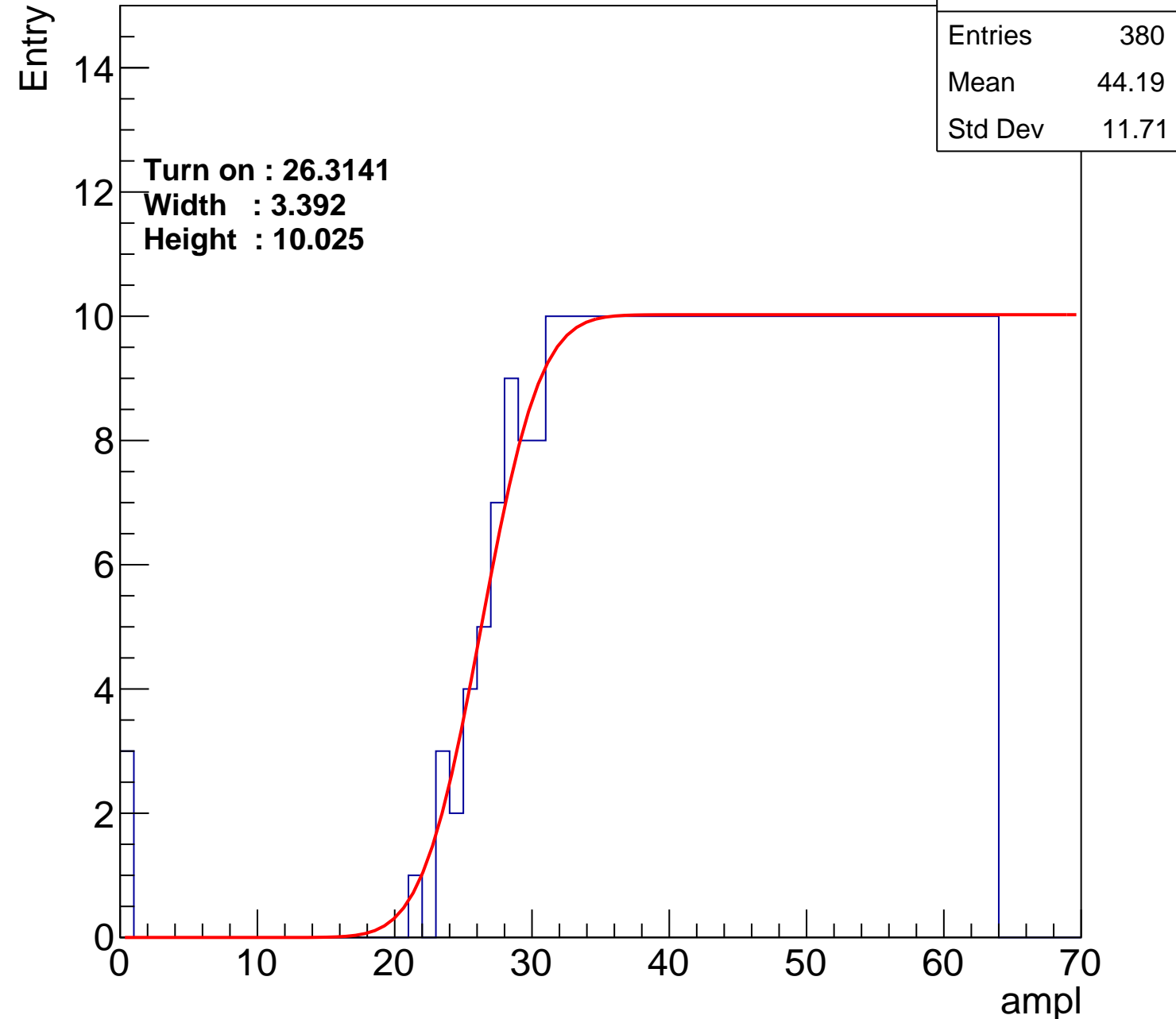
Width : 3.392

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch58

calib_packv5_042523_0143.root, FC#8, port C1

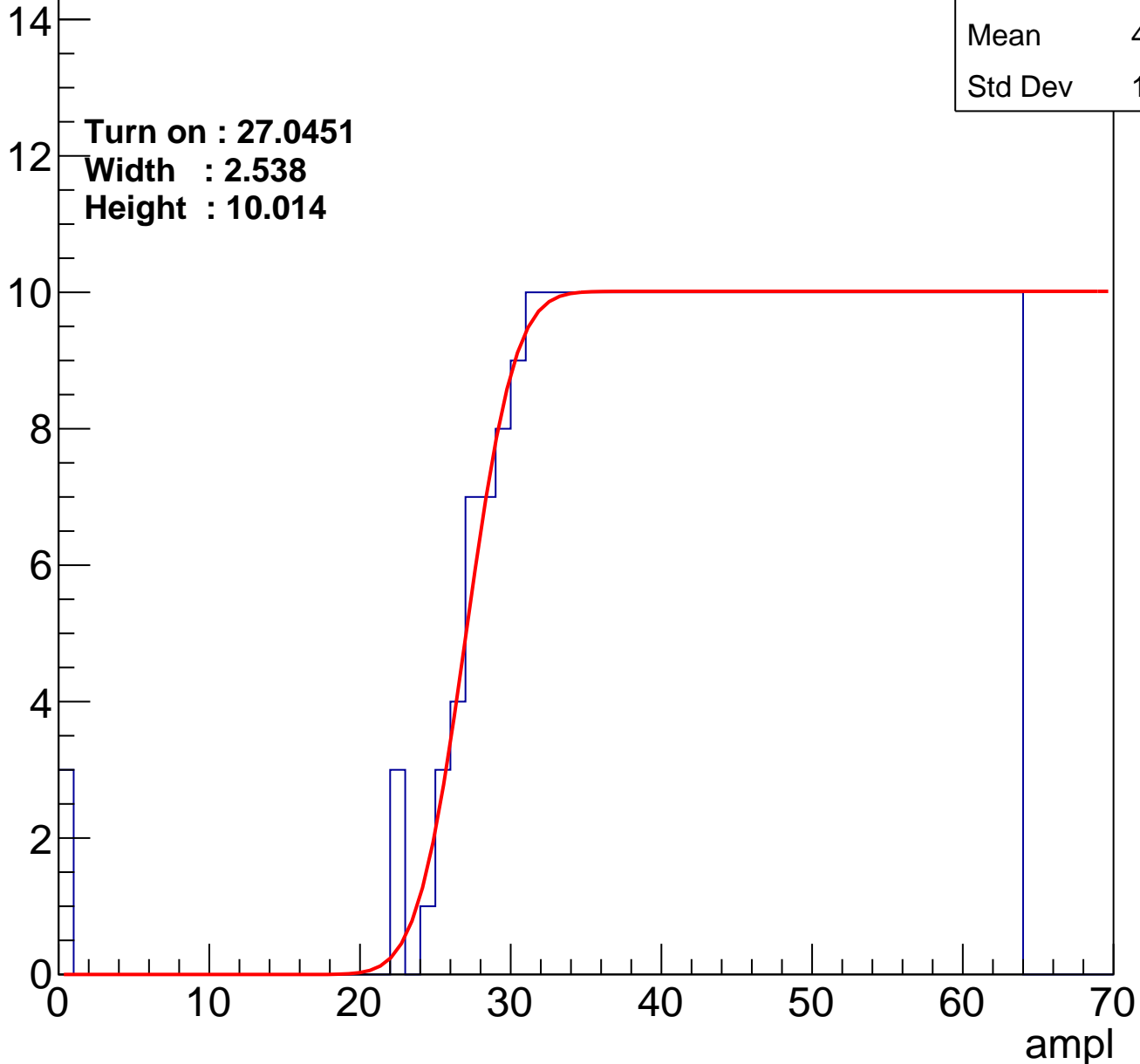
Entries	375
Mean	44.44
Std Dev	11.57

Turn on : 27.0451

Width : 2.538

Height : 10.014

Entry



B0L002S, U20-ch59

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.8
Std Dev	11.38

Turn on : 27.7799

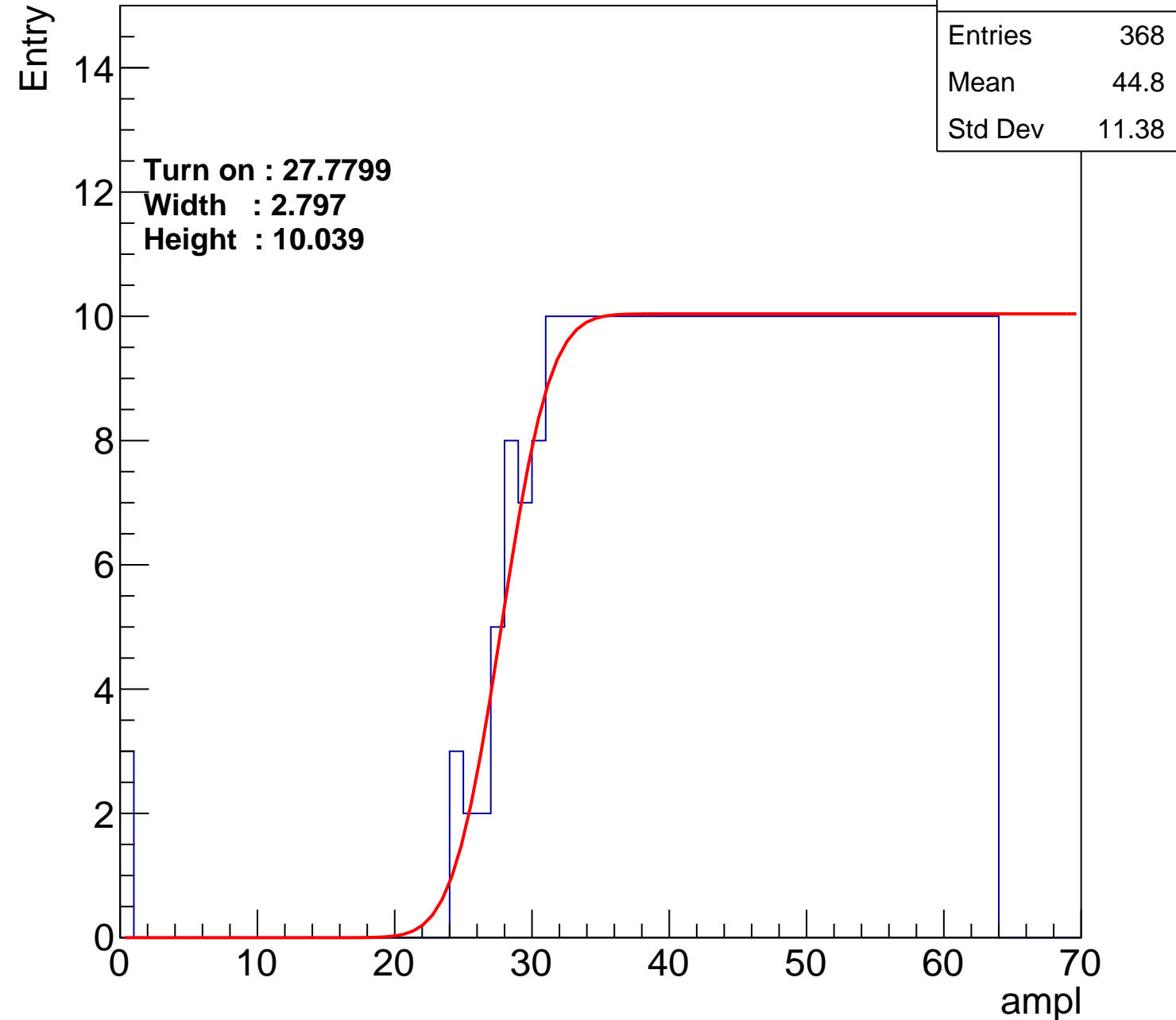
Width : 2.797

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch60

calib_packv5_042523_0143.root, FC#8, port C1

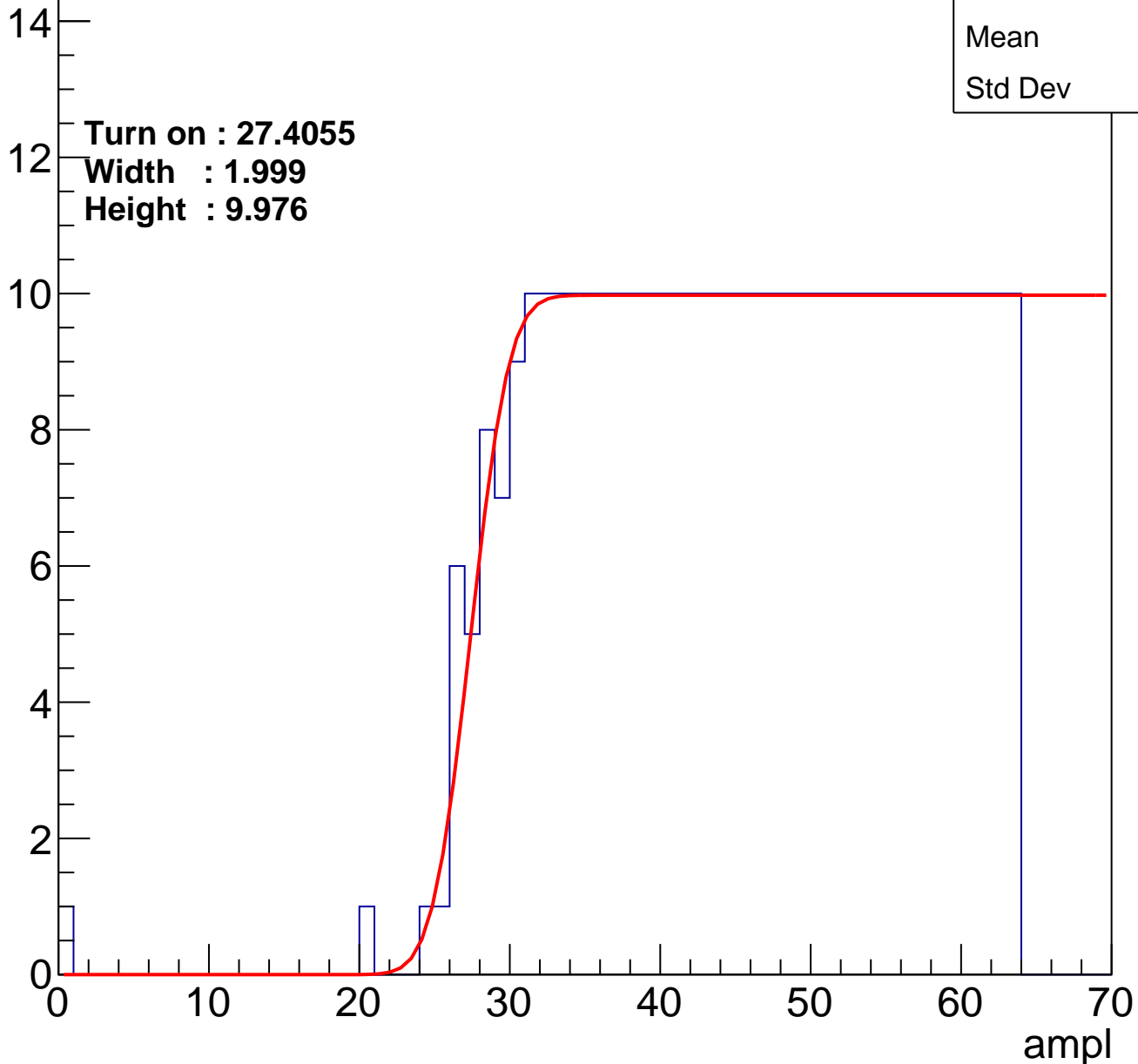
Entries	369
Mean	44.9
Std Dev	11

Turn on : 27.4055

Width : 1.999

Height : 9.976

Entry



B0L002S, U20-ch61

calib_packv5_042523_0143.root, FC#8, port C1

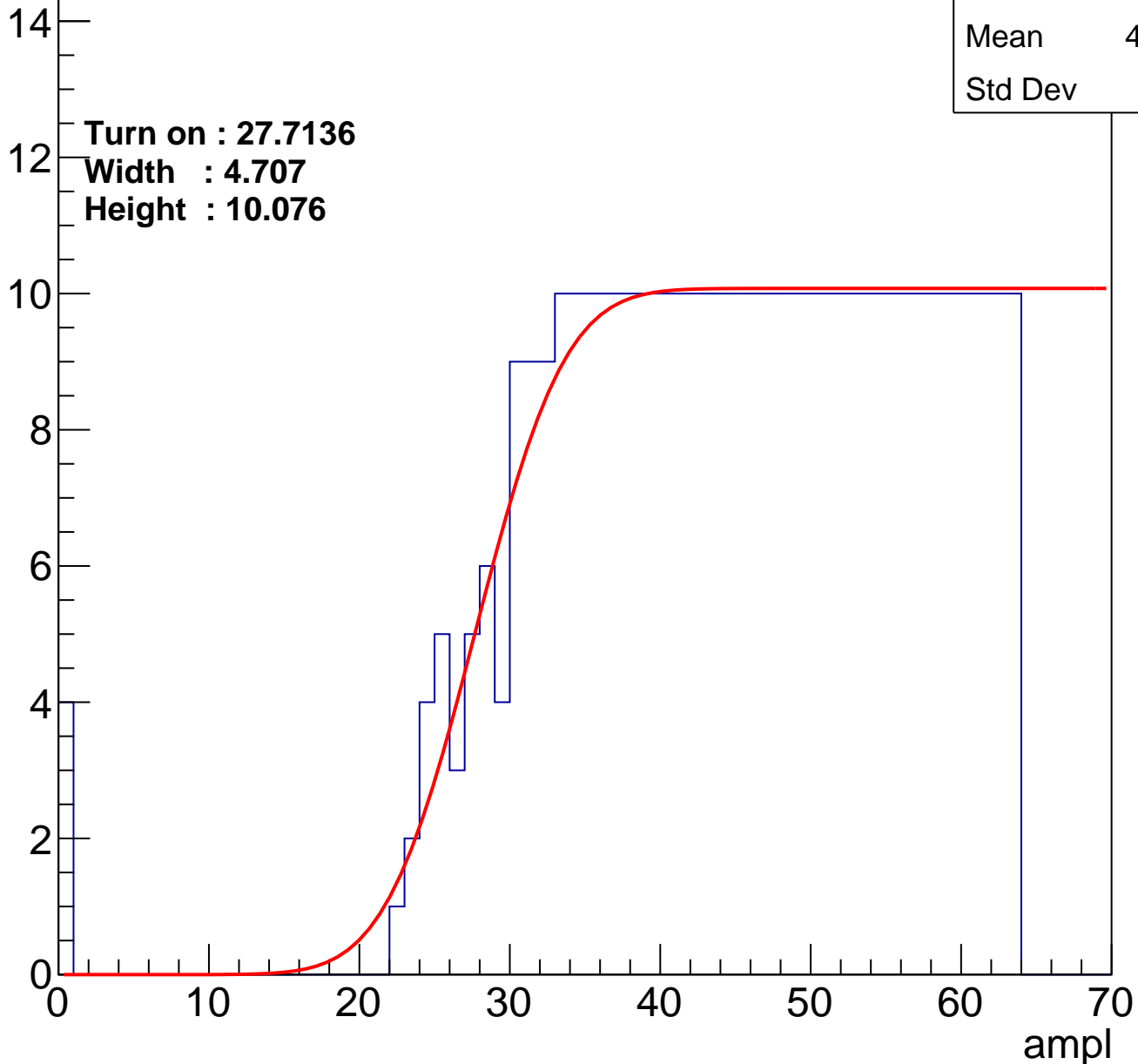
Entries	371
Mean	44.48
Std Dev	11.8

Turn on : 27.7136

Width : 4.707

Height : 10.076

Entry



B0L002S, U20-ch62

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.05
Std Dev	10.9

Turn on : 27.7534

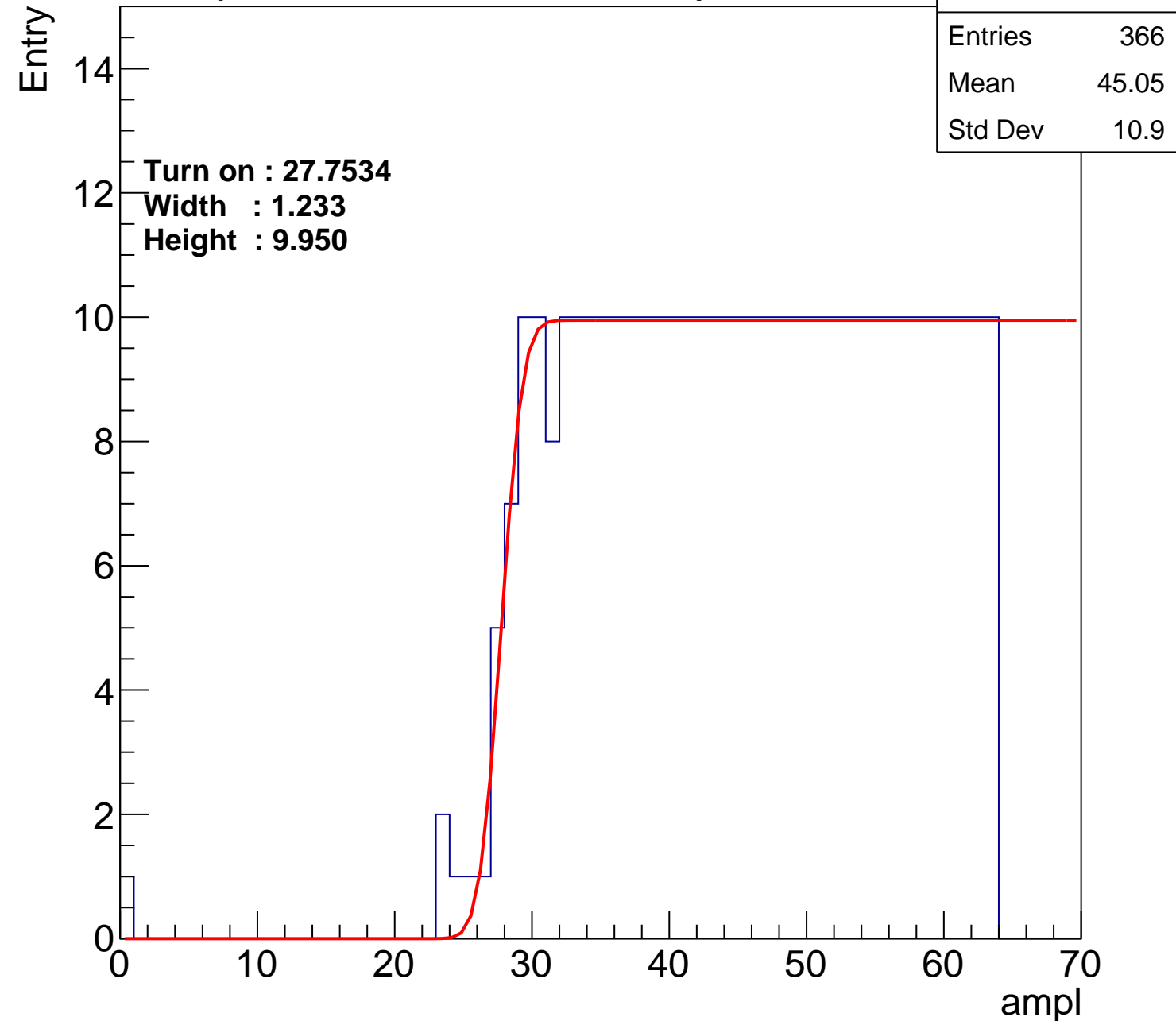
Width : 1.233

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch63

calib_packv5_042523_0143.root, FC#8, port C1

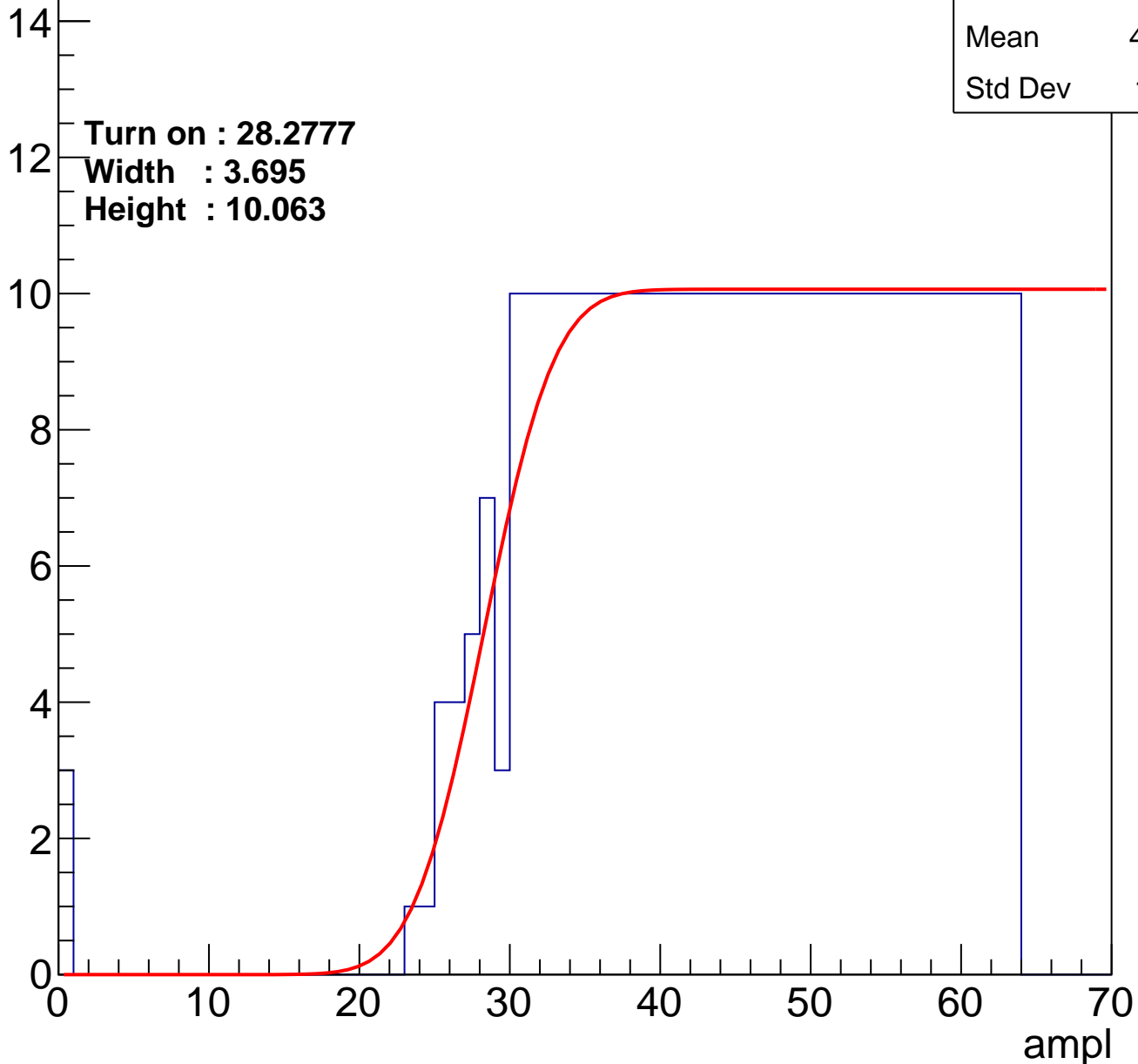
Entries	368
Mean	44.78
Std Dev	11.41

Turn on : 28.2777

Width : 3.695

Height : 10.063

Entry



B0L002S, U20-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.9
Std Dev	11.33

Turn on : 27.8312

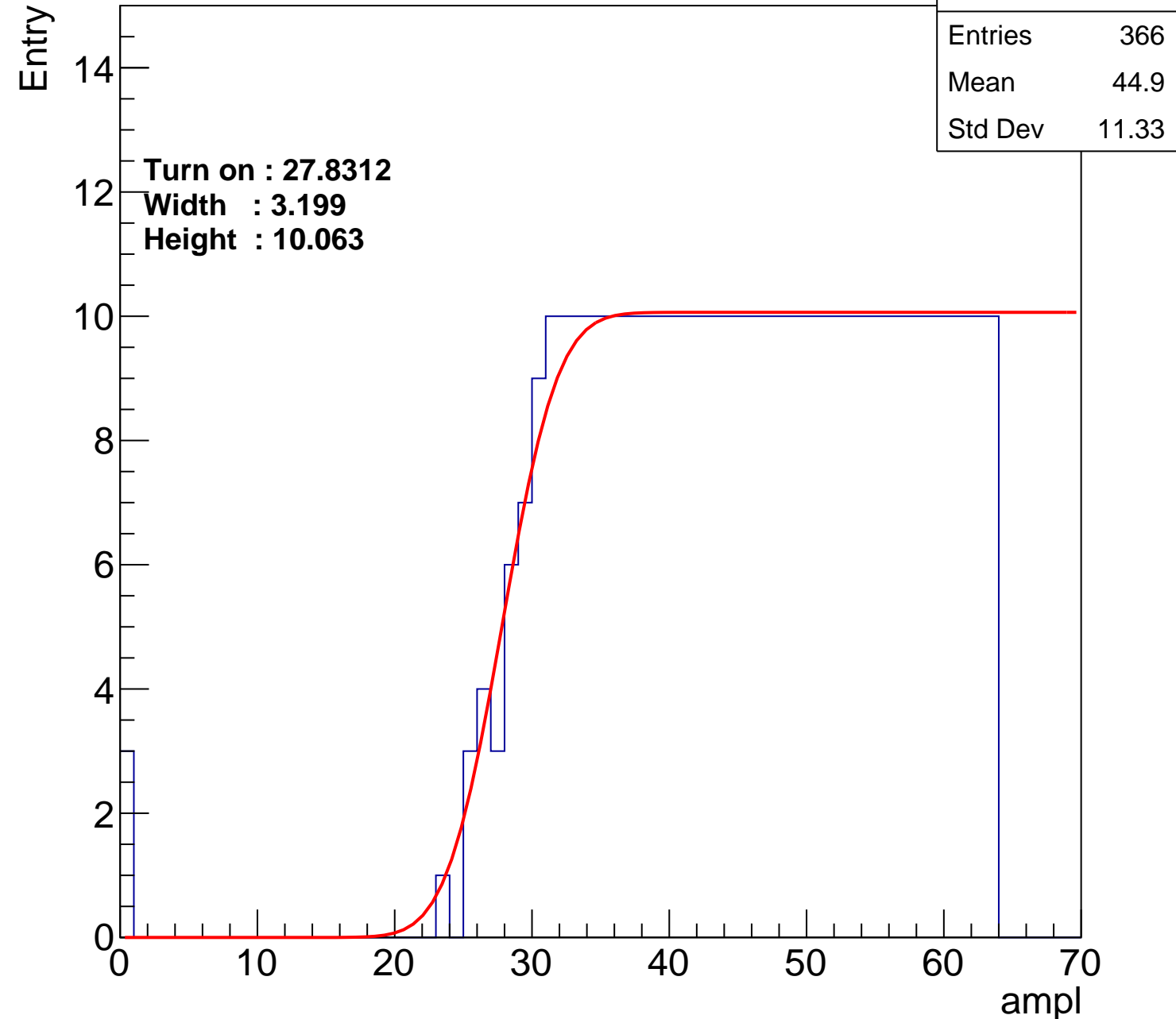
Width : 3.199

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.36
Std Dev	11.19

Turn on : 28.7117

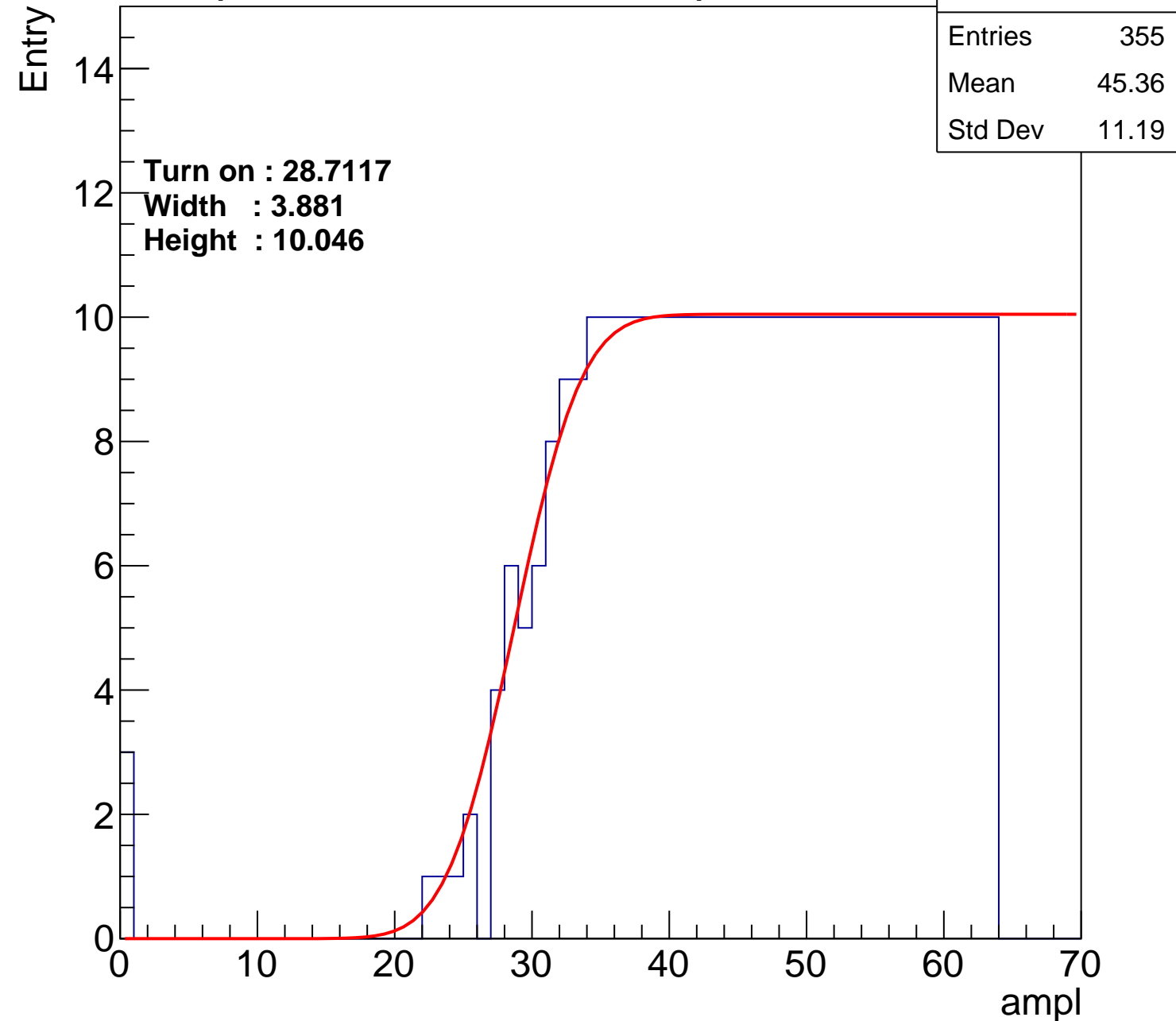
Width : 3.881

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch66

calib_packv5_042523_0143.root, FC#8, port C1

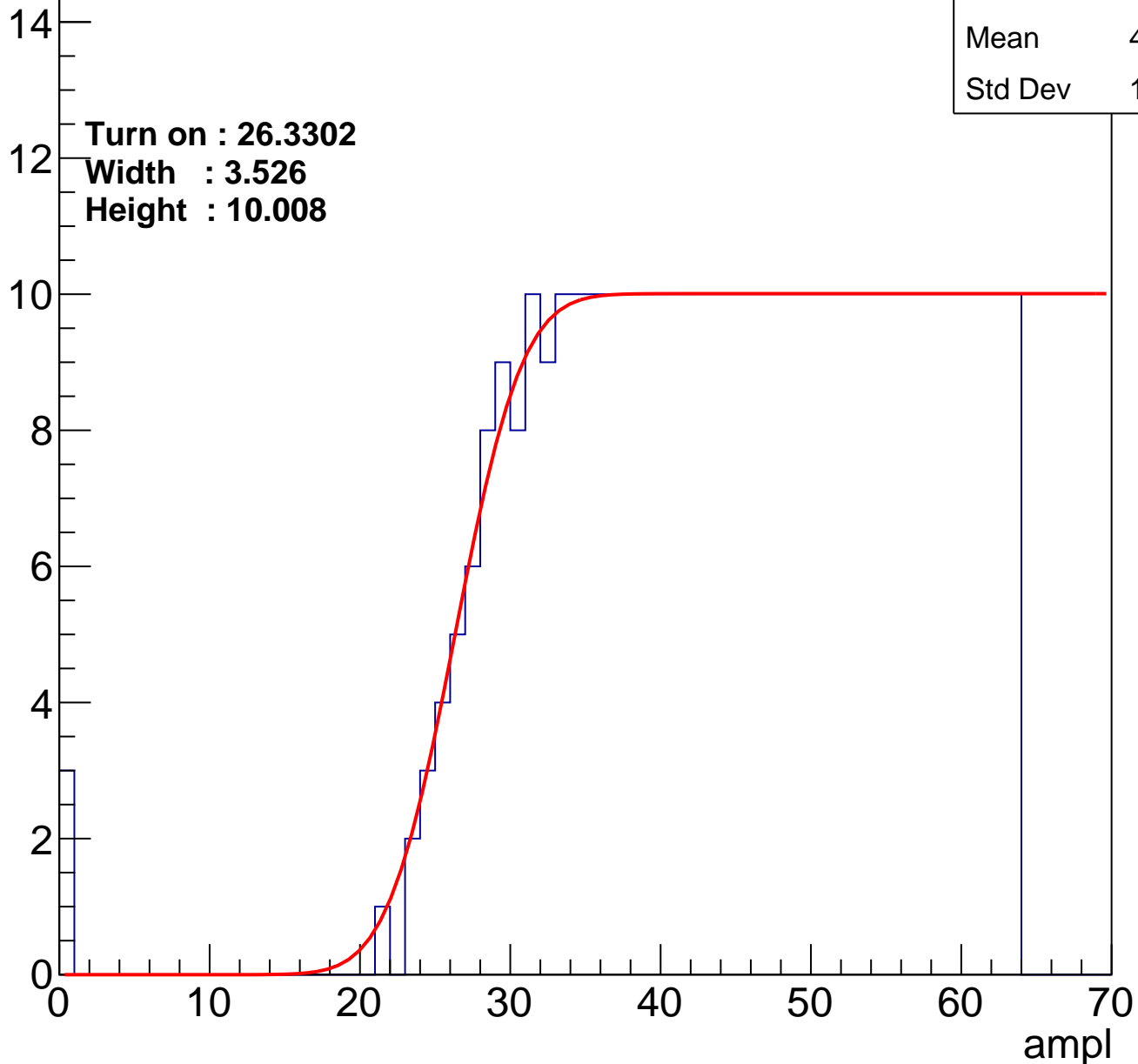
Entries	378
Mean	44.27
Std Dev	11.68

Turn on : 26.3302

Width : 3.526

Height : 10.008

Entry



B0L002S, U20-ch67

calib_packv5_042523_0143.root, FC#8, port C1

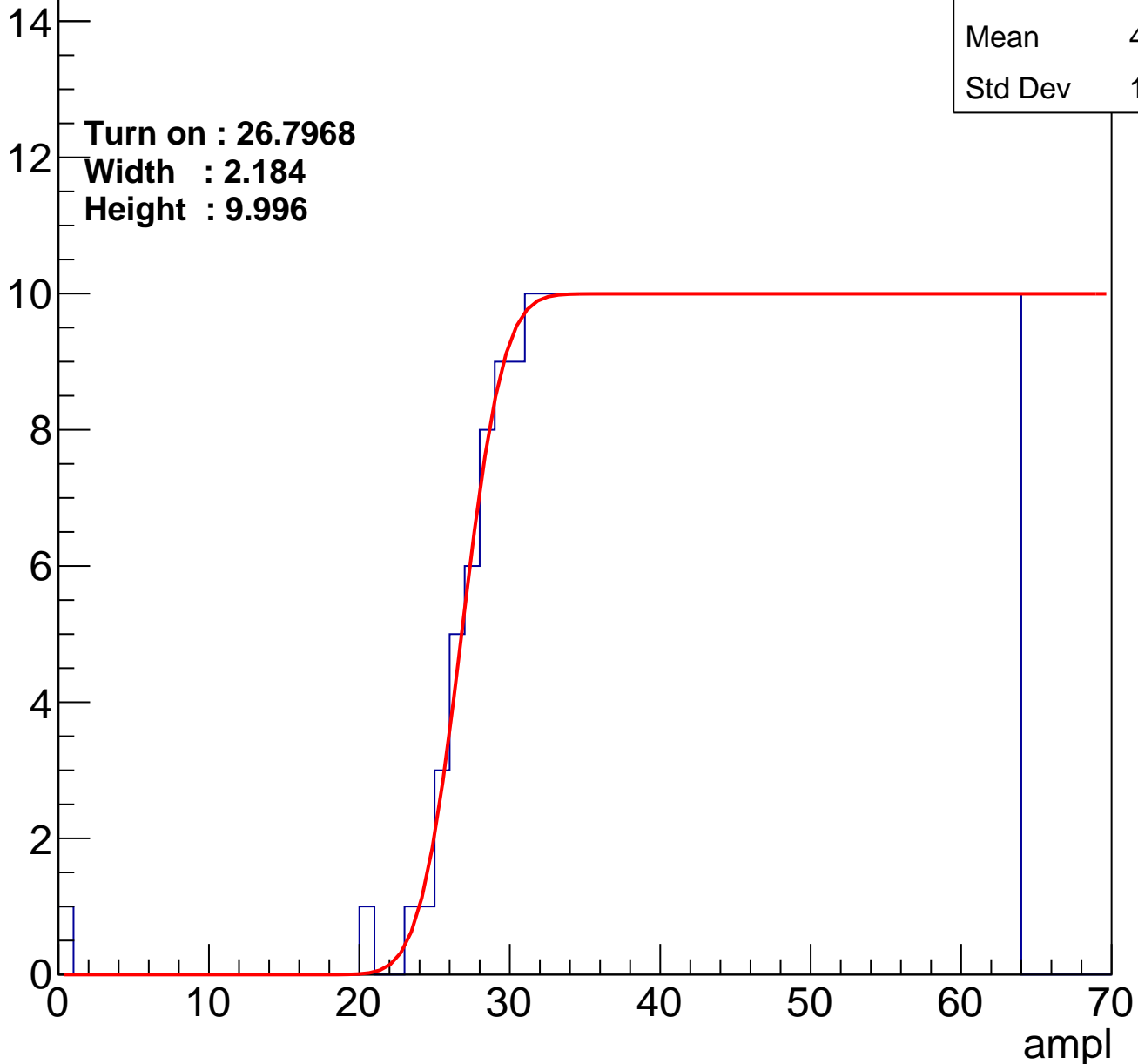
Entries	374
Mean	44.65
Std Dev	11.13

Turn on : 26.7968

Width : 2.184

Height : 9.996

Entry



B0L002S, U20-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.48
Std Dev	10.71

Turn on : 29.0572

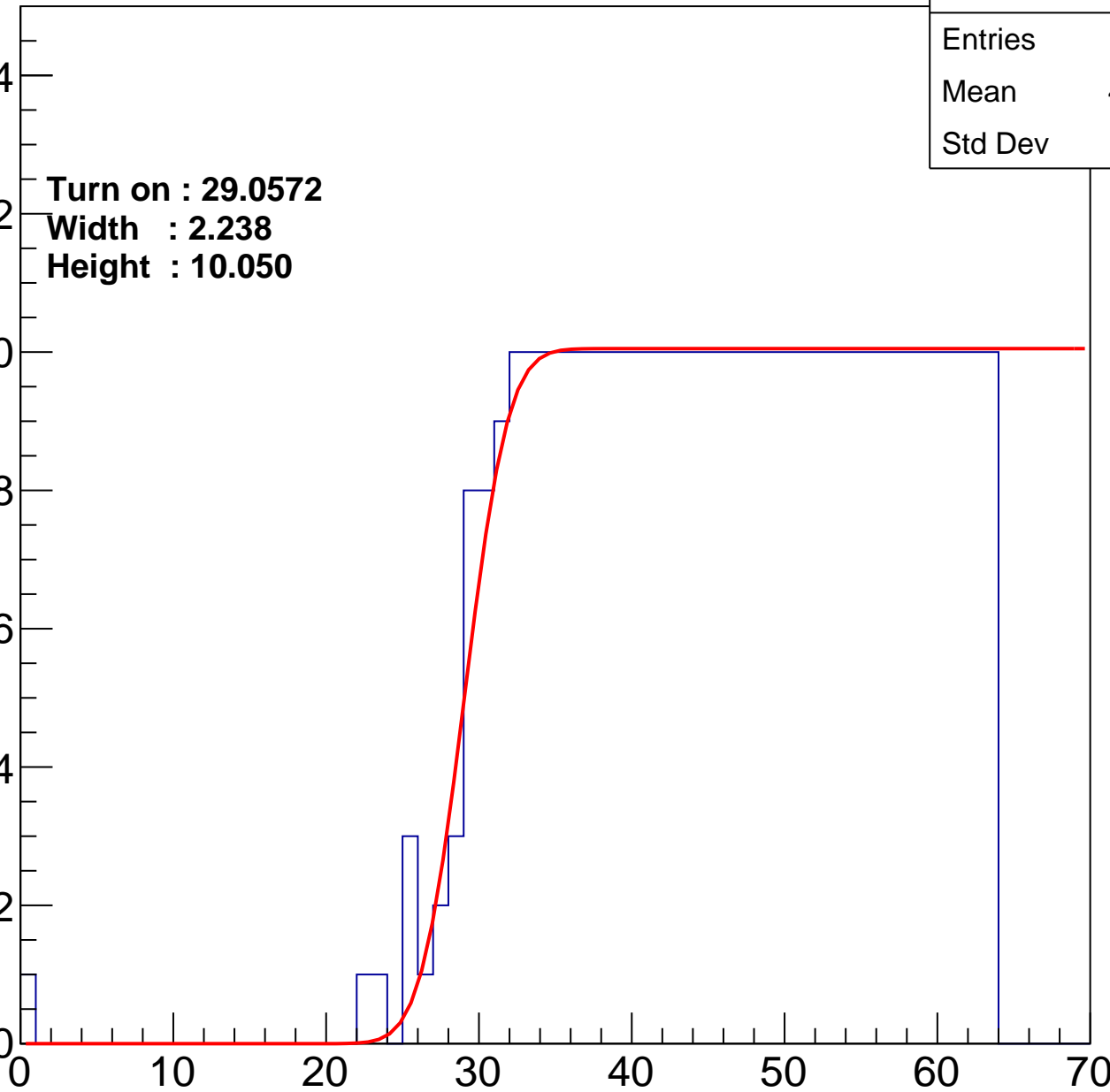
Width : 2.238

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch69

calib_packv5_042523_0143.root, FC#8, port C1

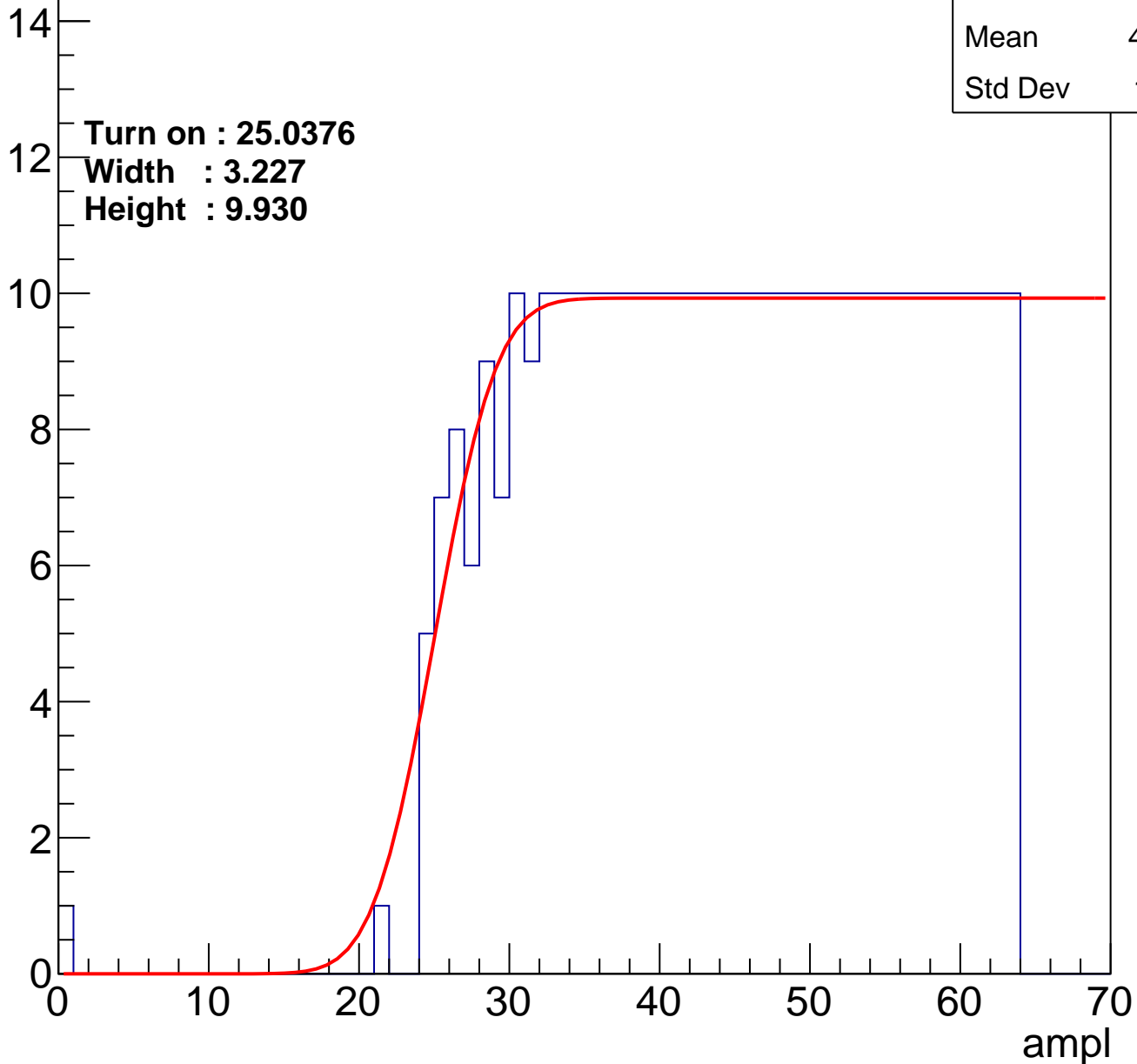
Entries	383
Mean	44.18
Std Dev	11.41

Turn on : 25.0376

Width : 3.227

Height : 9.930

Entry



B0L002S, U20-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.22
Std Dev	11.05

Turn on : 27.9636

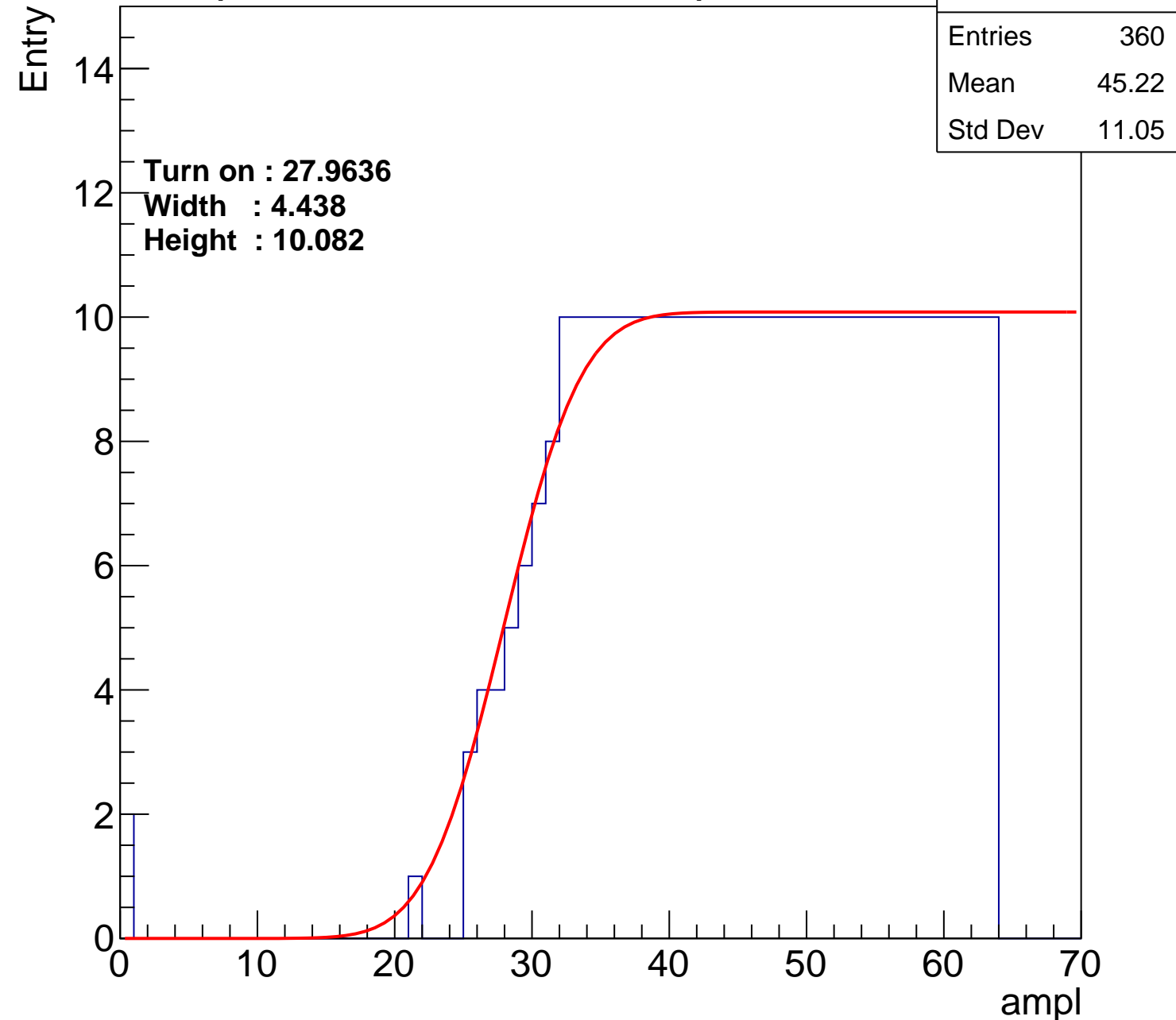
Width : 4.438

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch71

calib_packv5_042523_0143.root, FC#8, port C1

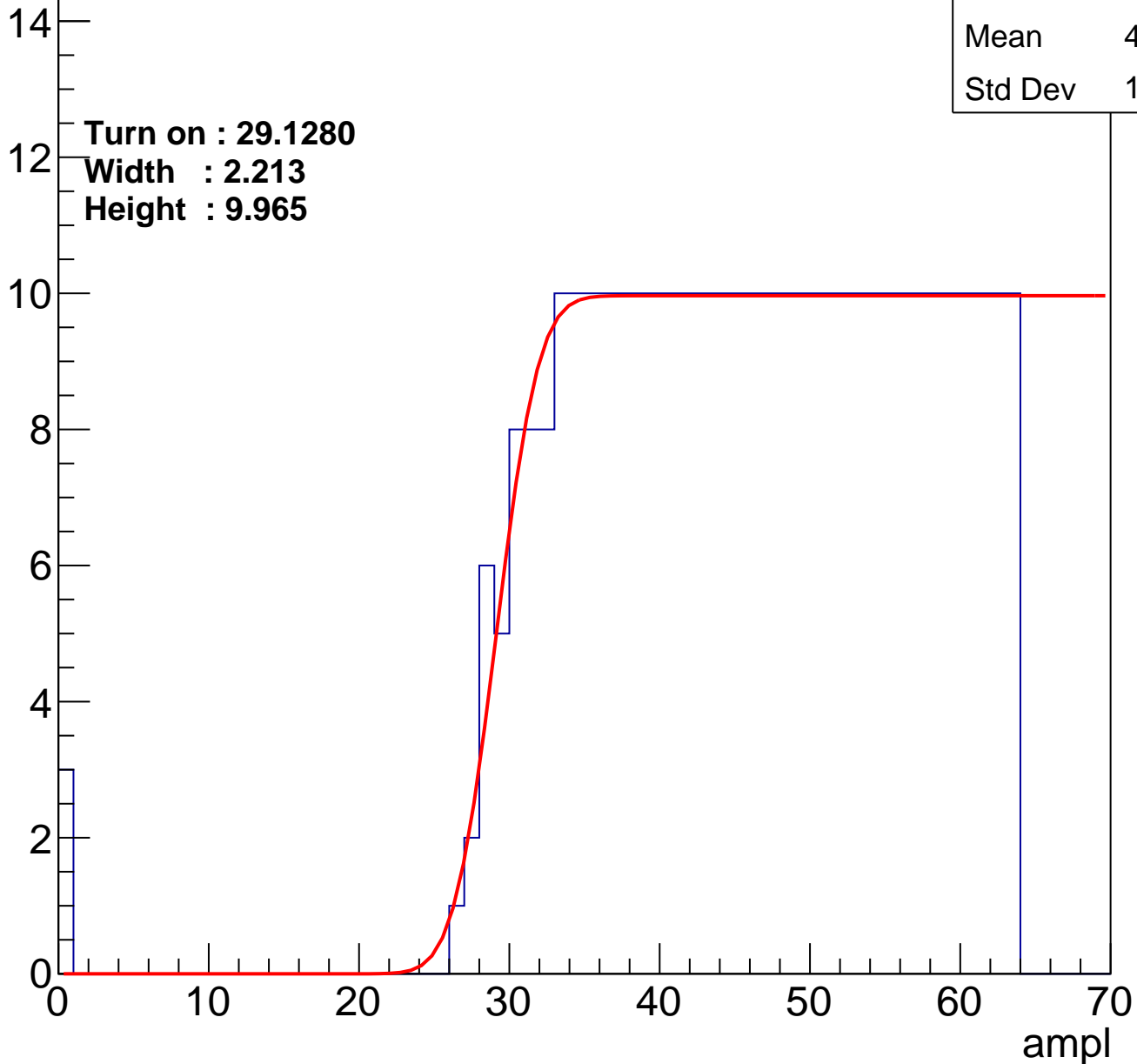
Entries	351
Mean	45.63
Std Dev	10.97

Turn on : 29.1280

Width : 2.213

Height : 9.965

Entry



B0L002S, U20-ch72

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 27.4794

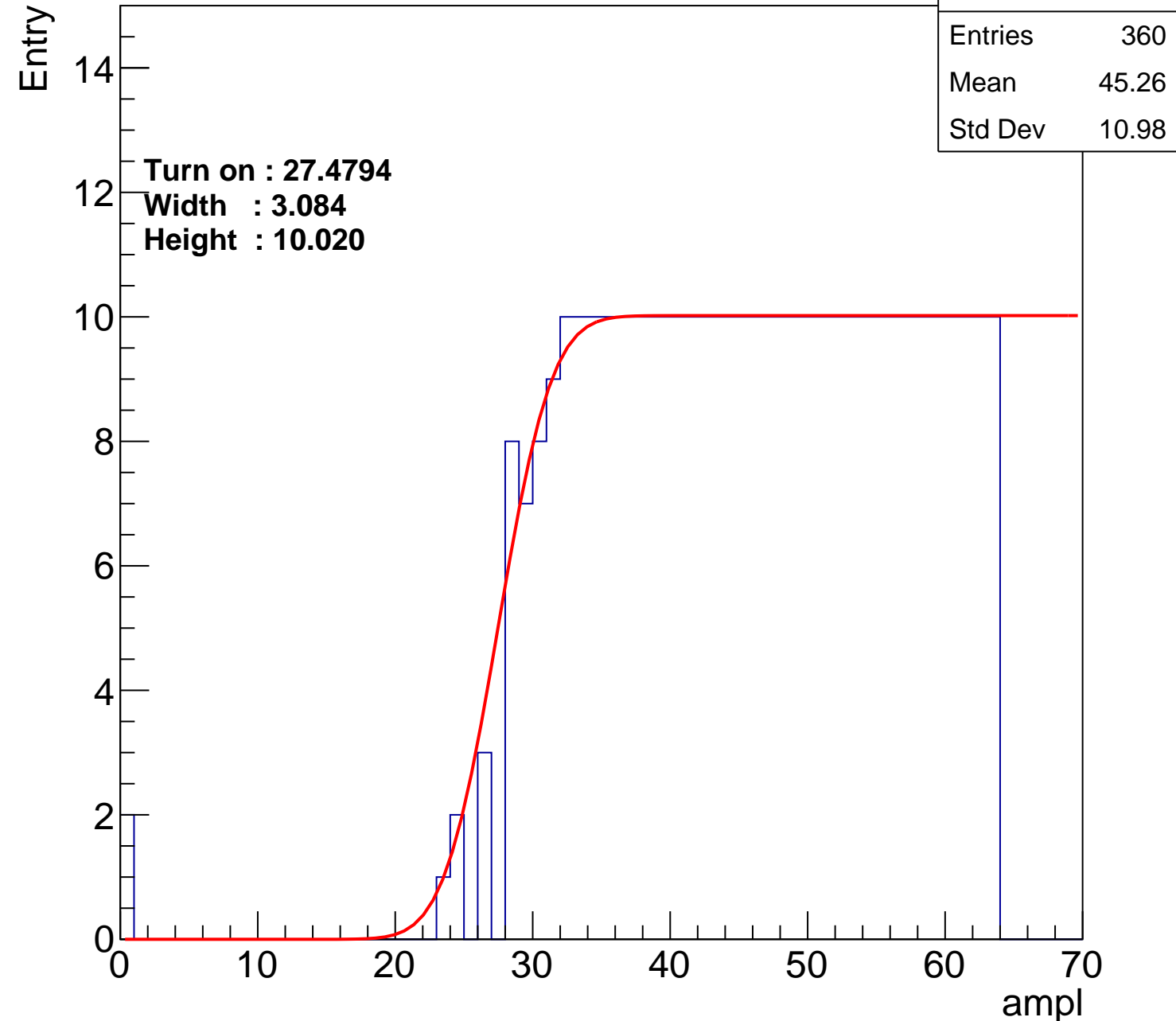
Width : 3.084

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.11
Std Dev	10.93

Turn on : 28.1525

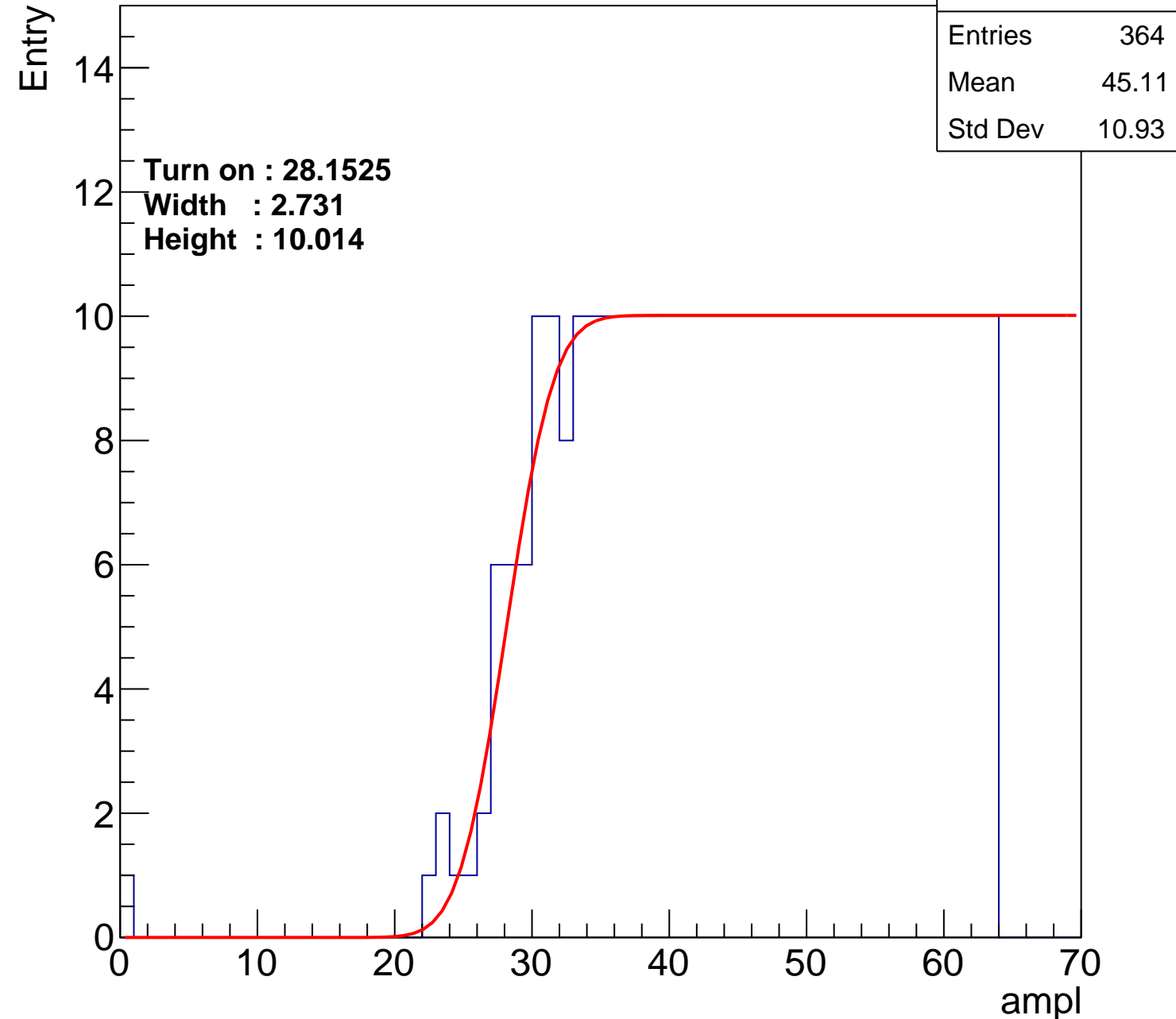
Width : 2.731

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.73
Std Dev	11.11

Turn on : 26.9275

Width : 3.463

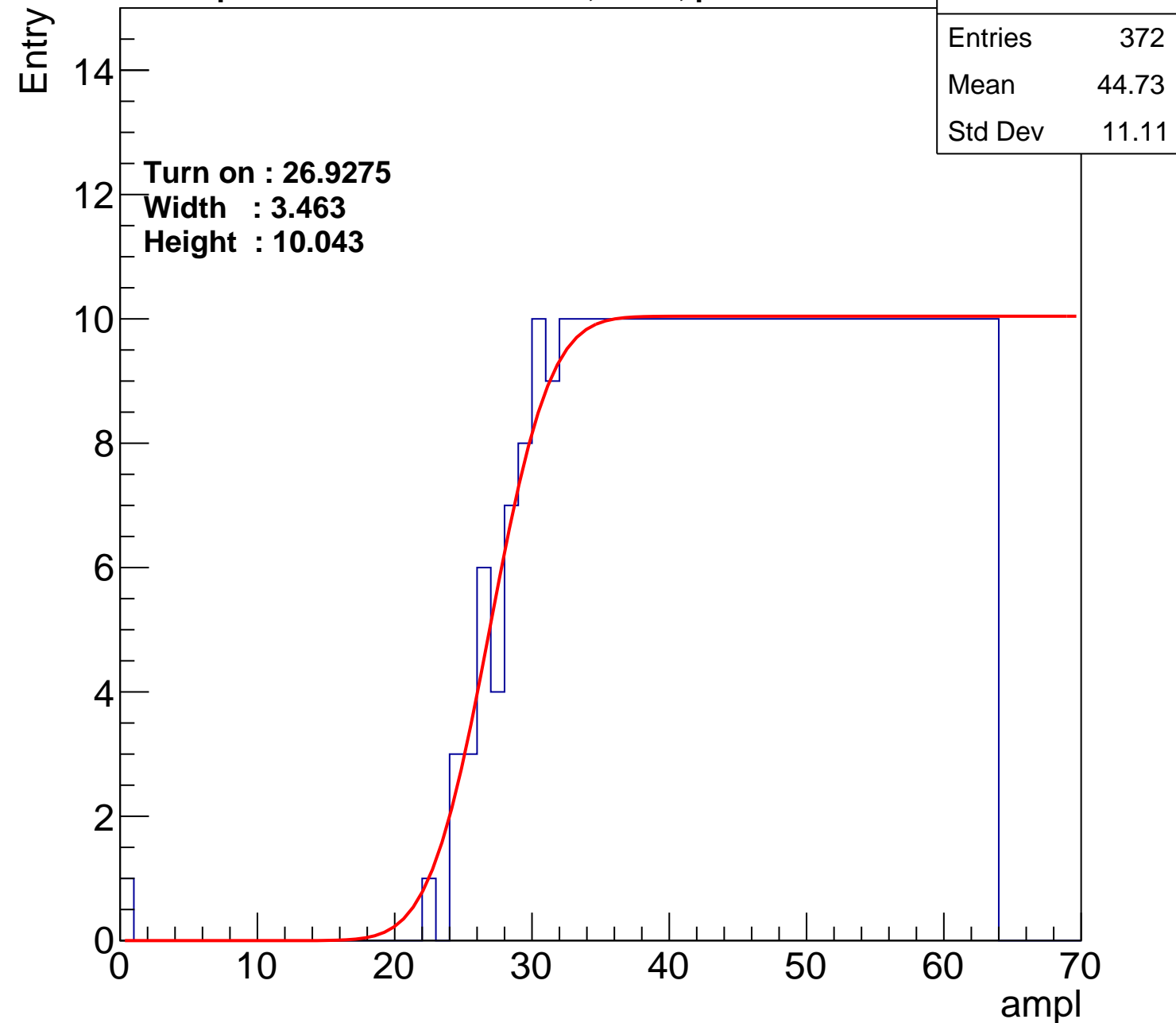
Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U20-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.31
Std Dev	11.37

Turn on : 25.9593

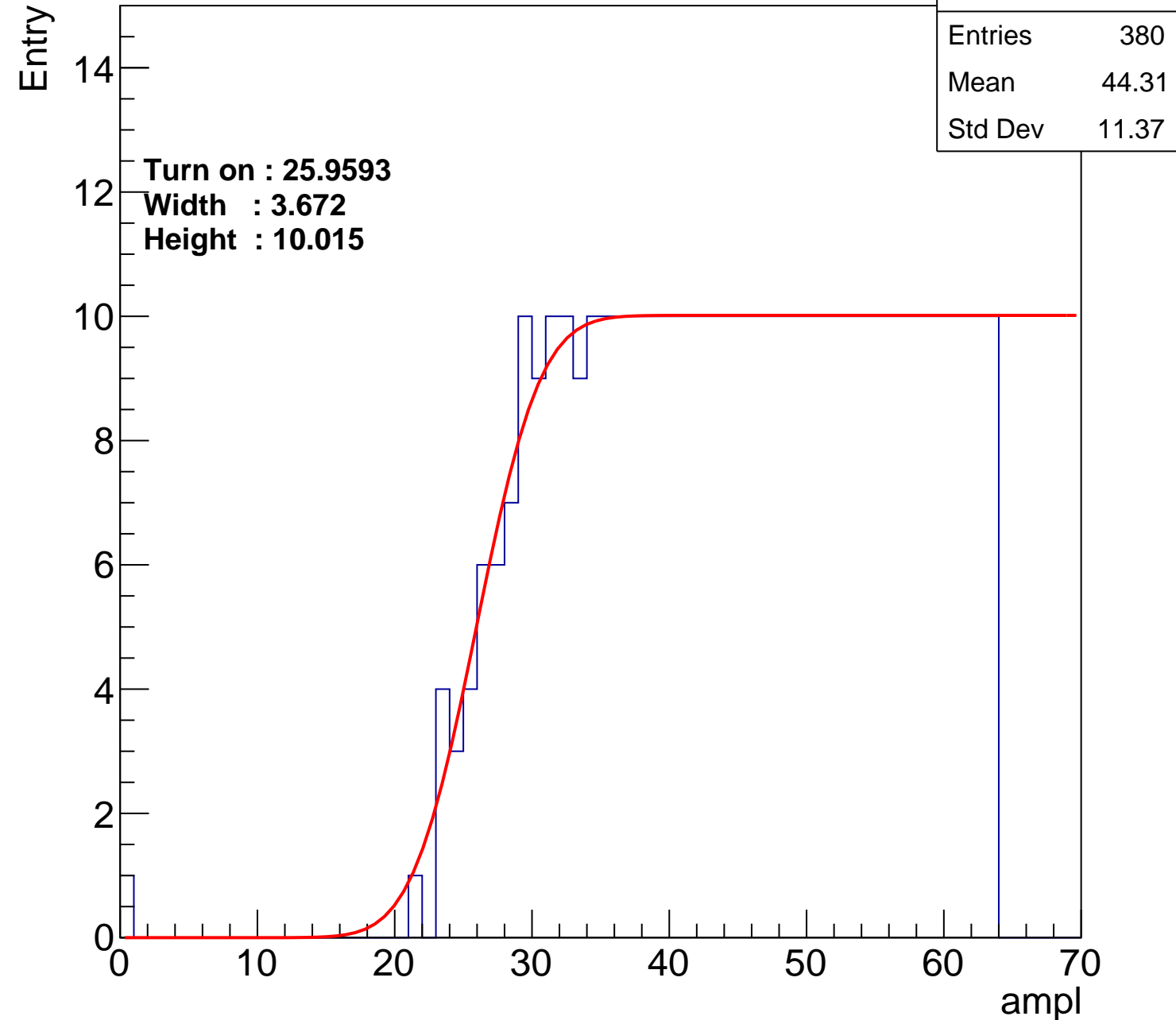
Width : 3.672

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch76

calib_packv5_042523_0143.root, FC#8, port C1

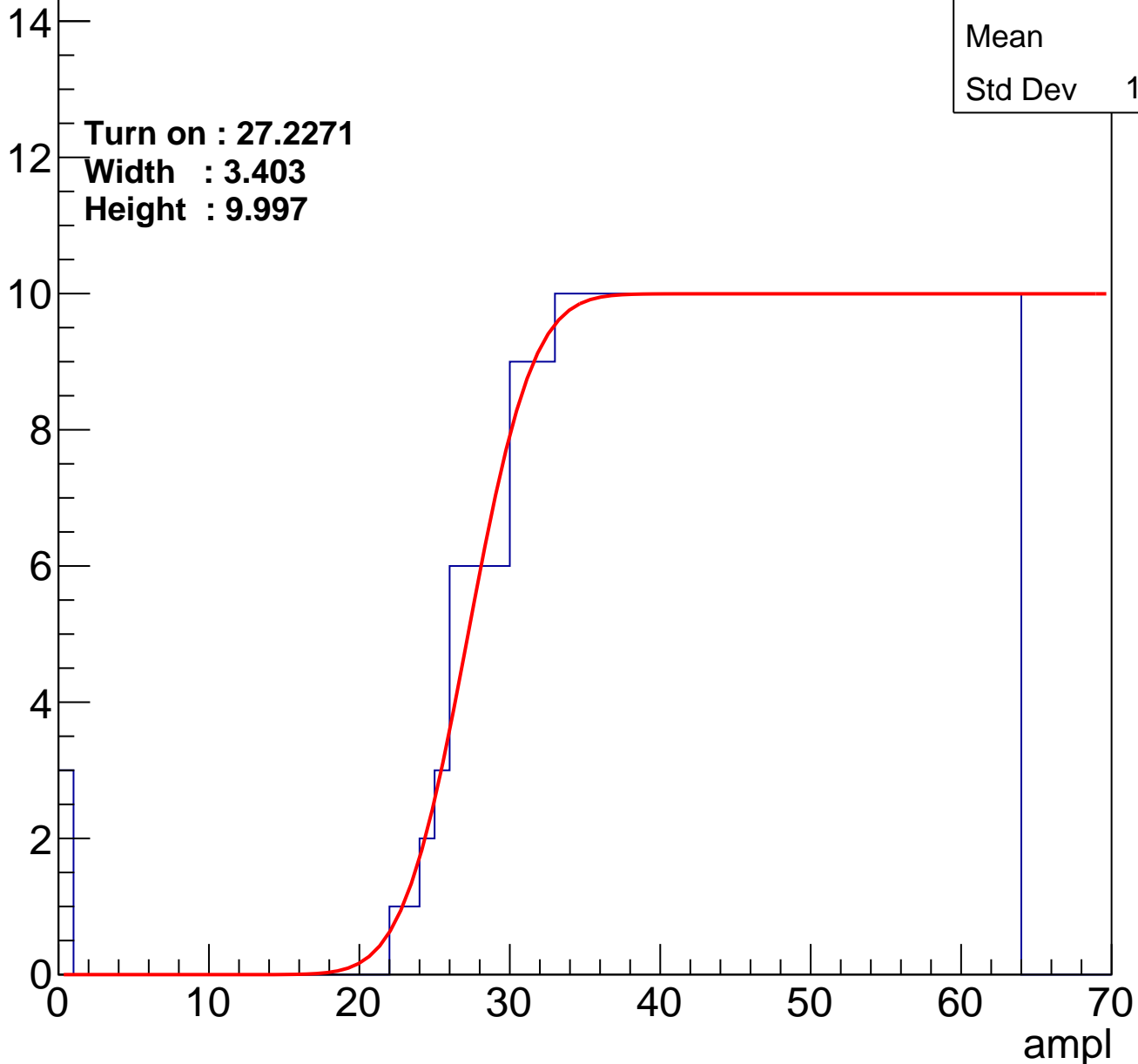
Entries	371
Mean	44.6
Std Dev	11.54

Turn on : 27.2271

Width : 3.403

Height : 9.997

Entry



B0L002S, U20-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.45
Std Dev	11.3

Turn on : 29.6727

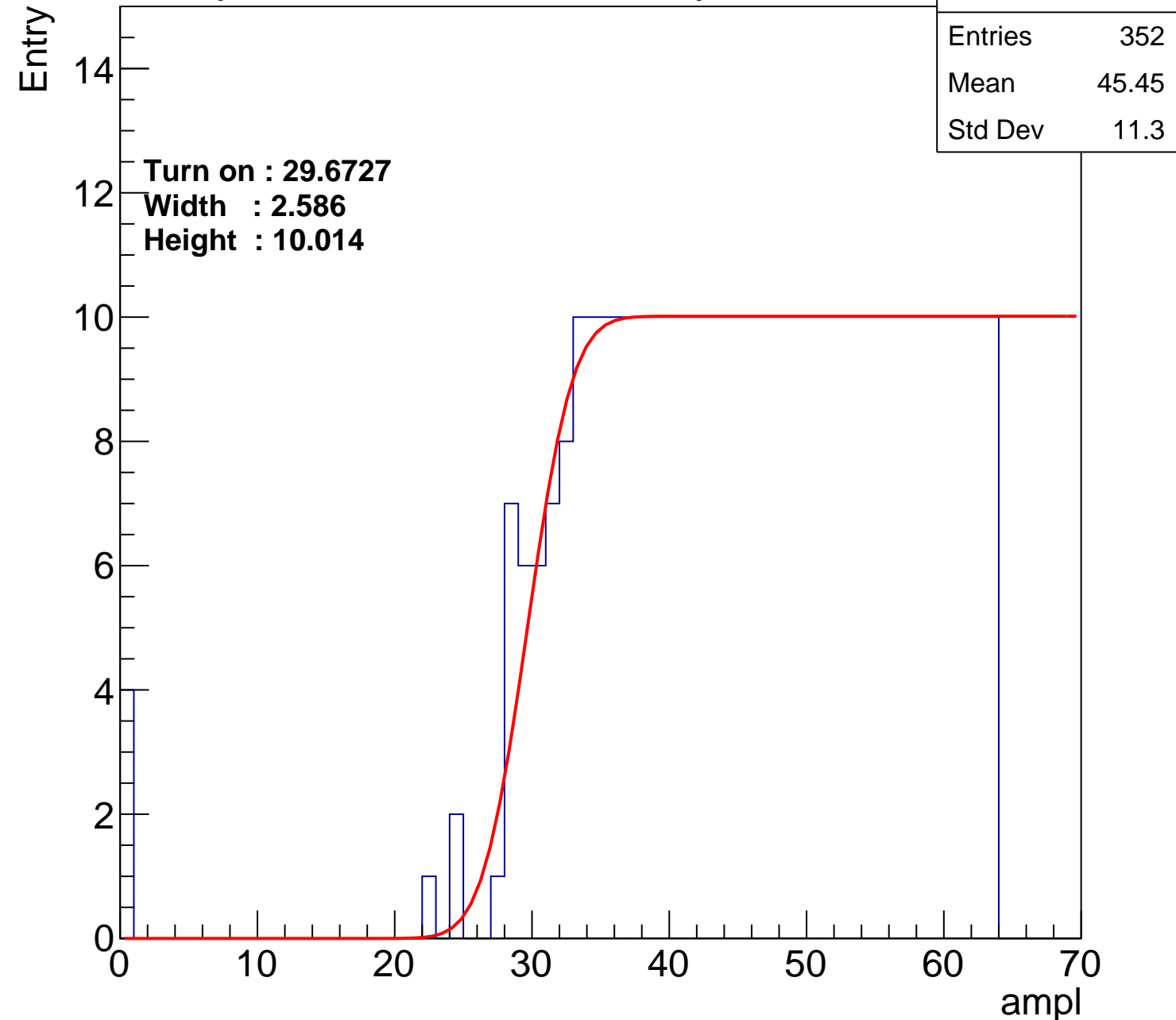
Width : 2.586

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch78

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.82
Std Dev	11.01

Turn on : 26.6204

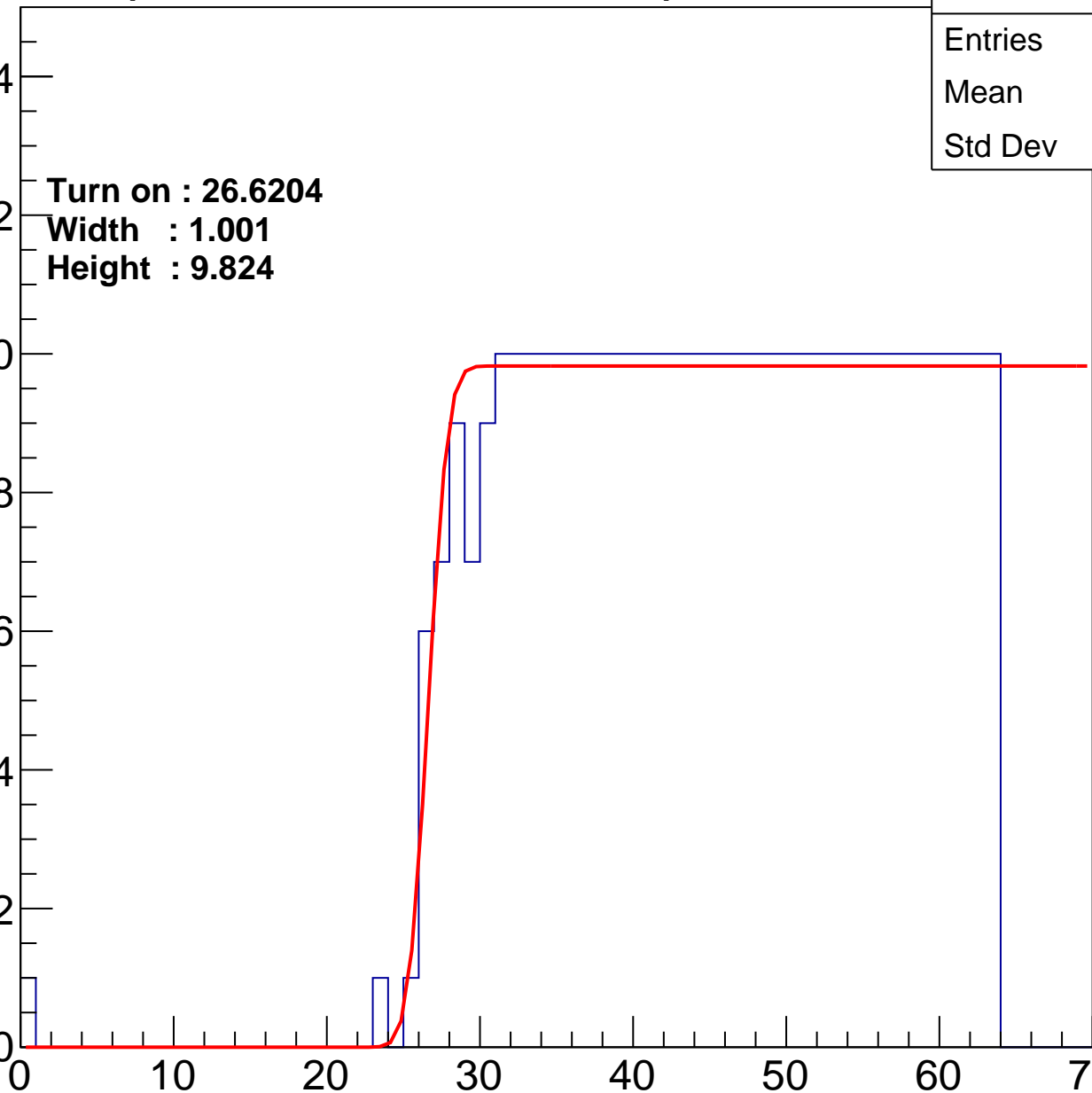
Width : 1.001

Height : 9.824

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch79

calib_packv5_042523_0143.root, FC#8, port C1

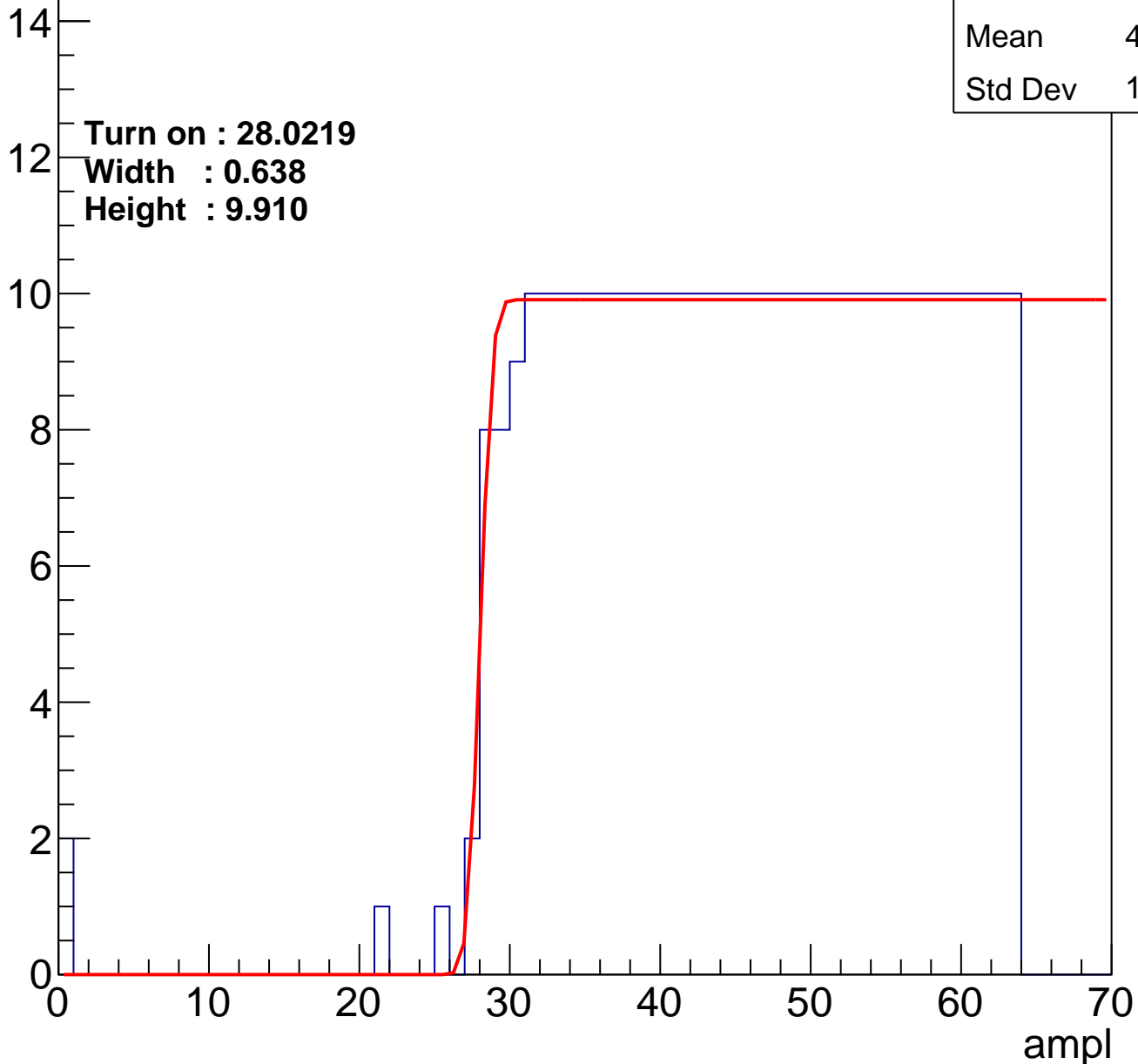
Entries	361
Mean	45.25
Std Dev	10.95

Turn on : 28.0219

Width : 0.638

Height : 9.910

Entry



B0L002S, U20-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.4
Std Dev	10.96

Turn on : 28.8265

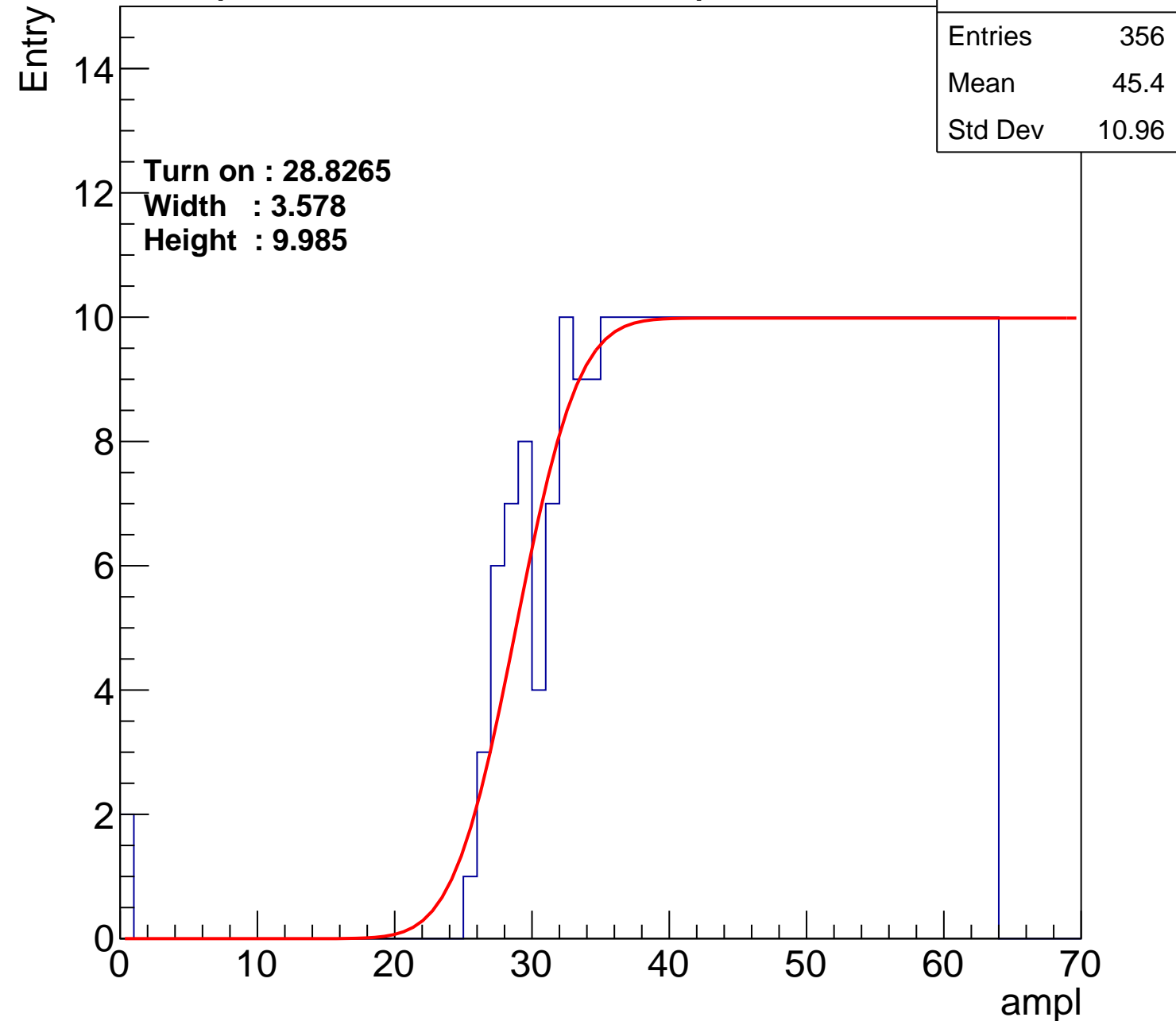
Width : 3.578

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 25.7481

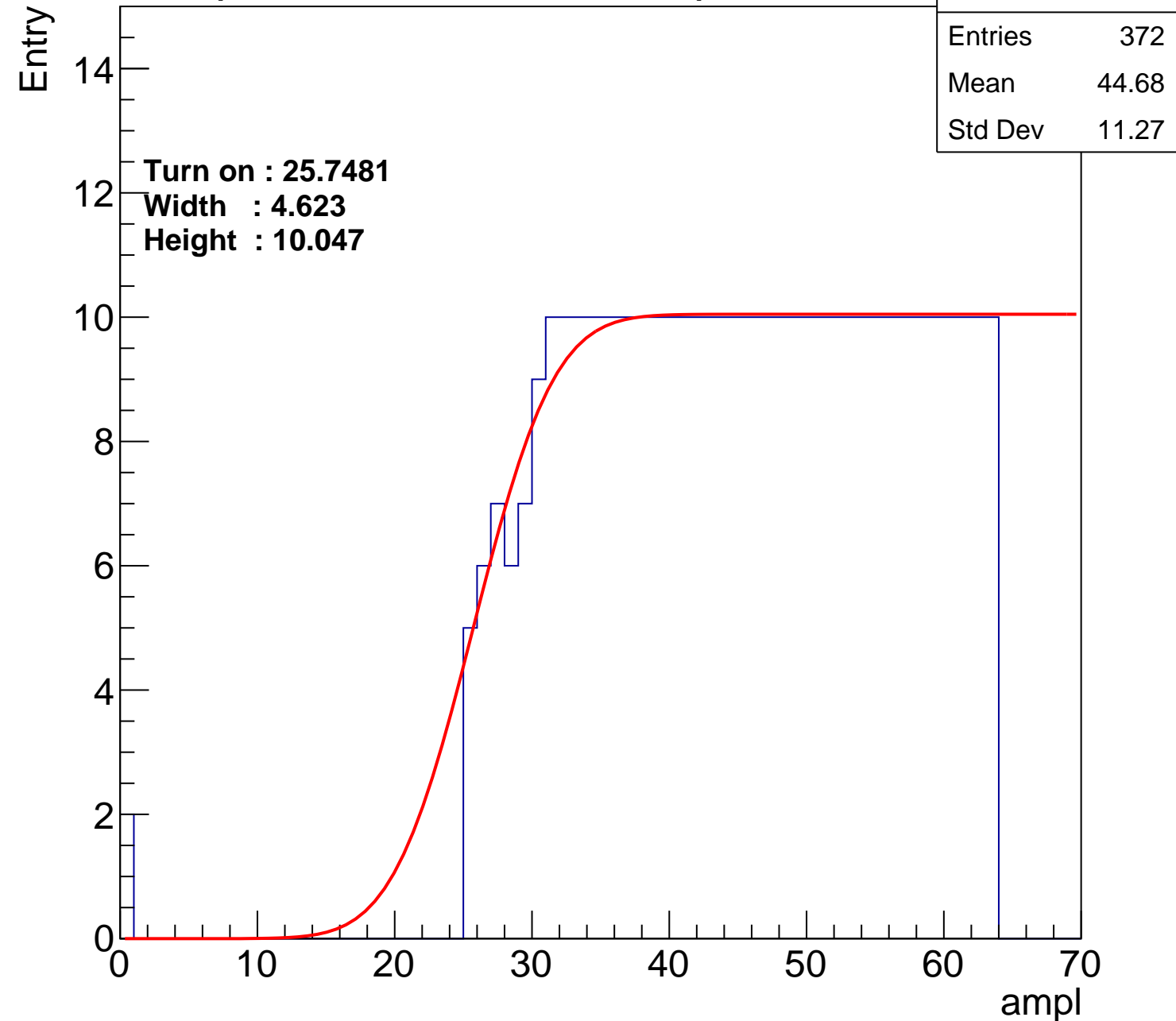
Width : 4.623

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.19
Std Dev	11.25

Turn on : 28.1890

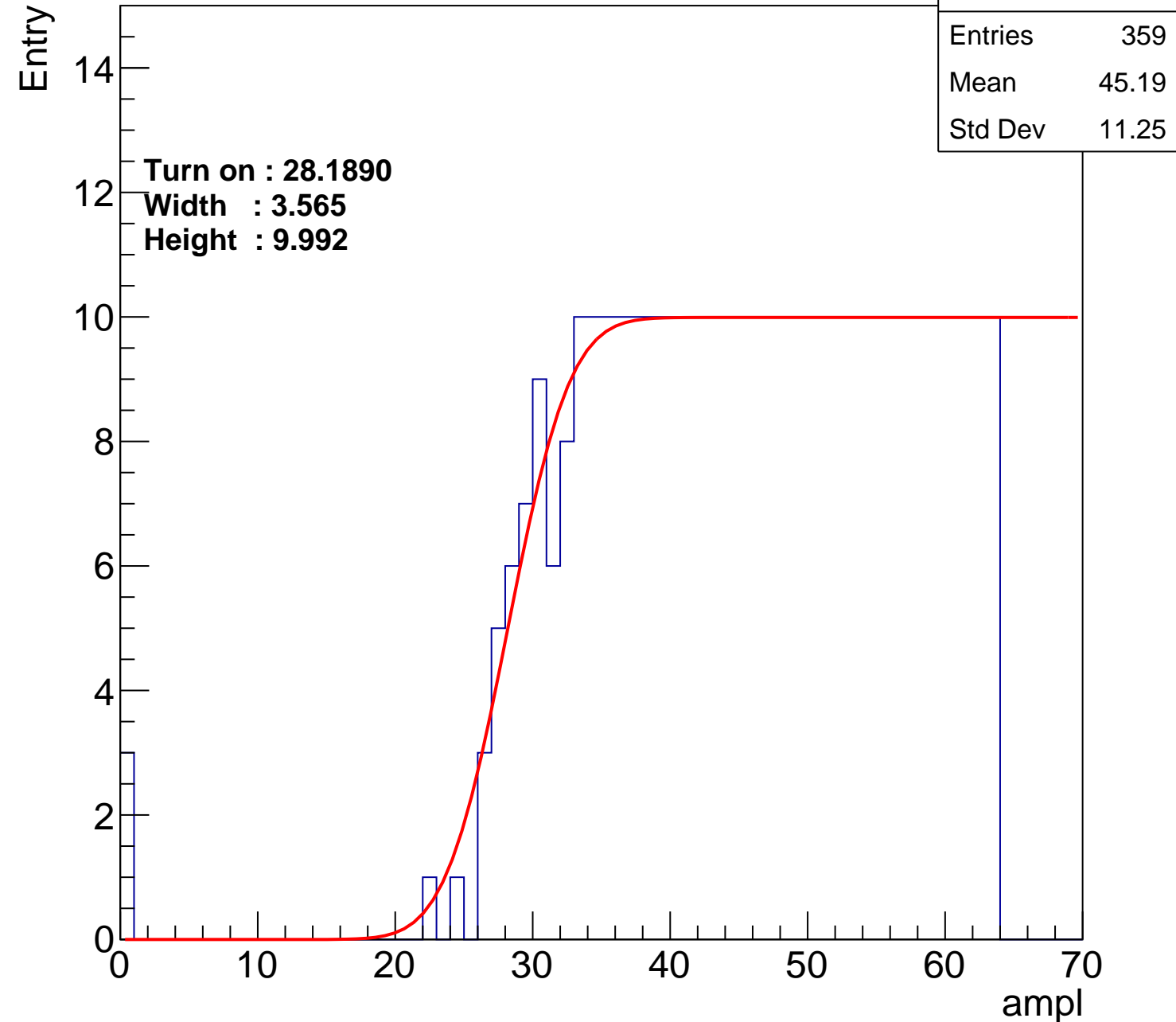
Width : 3.565

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch83

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.88

Turn on : 27.6853

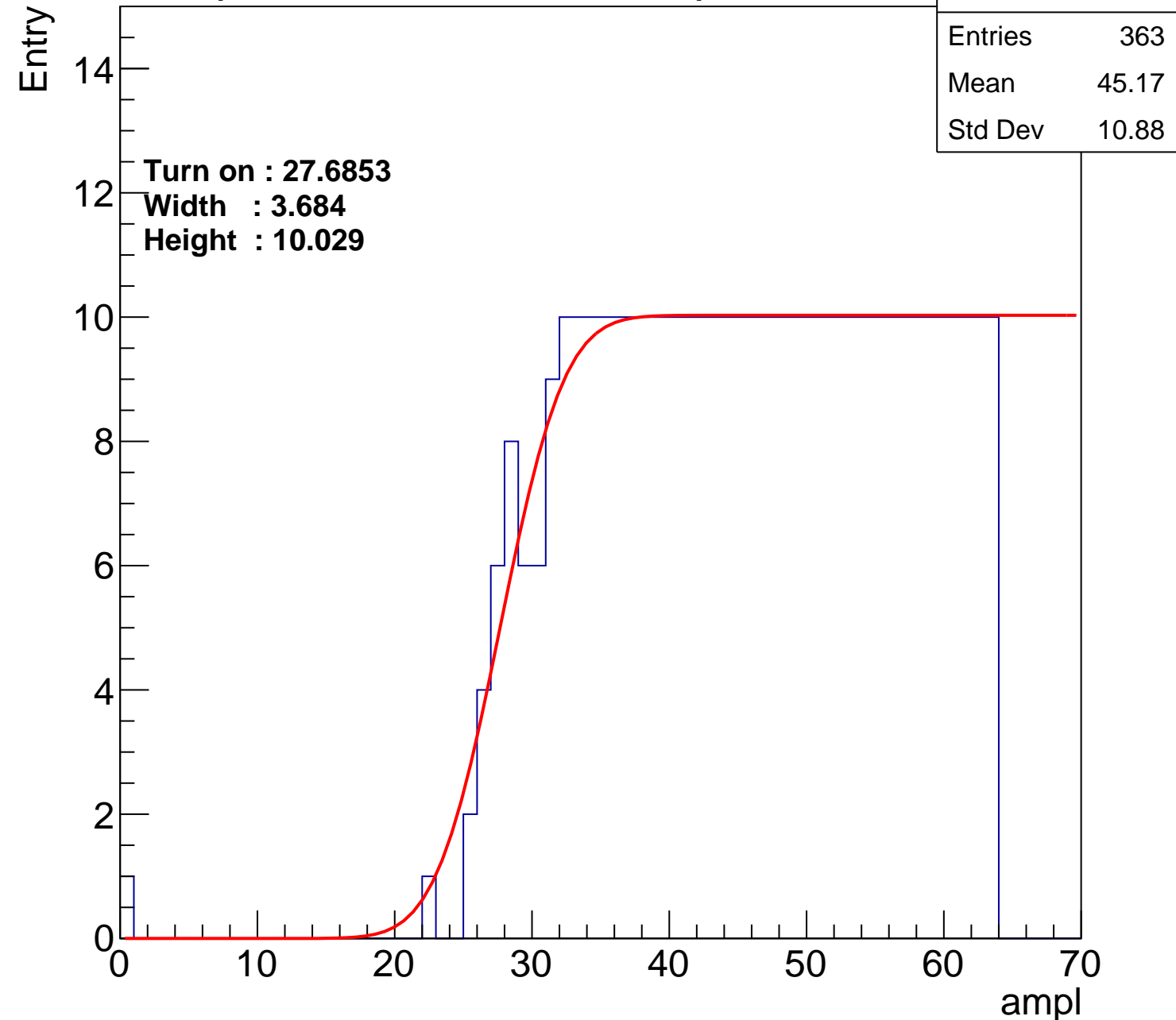
Width : 3.684

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch84

calib_packv5_042523_0143.root, FC#8, port C1

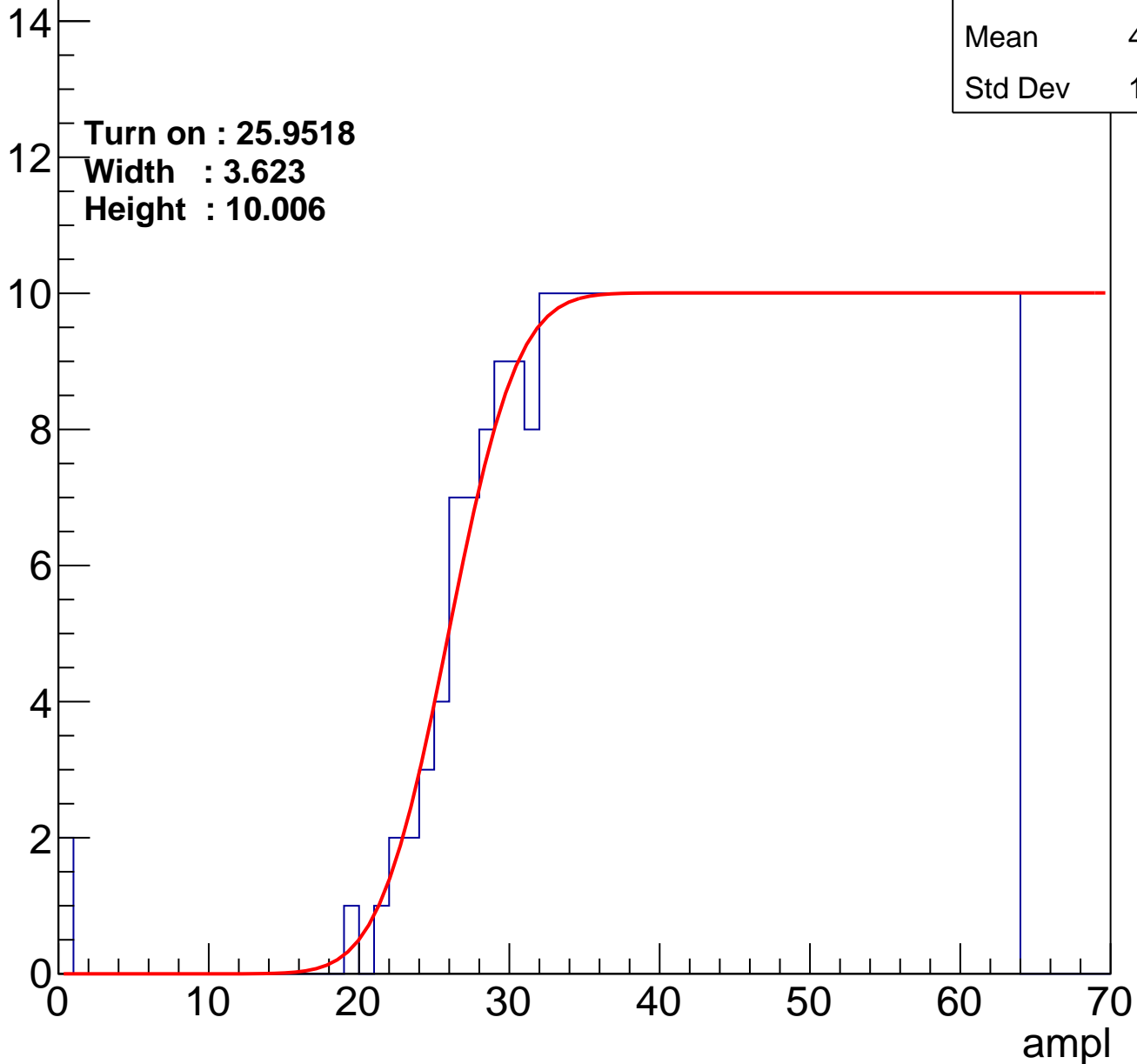
Entries	383
Mean	44.06
Std Dev	11.67

Turn on : 25.9518

Width : 3.623

Height : 10.006

Entry



B0L002S, U20-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	44.95
Std Dev	11.61

Turn on : 28.3279

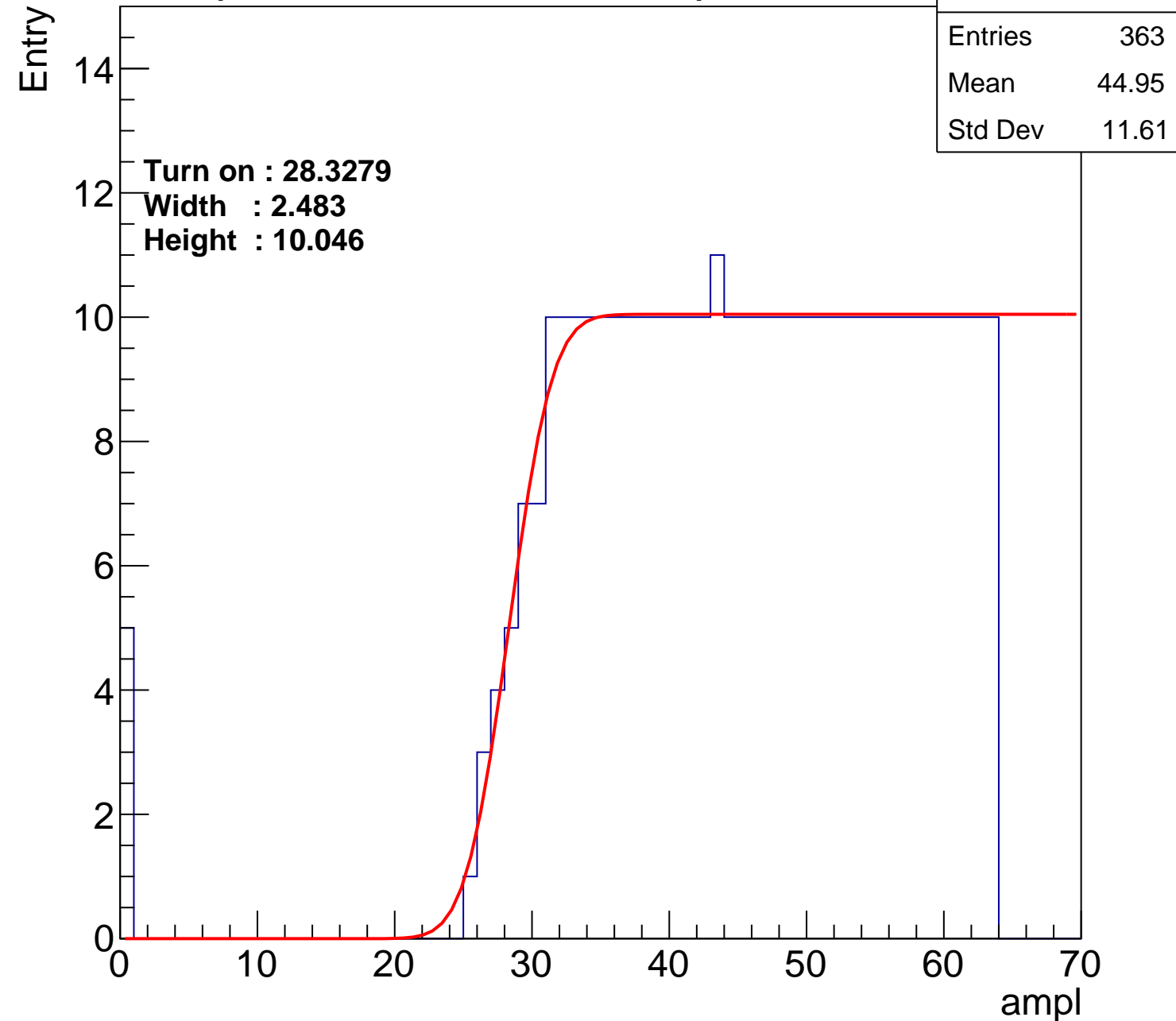
Width : 2.483

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.33
Std Dev	11.26

Turn on : 26.0026

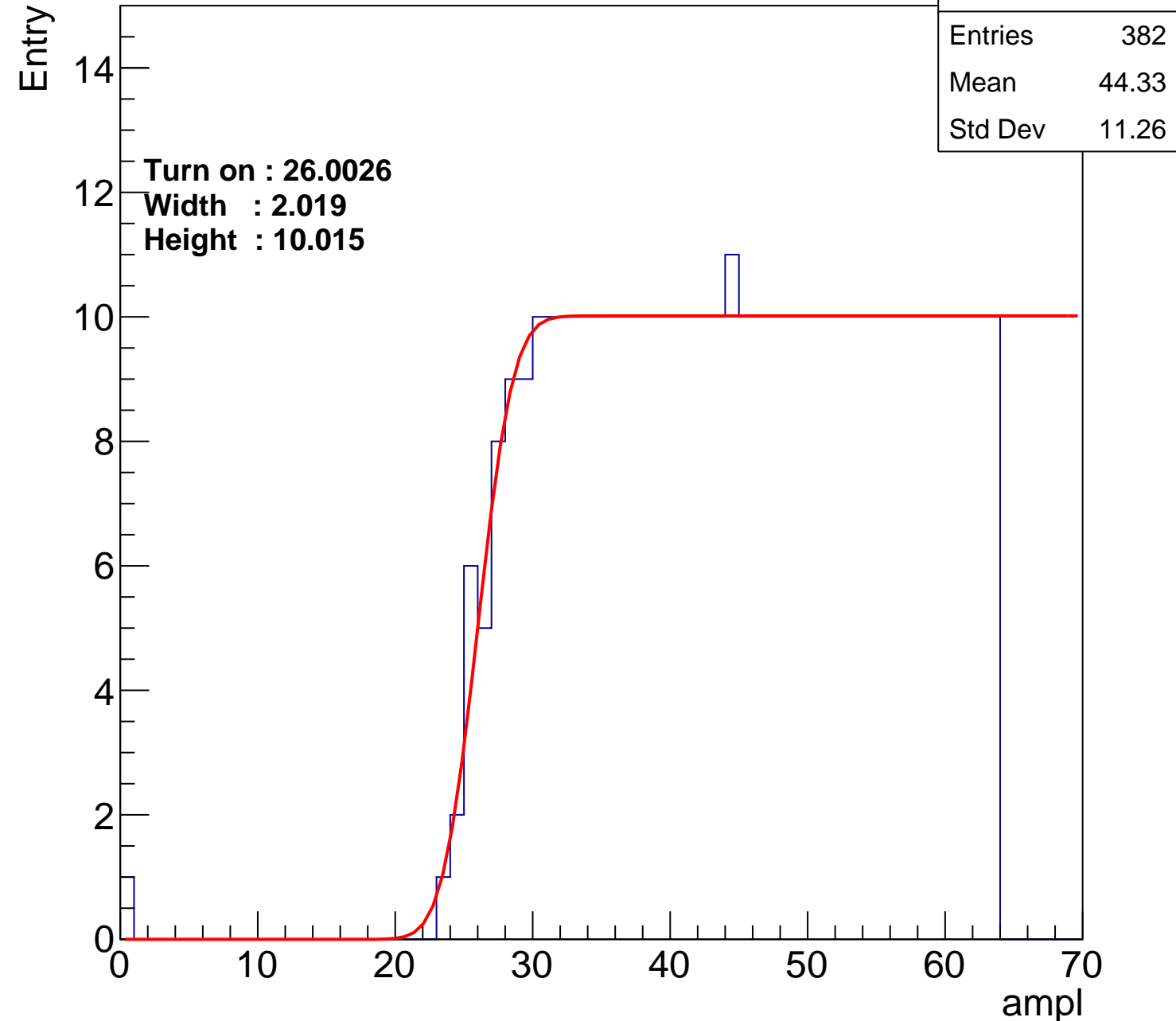
Width : 2.019

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.81
Std Dev	11.4

Turn on : 27.8933

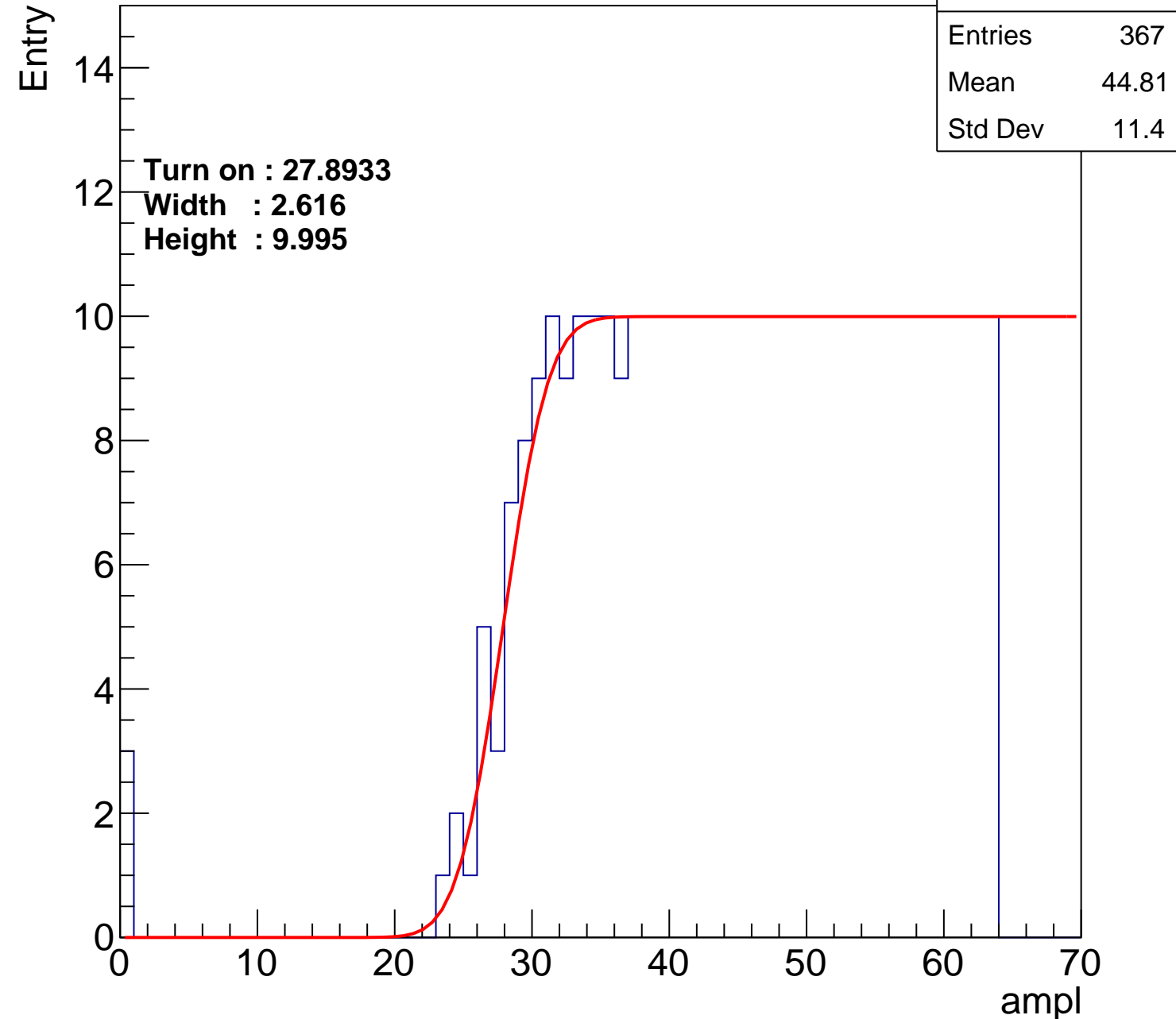
Width : 2.616

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch88

calib_packv5_042523_0143.root, FC#8, port C1

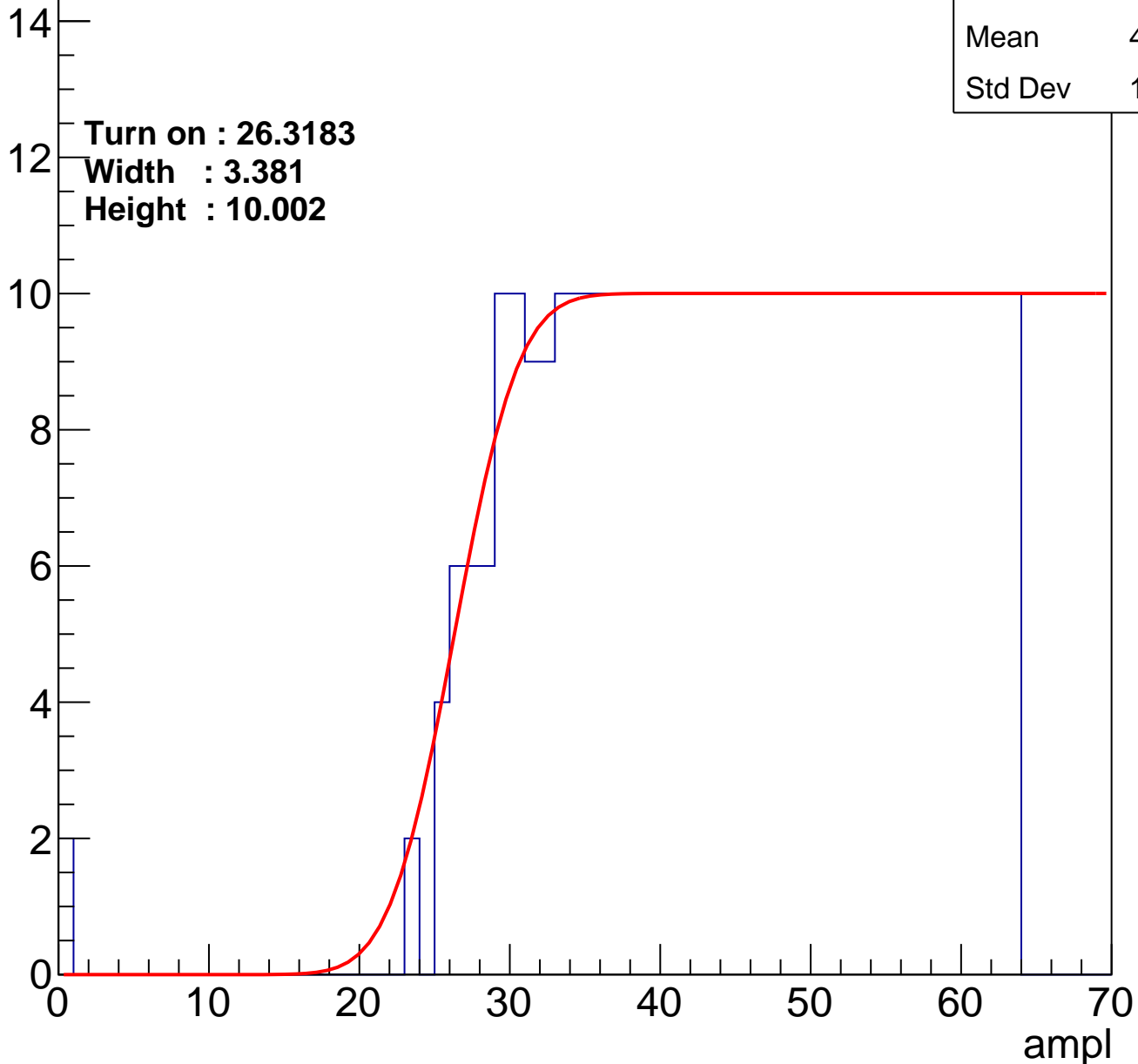
Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 26.3183

Width : 3.381

Height : 10.002

Entry



B0L002S, U20-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.61
Std Dev	11.2

Turn on : 26.7645

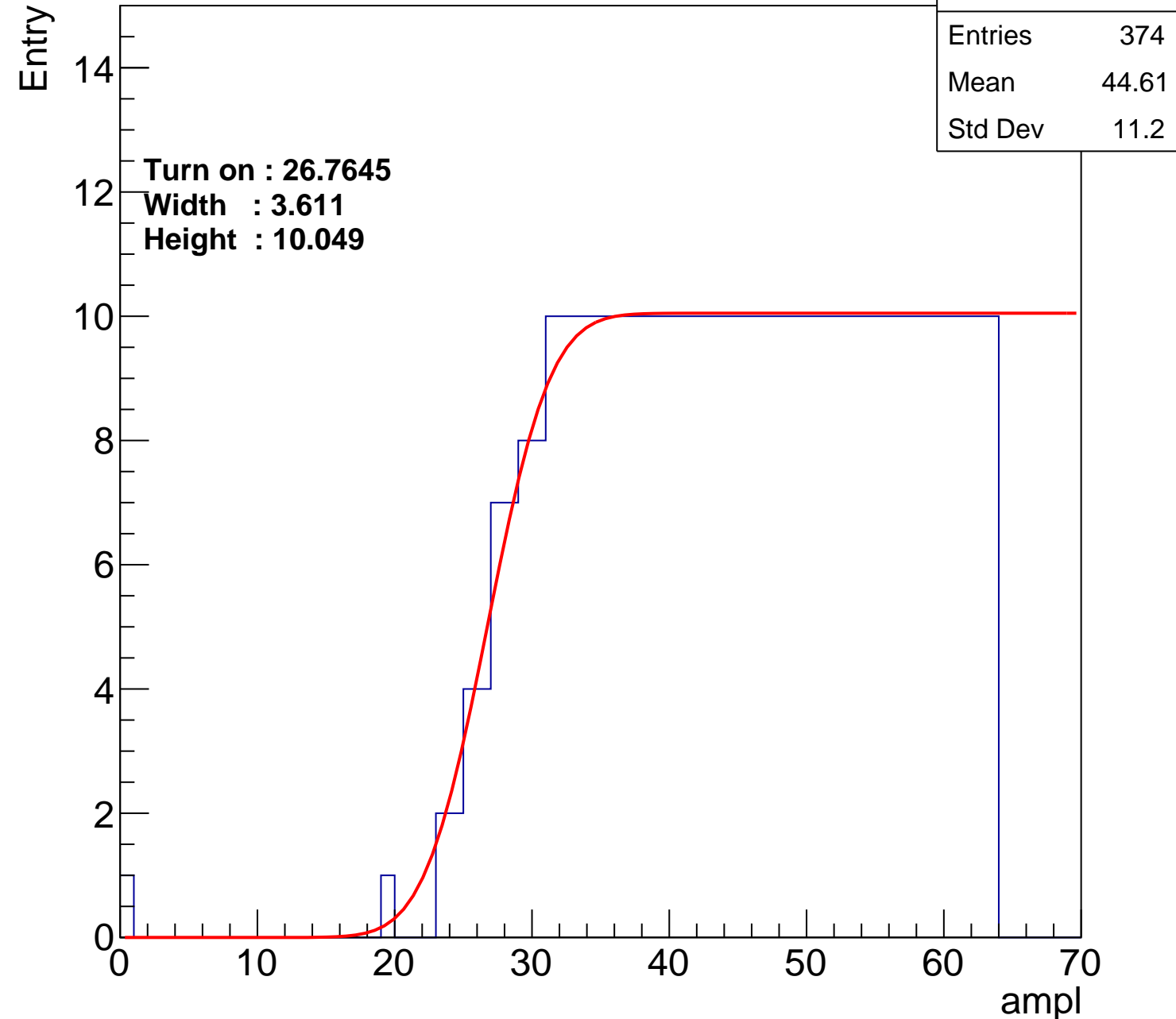
Width : 3.611

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.8
Std Dev	11.38

Turn on : 27.6628

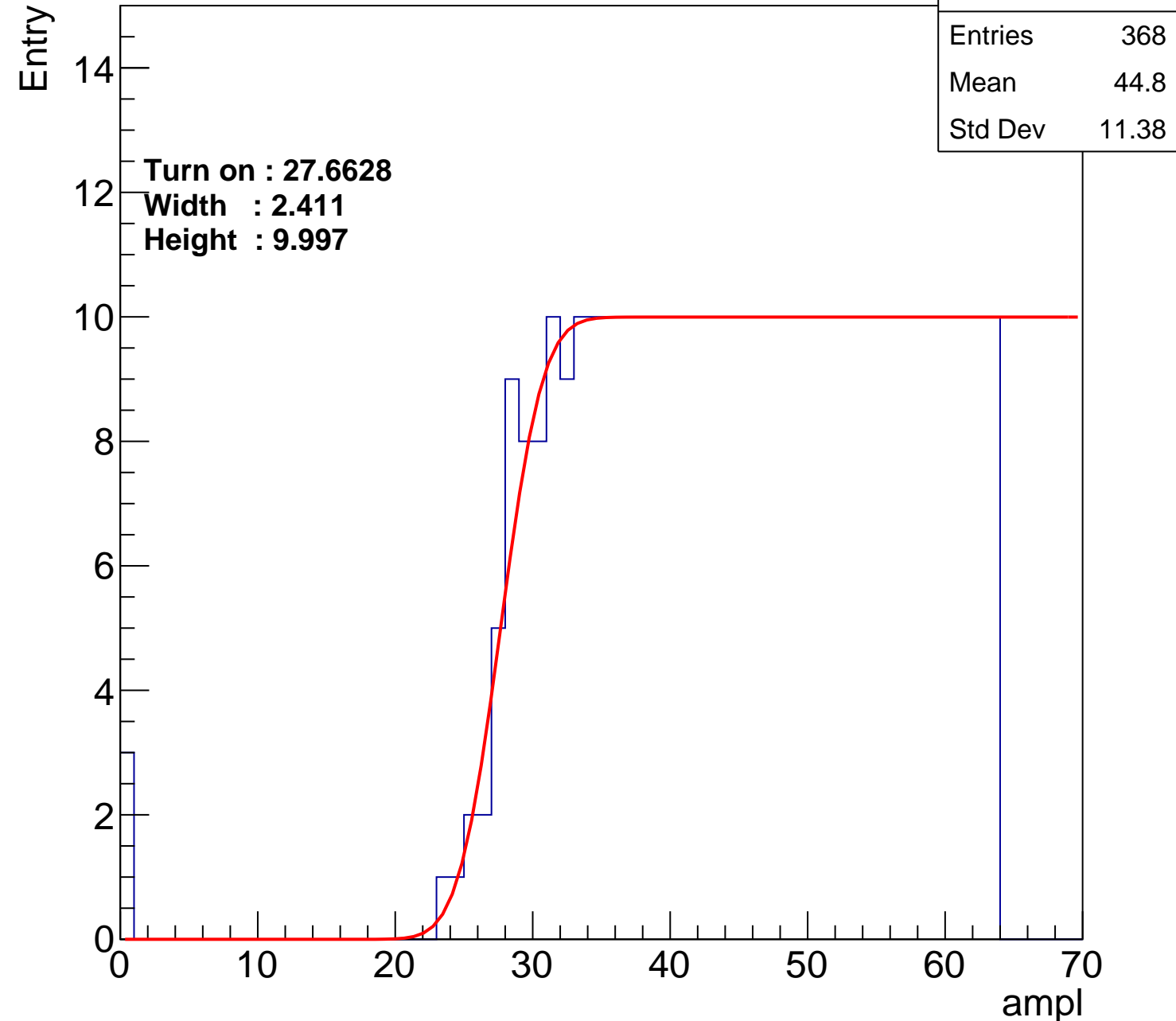
Width : 2.411

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch91

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.62
Std Dev	10.85

Turn on : 29.7270

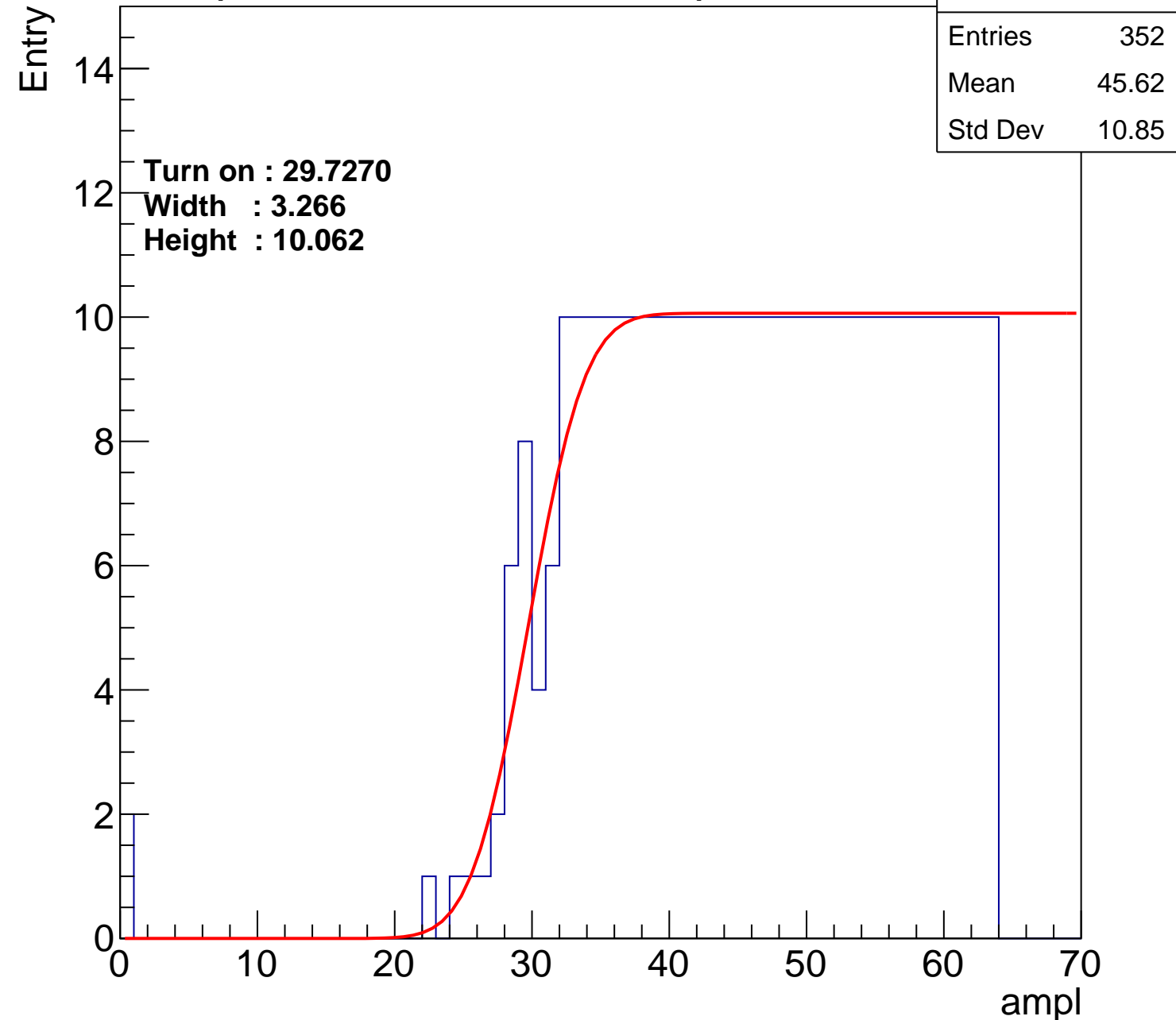
Width : 3.266

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch92

calib_packv5_042523_0143.root, FC#8, port C1

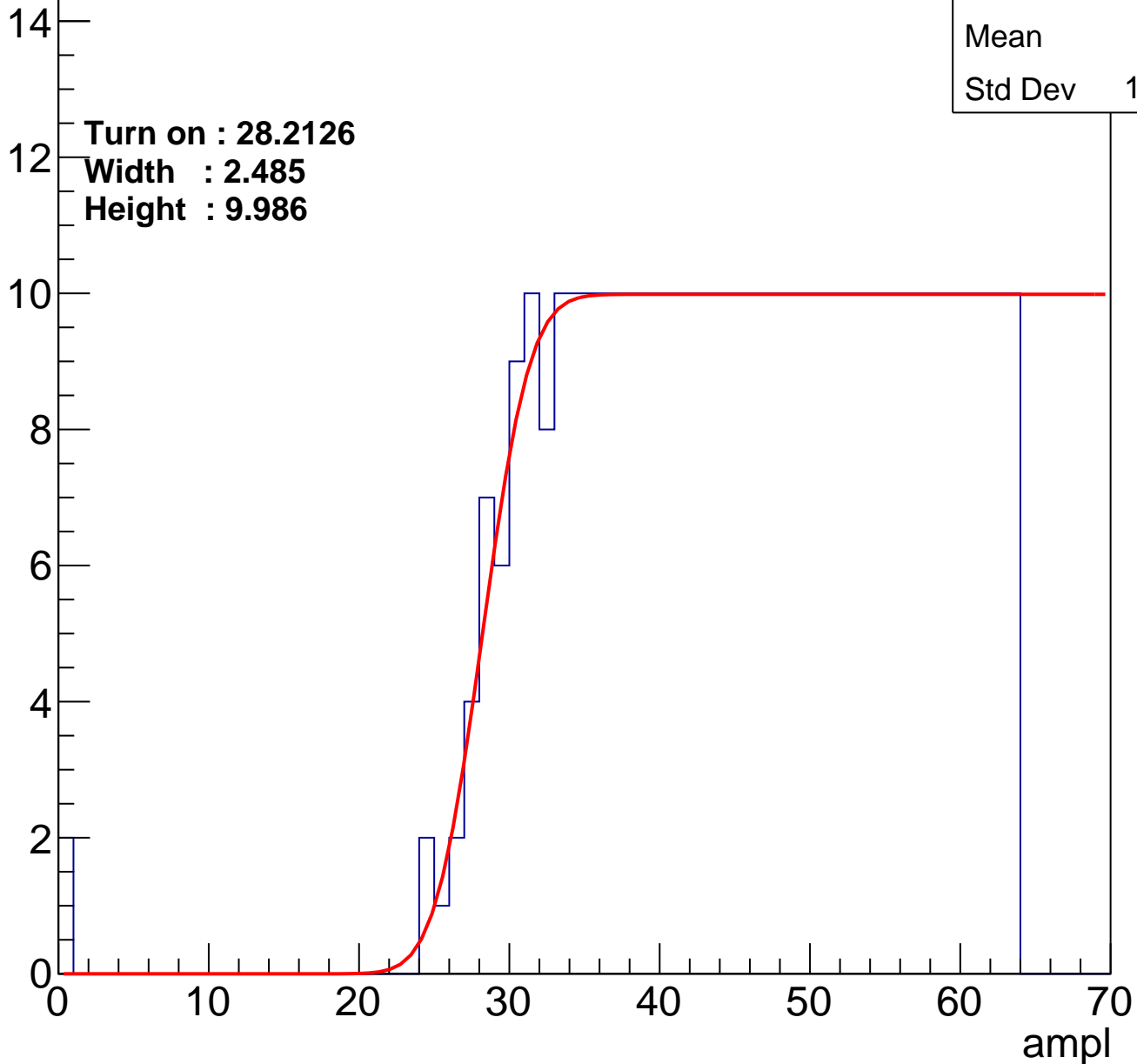
Entries	361
Mean	45.2
Std Dev	11.02

Turn on : 28.2126

Width : 2.485

Height : 9.986

Entry



B0L002S, U20-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.23
Std Dev	11.18

Turn on : 28.4721

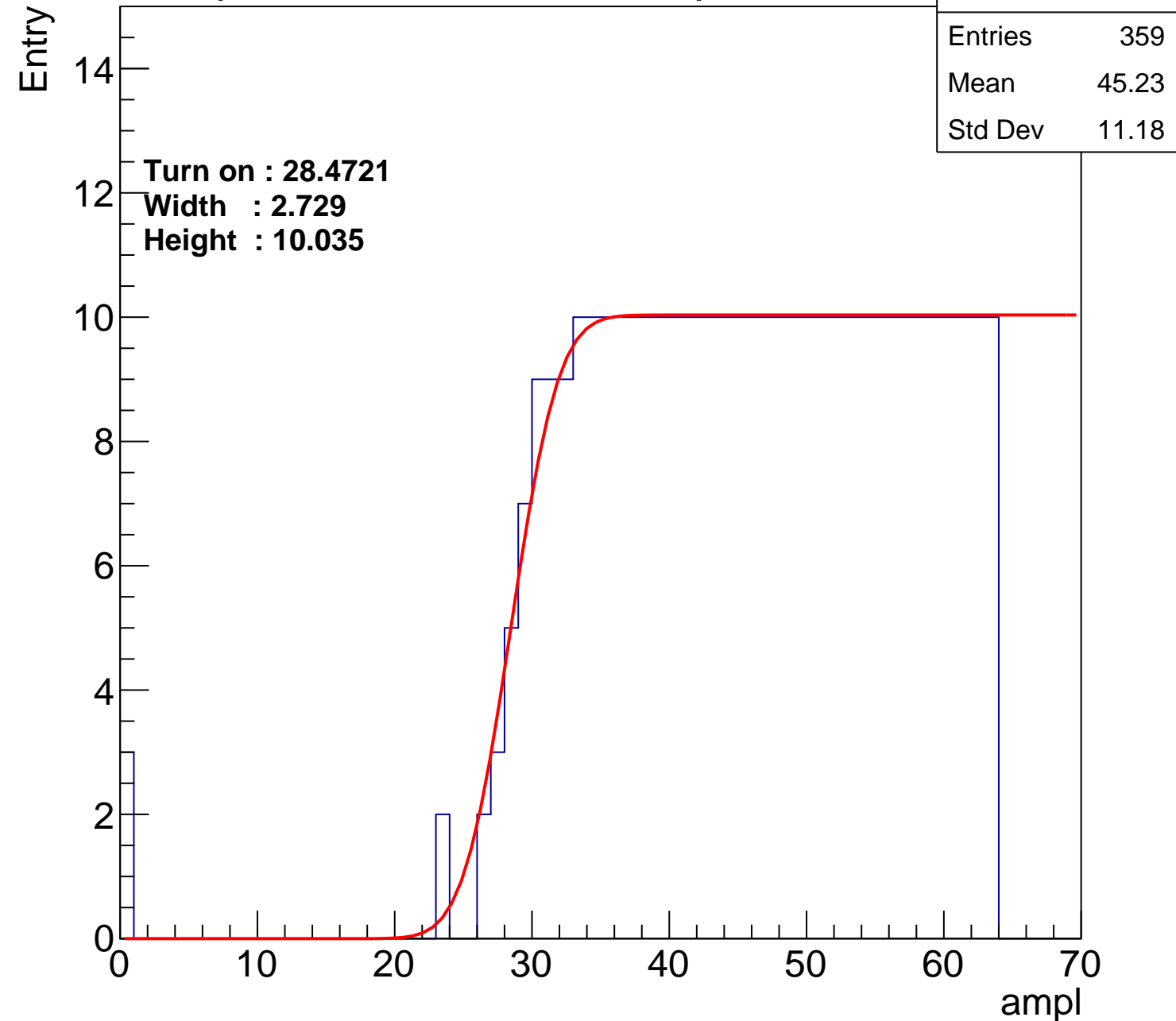
Width : 2.729

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	347
Mean	45.56
Std Dev	11.5

Turn on : 29.9534

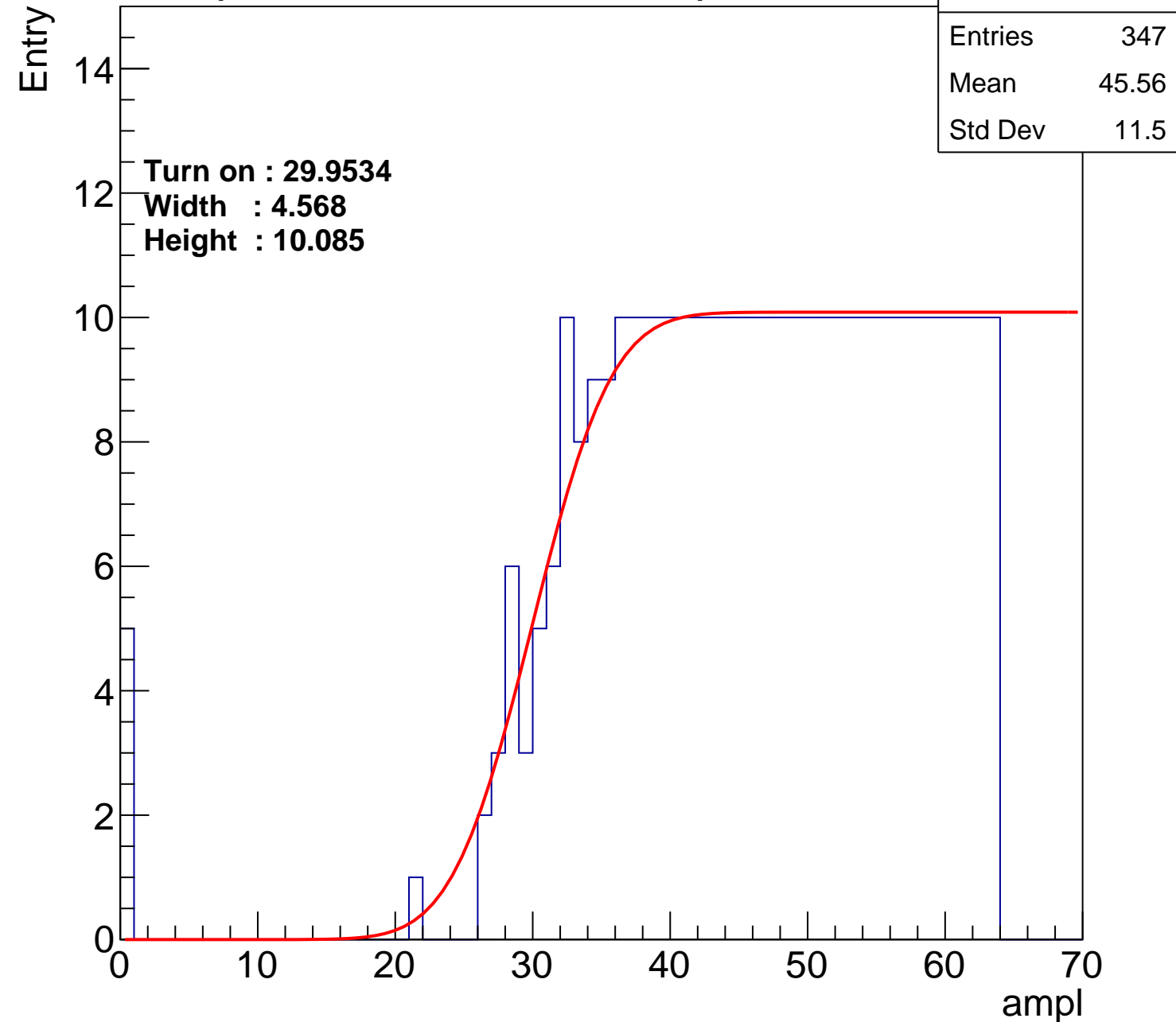
Width : 4.568

Height : 10.085

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.26
Std Dev	10.98

Turn on : 28.4701

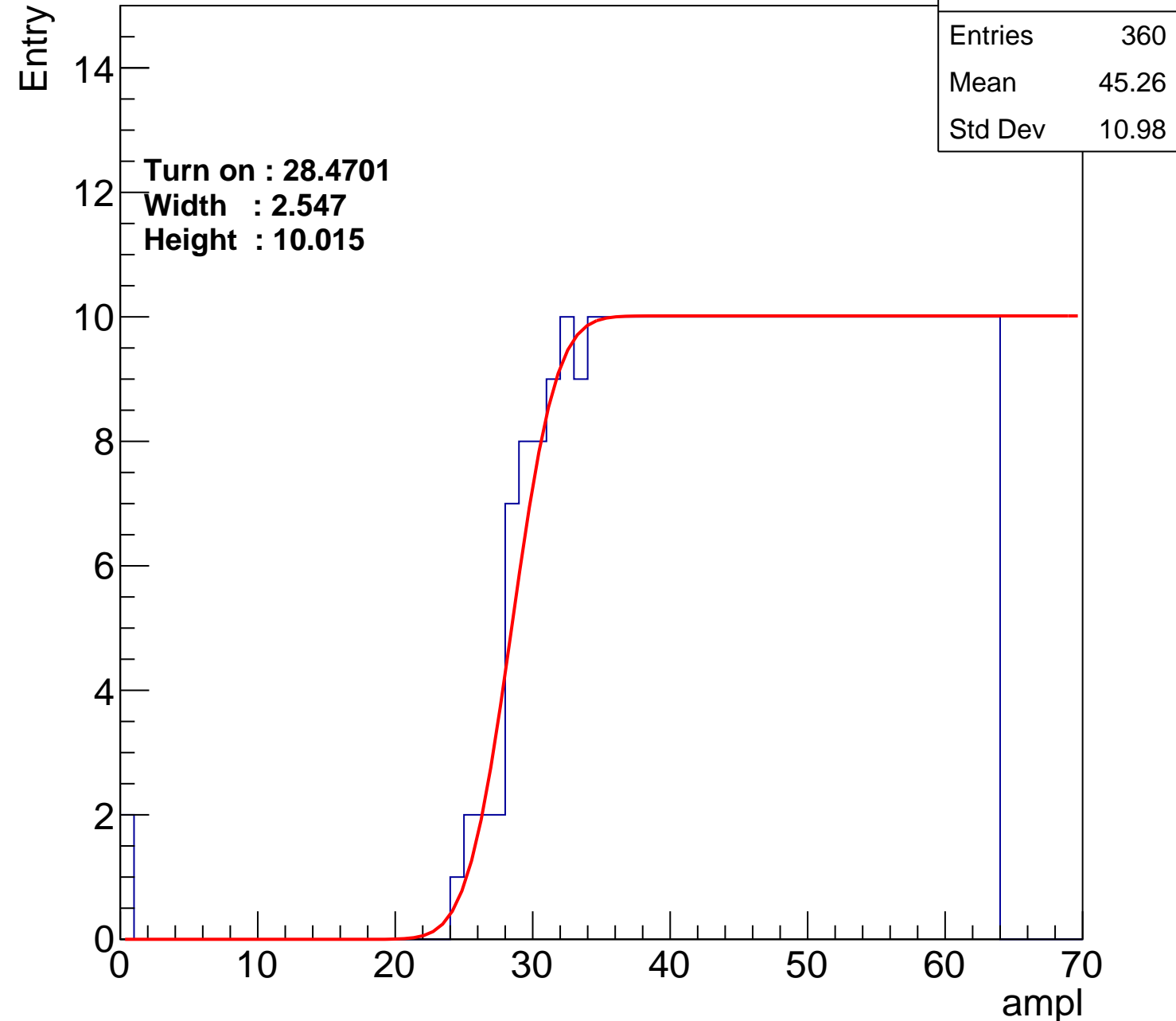
Width : 2.547

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.32
Std Dev	11.09

Turn on : 28.2402

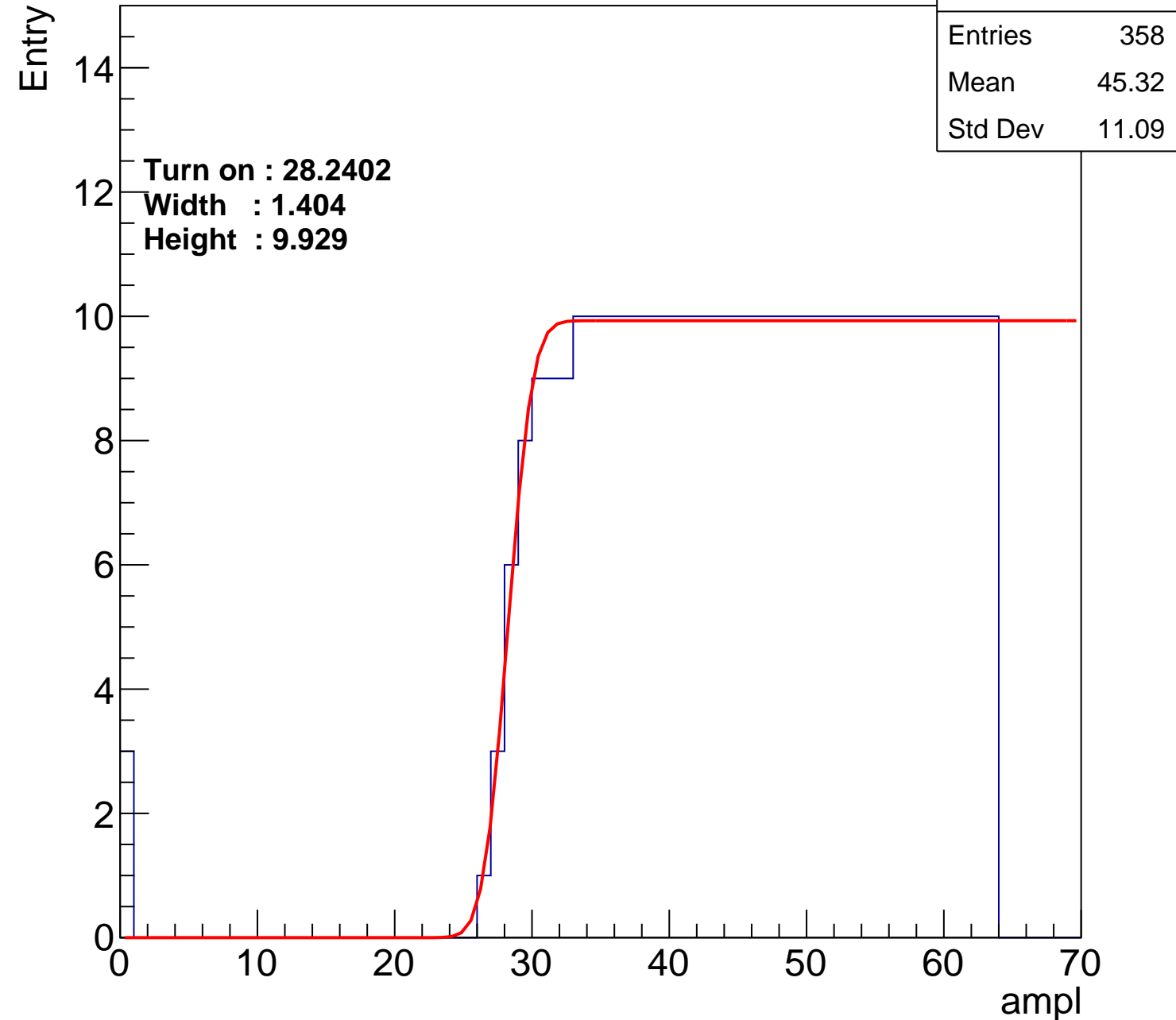
Width : 1.404

Height : 9.929

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.05
Std Dev	11.64

Turn on : 28.7759

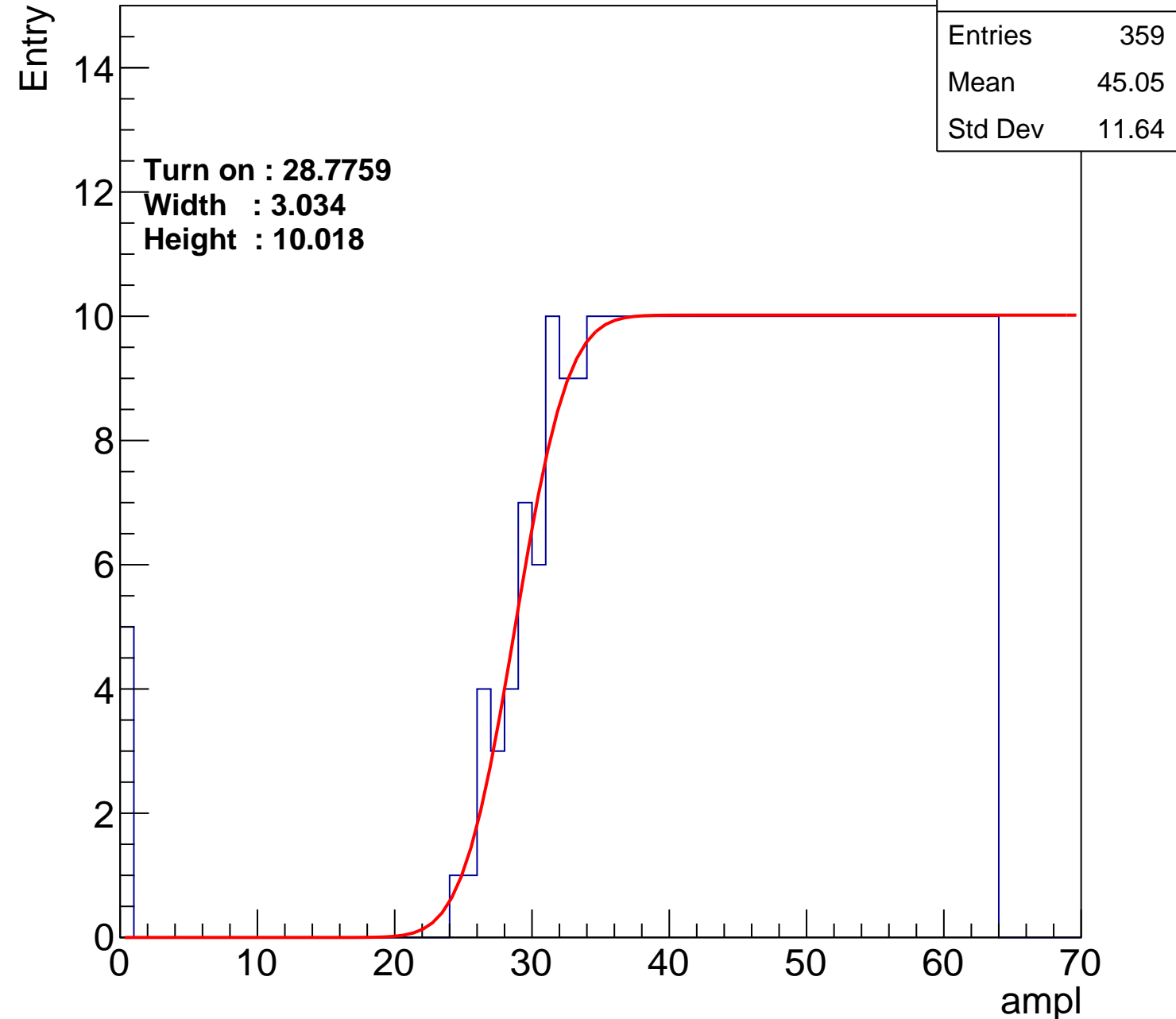
Width : 3.034

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.09
Std Dev	11.59

Turn on : 28.7331

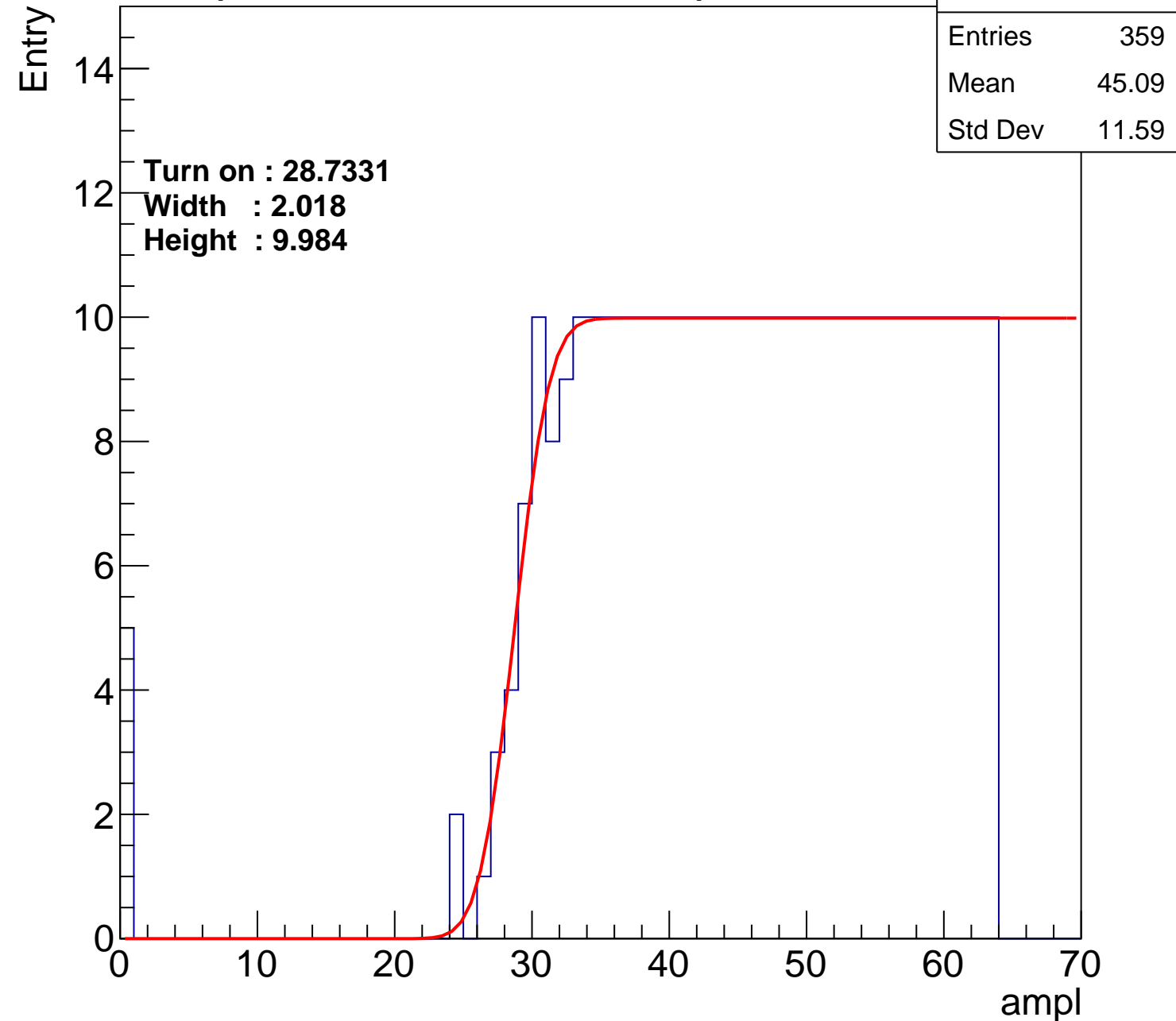
Width : 2.018

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.4
Std Dev	10.72

Turn on : 28.4448

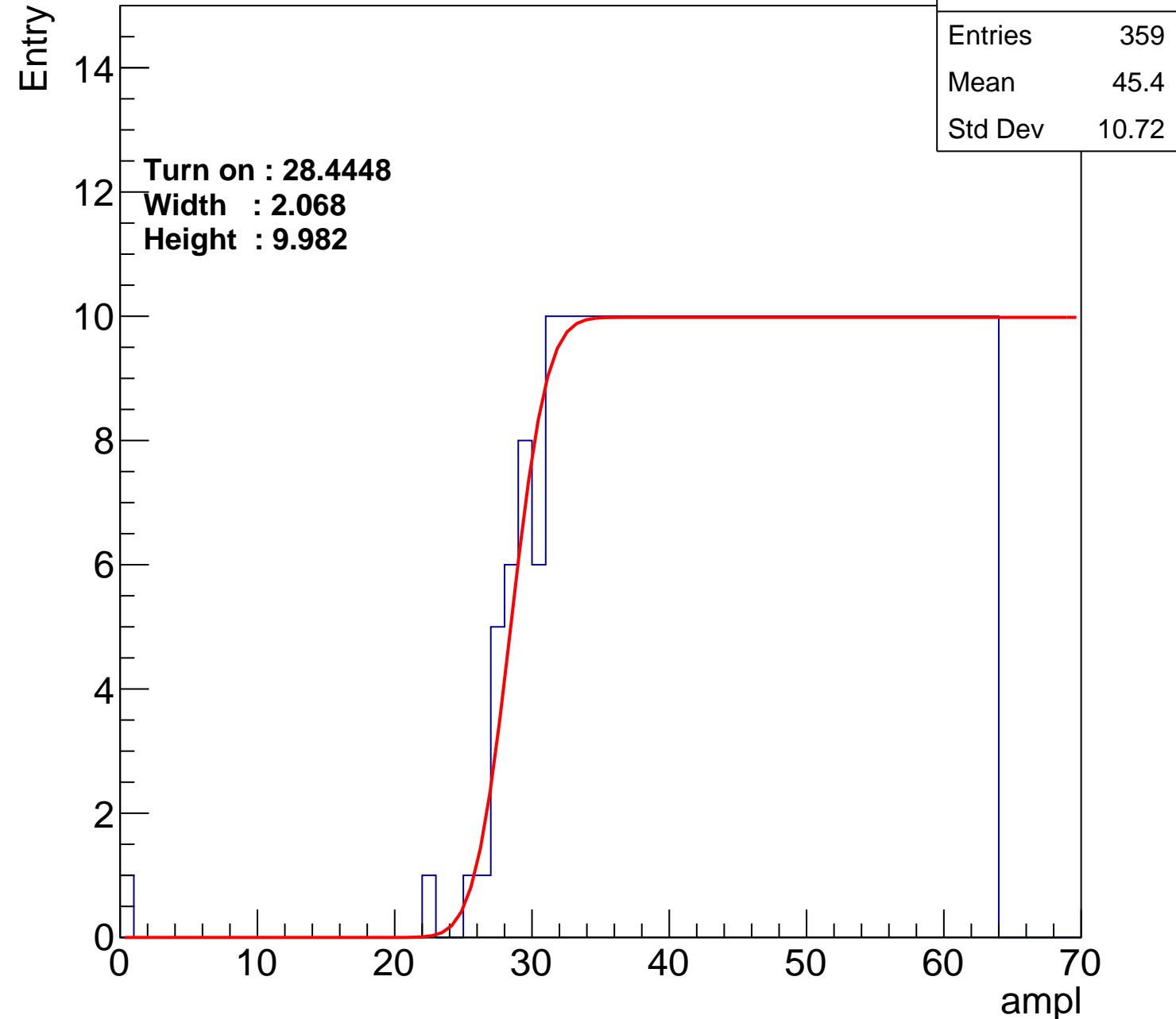
Width : 2.068

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.4072

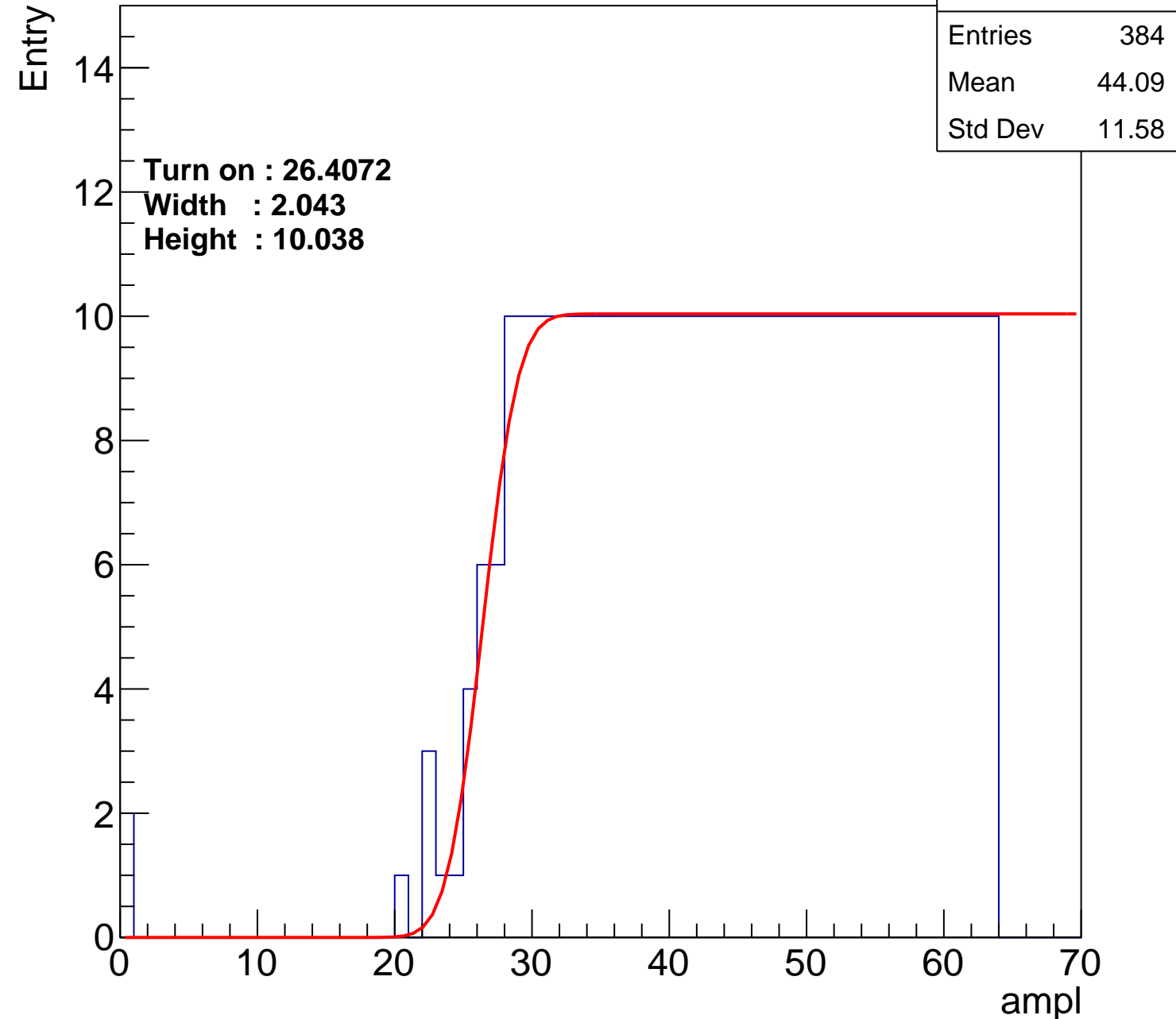
Width : 2.043

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	339
Mean	46.26
Std Dev	10.51

Turn on : 30.3540

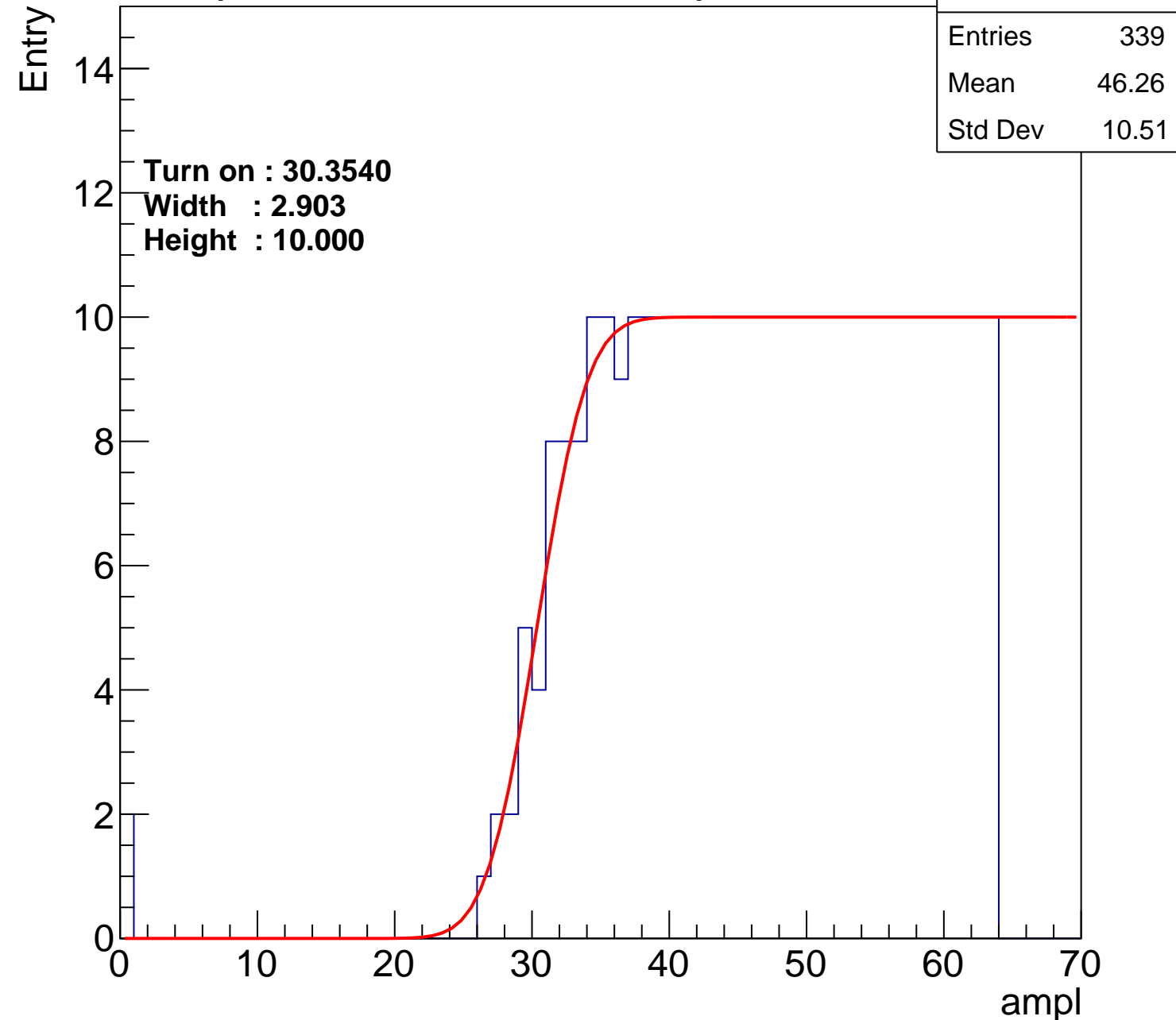
Width : 2.903

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch102

calib_packv5_042523_0143.root, FC#8, port C1

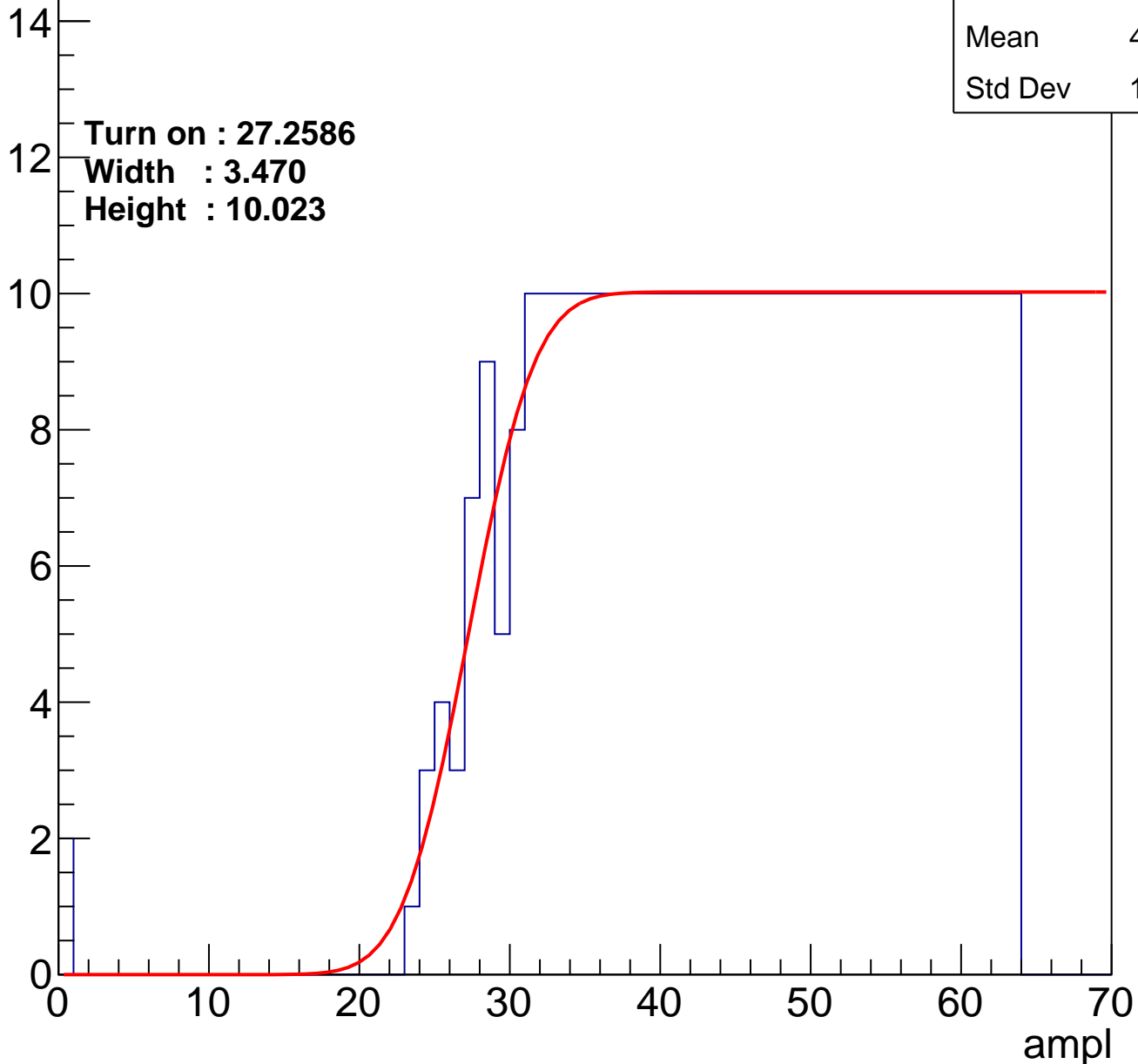
Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.2586

Width : 3.470

Height : 10.023

Entry



B0L002S, U20-ch103

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.25
Std Dev	11.67

Turn on : 26.7225

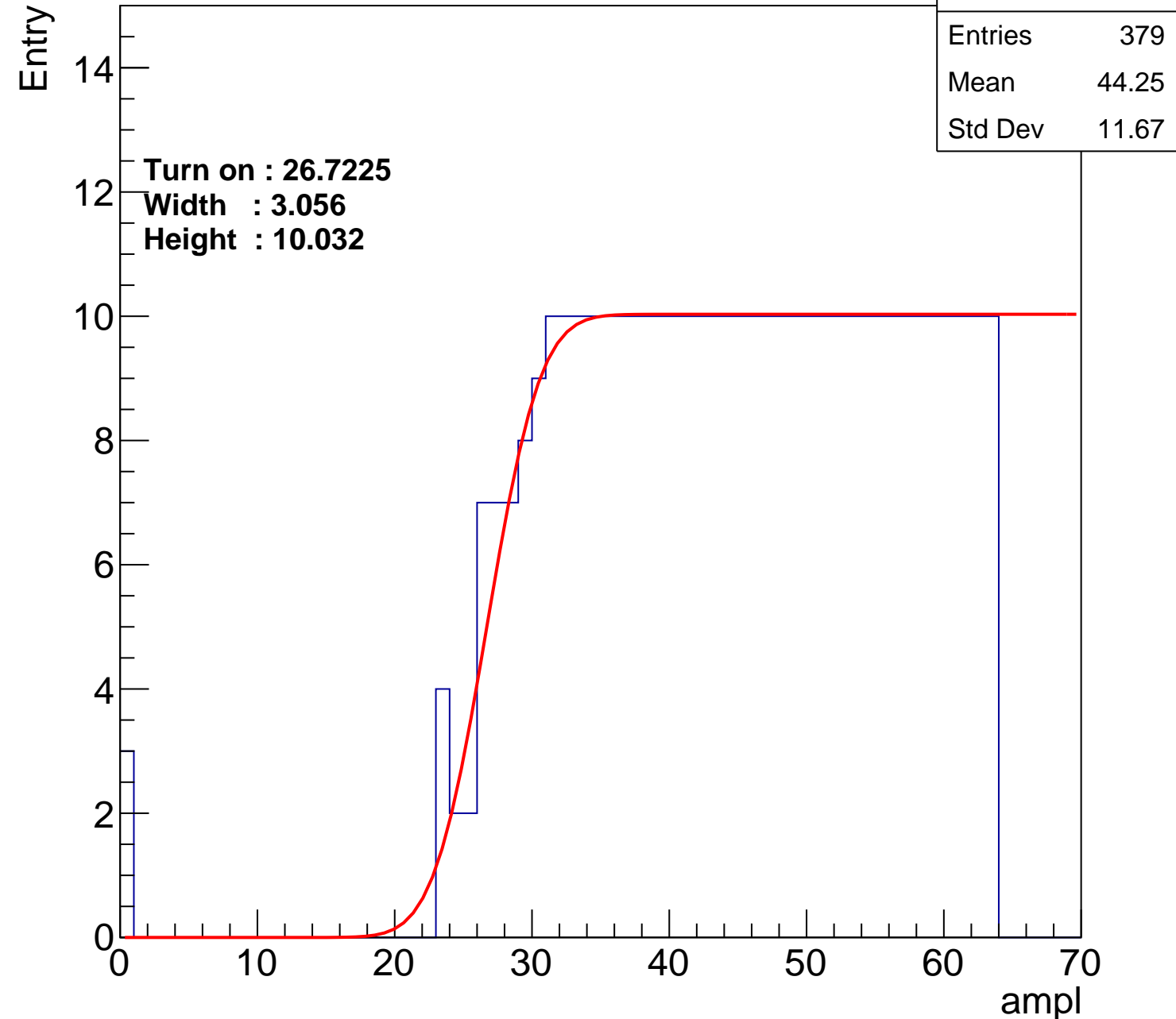
Width : 3.056

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45
Std Dev	11.48

Turn on : 28.8351

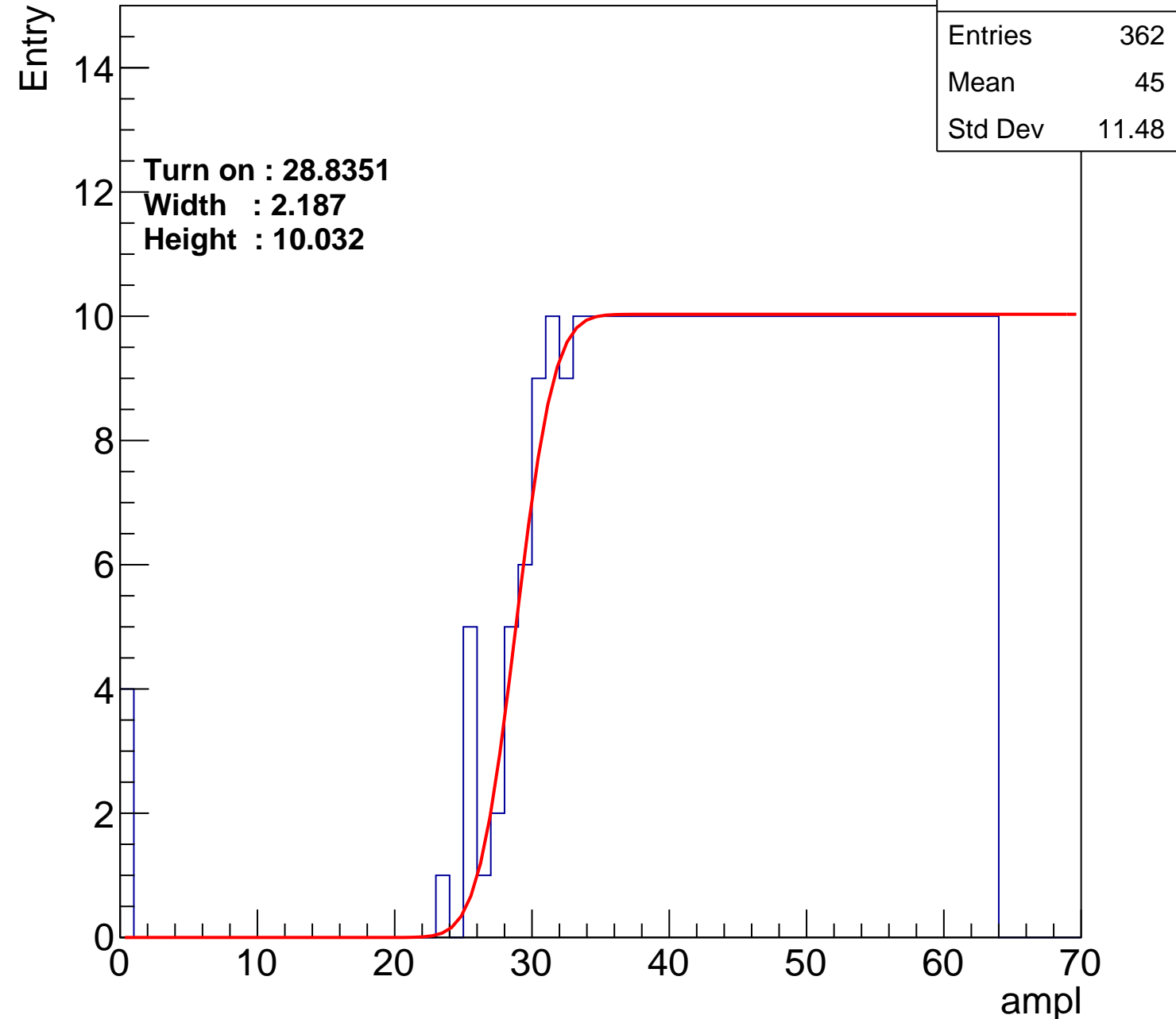
Width : 2.187

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 26.8185

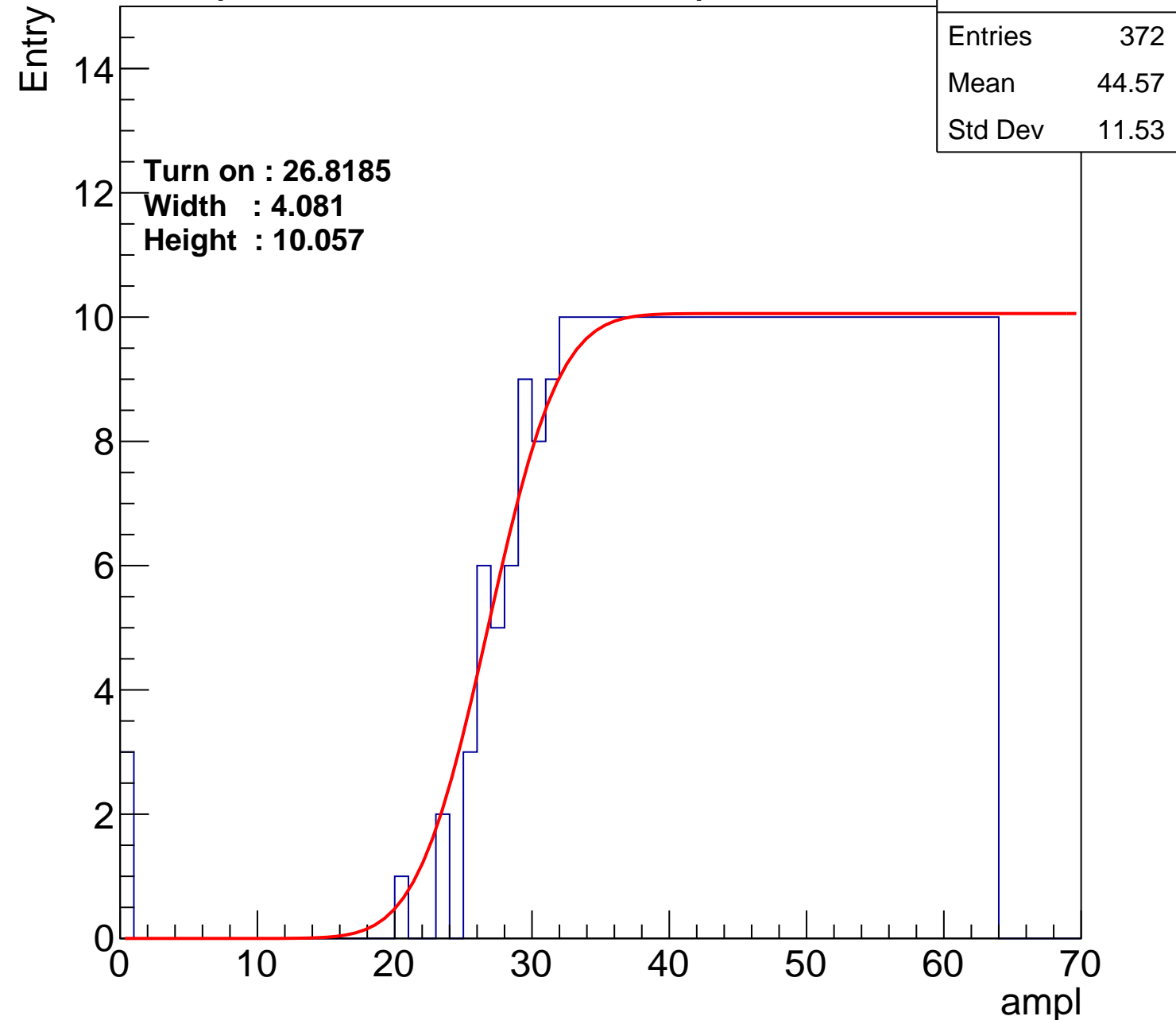
Width : 4.081

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	346
Mean	45.84
Std Dev	10.92

Turn on : 29.8958

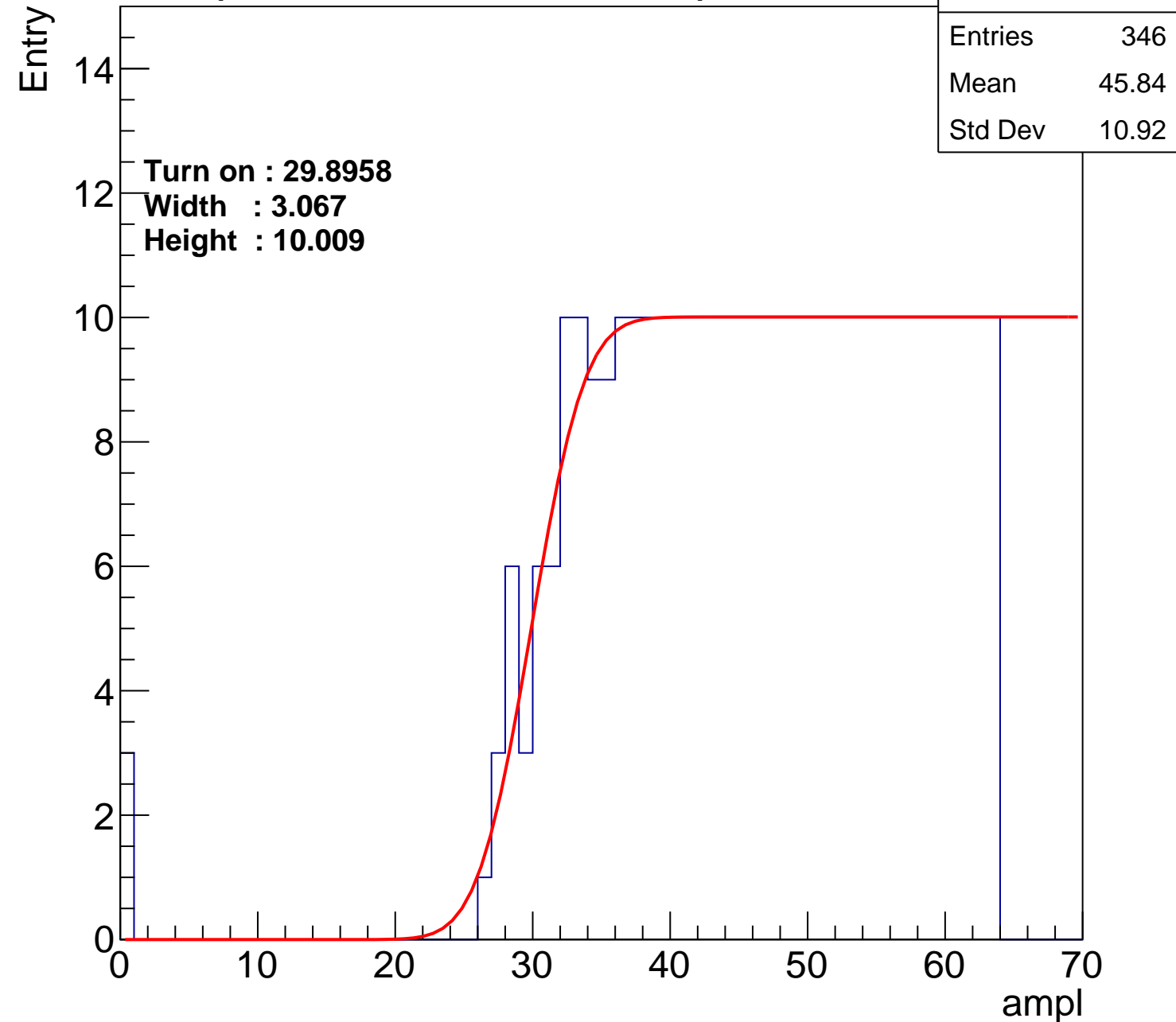
Width : 3.067

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	359
Mean	45.37
Std Dev	10.77

Turn on : 28.4674

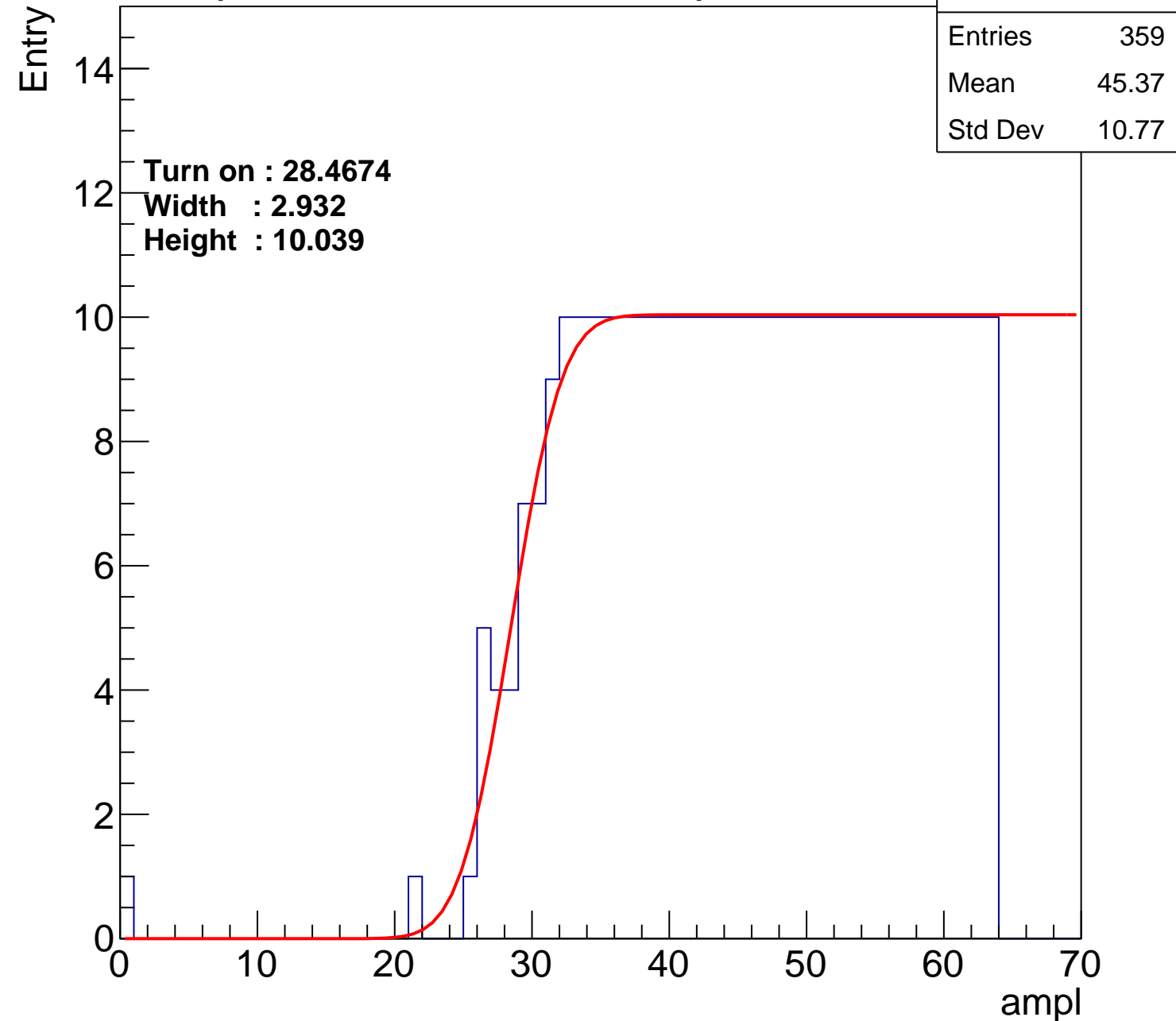
Width : 2.932

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.8
Std Dev	11.43

Turn on : 28.3516

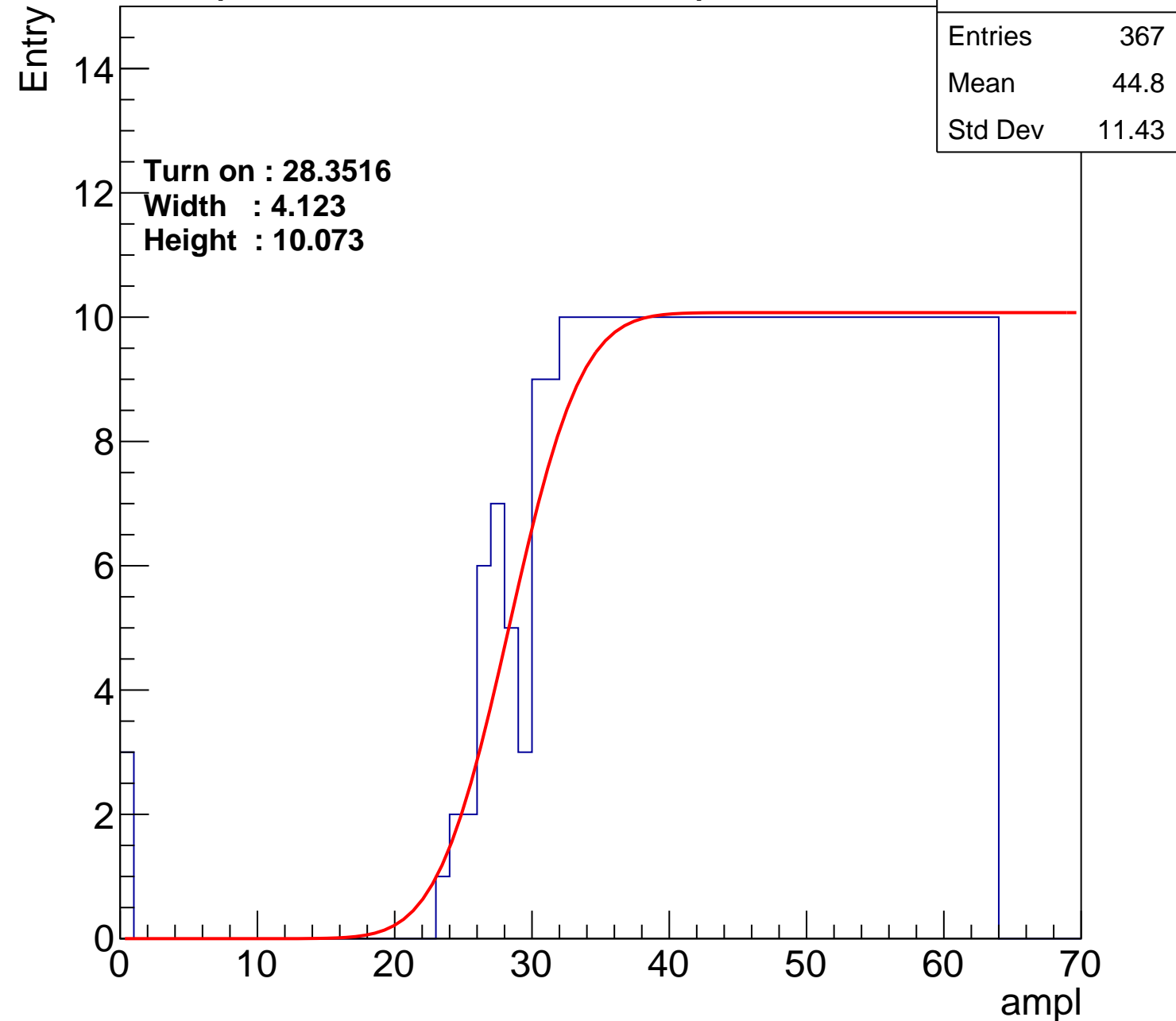
Width : 4.123

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch109

calib_packv5_042523_0143.root, FC#8, port C1

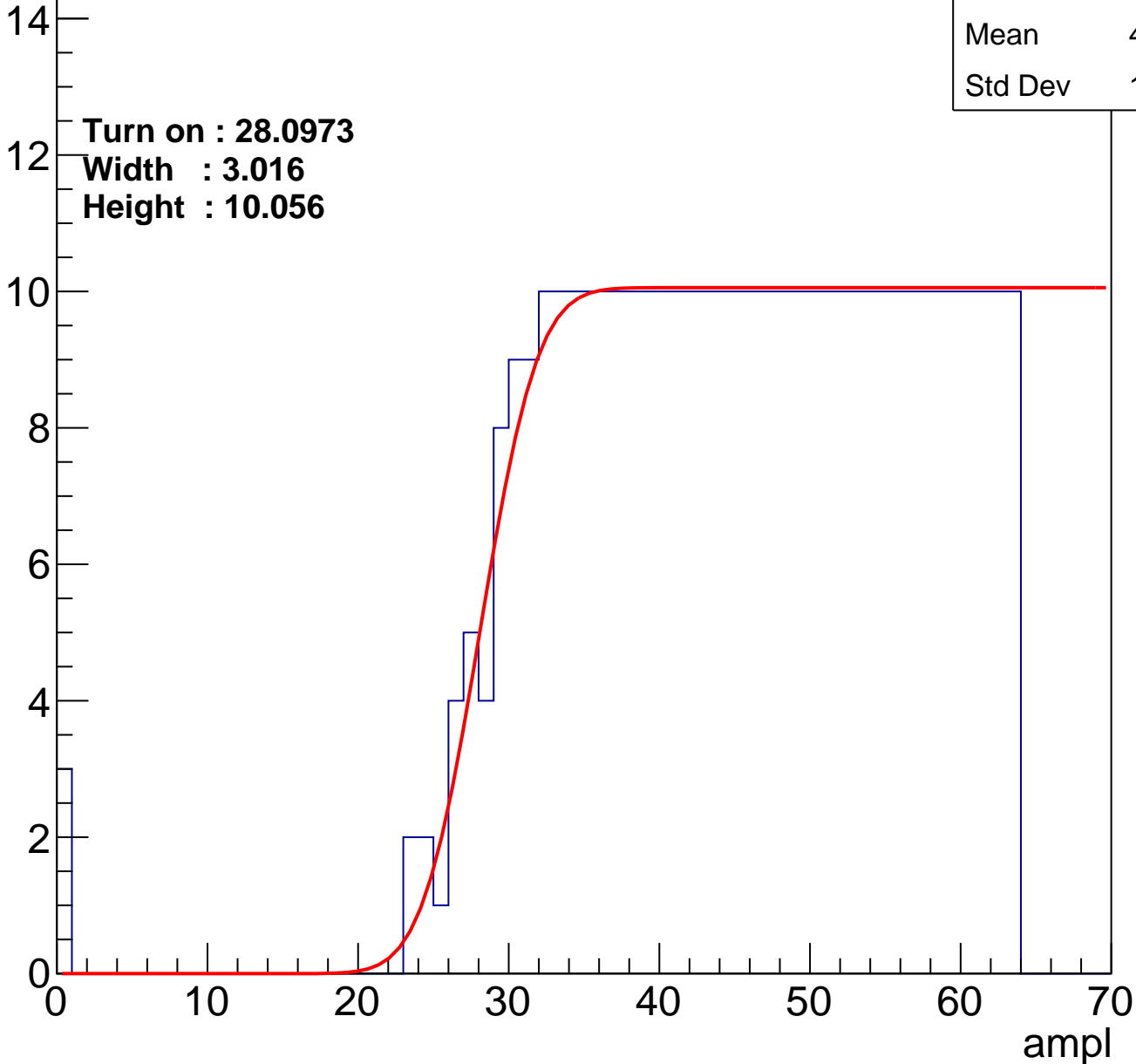
Entry

Entries	367
Mean	44.83
Std Dev	11.39

Turn on : 28.0973

Width : 3.016

Height : 10.056



B0L002S, U20-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.77
Std Dev	11.77

Turn on : 25.4793

Width : 3.076

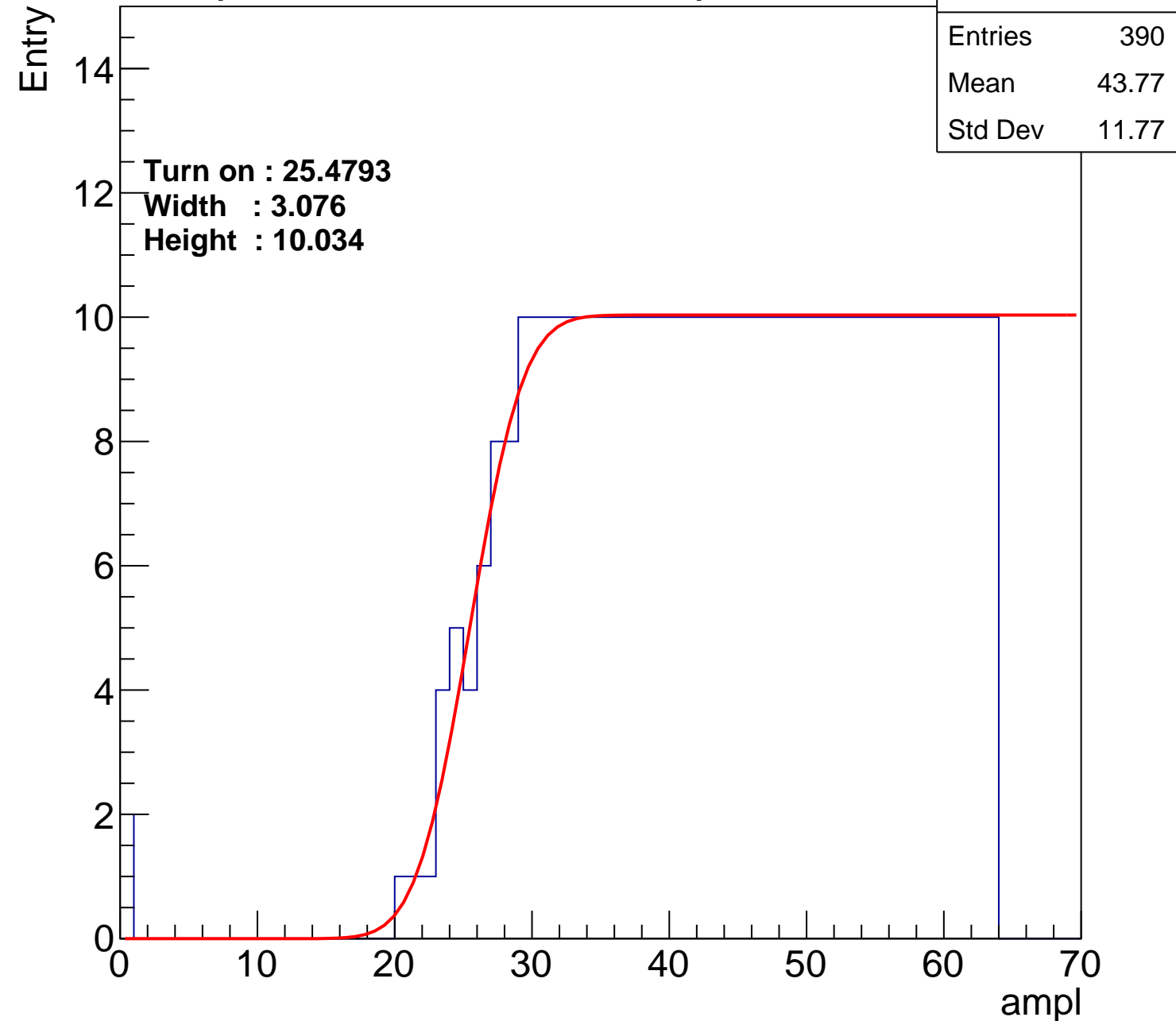
Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U20-ch111

calib_packv5_042523_0143.root, FC#8, port C1

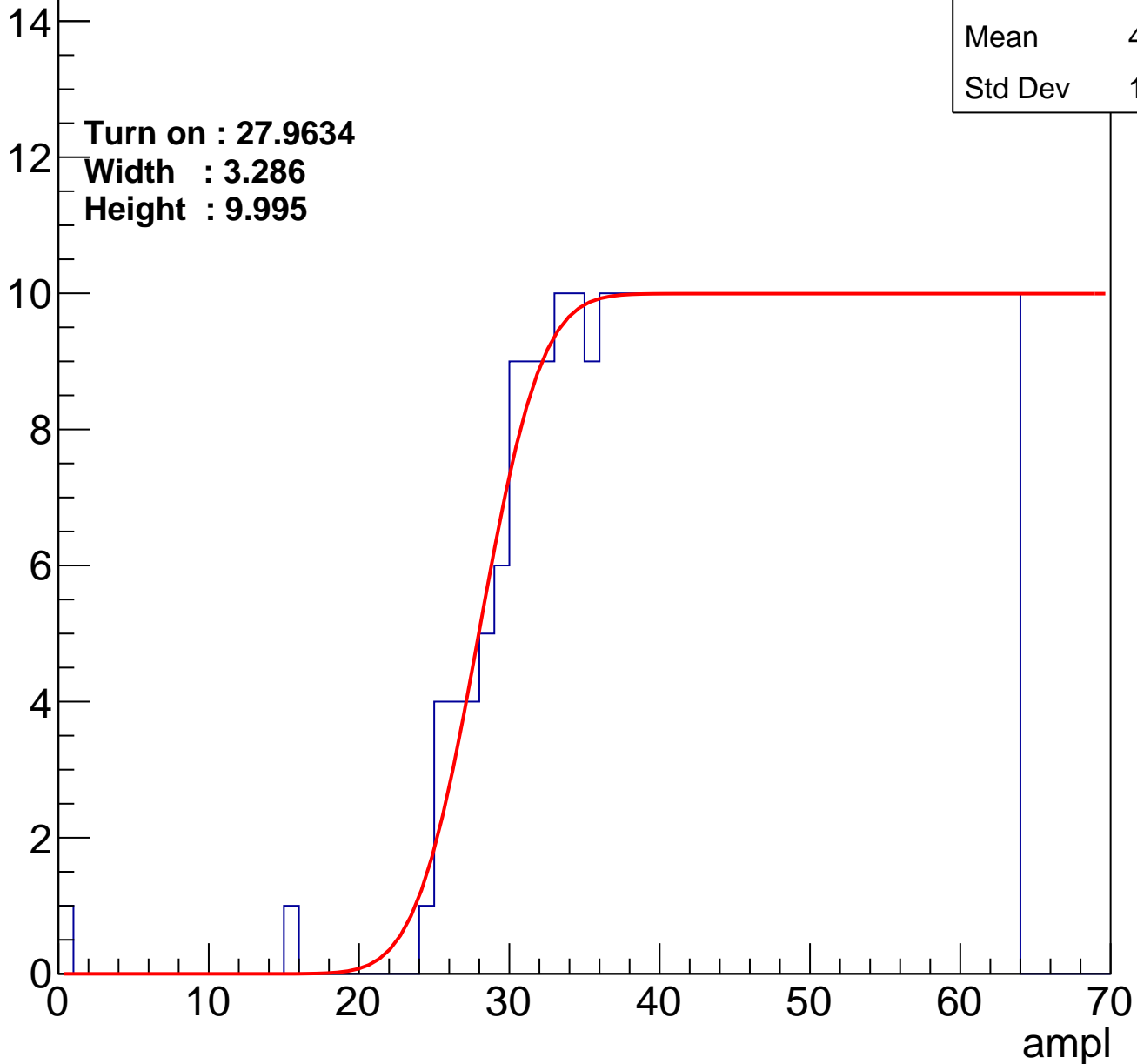
Entries	362
Mean	45.16
Std Dev	10.96

Turn on : 27.9634

Width : 3.286

Height : 9.995

Entry



B0L002S, U20-ch112

calib_packv5_042523_0143.root, FC#8, port C1

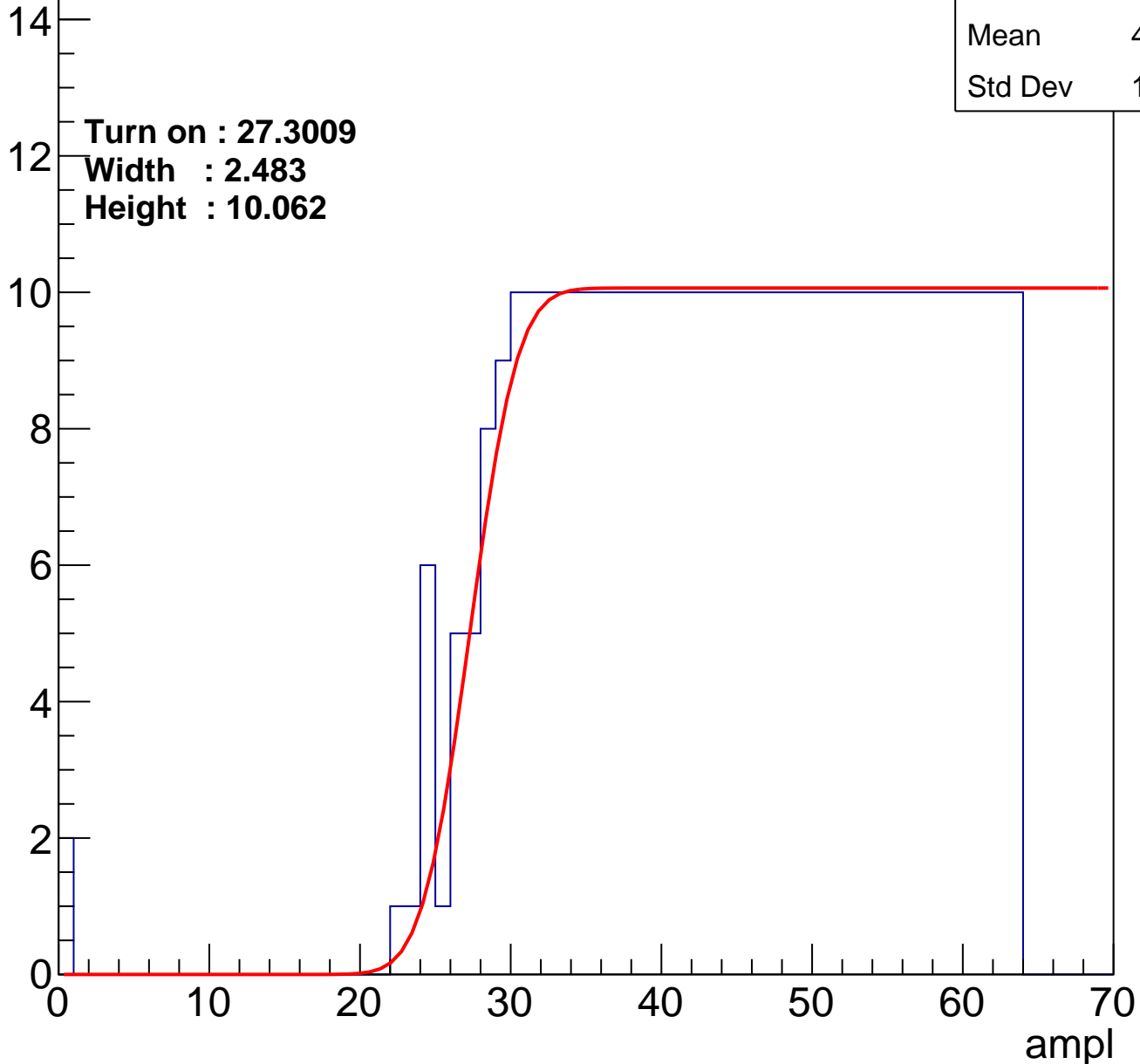
Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 27.3009

Width : 2.483

Height : 10.062

Entry



B0L002S, U20-ch113

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.61
Std Dev	11.19

Turn on : 27.0516

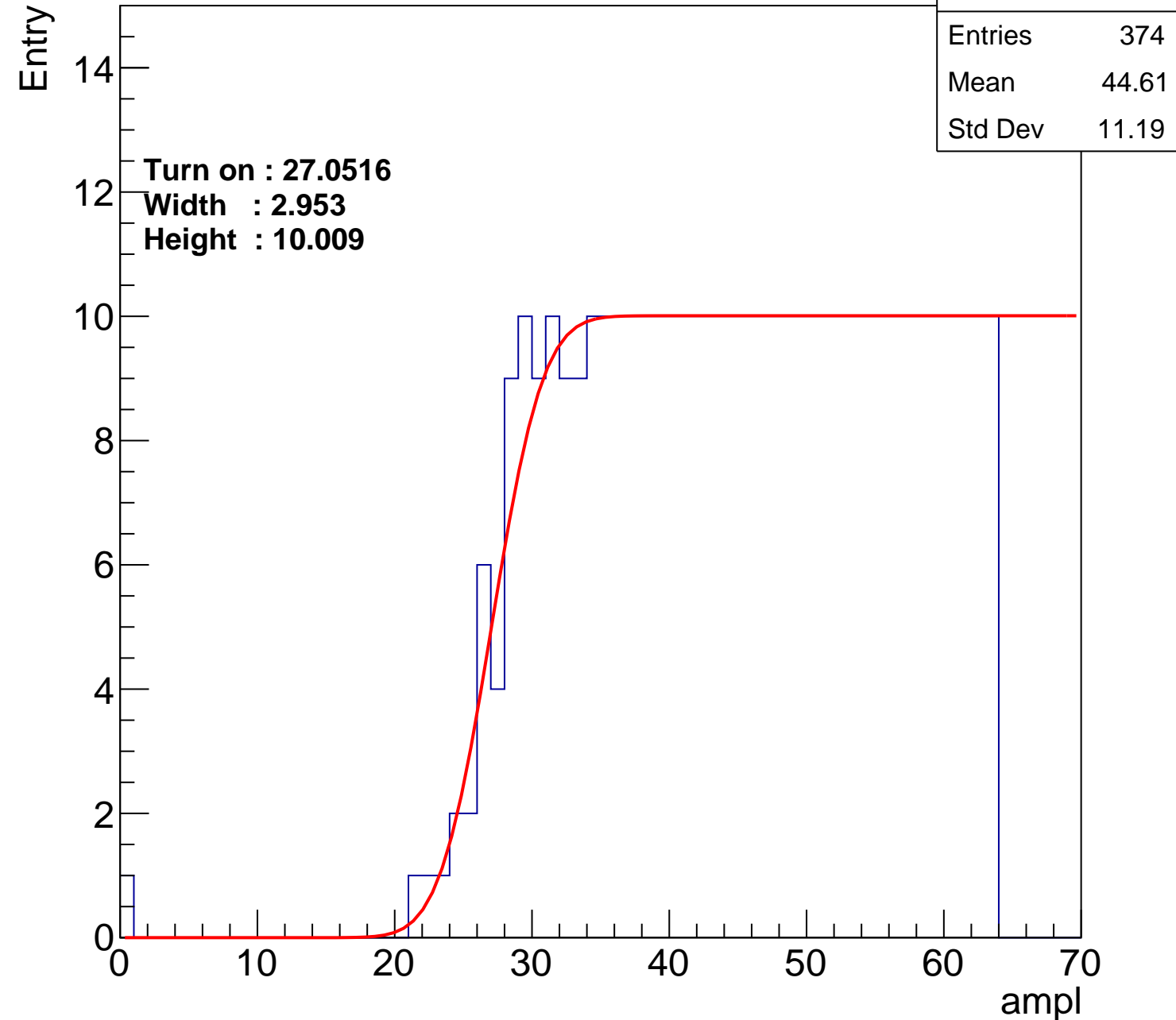
Width : 2.953

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch114

calib_packv5_042523_0143.root, FC#8, port C1

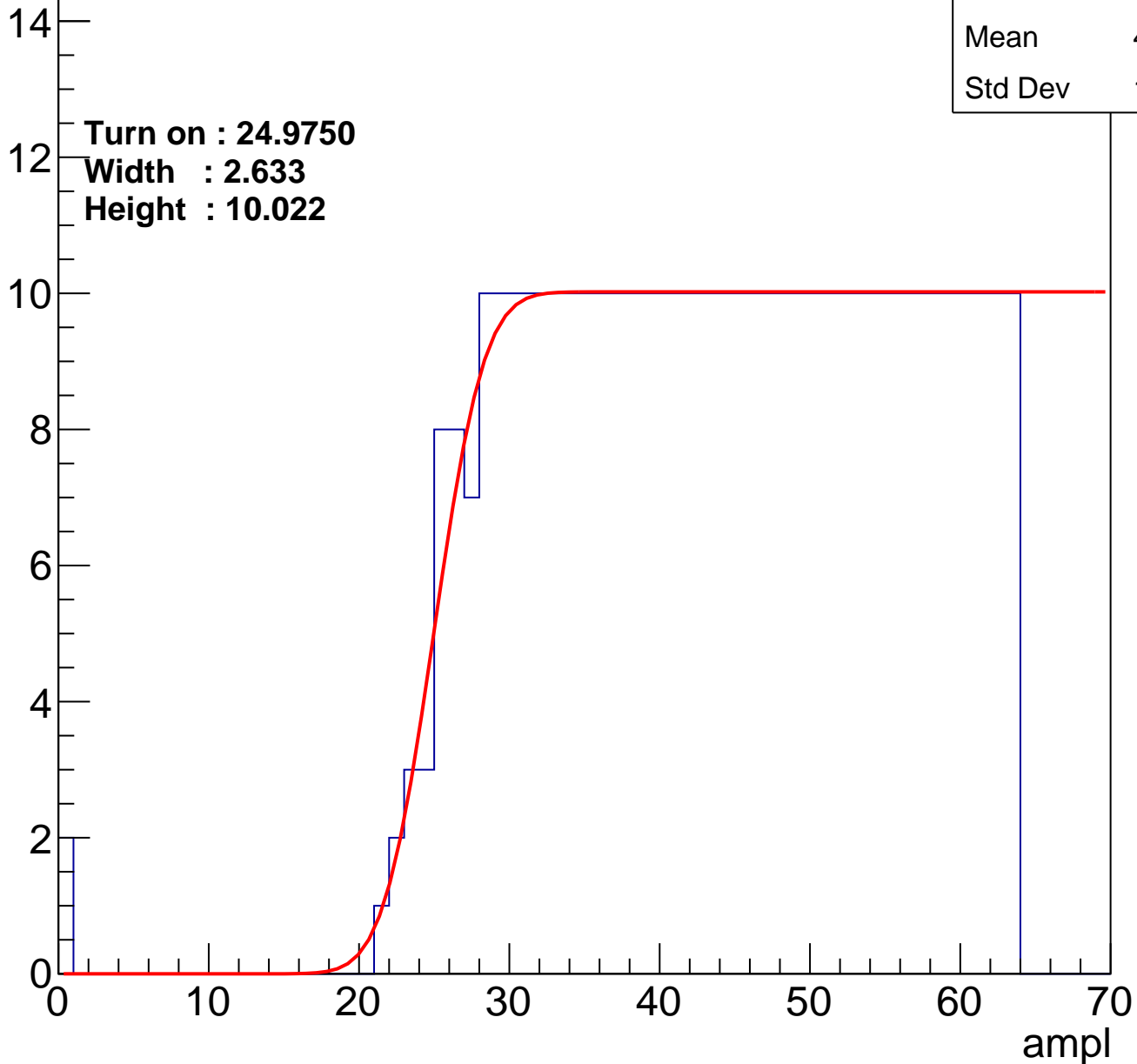
Entries	394
Mean	43.61
Std Dev	11.81

Turn on : 24.9750

Width : 2.633

Height : 10.022

Entry



B0L002S, U20-ch115

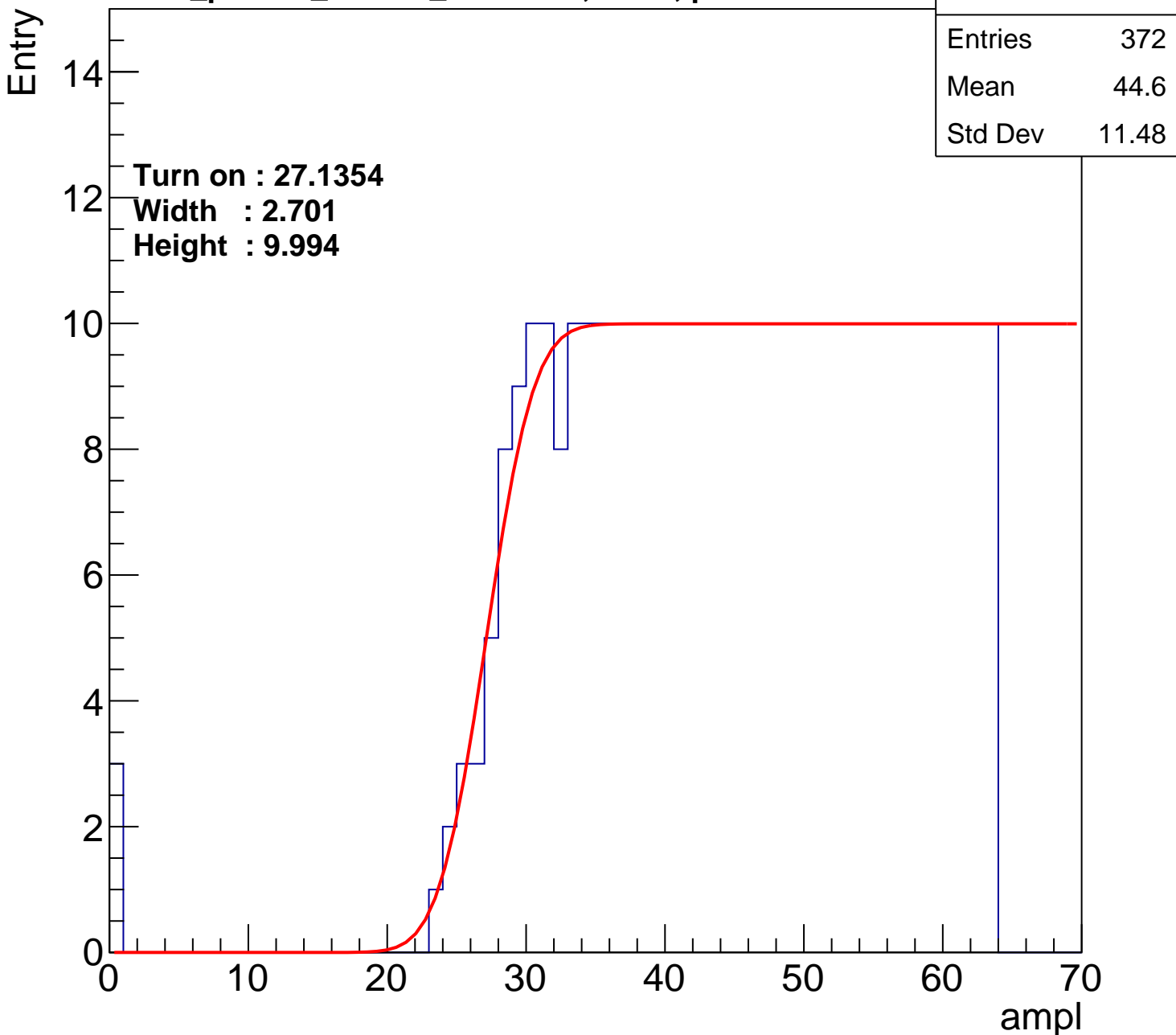
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 27.1354

Width : 2.701

Height : 9.994

Entries	372
Mean	44.6
Std Dev	11.48



B0L002S, U20-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.9
Std Dev	11.18

Turn on : 27.8296

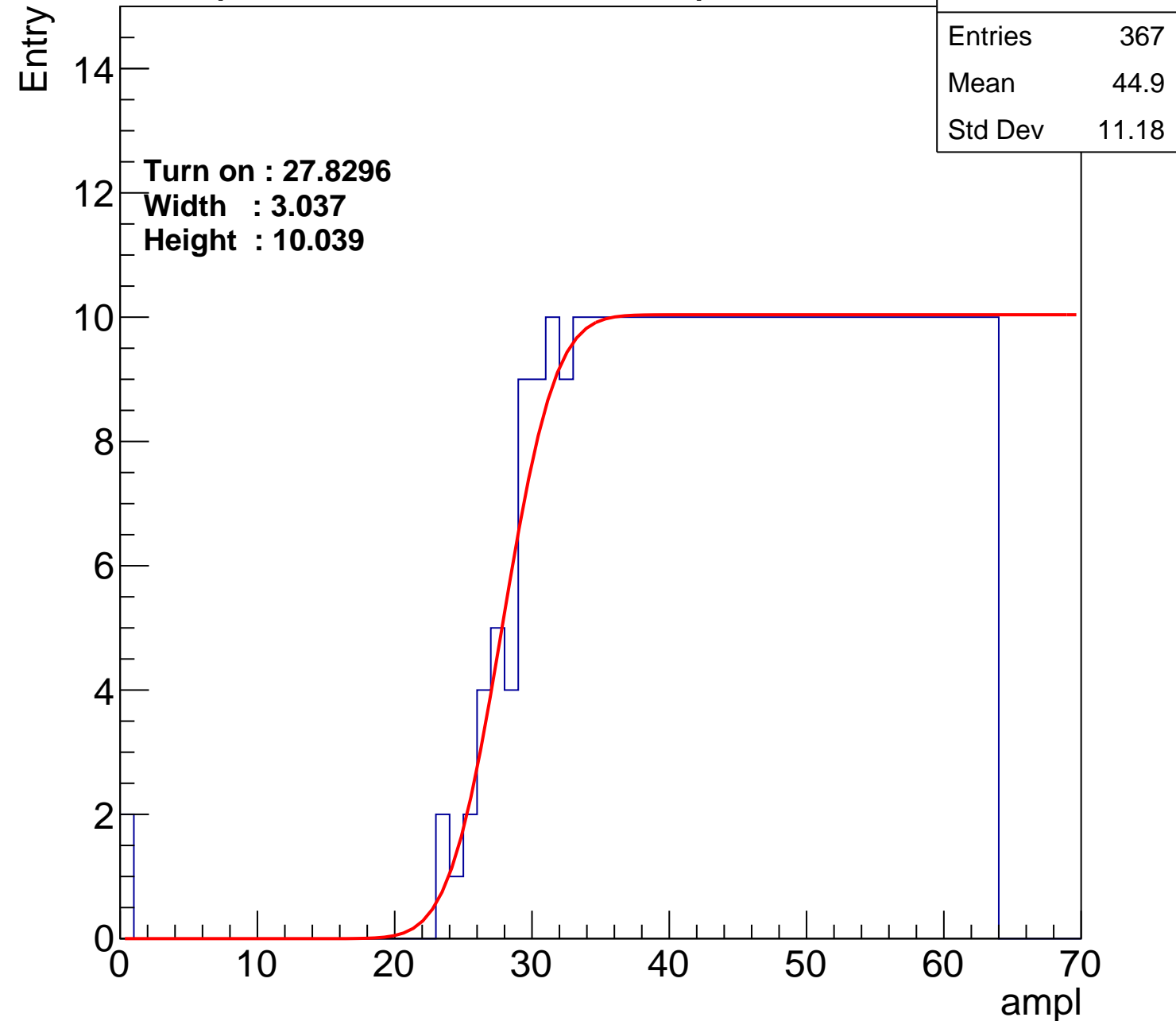
Width : 3.037

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch117

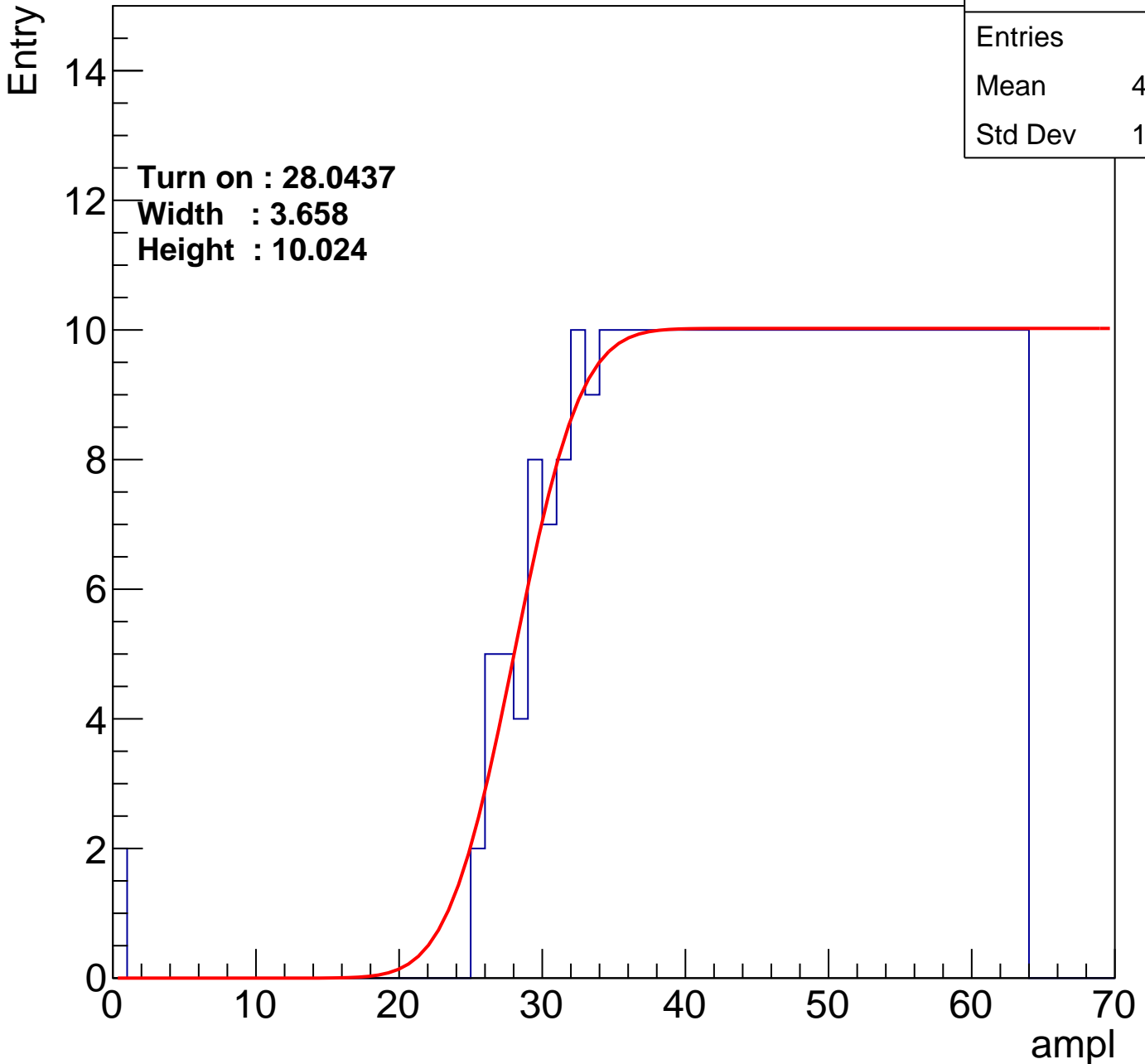
calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.23
Std Dev	11.02

Turn on : 28.0437

Width : 3.658

Height : 10.024



B0L002S, U20-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.82
Std Dev	11.02

Turn on : 27.5583

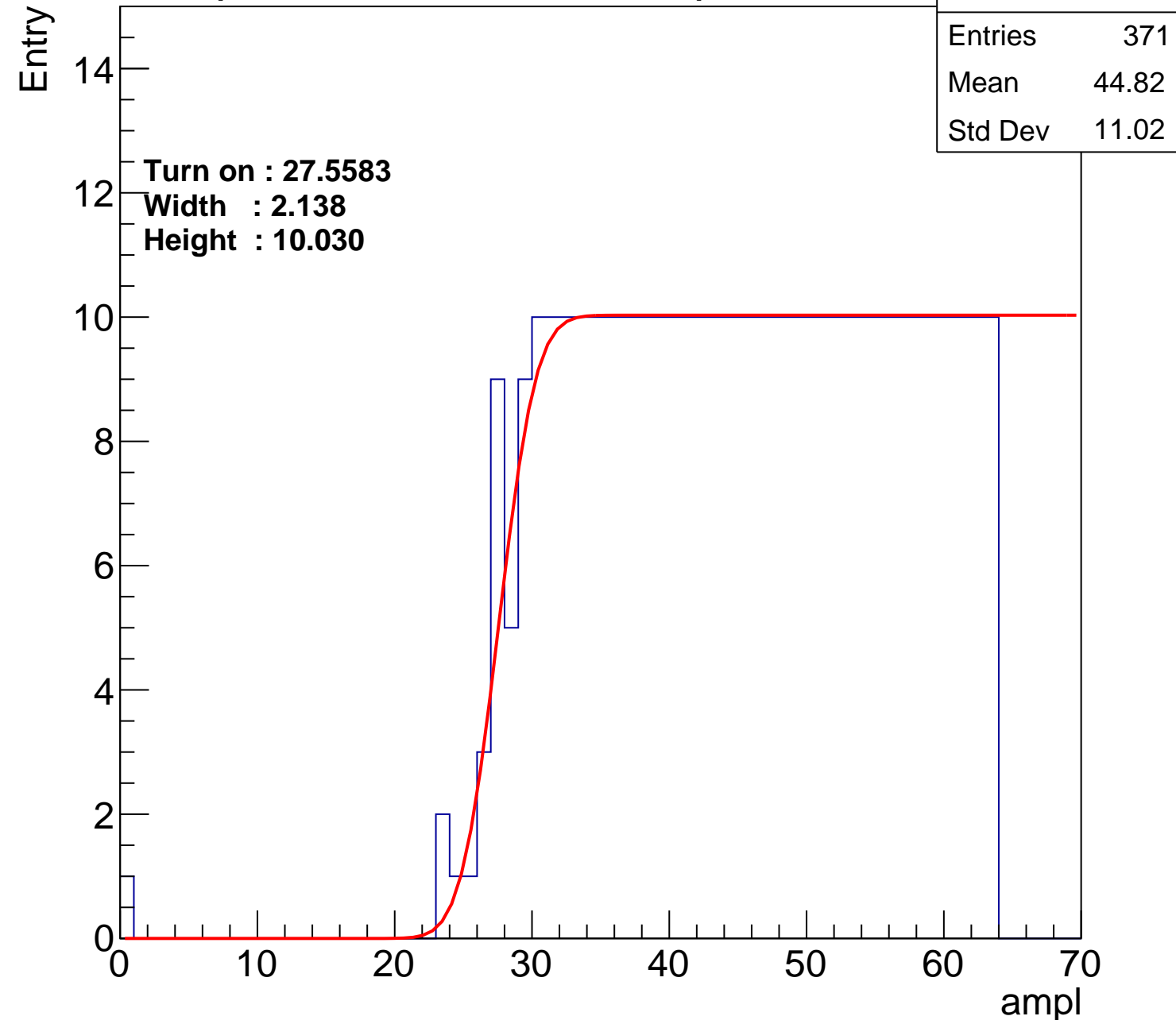
Width : 2.138

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.77
Std Dev	11.09

Turn on : 27.2223

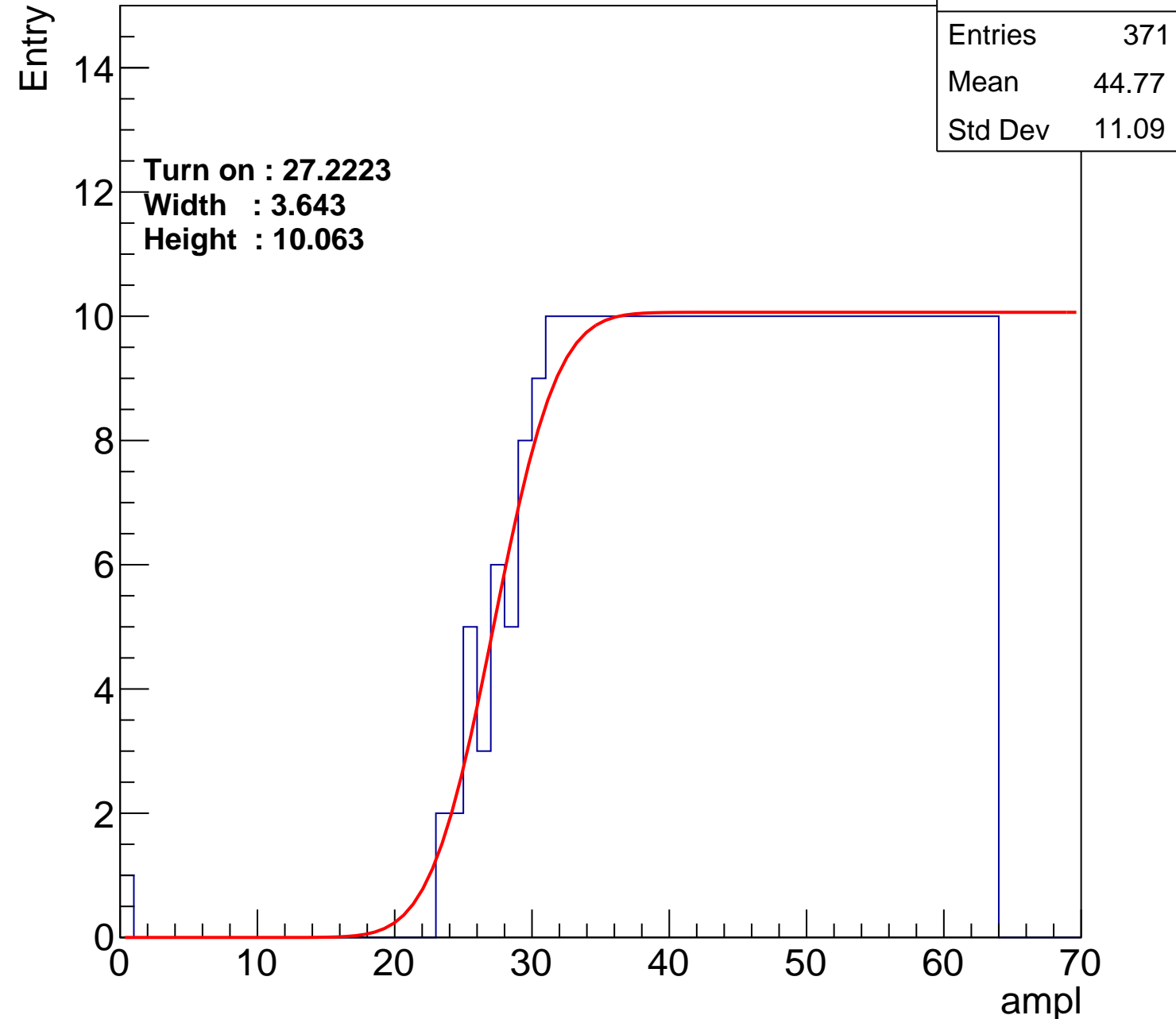
Width : 3.643

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch120

calib_packv5_042523_0143.root, FC#8, port C1

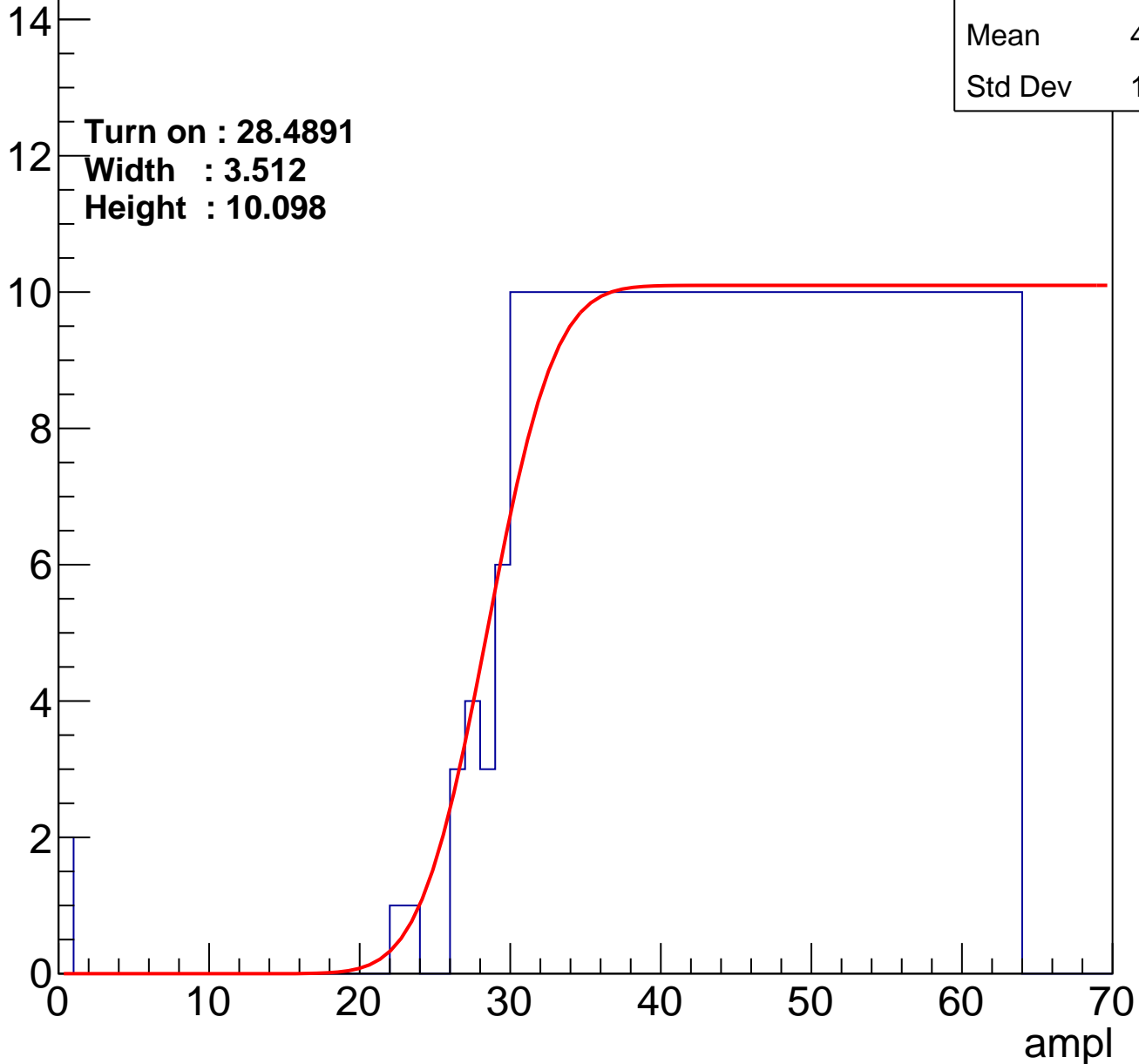
Entries	360
Mean	45.27
Std Dev	10.97

Turn on : 28.4891

Width : 3.512

Height : 10.098

Entry



B0L002S, U20-ch121

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.2
Std Dev	10.84

Turn on : 28.1188

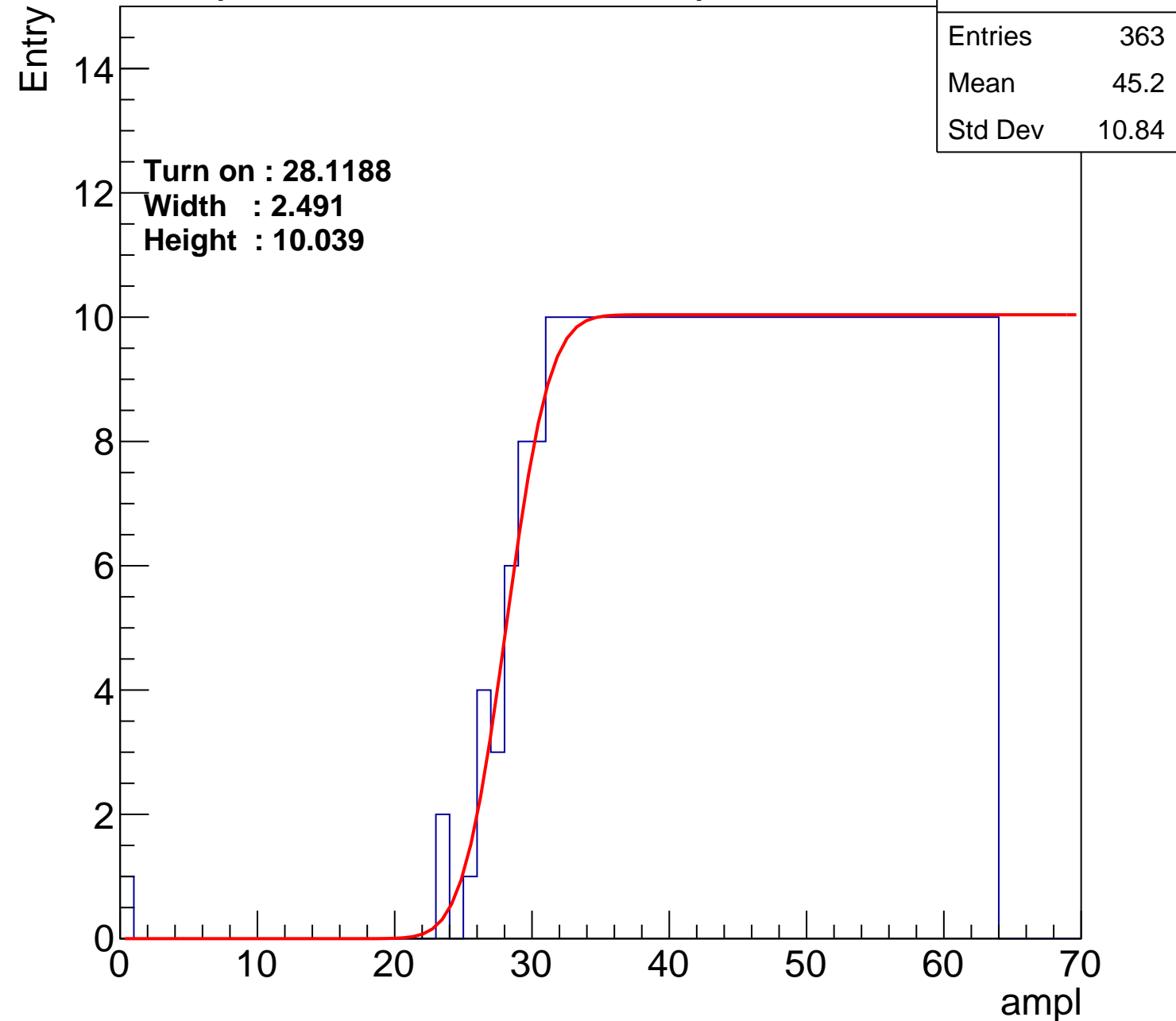
Width : 2.491

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch122

calib_packv5_042523_0143.root, FC#8, port C1

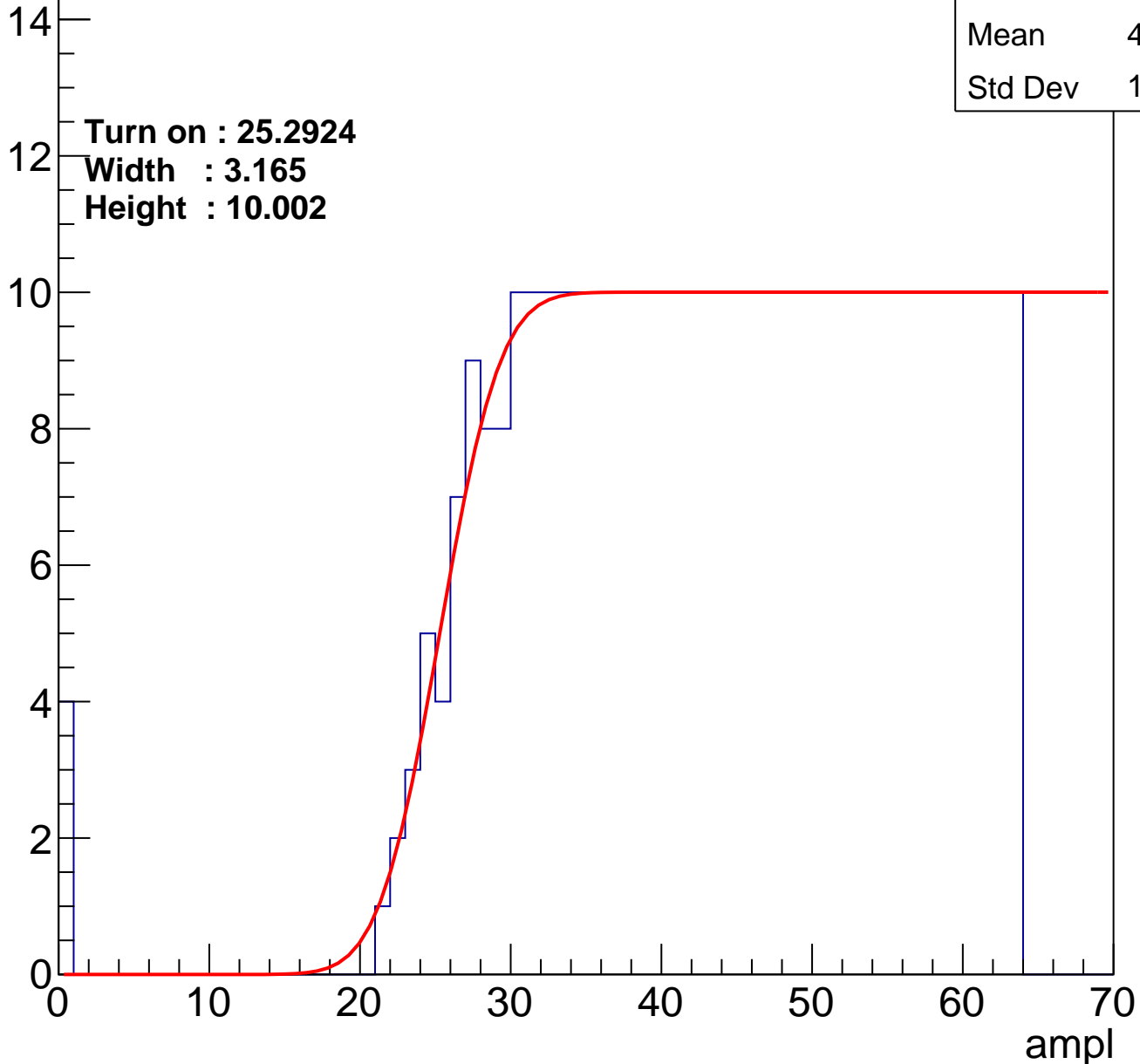
Entries	391
Mean	43.59
Std Dev	12.13

Turn on : 25.2924

Width : 3.165

Height : 10.002

Entry



B0L002S, U20-ch123

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.41
Std Dev	11.17

Turn on : 29.2114

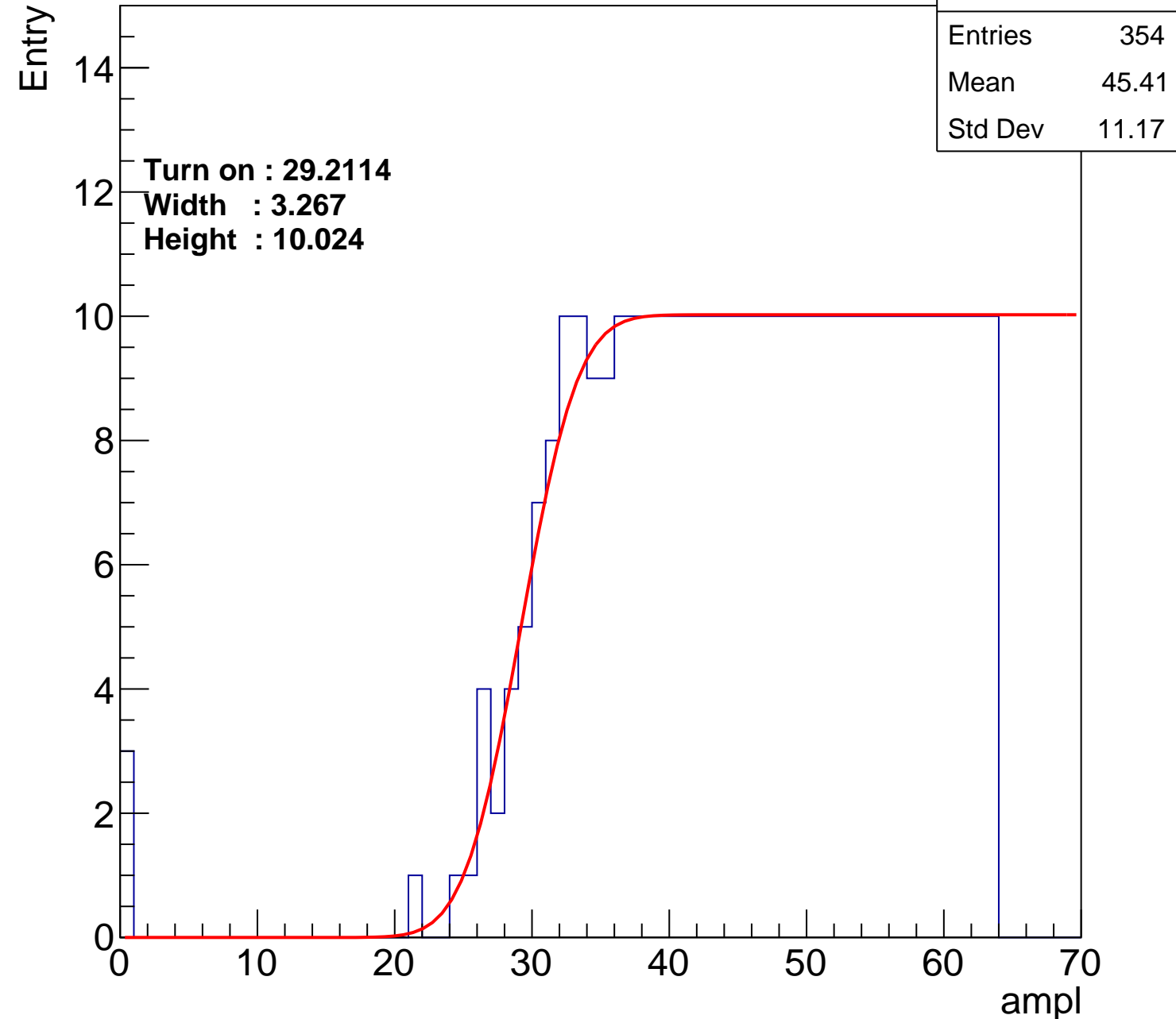
Width : 3.267

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.12
Std Dev	10.9

Turn on : 27.7343

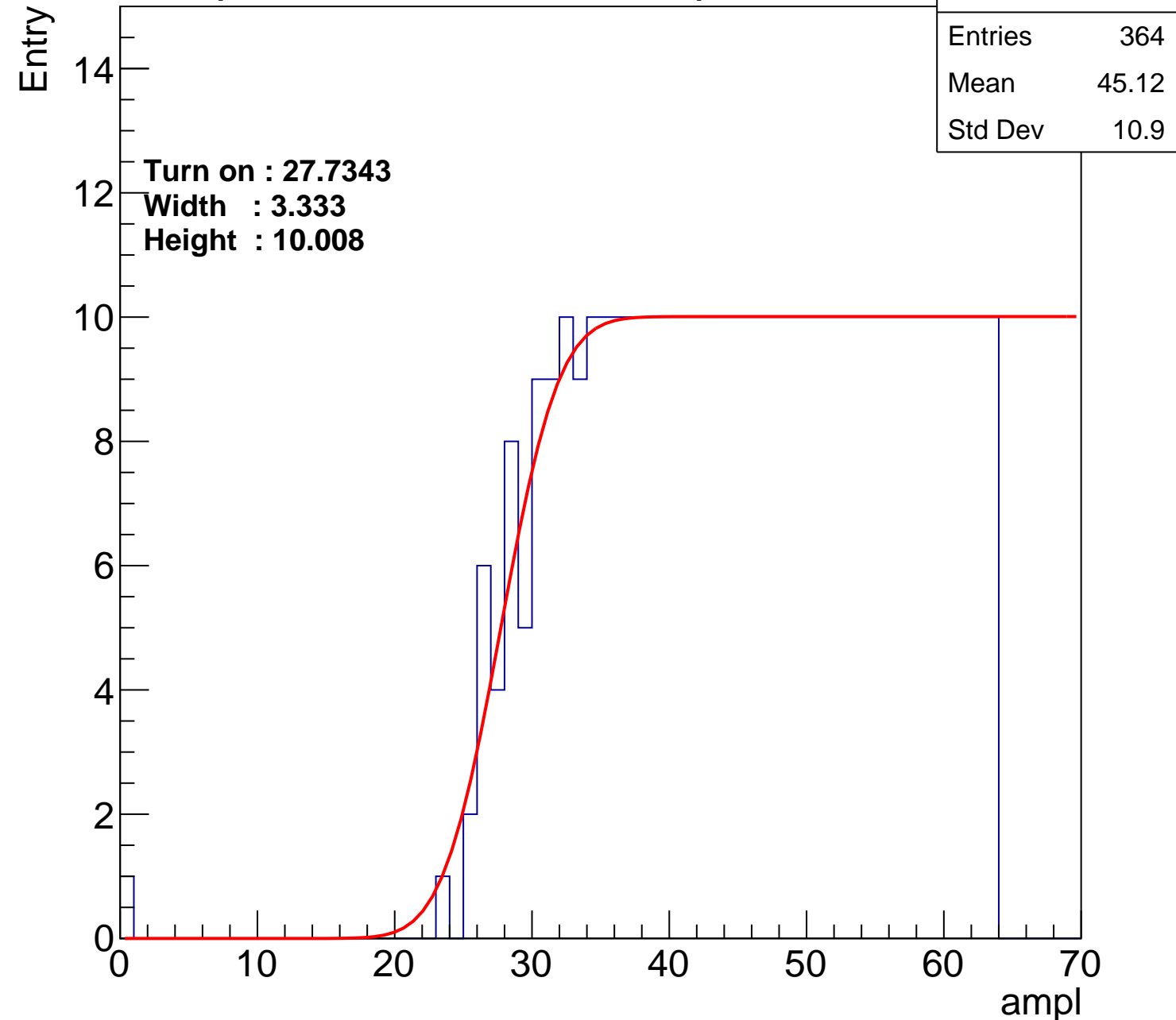
Width : 3.333

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch125

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45
Std Dev	11.13

Turn on : 28.0017

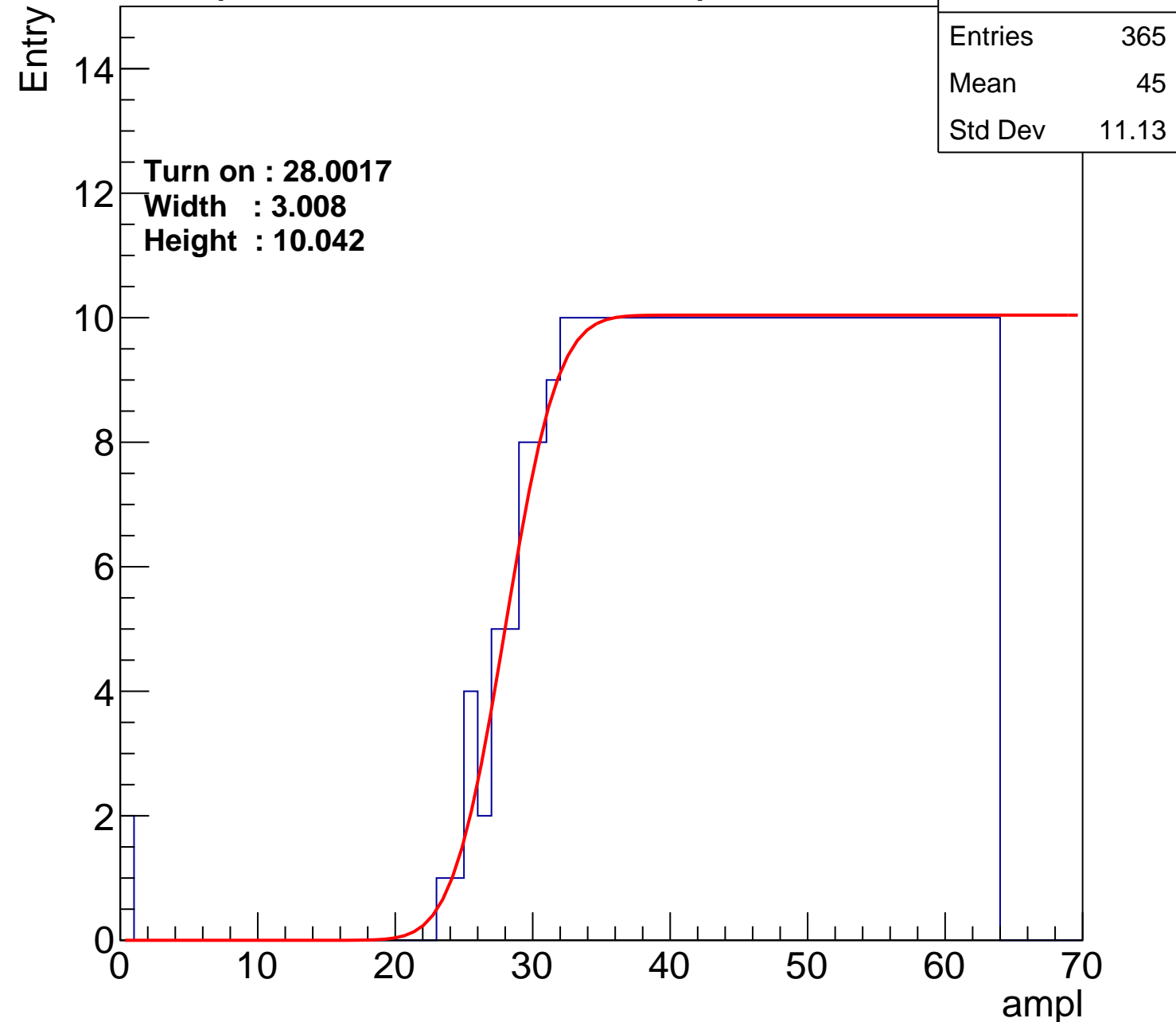
Width : 3.008

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.53
Std Dev	11.54

Turn on : 27.7132

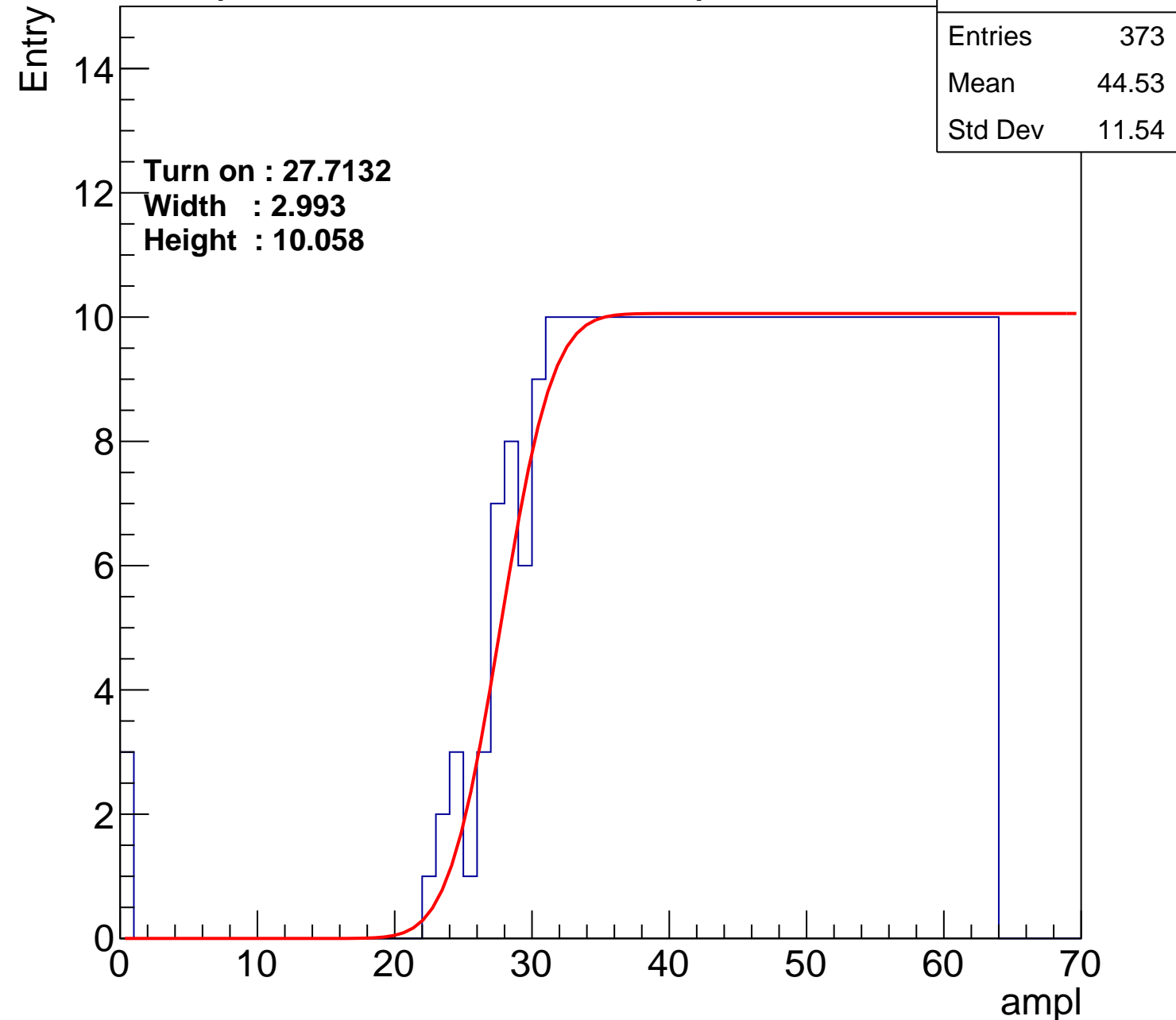
Width : 2.993

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 28.2334

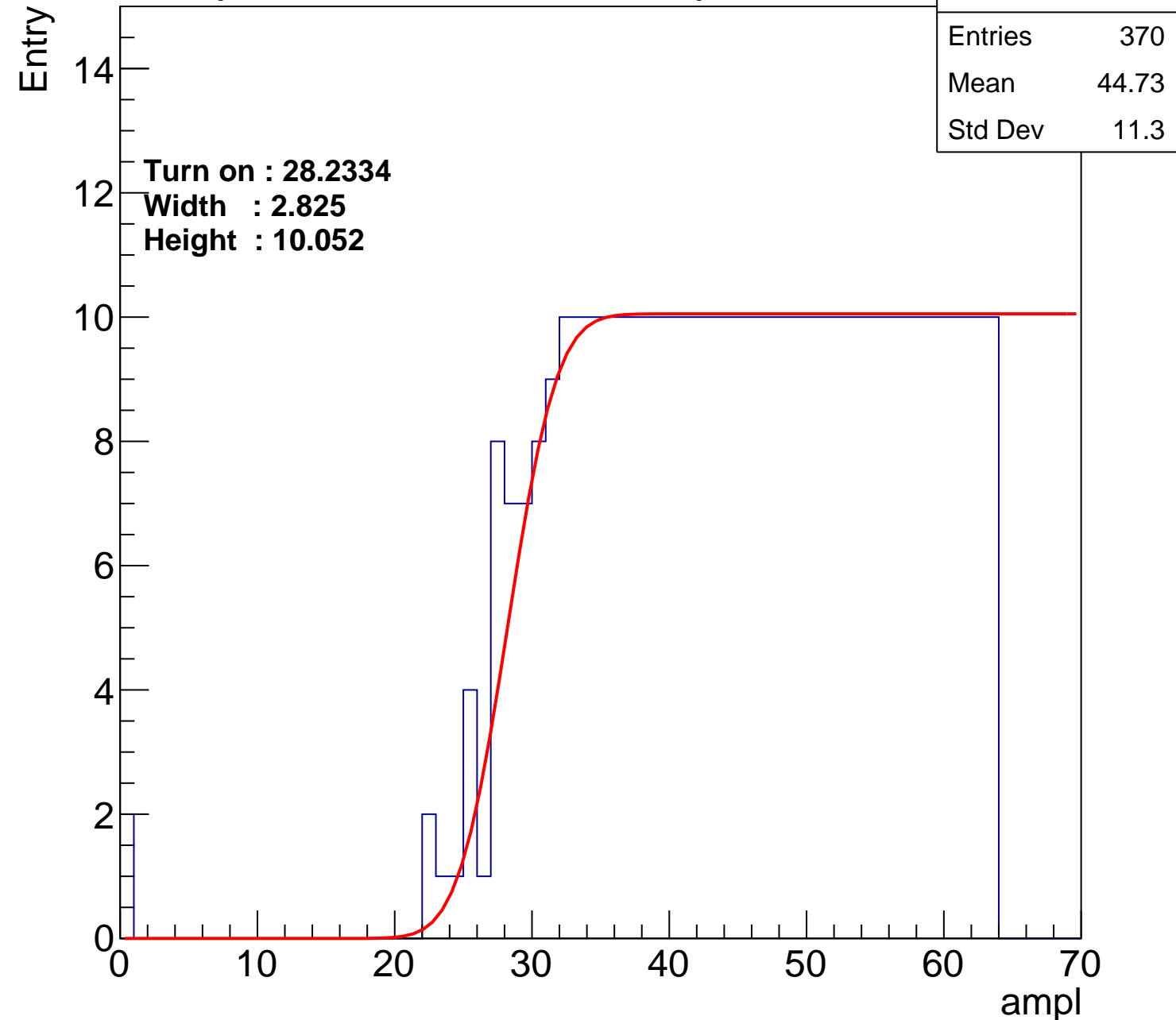
Width : 2.825

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U20-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.73
Std Dev	11.3

Turn on : 28.2334

Width : 2.825

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl

