

B1L002S, U1-ch0

calib_packv5_042523_0143.root, FC#10, port B3

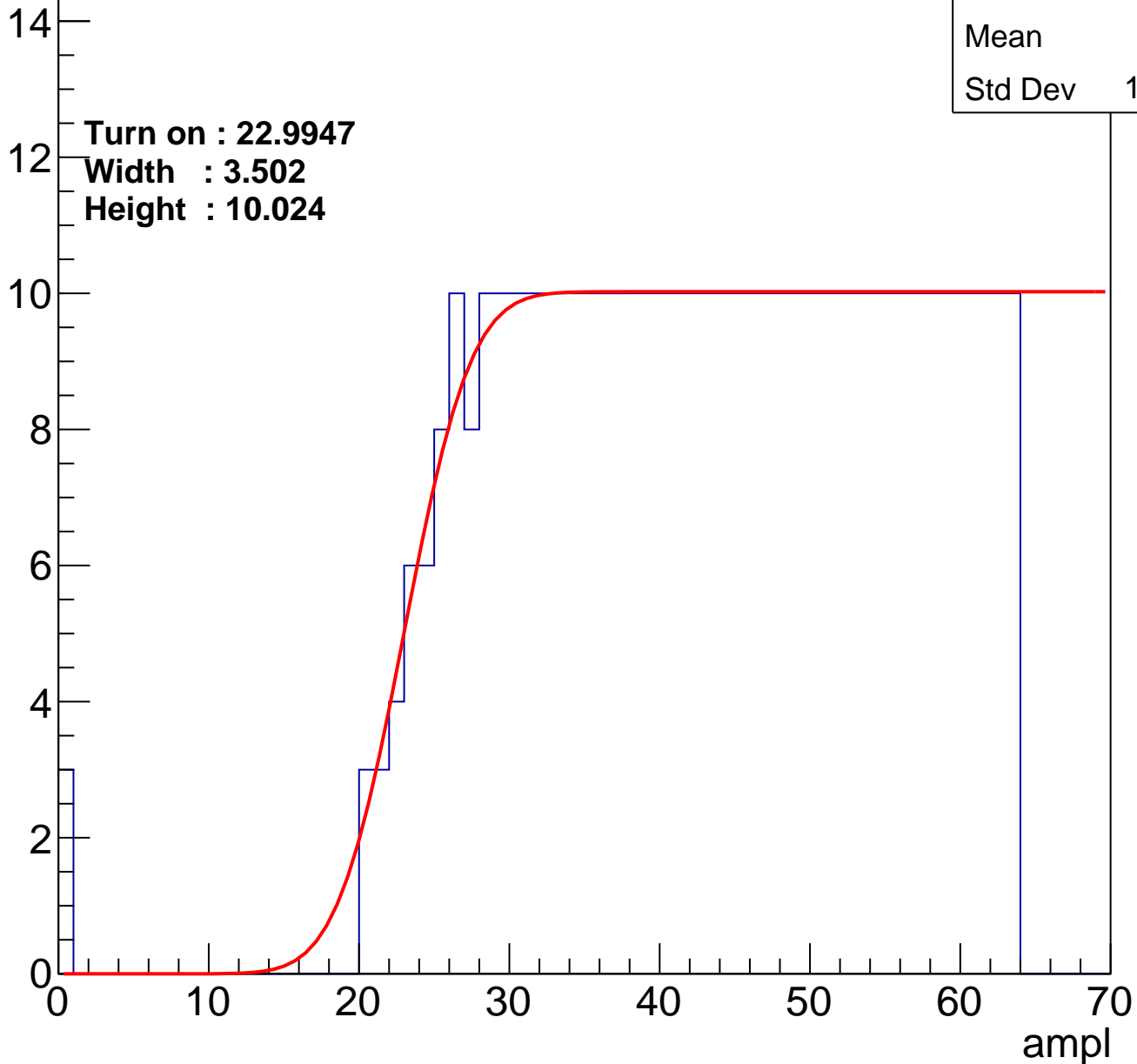
Entries	411
Mean	42.7
Std Dev	12.43

Turn on : 22.9947

Width : 3.502

Height : 10.024

Entry



B1L002S, U1-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.3
Std Dev	11.59

Turn on : 26.8131

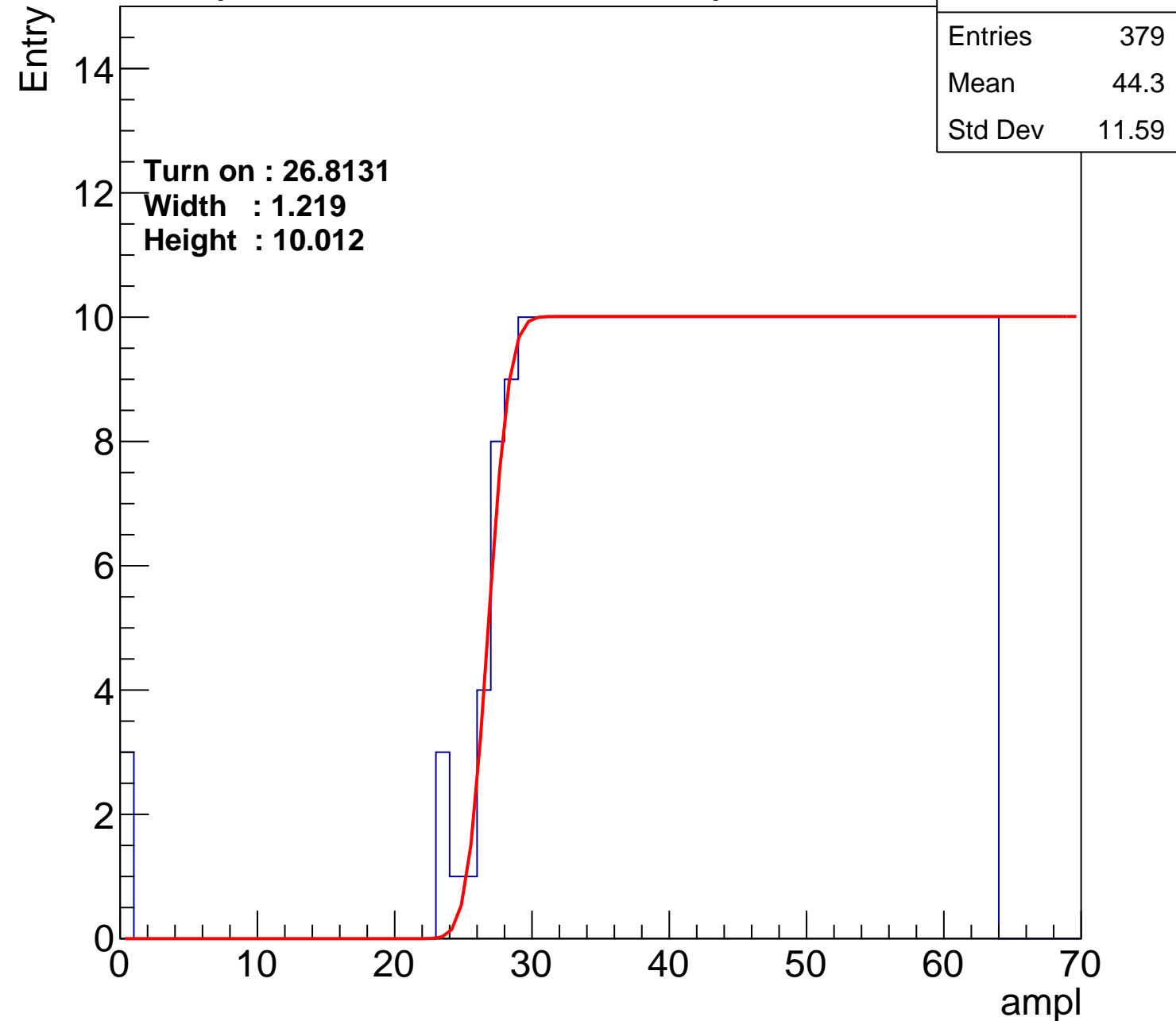
Width : 1.219

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.3937

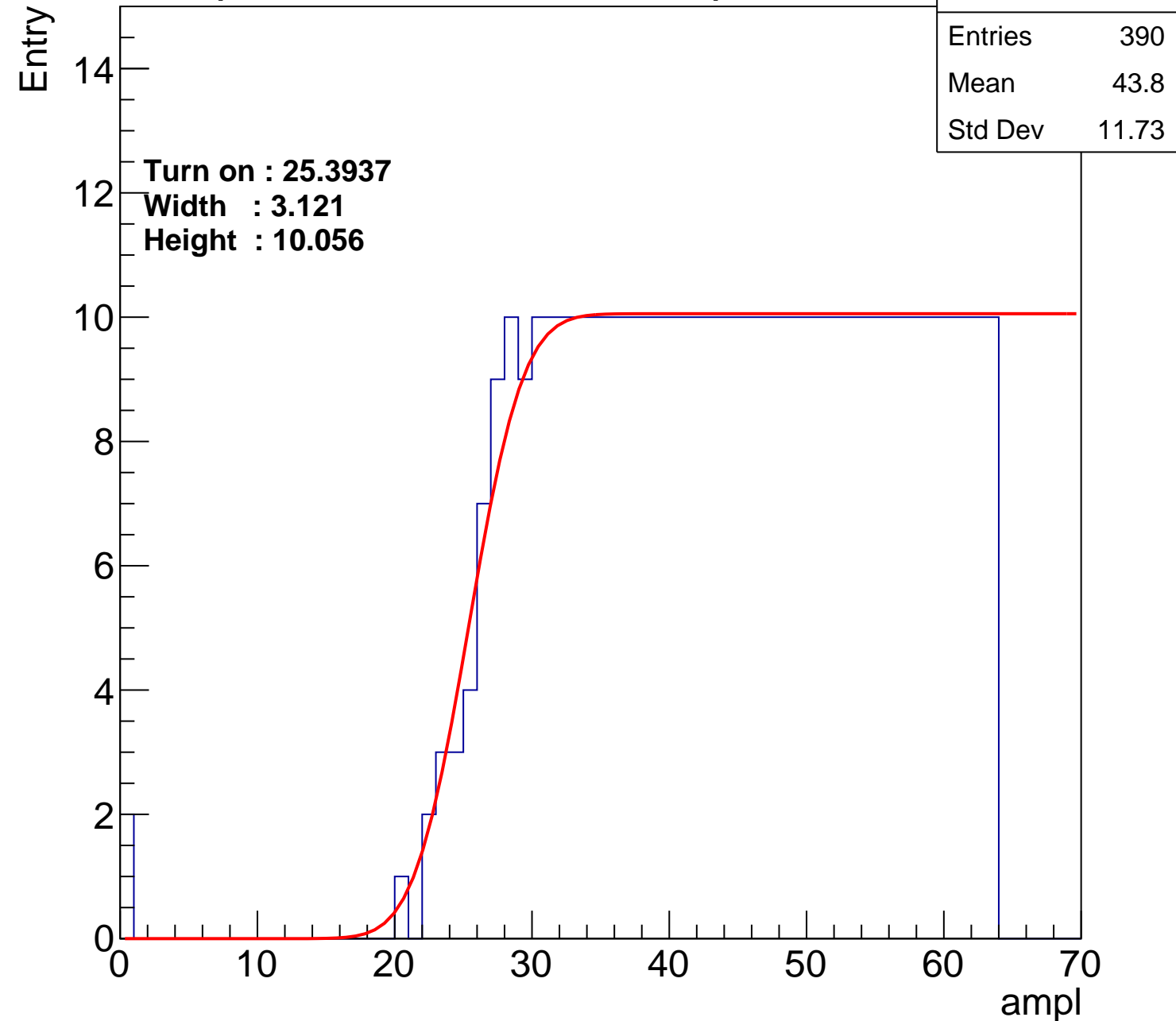
Width : 3.121

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch3

calib_packv5_042523_0143.root, FC#10, port B3

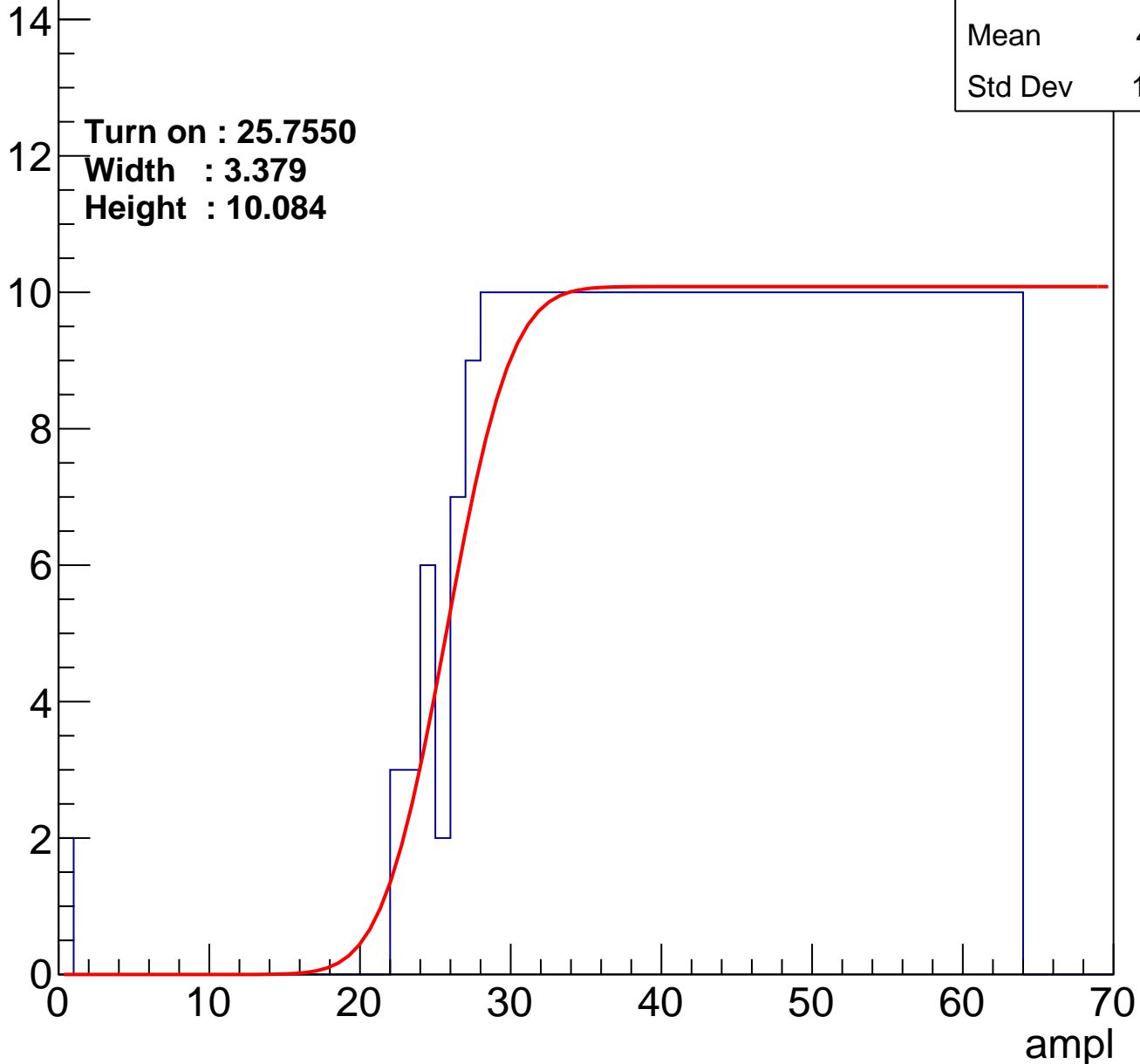
Entries	392
Mean	43.71
Std Dev	11.77

Turn on : 25.7550

Width : 3.379

Height : 10.084

Entry



B1L002S, U1-ch4

calib_packv5_042523_0143.root, FC#10, port B3

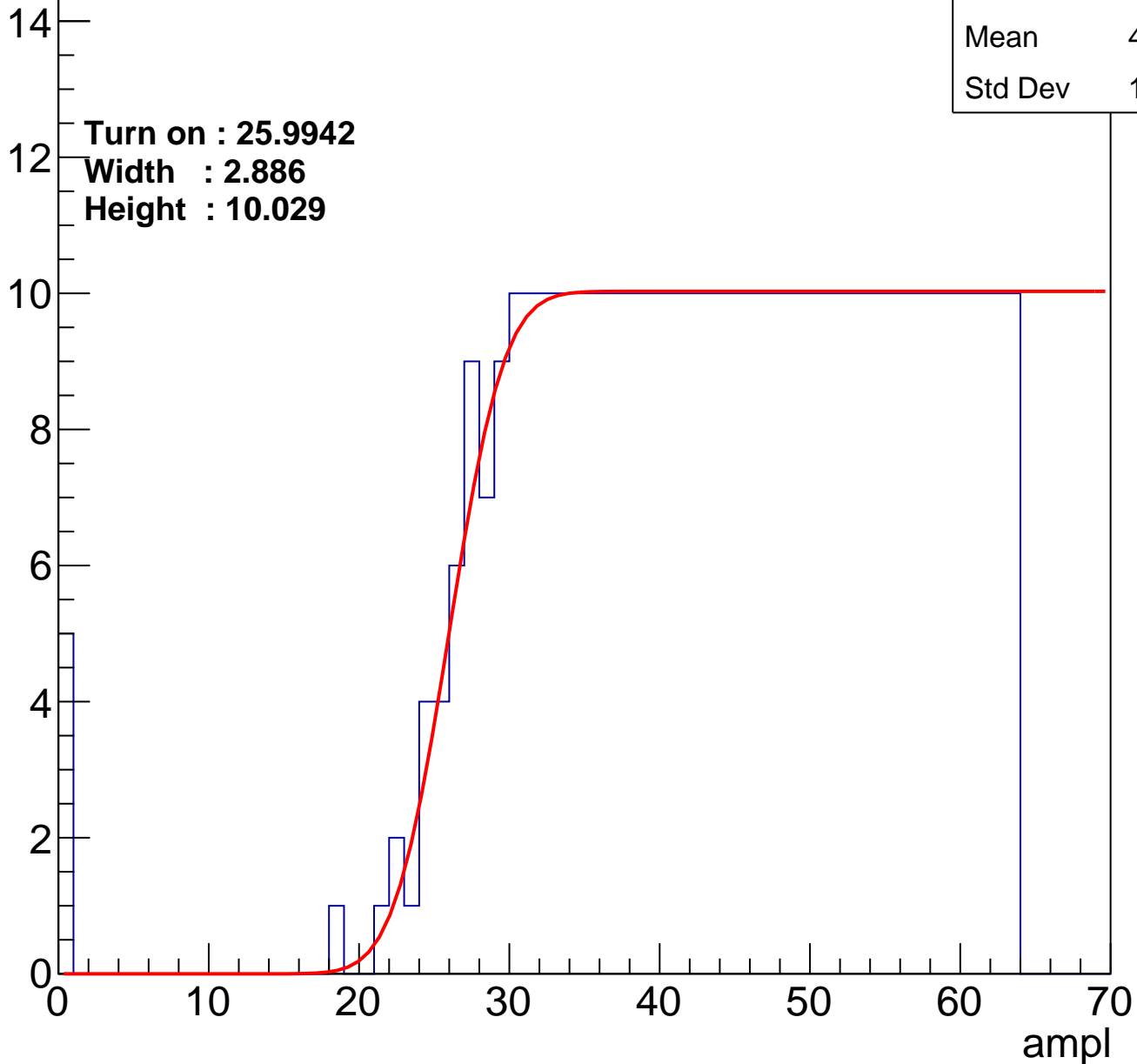
Entries	389
Mean	43.62
Std Dev	12.26

Turn on : 25.9942

Width : 2.886

Height : 10.029

Entry



B1L002S, U1-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.78
Std Dev	11.87

Turn on : 25.2443

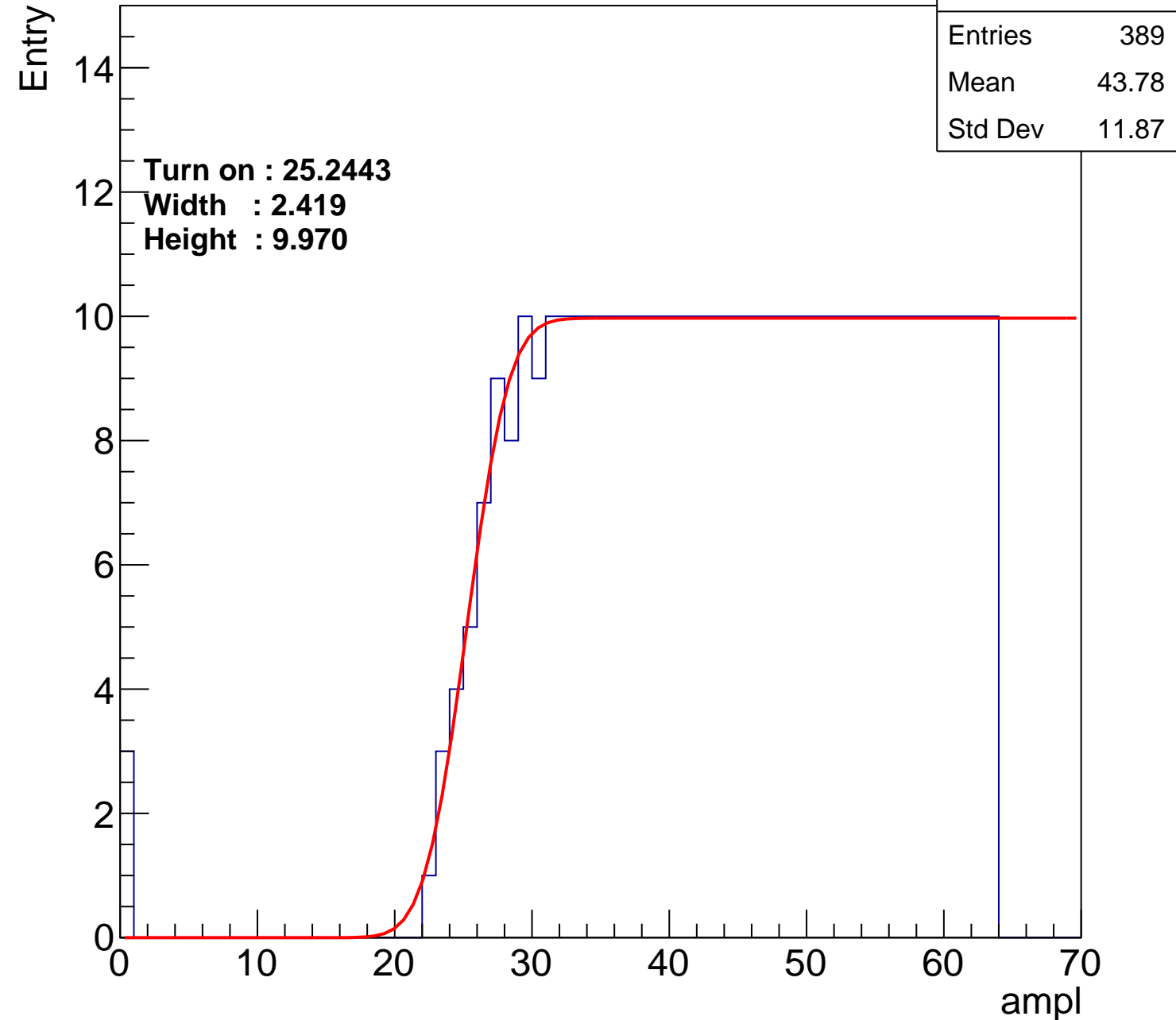
Width : 2.419

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch6

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.59
Std Dev	11.73

Turn on : 24.6782

Width : 2.874

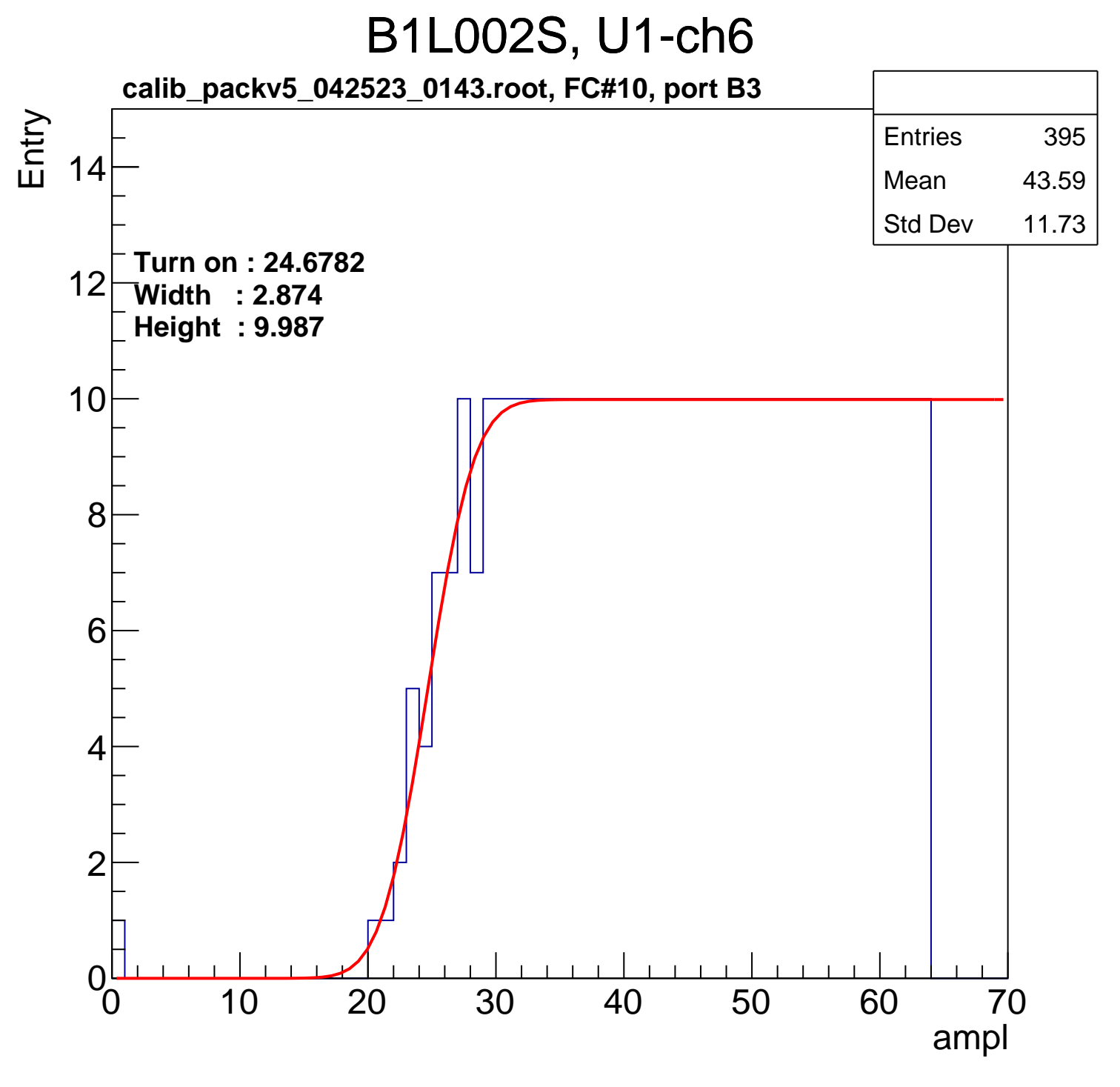
Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch7

calib_packv5_042523_0143.root, FC#10, port B3

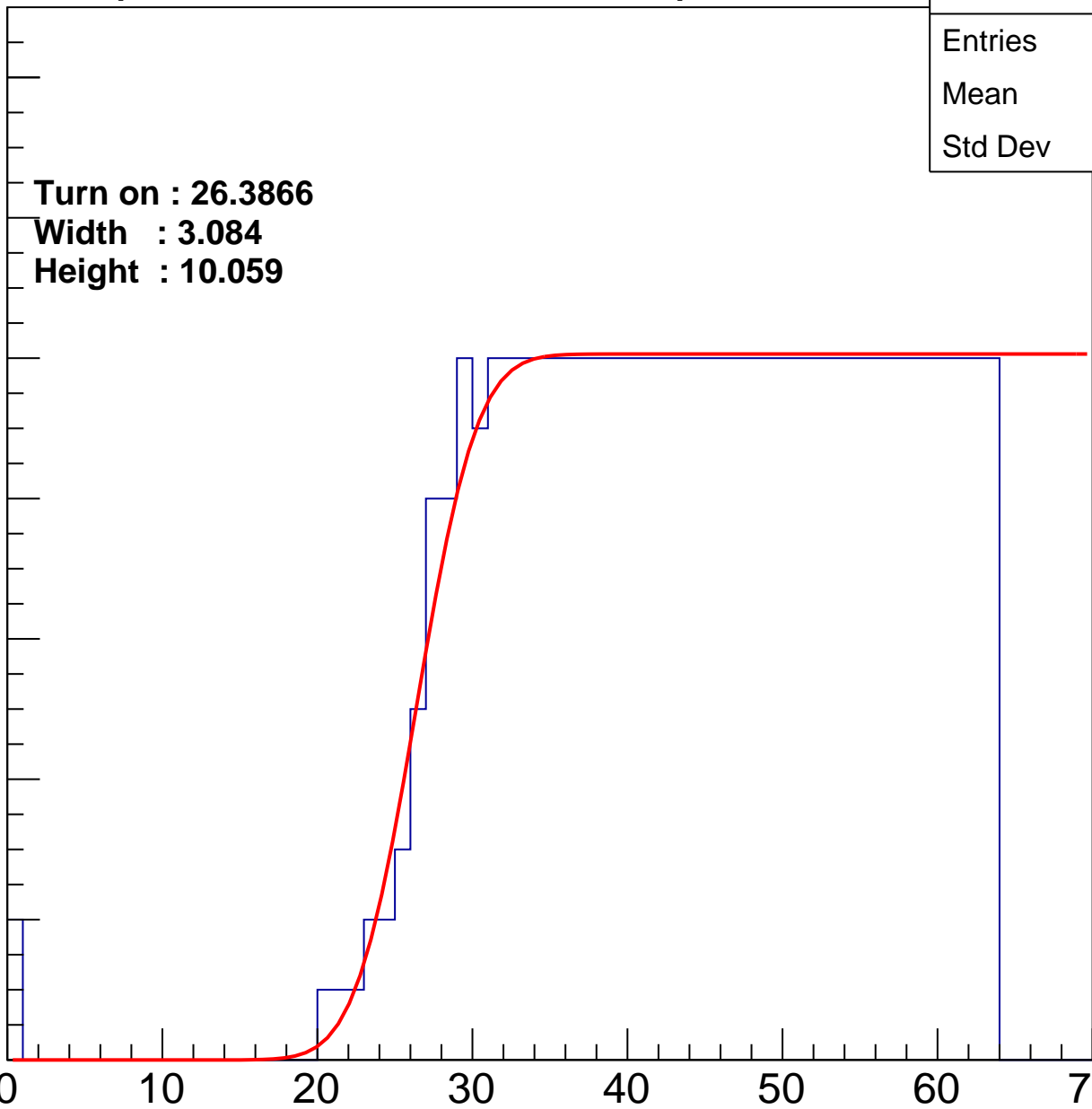
Entry

14
12
10
8
6
4
2
0

Turn on : 26.3866
Width : 3.084
Height : 10.059

Entries	382
Mean	44.17
Std Dev	11.57

ampl



B1L002S, U1-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.34
Std Dev	12.15

Turn on : 24.7940

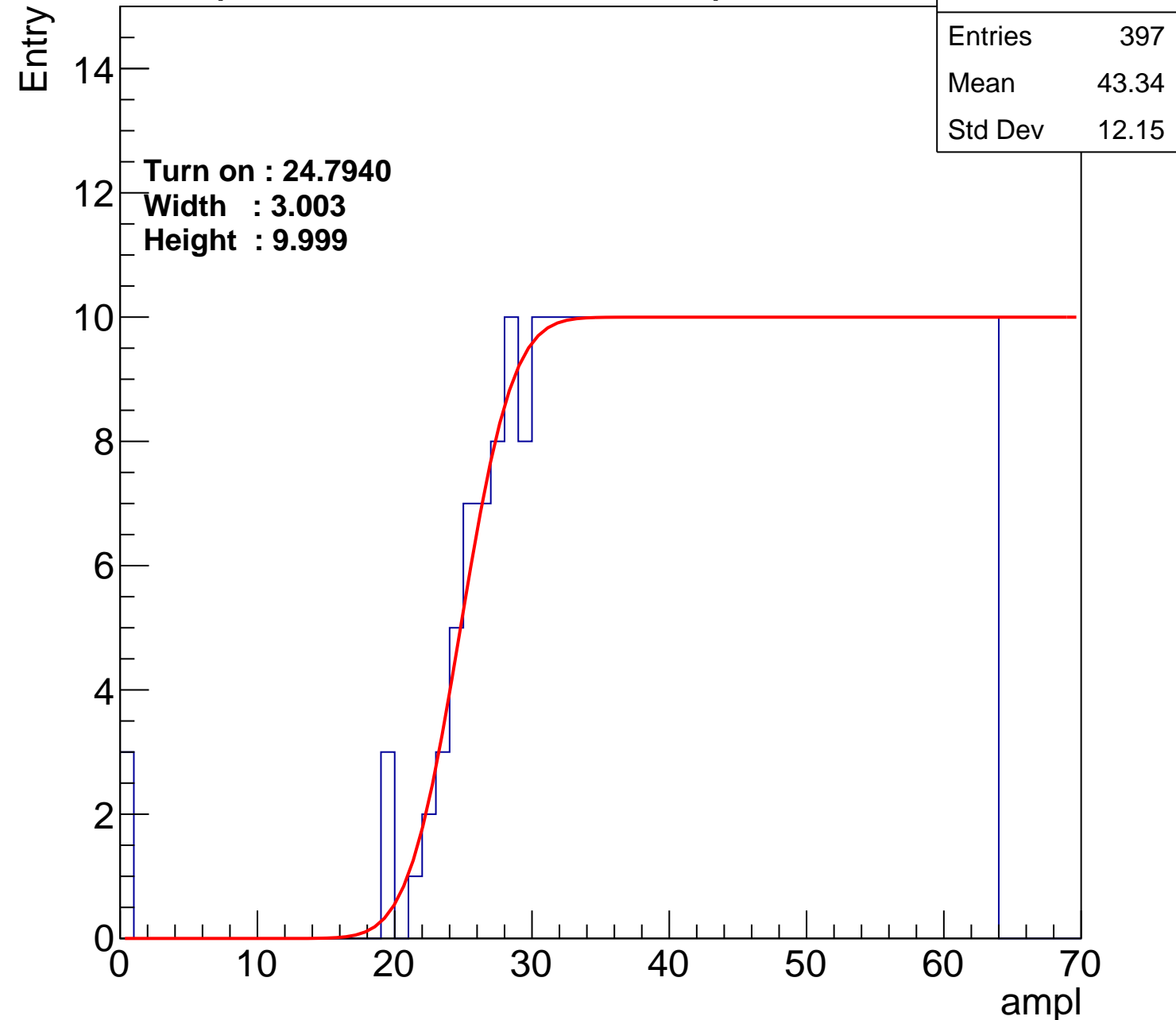
Width : 3.003

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch9

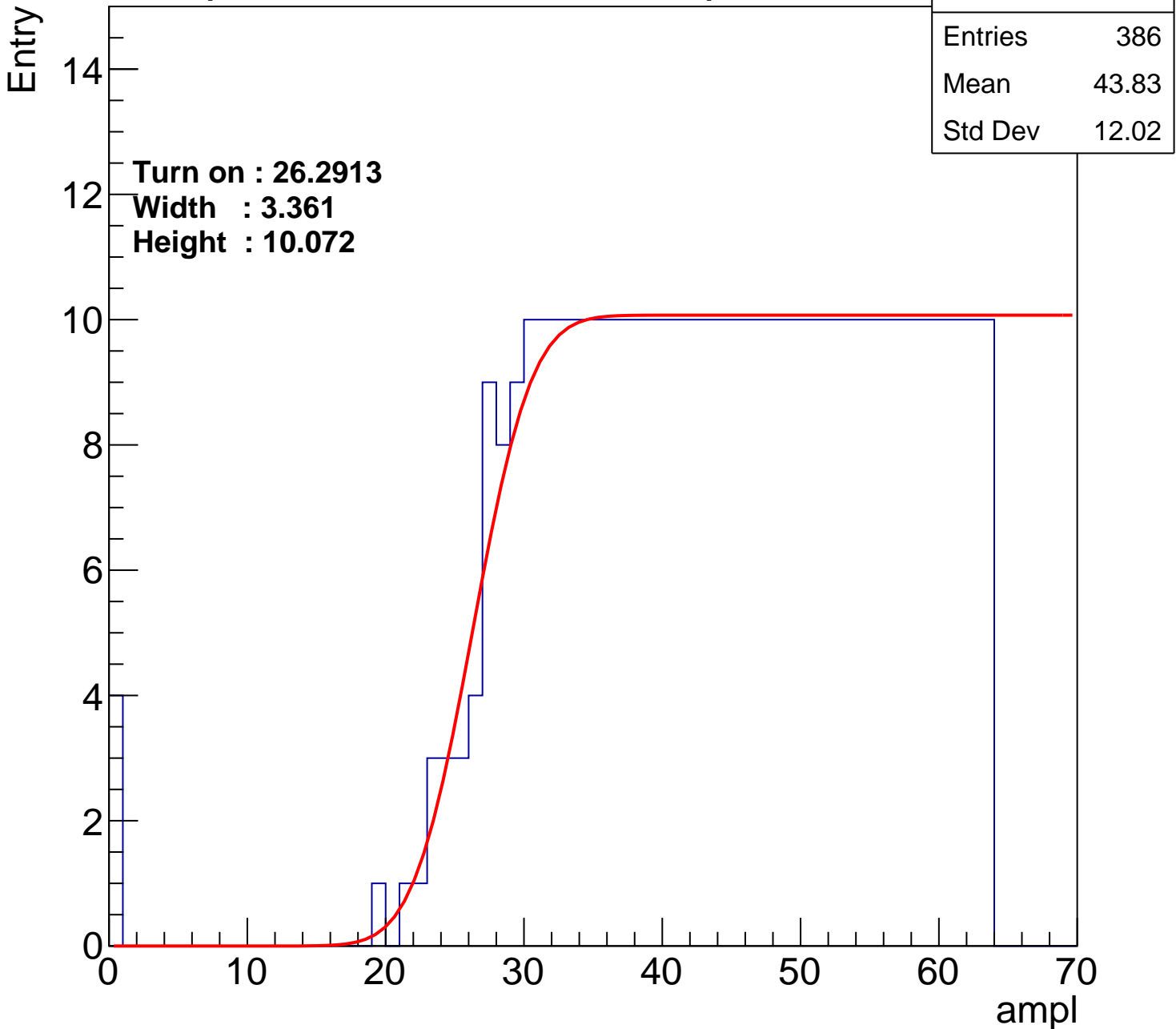
calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.83
Std Dev	12.02

Turn on : 26.2913

Width : 3.361

Height : 10.072



B1L002S, U1-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.17
Std Dev	12.31

Turn on : 25.0958

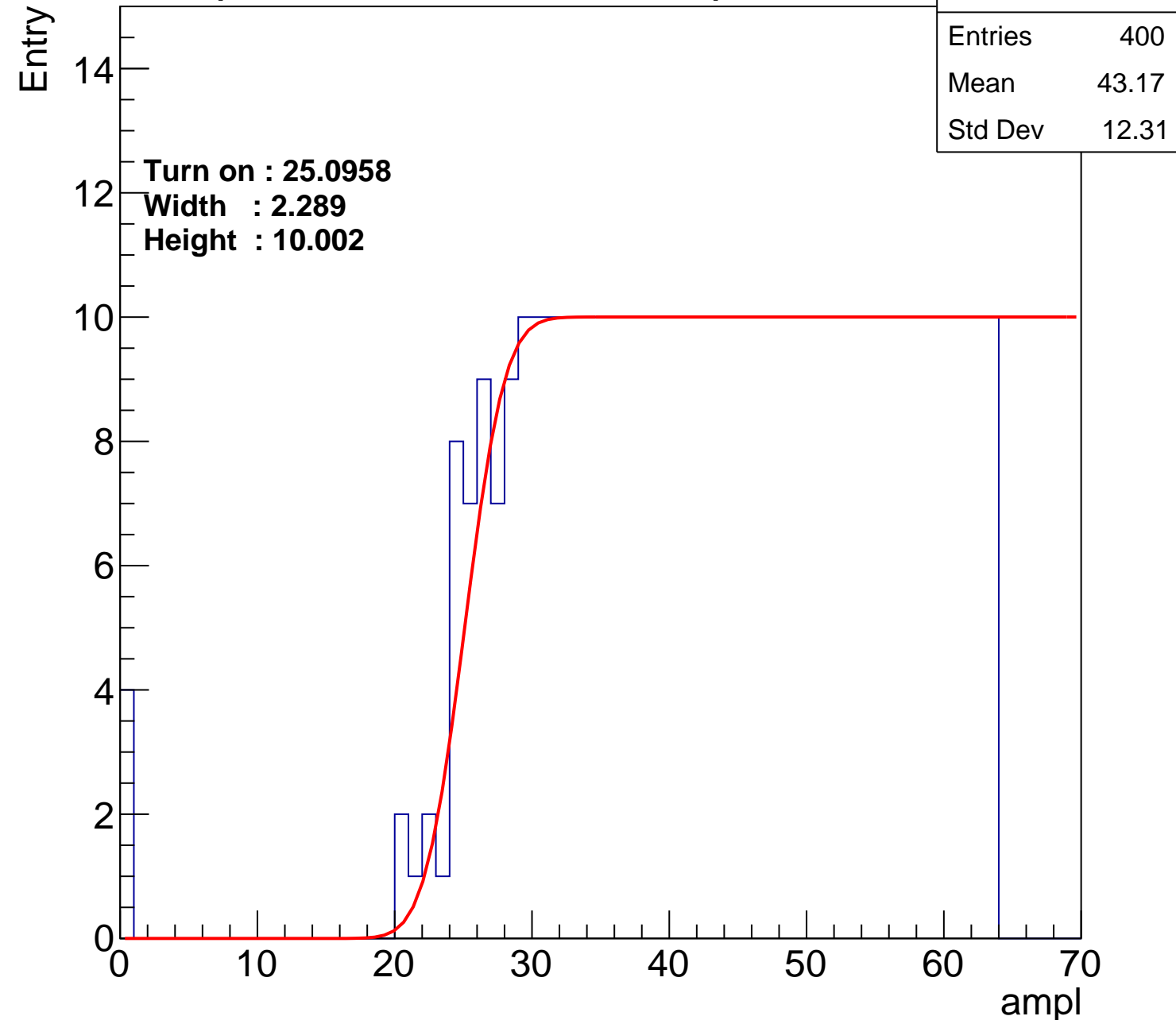
Width : 2.289

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	361
Mean	45.2
Std Dev	11.02

Turn on : 28.4897

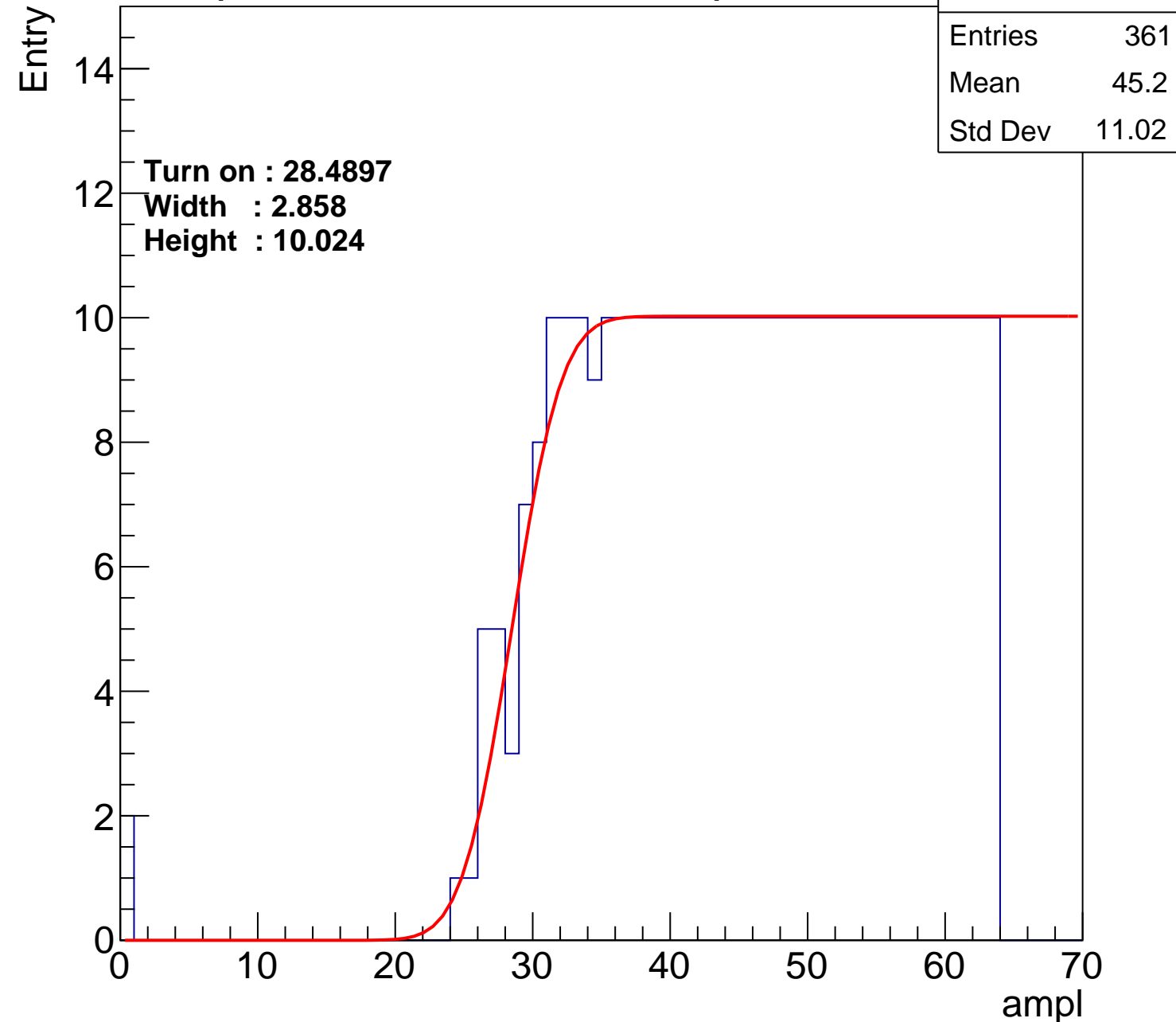
Width : 2.858

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch12

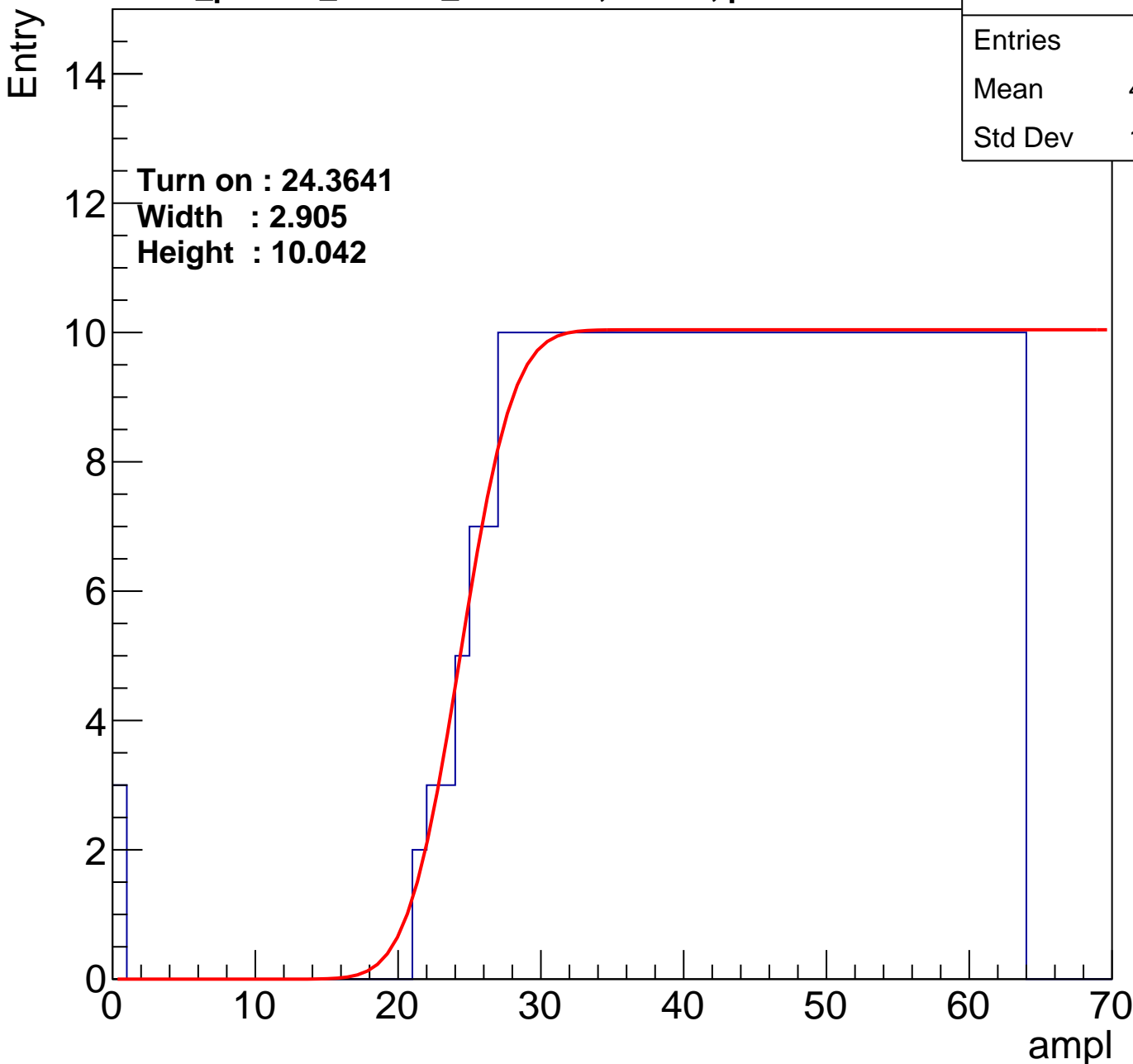
calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.26
Std Dev	12.12

Turn on : 24.3641

Width : 2.905

Height : 10.042



B1L002S, U1-ch13

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.28
Std Dev	11.48

Turn on : 25.9864

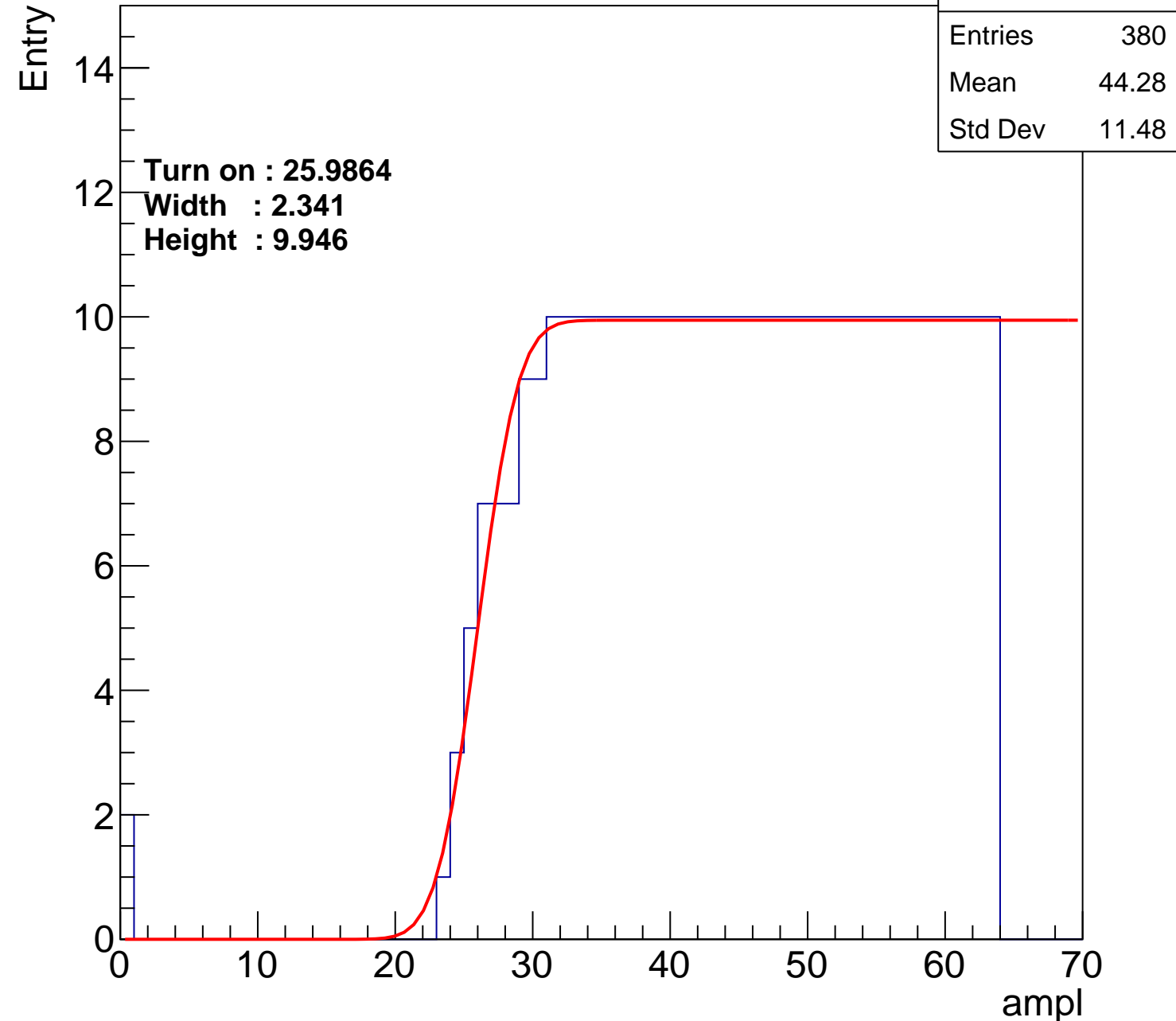
Width : 2.341

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 26.8120

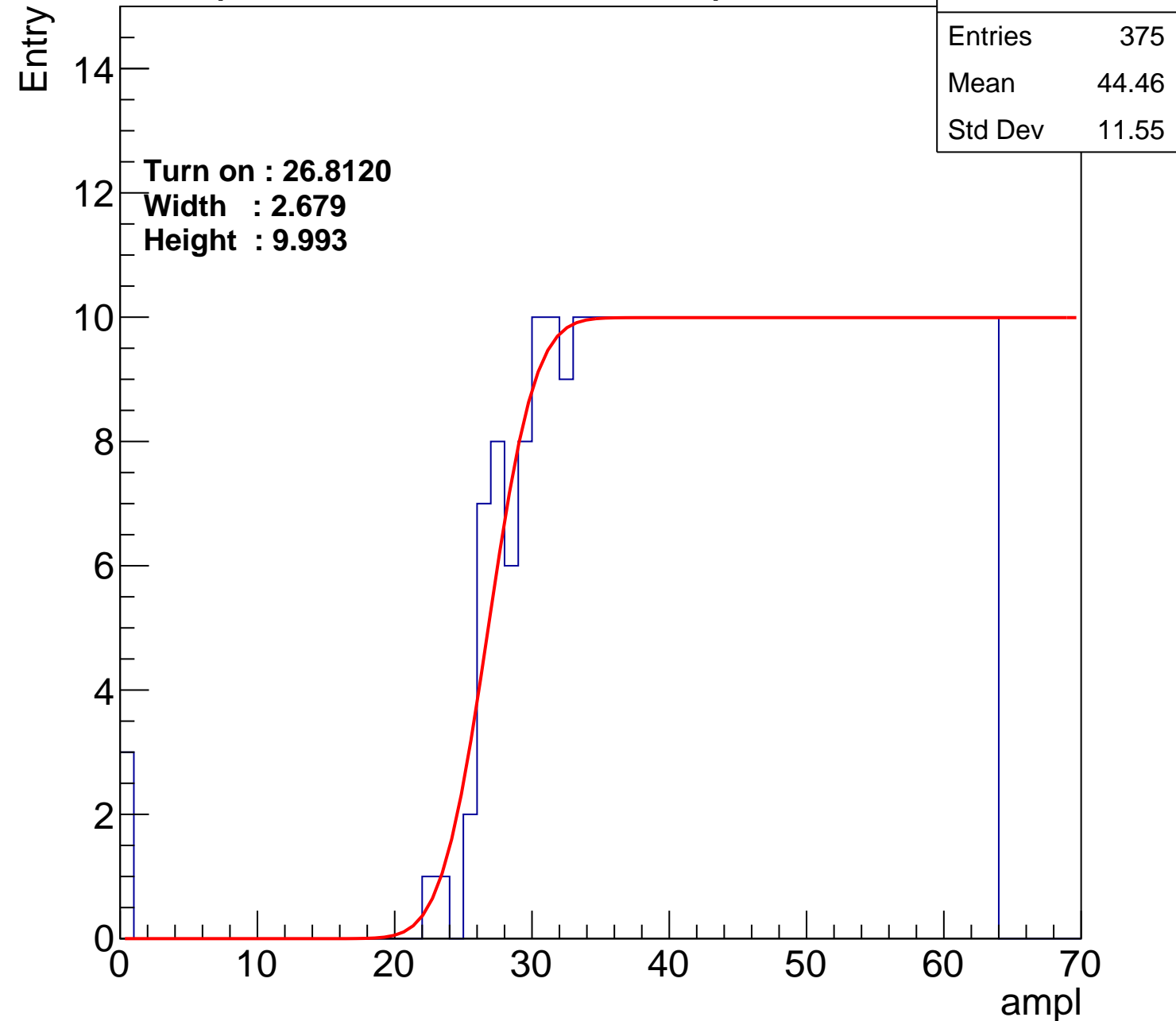
Width : 2.679

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	42.81
Std Dev	12.83

Turn on : 24.5508

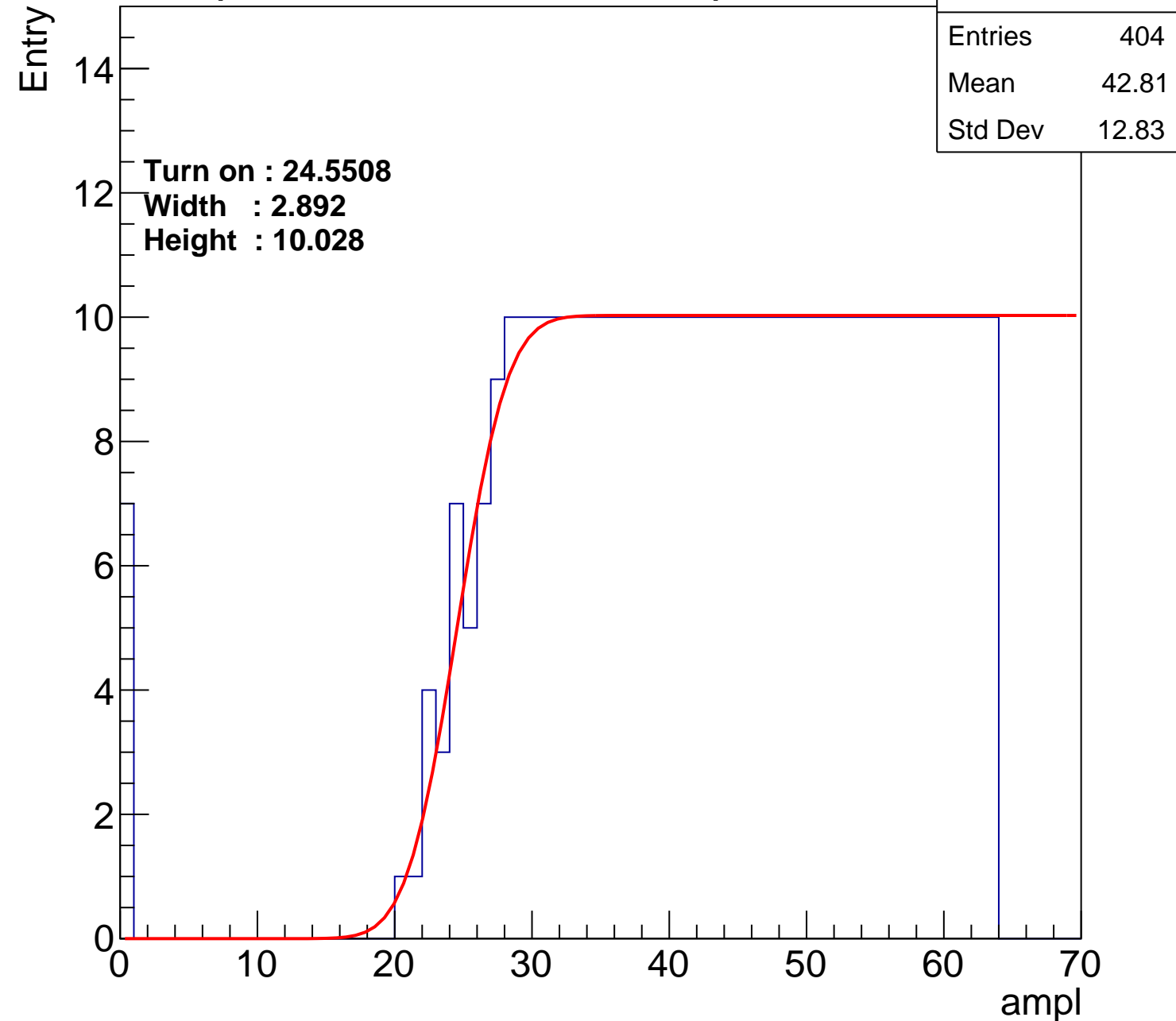
Width : 2.892

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.69
Std Dev	11.87

Turn on : 25.2800

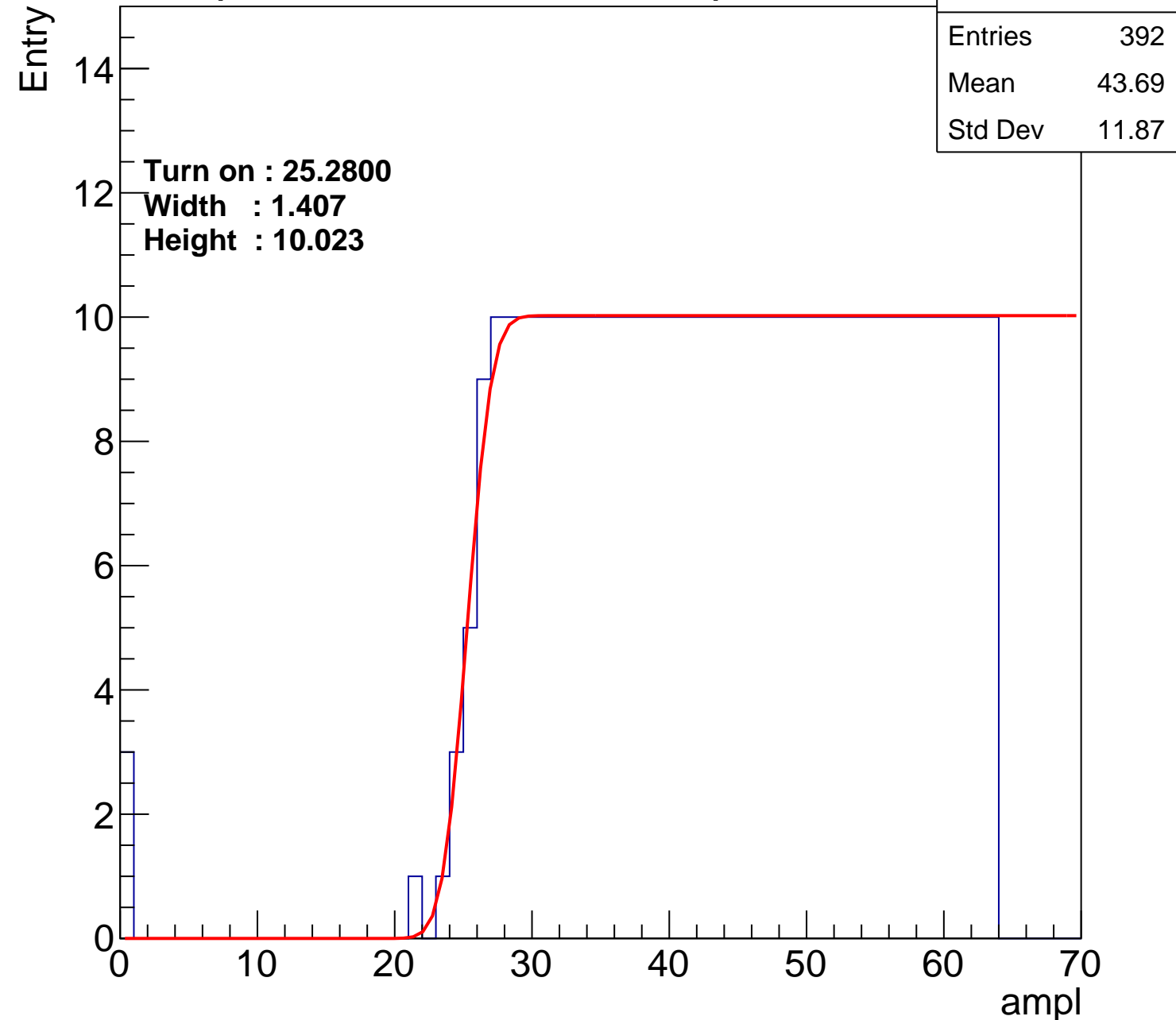
Width : 1.407

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch17

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.67
Std Dev	11.84

Turn on : 25.2777

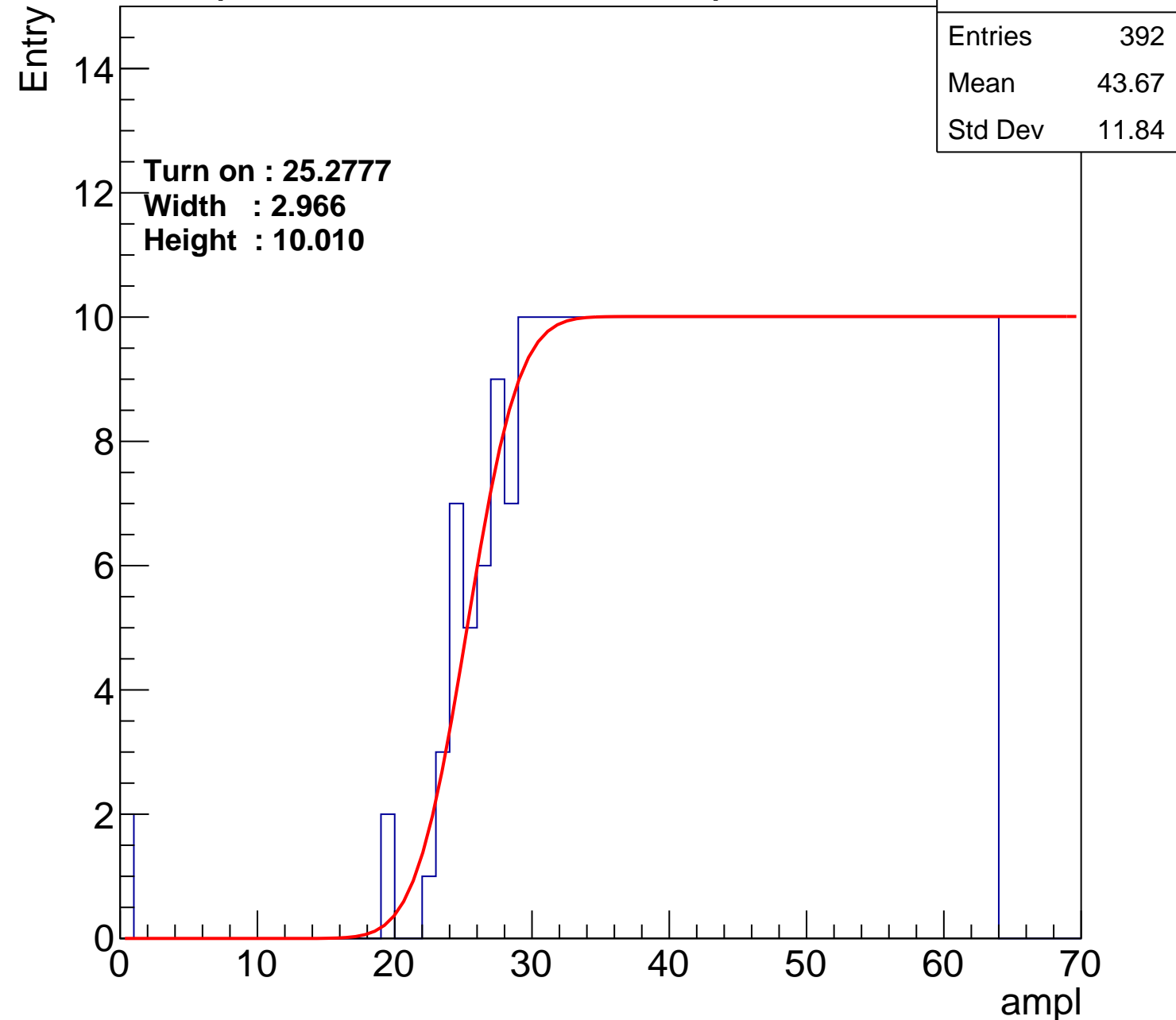
Width : 2.966

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.68
Std Dev	11.63

Turn on : 24.8514

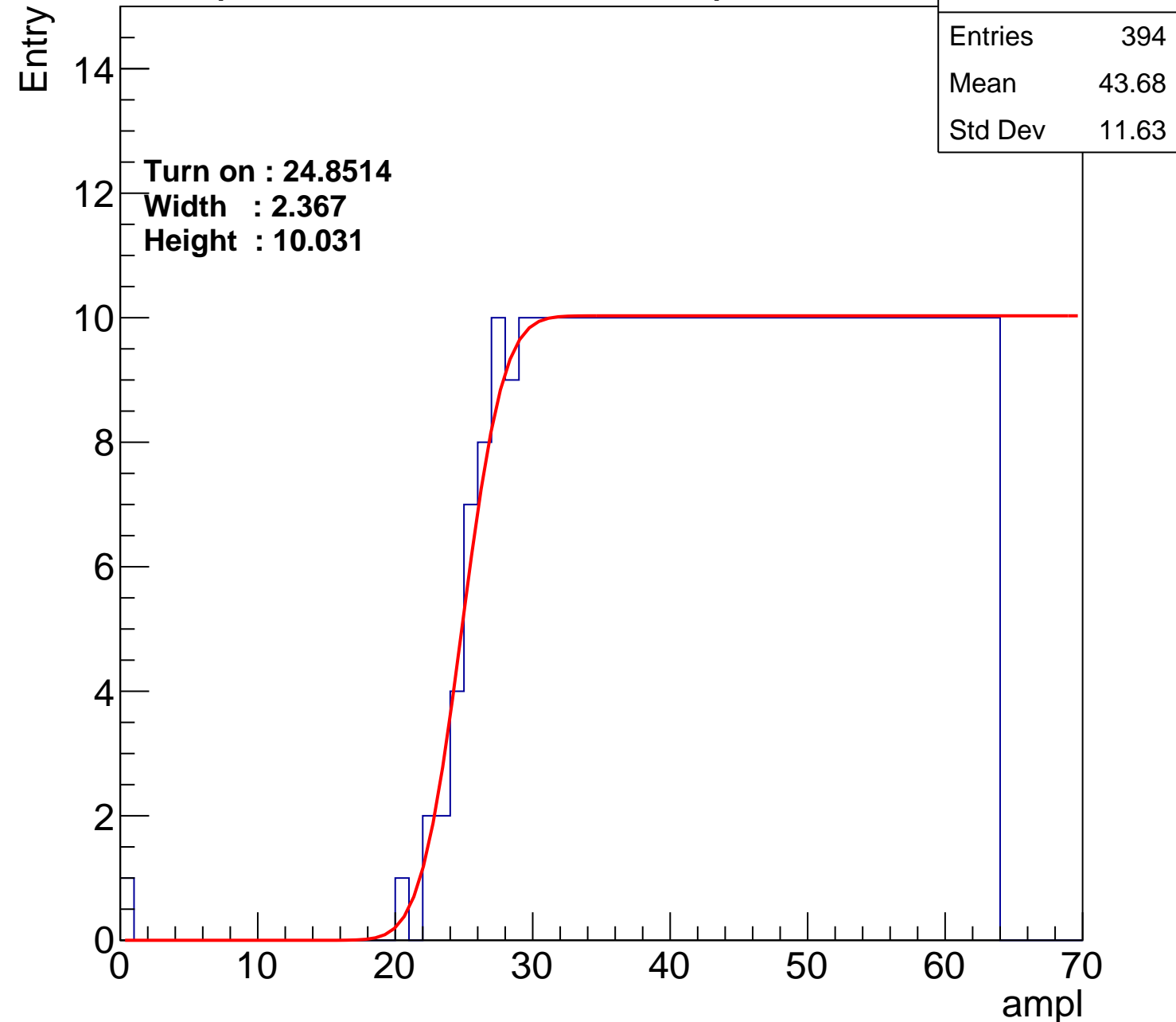
Width : 2.367

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch19

calib_packv5_042523_0143.root, FC#10, port B3

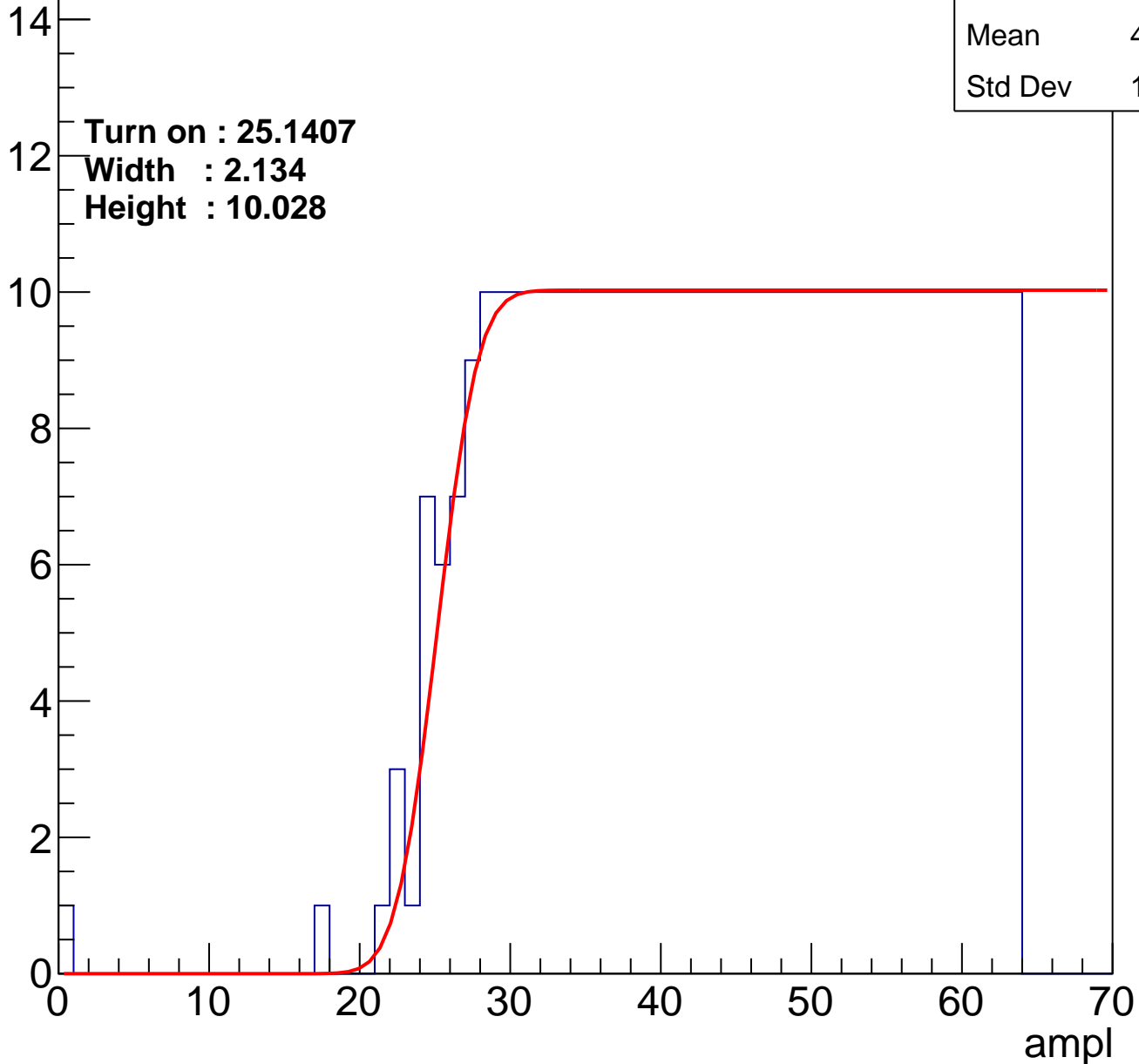
Entries	396
Mean	43.56
Std Dev	11.73

Turn on : 25.1407

Width : 2.134

Height : 10.028

Entry



B1L002S, U1-ch20

calib_packv5_042523_0143.root, FC#10, port B3

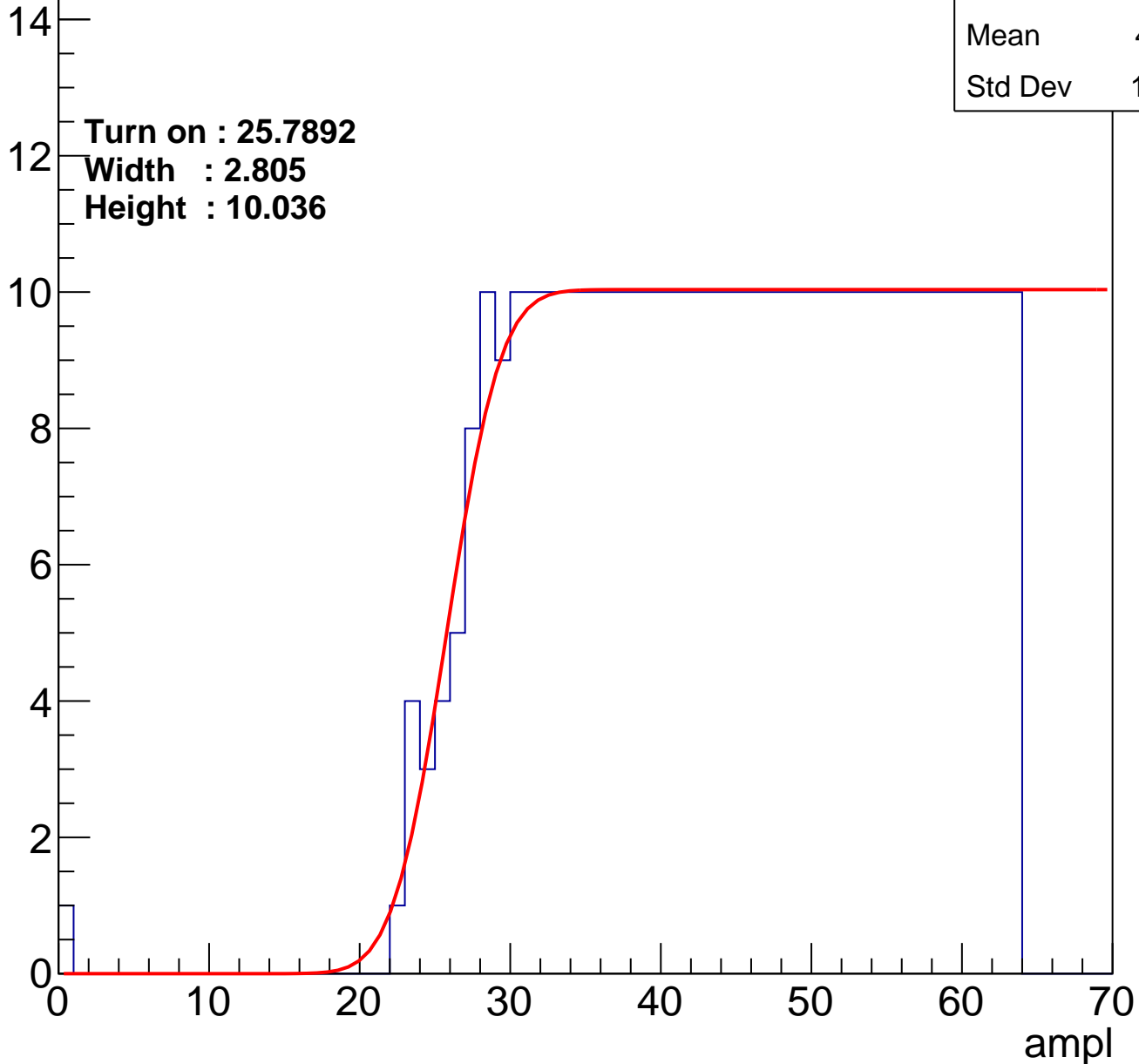
Entries	385
Mean	44.11
Std Dev	11.42

Turn on : 25.7892

Width : 2.805

Height : 10.036

Entry



B1L002S, U1-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.98
Std Dev	11.47

Turn on : 24.9719

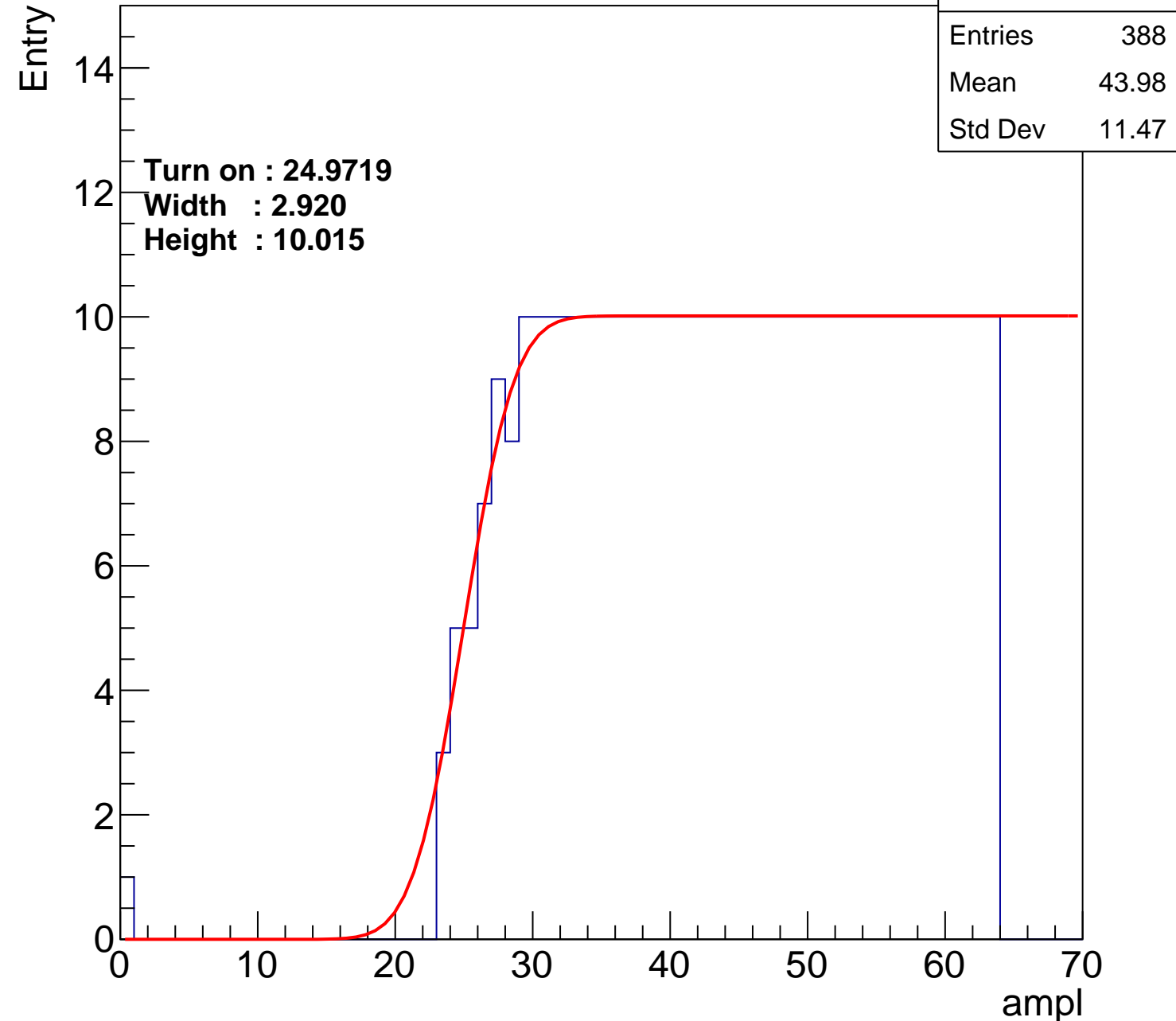
Width : 2.920

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	408
Mean	42.76
Std Dev	12.59

Turn on : 23.4916

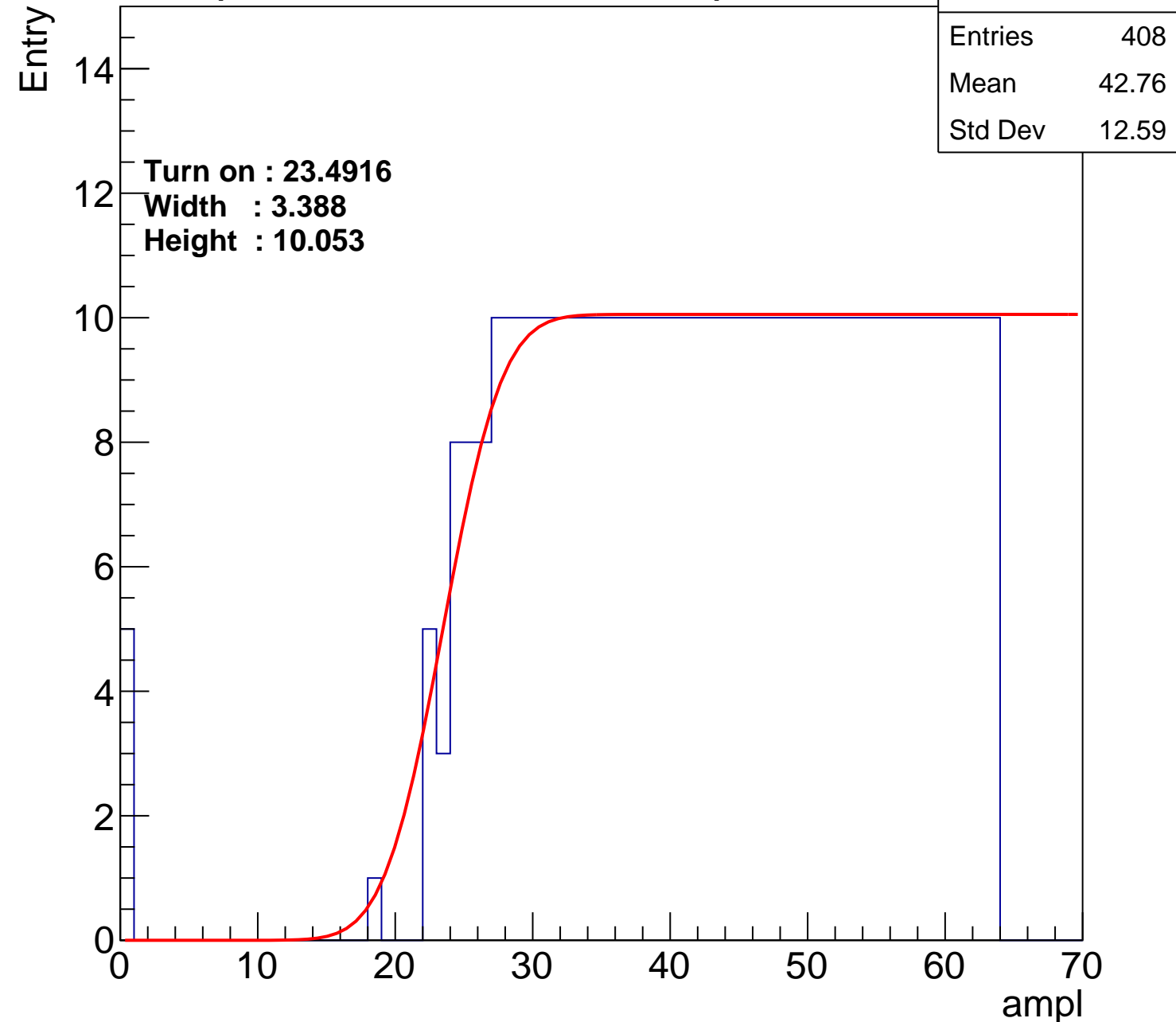
Width : 3.388

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch23

calib_packv5_042523_0143.root, FC#10, port B3

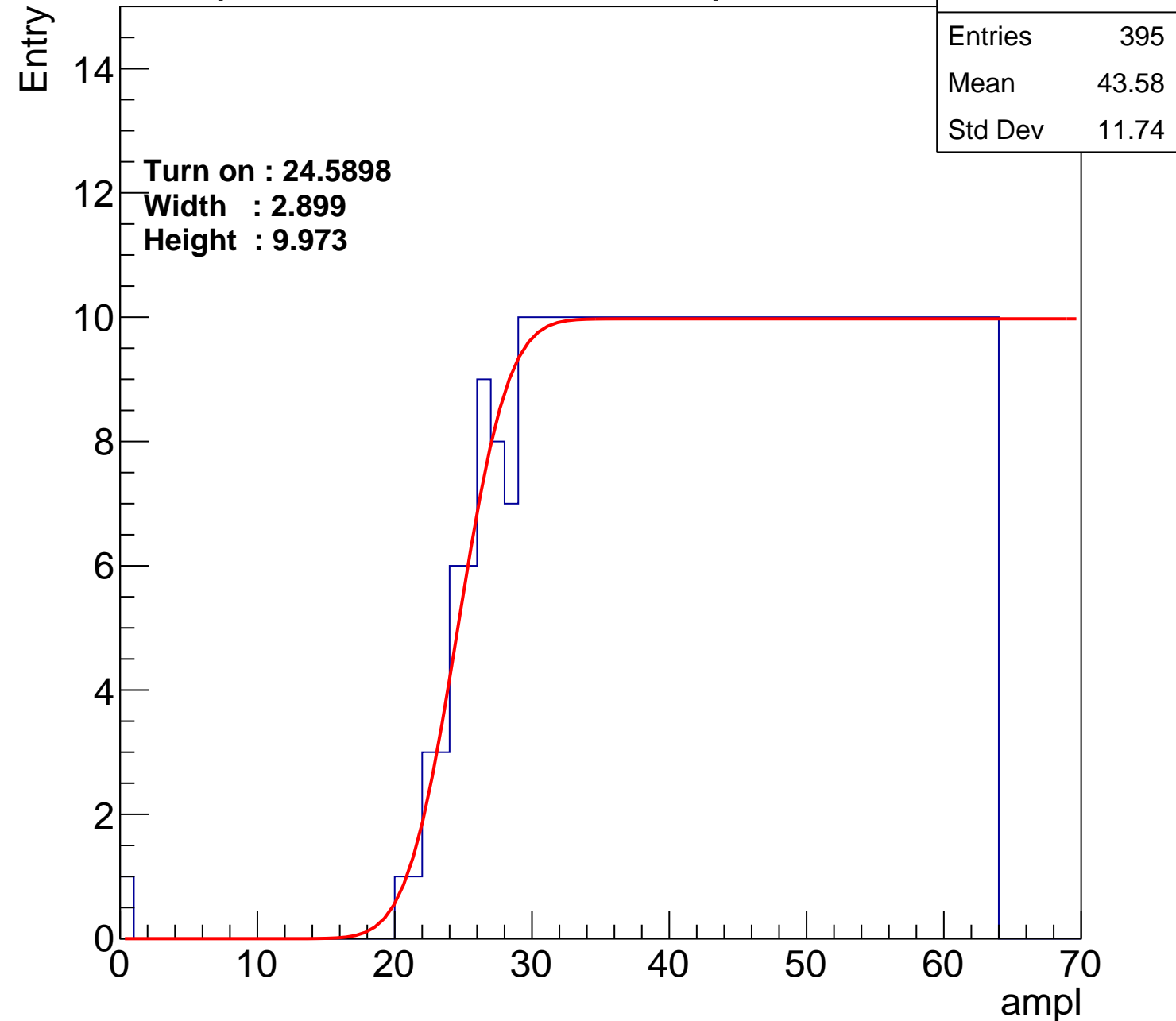
Entry

14
12
10
8
6
4
2
0

Turn on : 24.5898
Width : 2.899
Height : 9.973

Entries	395
Mean	43.58
Std Dev	11.74

ampl



B1L002S, U1-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.83
Std Dev	11.98

Turn on : 26.3467

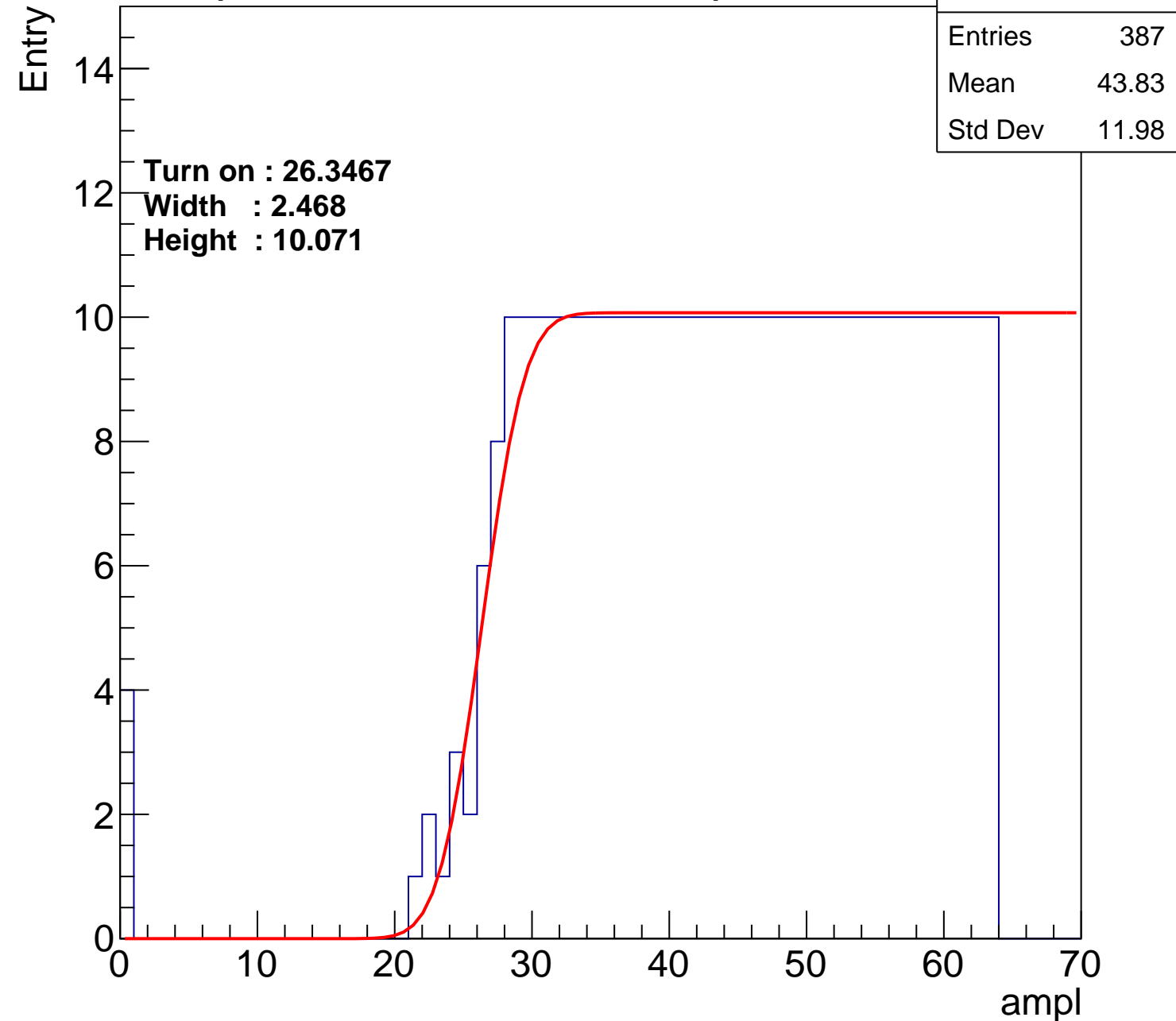
Width : 2.468

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.54
Std Dev	11.87

Turn on : 24.5335

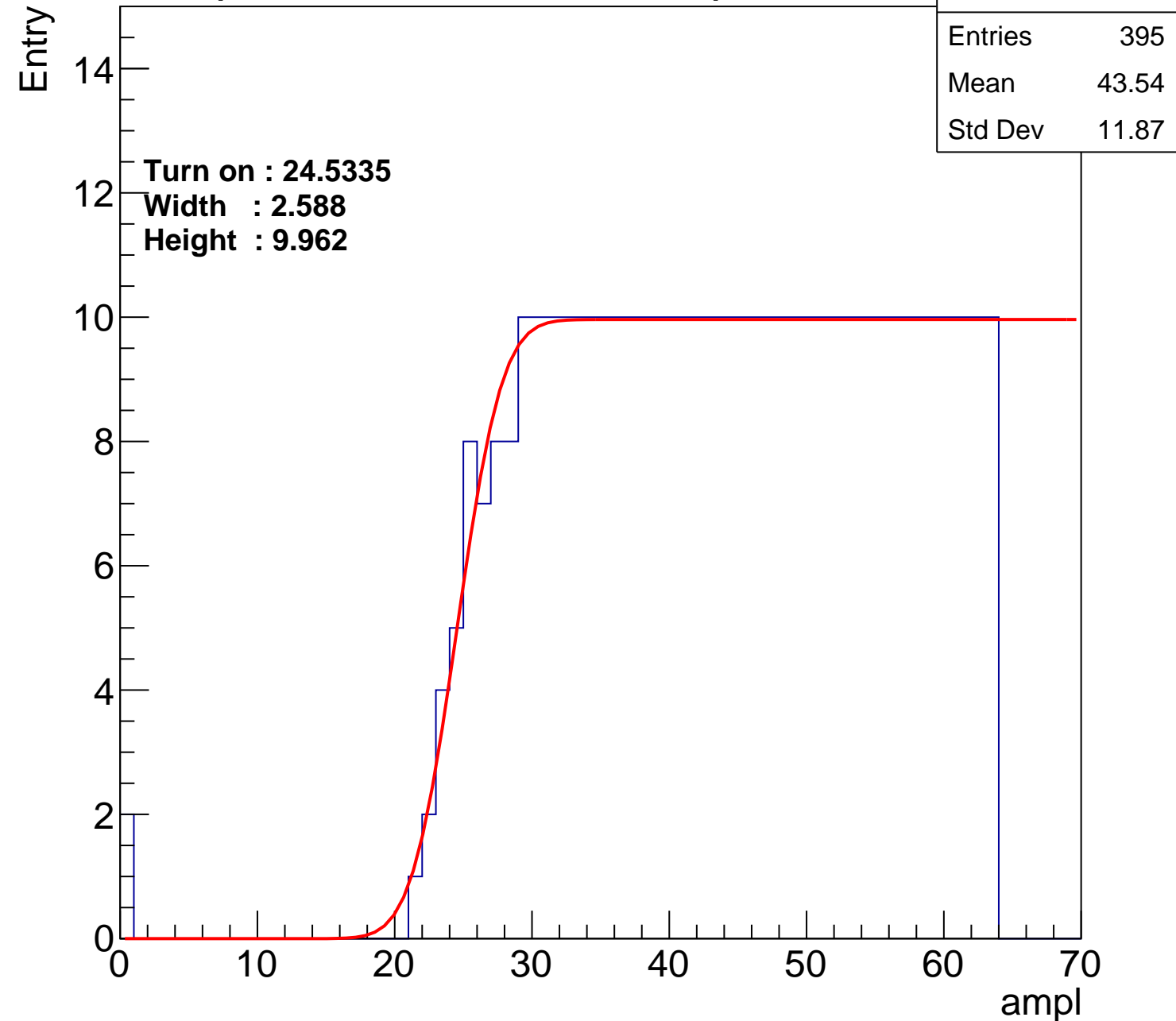
Width : 2.588

Height : 9.962

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.68
Std Dev	11.69

Turn on : 25.3193

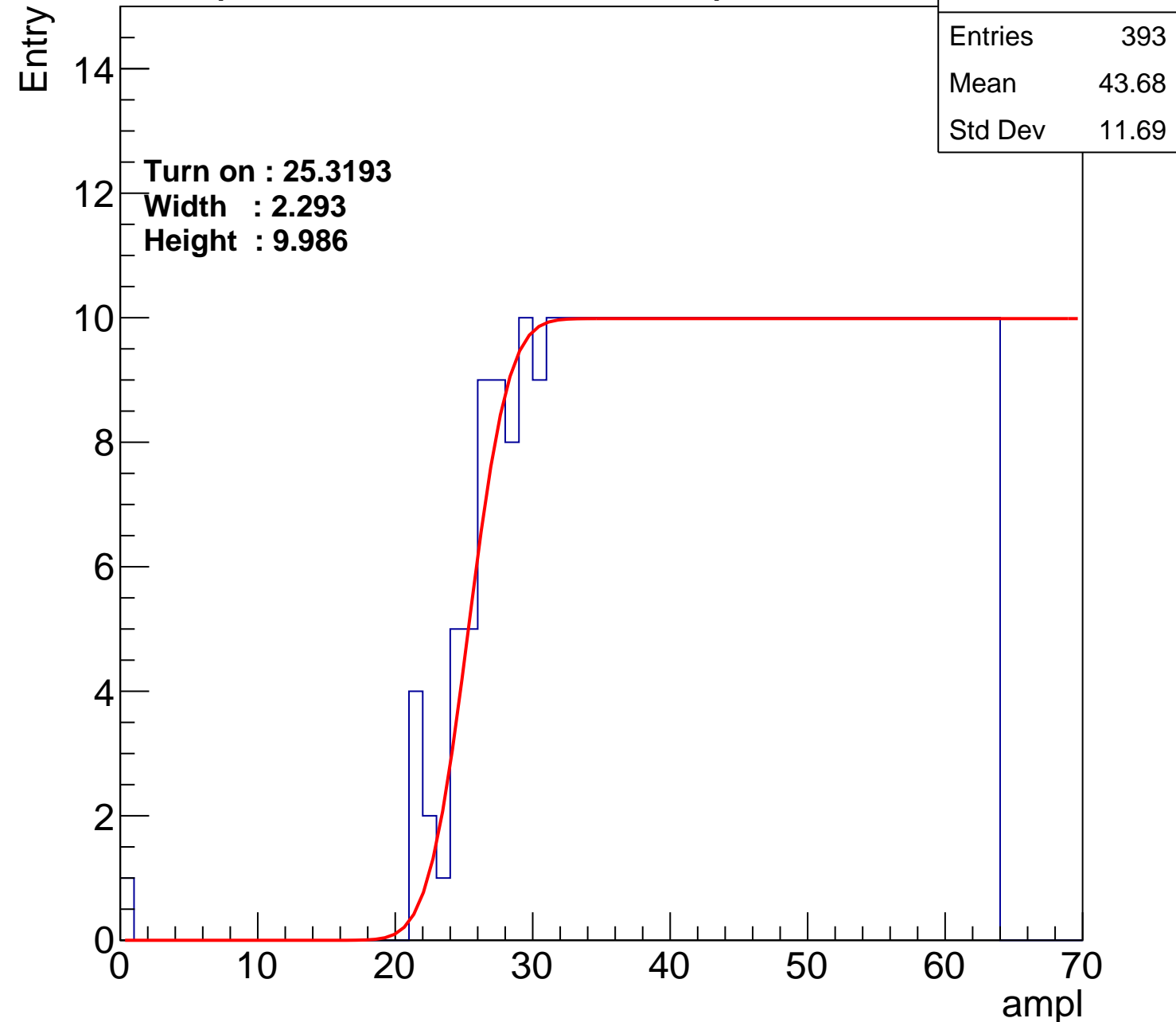
Width : 2.293

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.83
Std Dev	11.68

Turn on : 25.1112

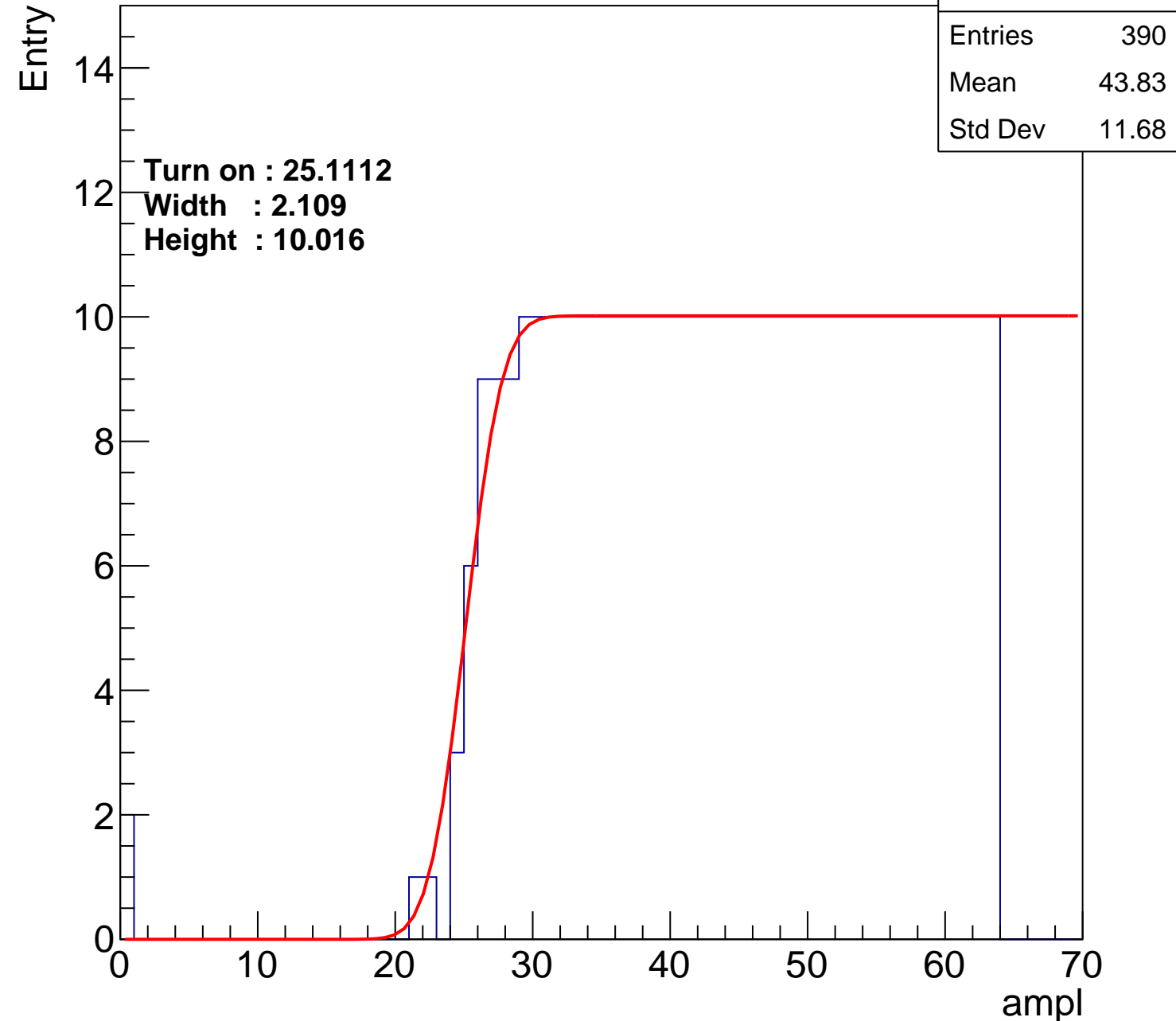
Width : 2.109

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch28

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.03
Std Dev	11.92

Turn on : 26.3316

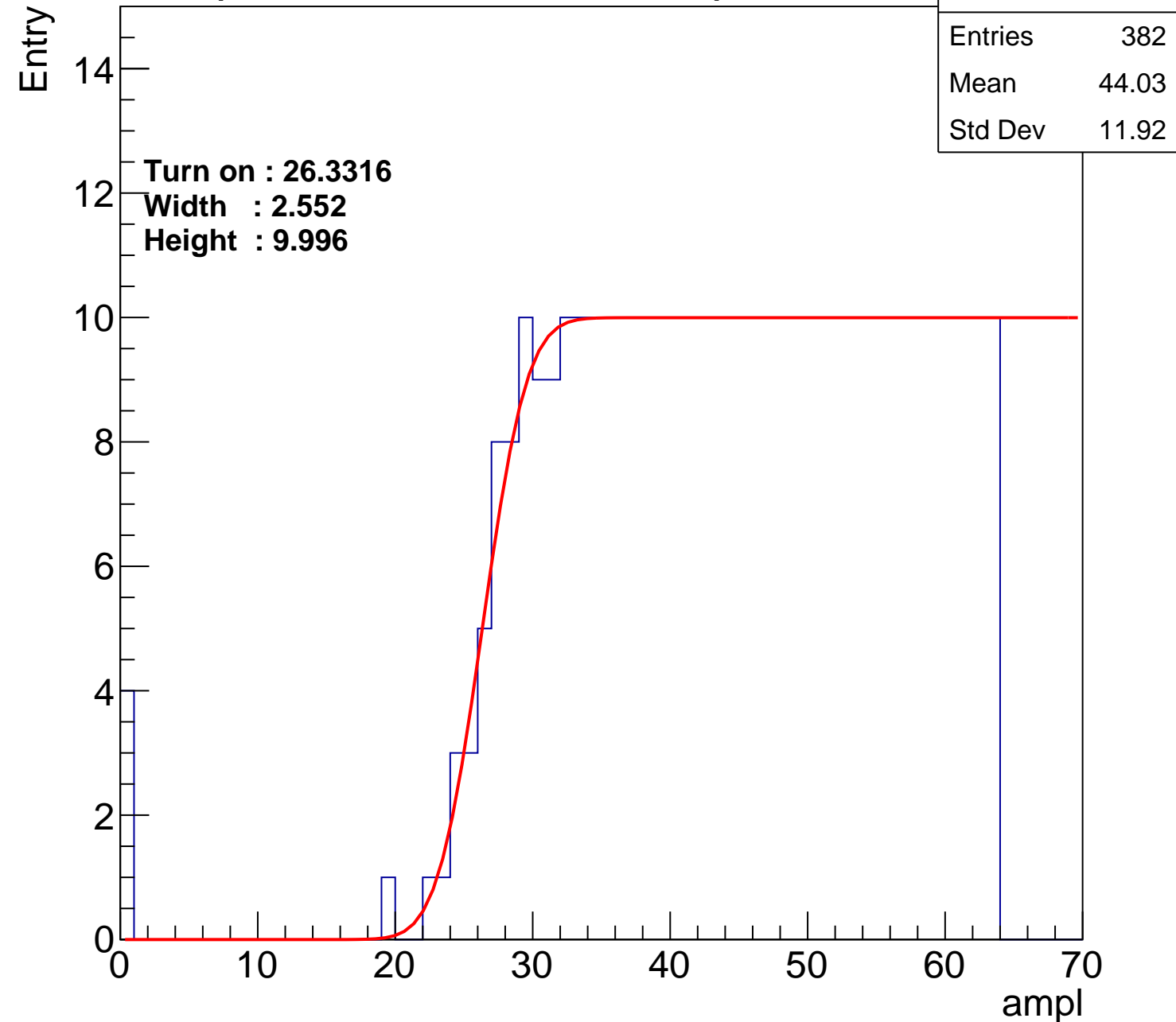
Width : 2.552

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.75
Std Dev	12.05

Turn on : 26.1003

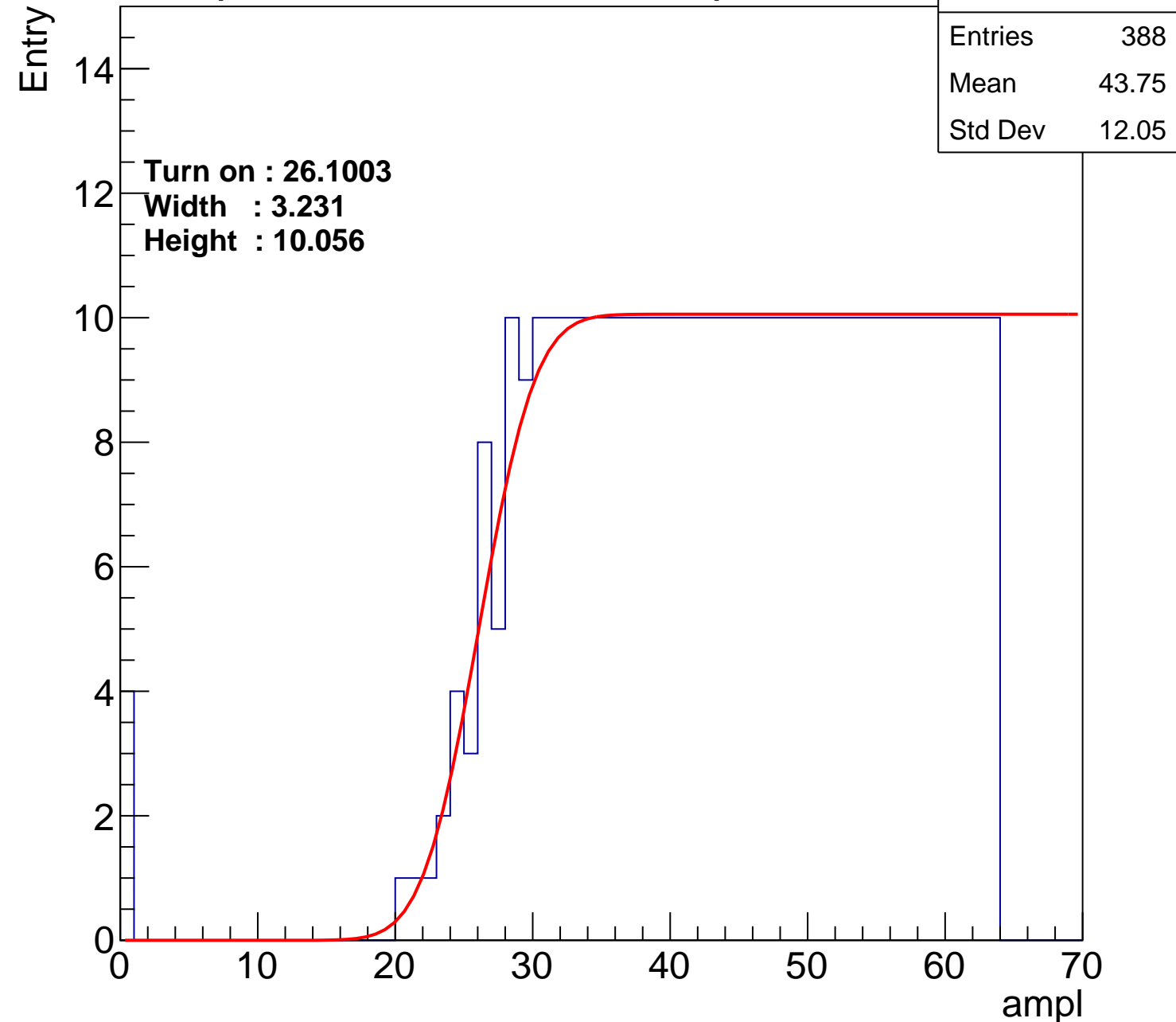
Width : 3.231

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch30

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.86
Std Dev	11.97

Turn on : 26.0735

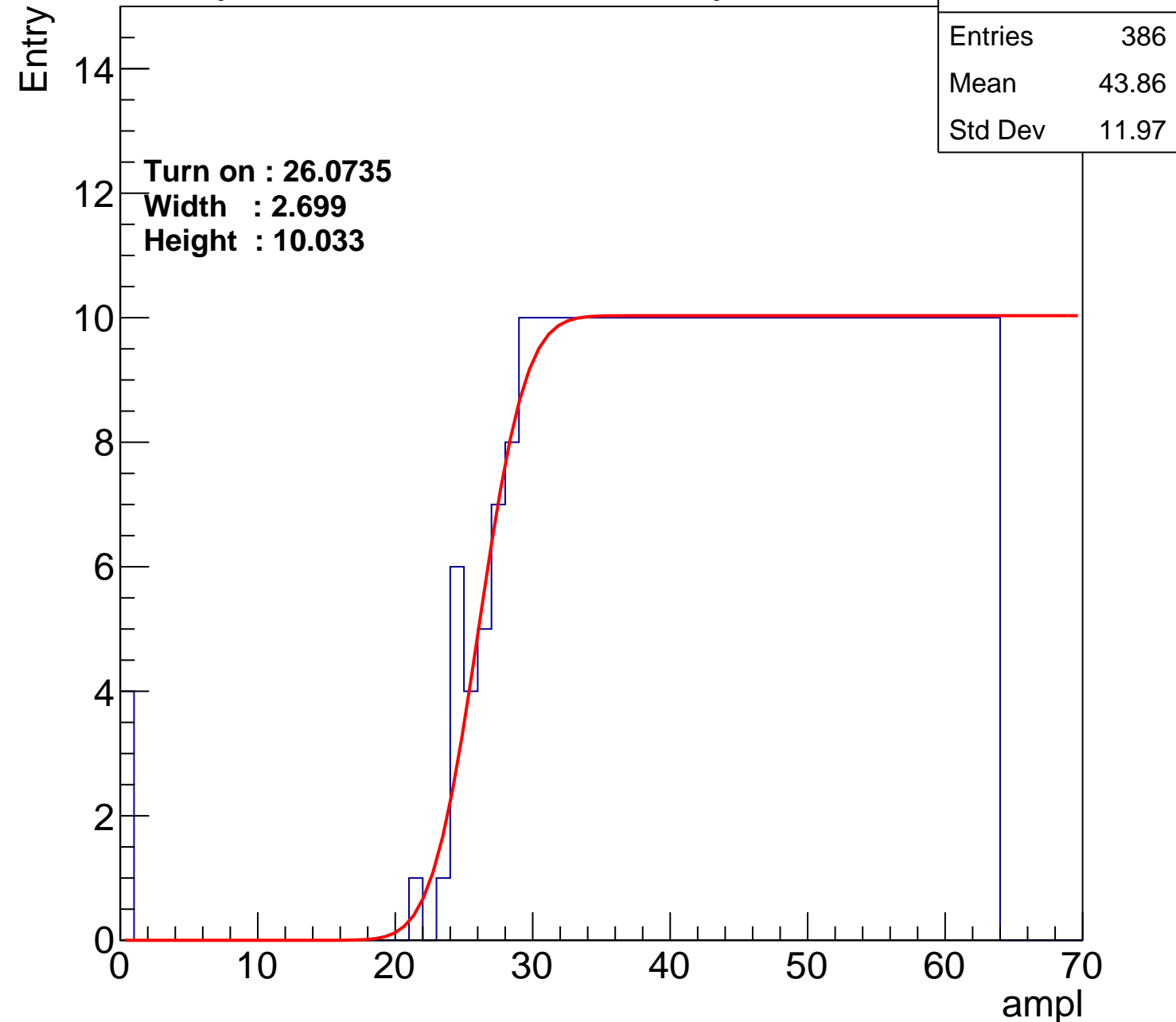
Width : 2.699

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.37
Std Dev	11.94

Turn on : 24.2623

Width : 0.641

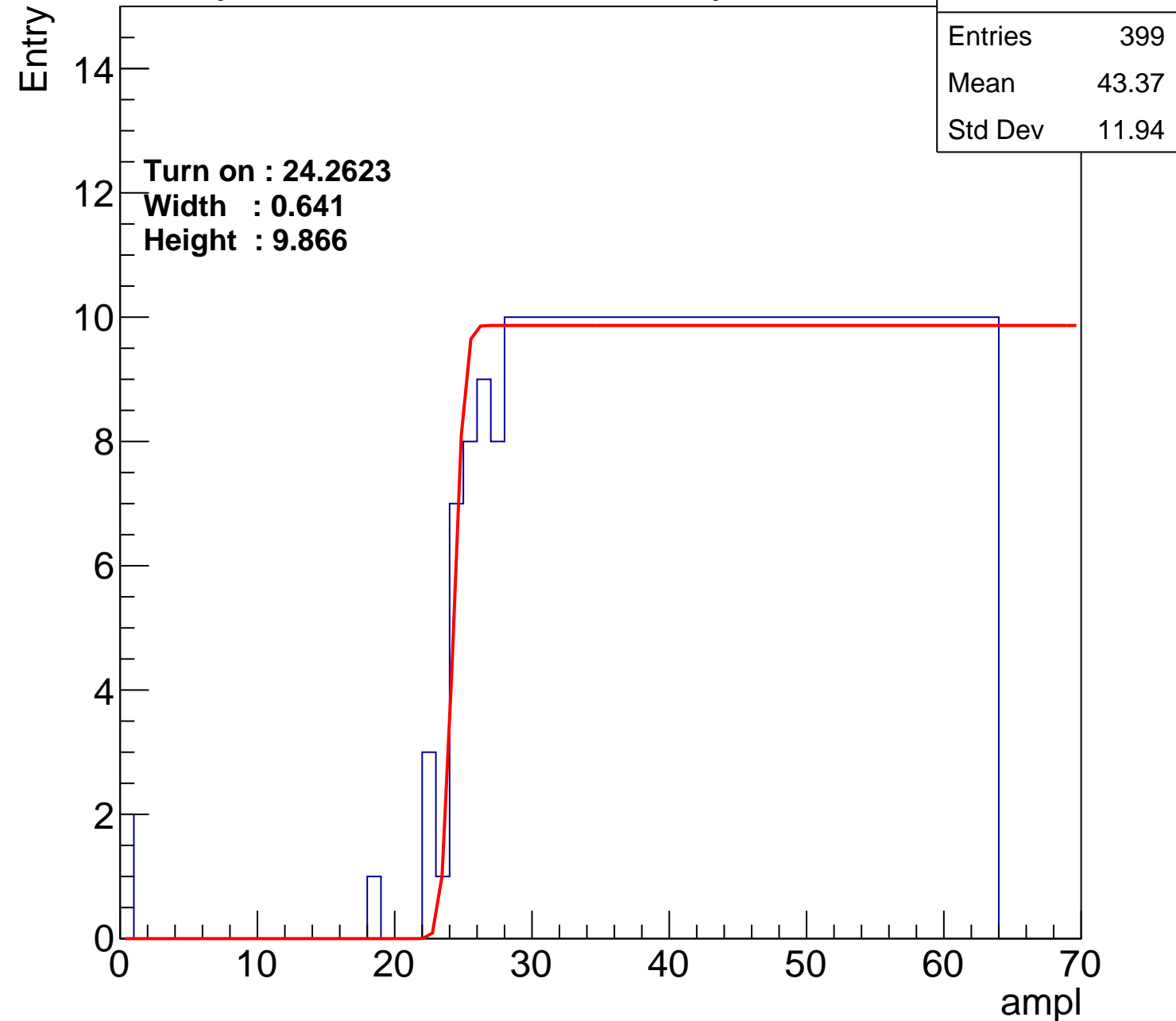
Height : 9.866

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch32

calib_packv5_042523_0143.root, FC#10, port B3

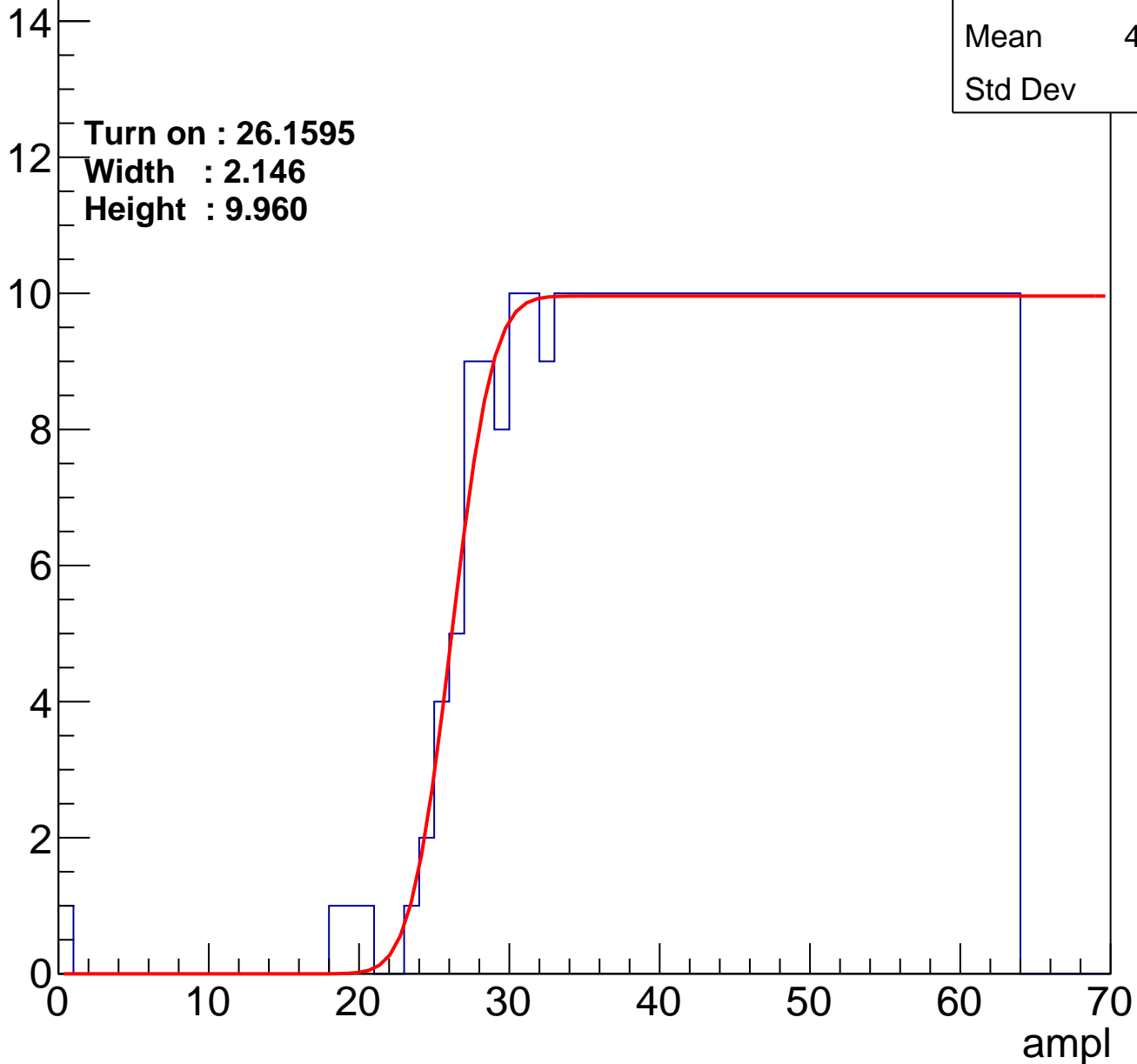
Entries	381
Mean	44.26
Std Dev	11.4

Turn on : 26.1595

Width : 2.146

Height : 9.960

Entry



B1L002S, U1-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.32
Std Dev	11.34

Turn on : 26.0012

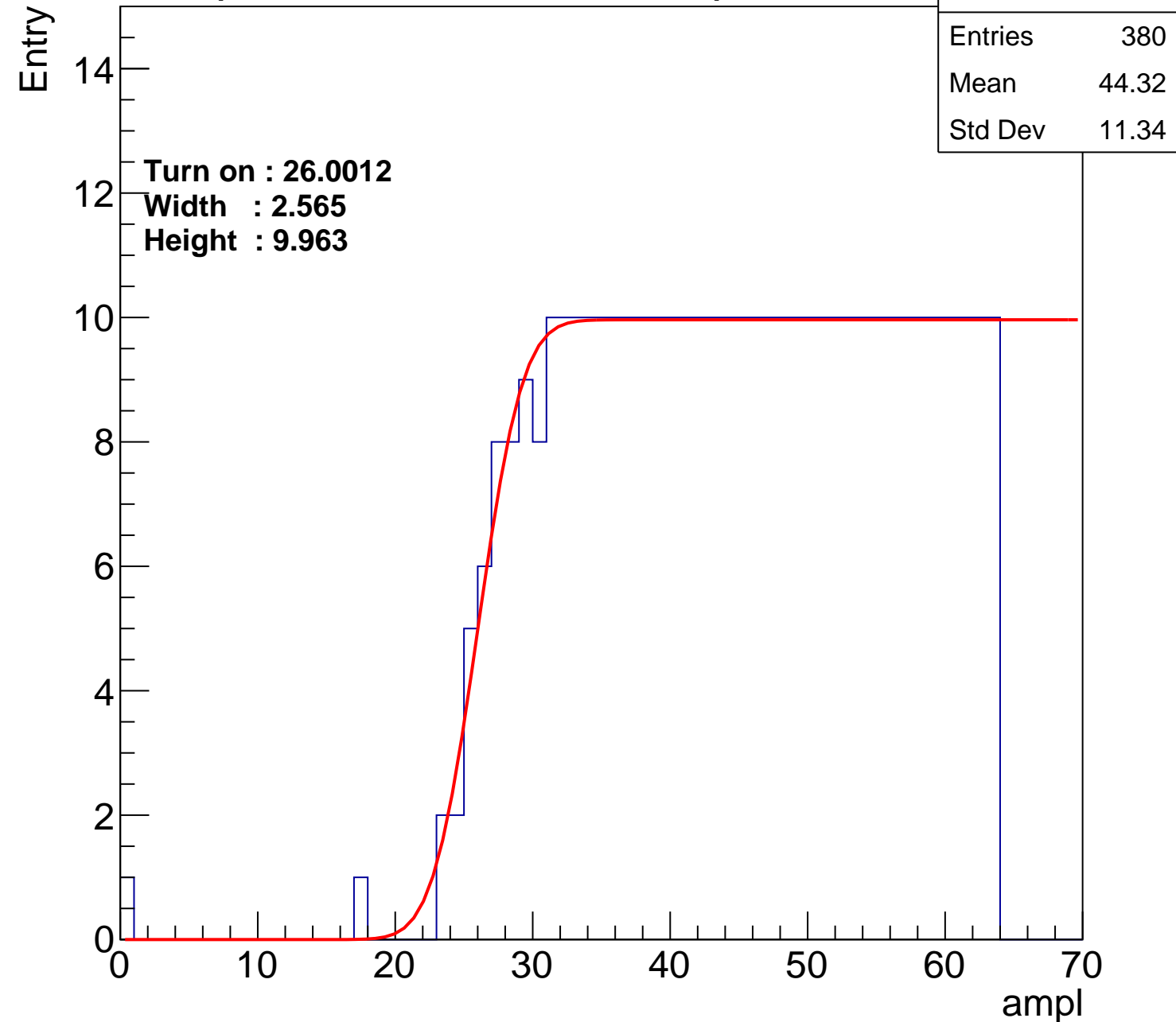
Width : 2.565

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.71
Std Dev	12.05

Turn on : 25.7796

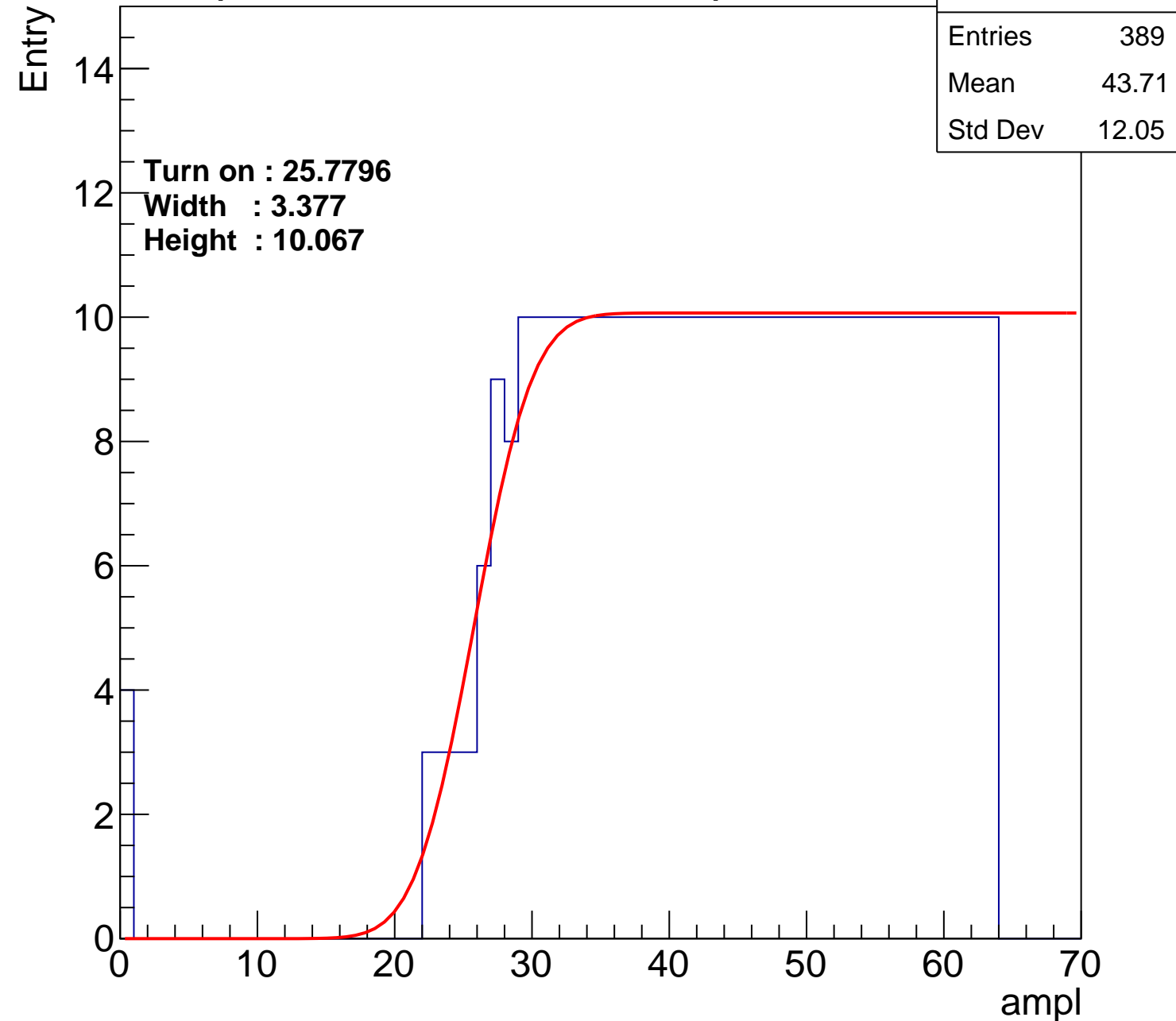
Width : 3.377

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.35
Std Dev	11.81

Turn on : 24.0649

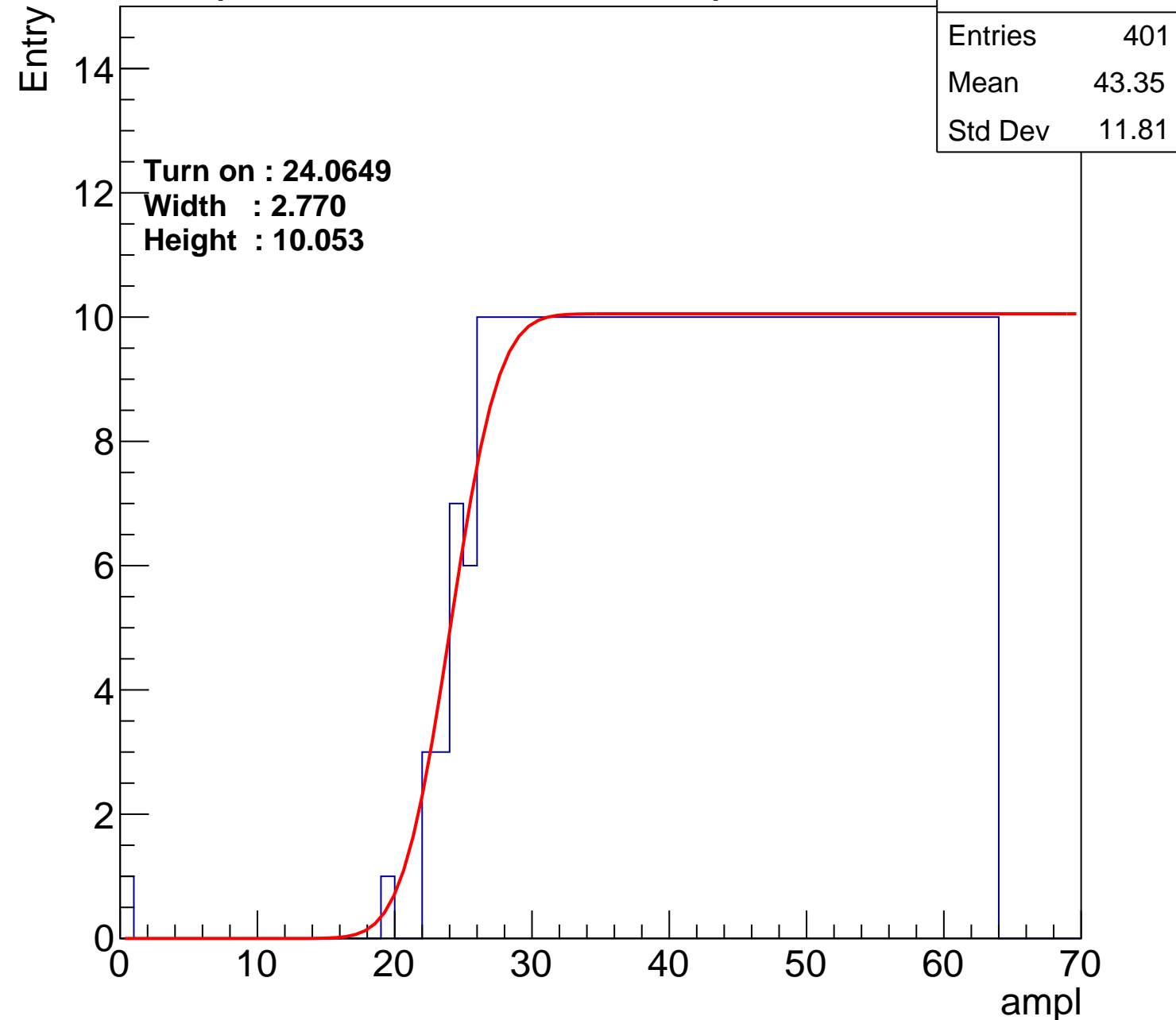
Width : 2.770

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch36

calib_packv5_042523_0143.root, FC#10, port B3

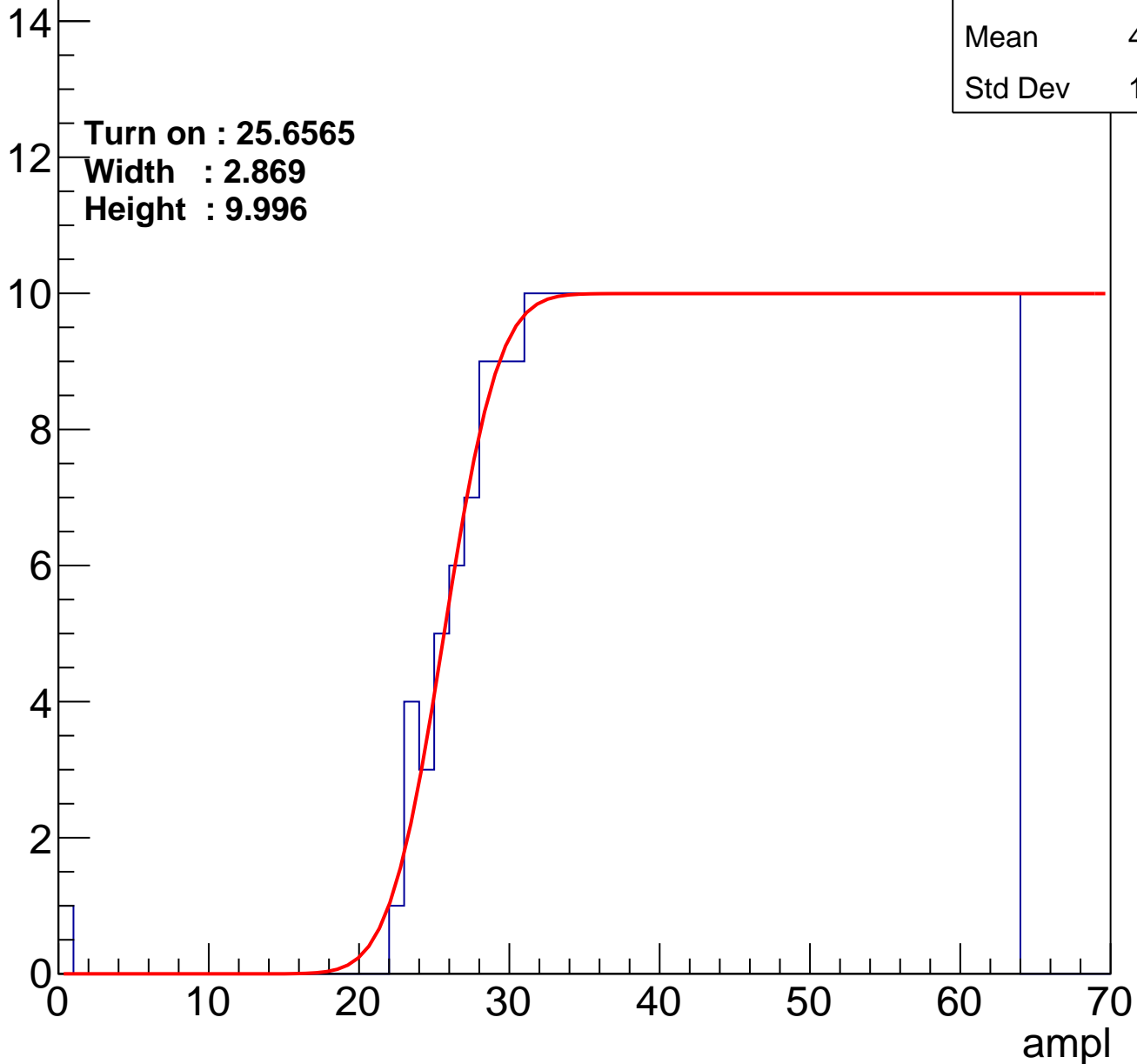
Entries	384
Mean	44.14
Std Dev	11.43

Turn on : 25.6565

Width : 2.869

Height : 9.996

Entry



B1L002S, U1-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.67
Std Dev	11.82

Turn on : 25.2006

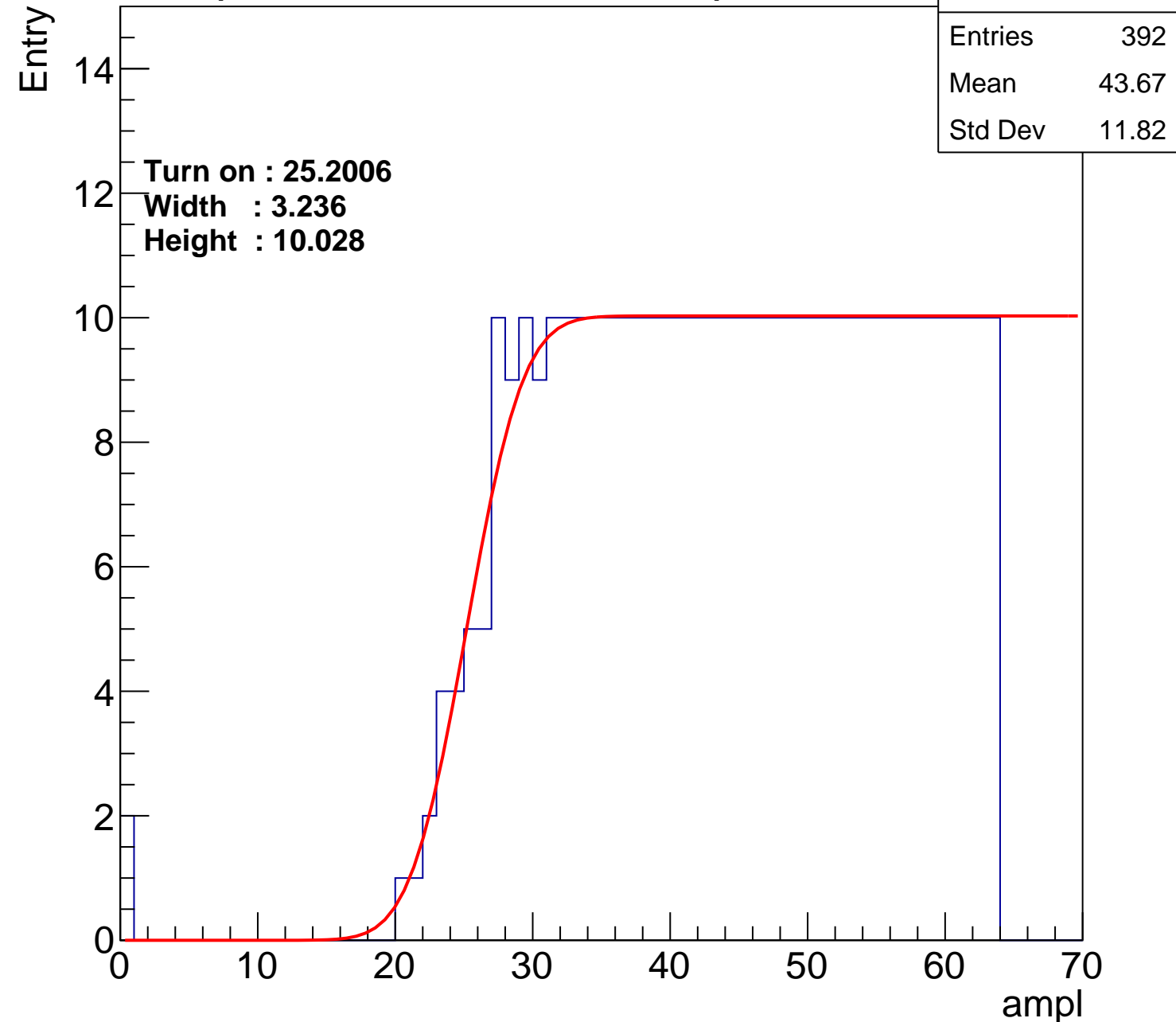
Width : 3.236

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch38

calib_packv5_042523_0143.root, FC#10, port B3

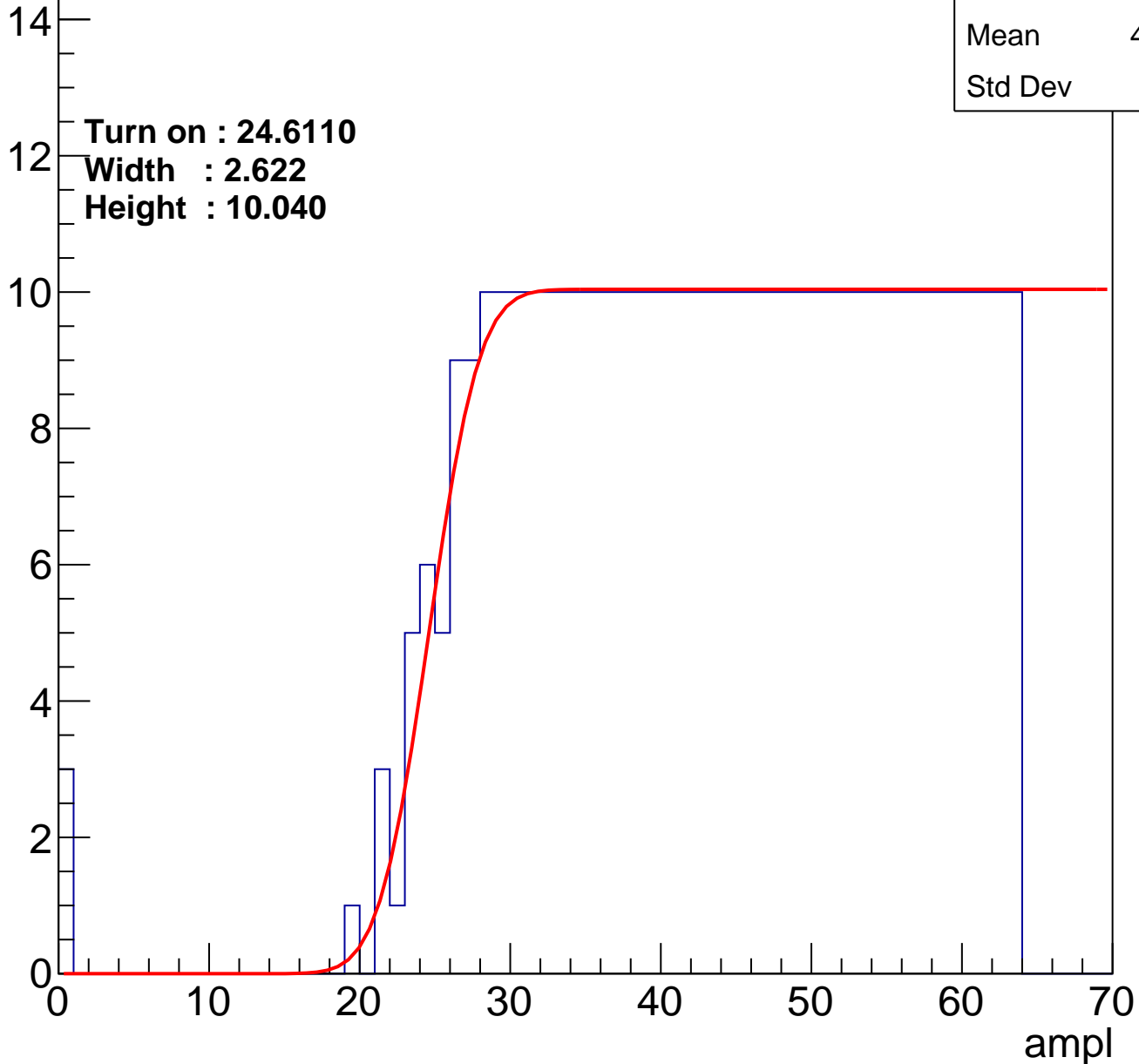
Entries	402
Mean	43.15
Std Dev	12.2

Turn on : 24.6110

Width : 2.622

Height : 10.040

Entry



B1L002S, U1-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.54

Turn on : 26.0443

Width : 2.635

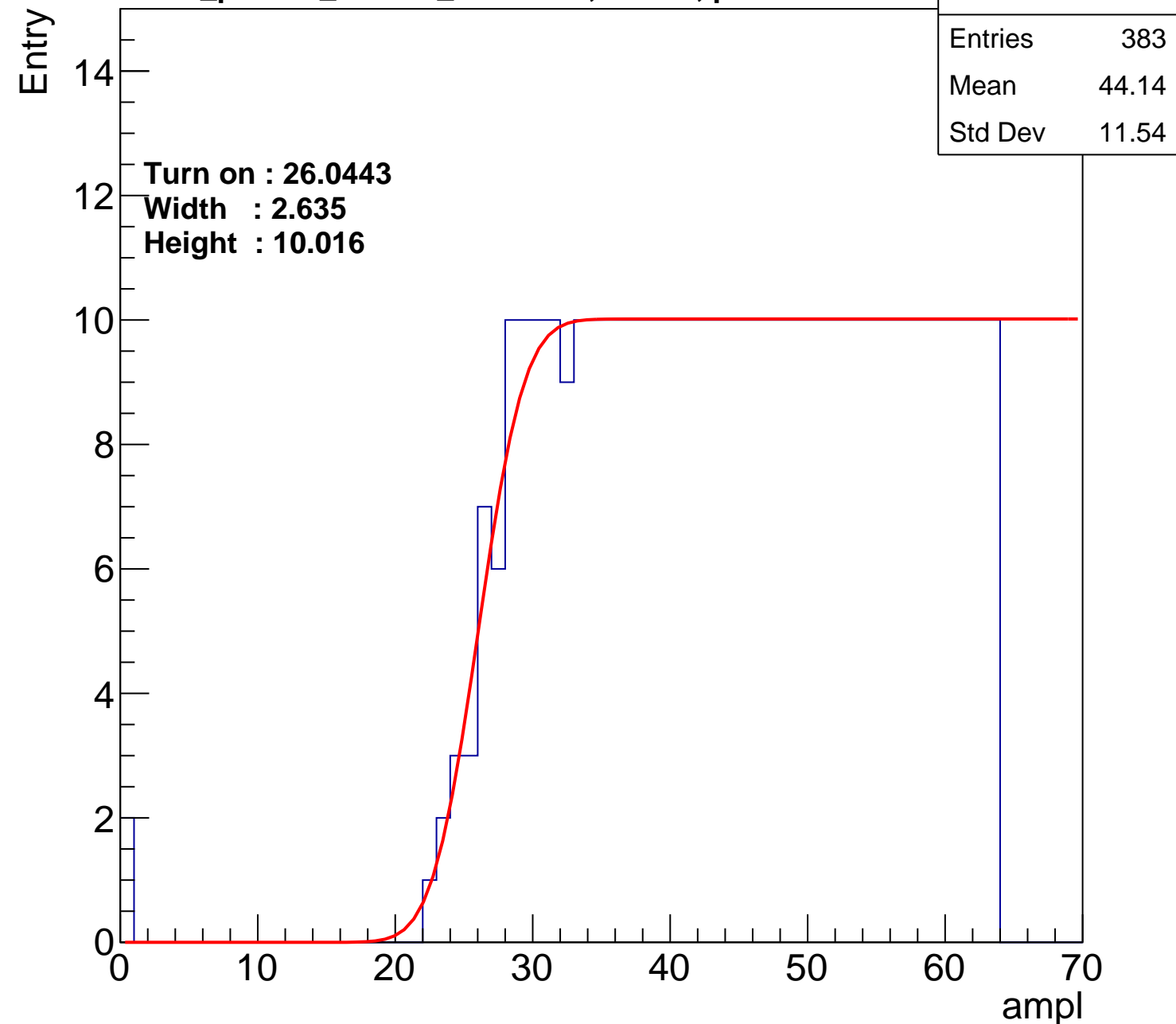
Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch40

calib_packv5_042523_0143.root, FC#10, port B3

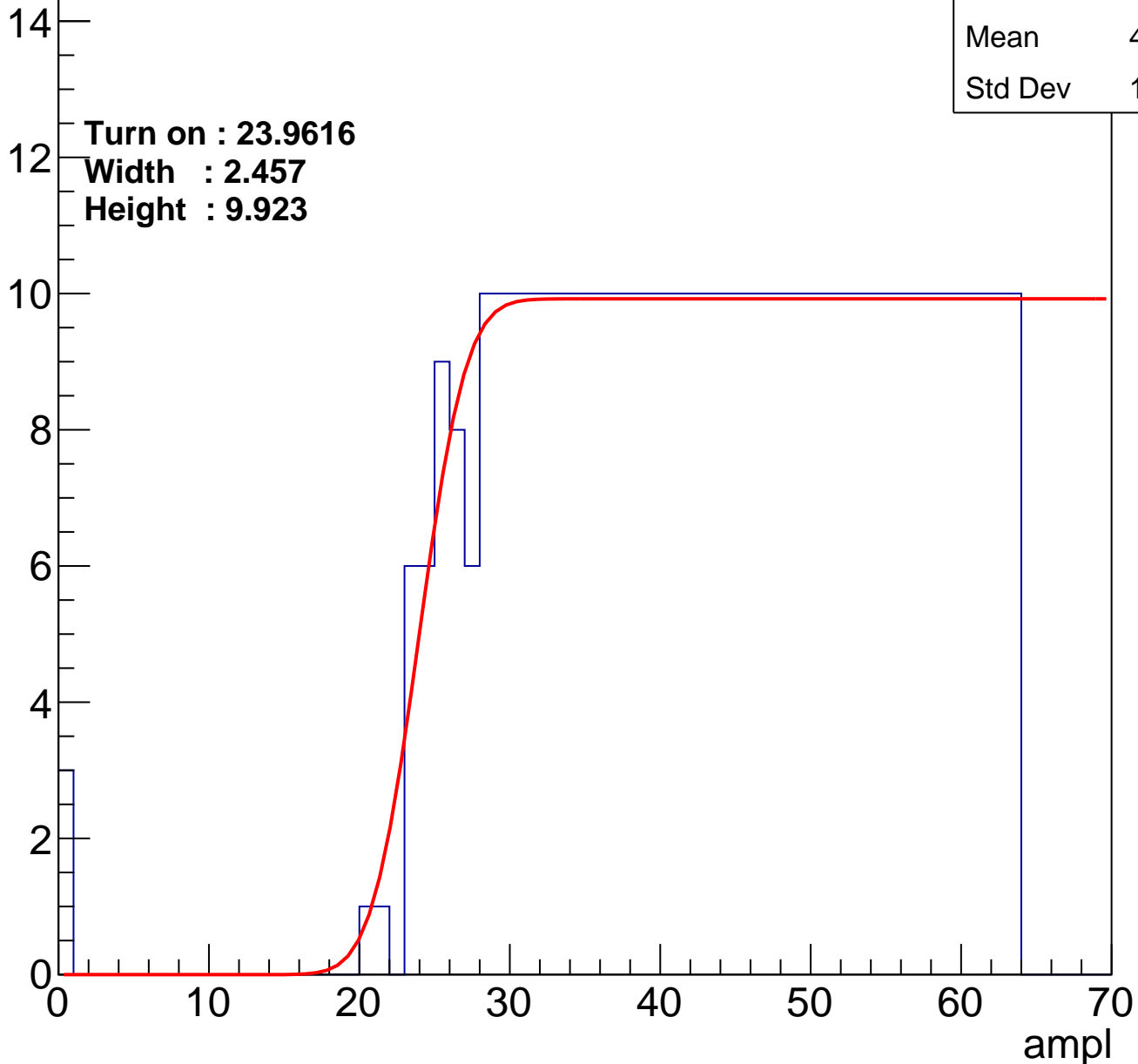
Entries	400
Mean	43.24
Std Dev	12.14

Turn on : 23.9616

Width : 2.457

Height : 9.923

Entry



B1L002S, U1-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.3
Std Dev	11.46

Turn on : 26.6488

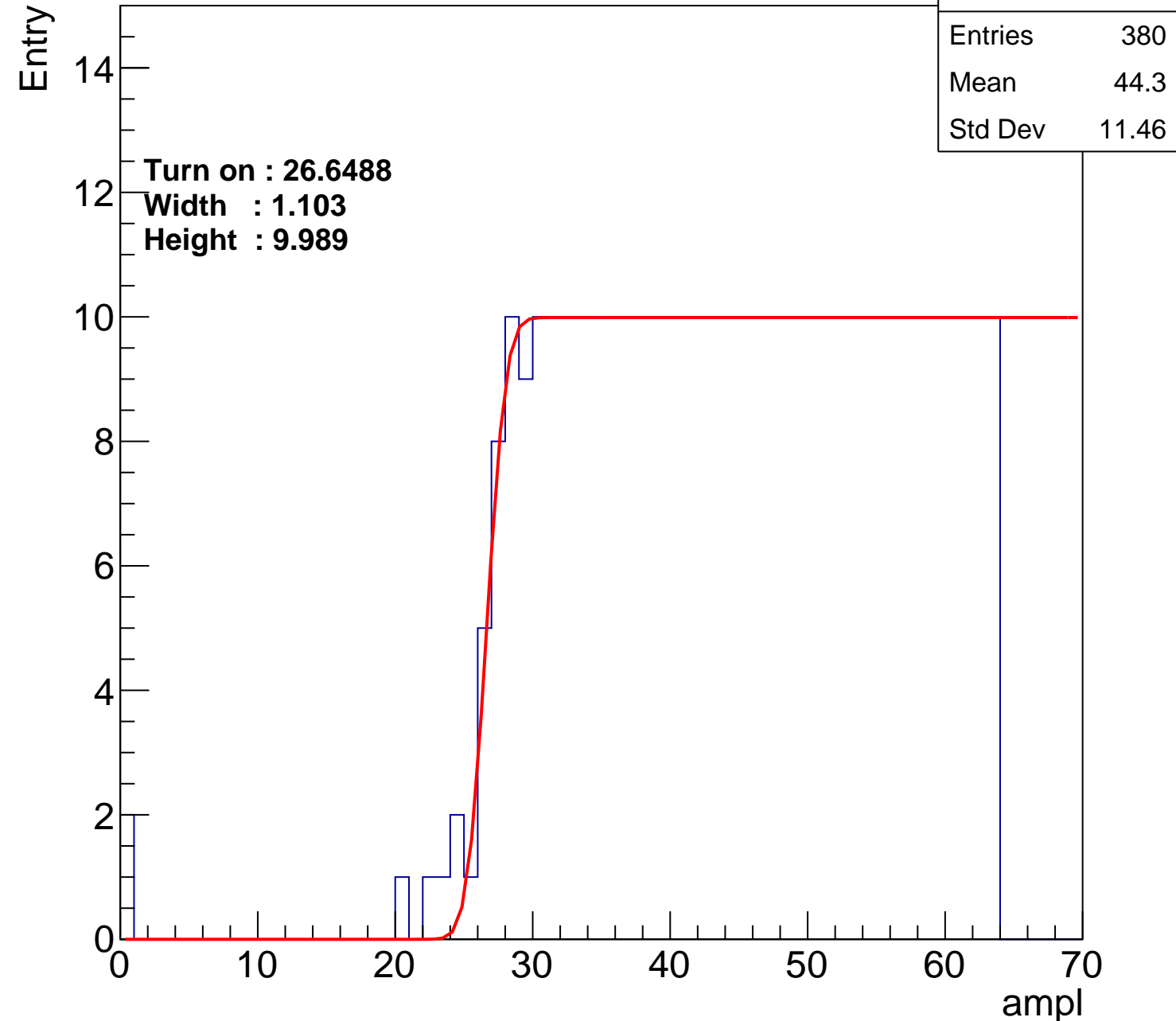
Width : 1.103

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.71
Std Dev	11.93

Turn on : 25.3215

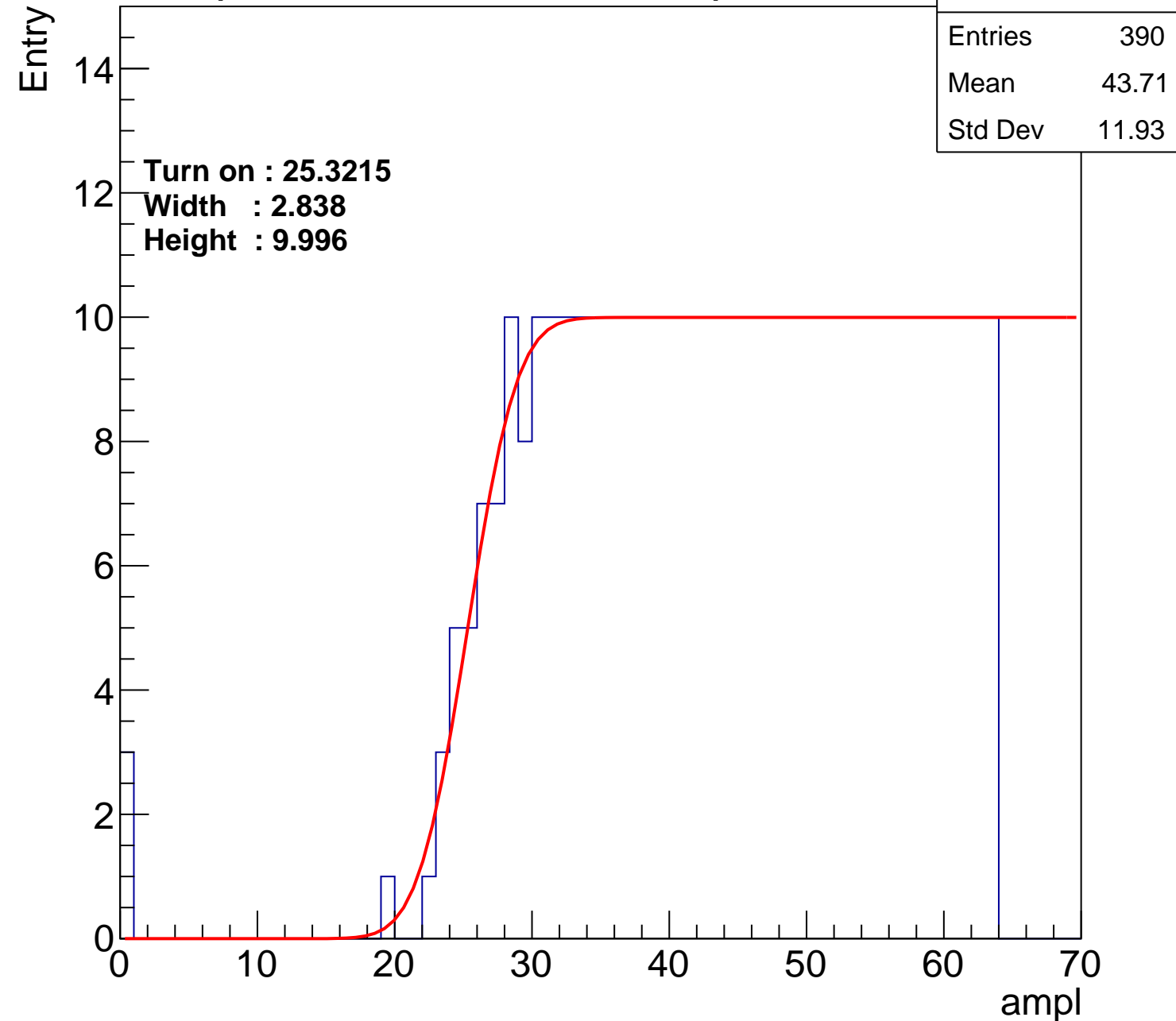
Width : 2.838

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch43

calib_packv5_042523_0143.root, FC#10, port B3

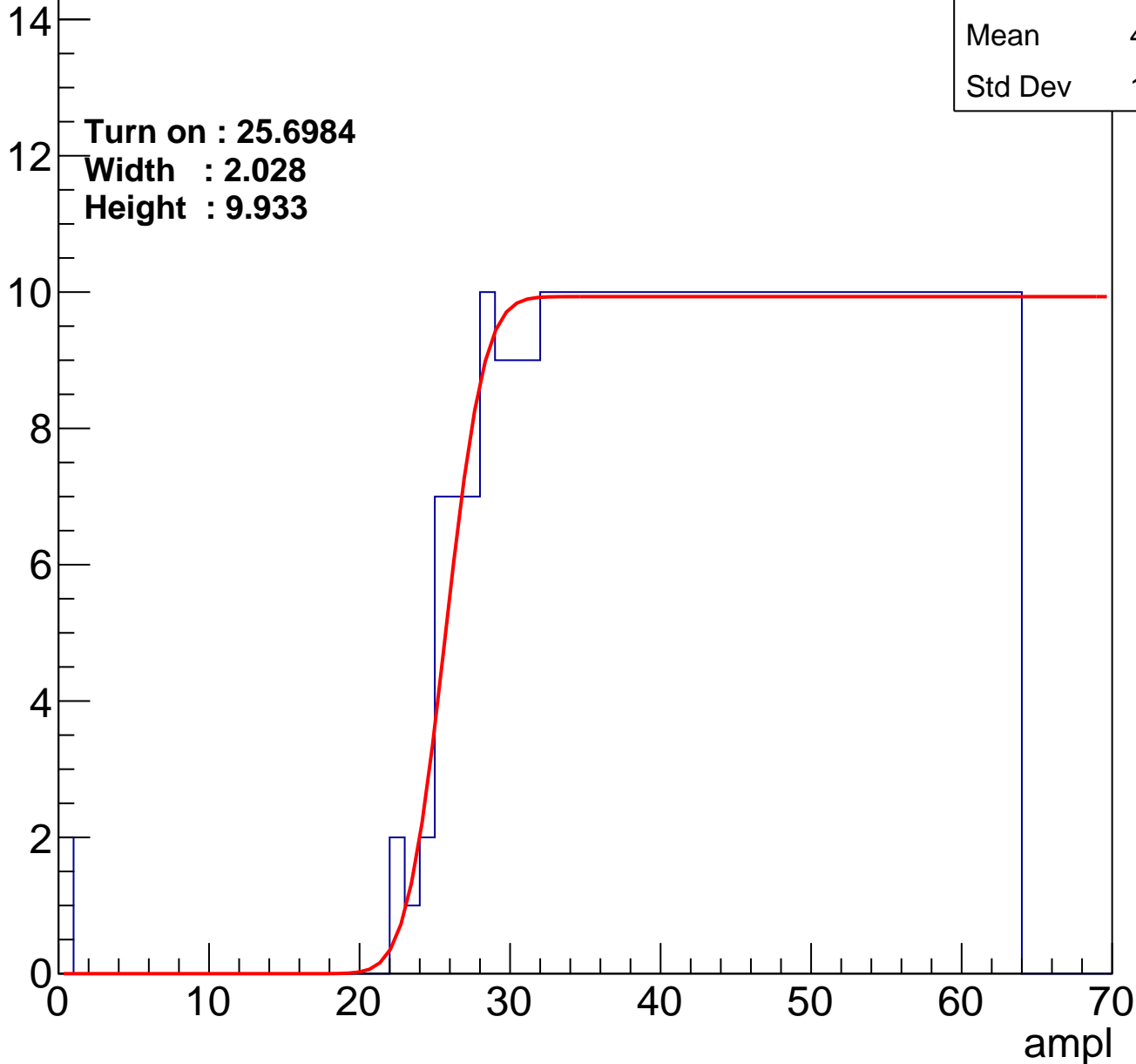
Entry

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.6984

Width : 2.028

Height : 9.933



B1L002S, U1-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.56
Std Dev	11.74

Turn on : 25.0364

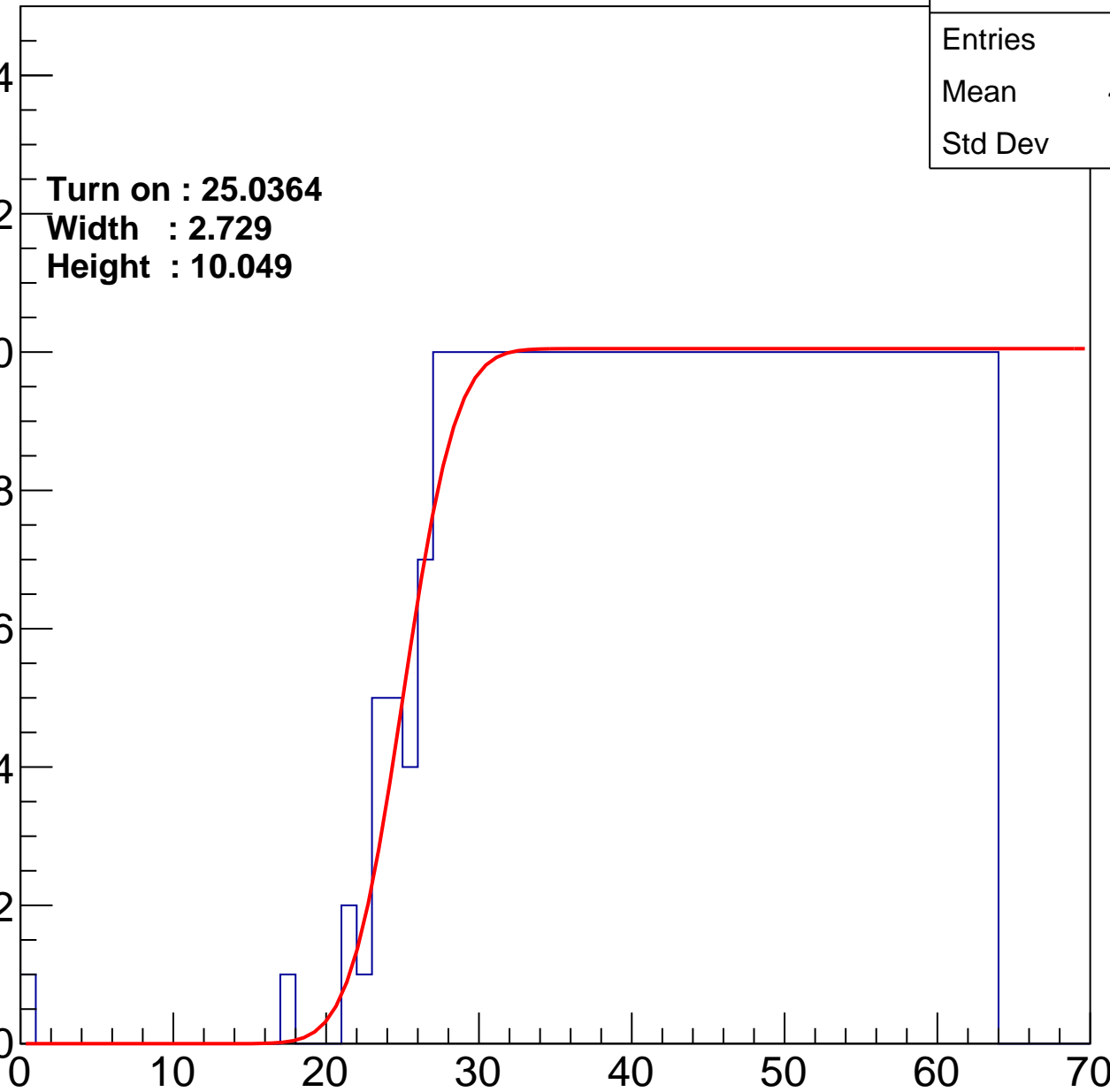
Width : 2.729

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.5
Std Dev	12.17

Turn on : 24.7873

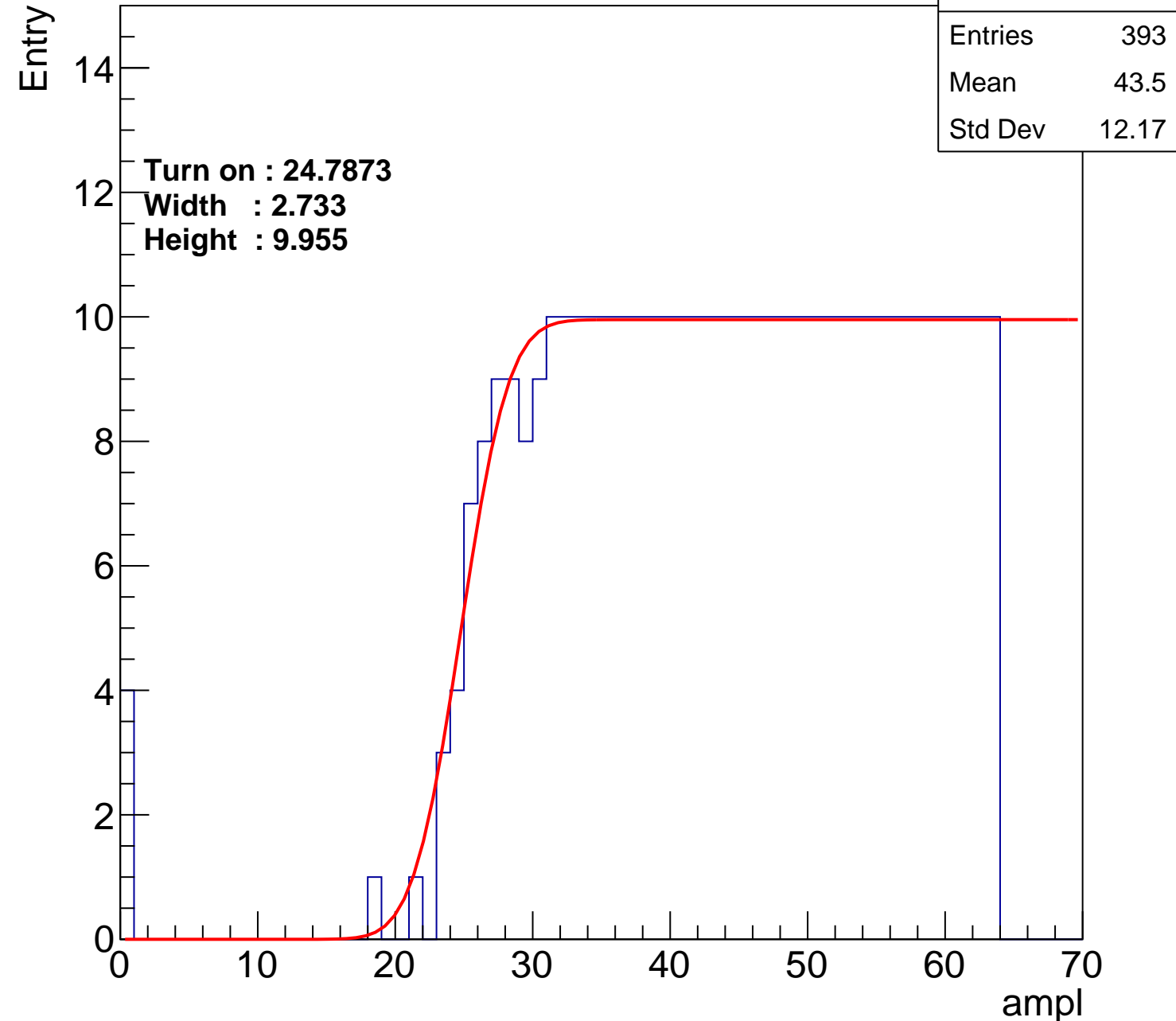
Width : 2.733

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch46

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.0703

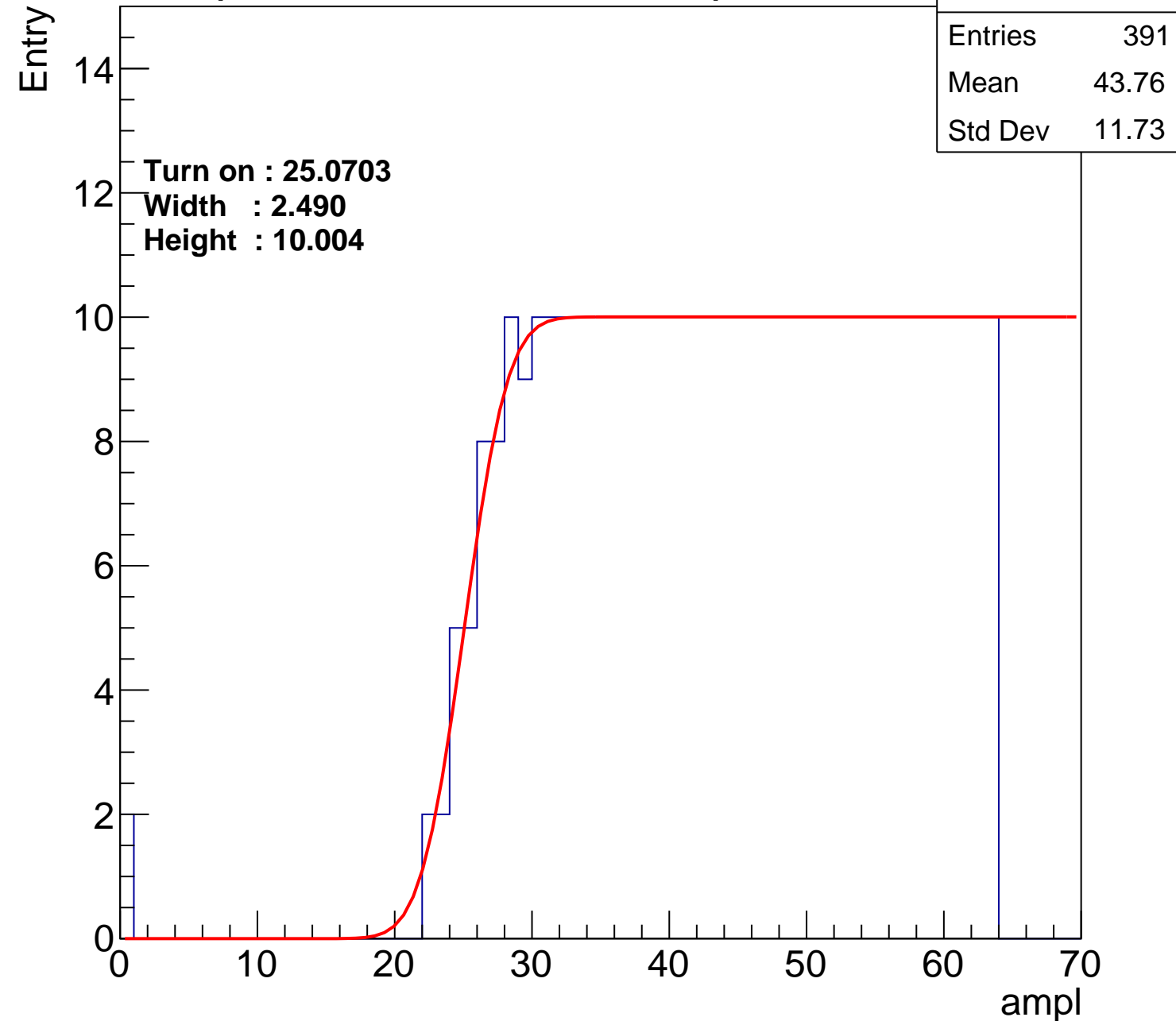
Width : 2.490

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.98
Std Dev	12.14

Turn on : 23.8444

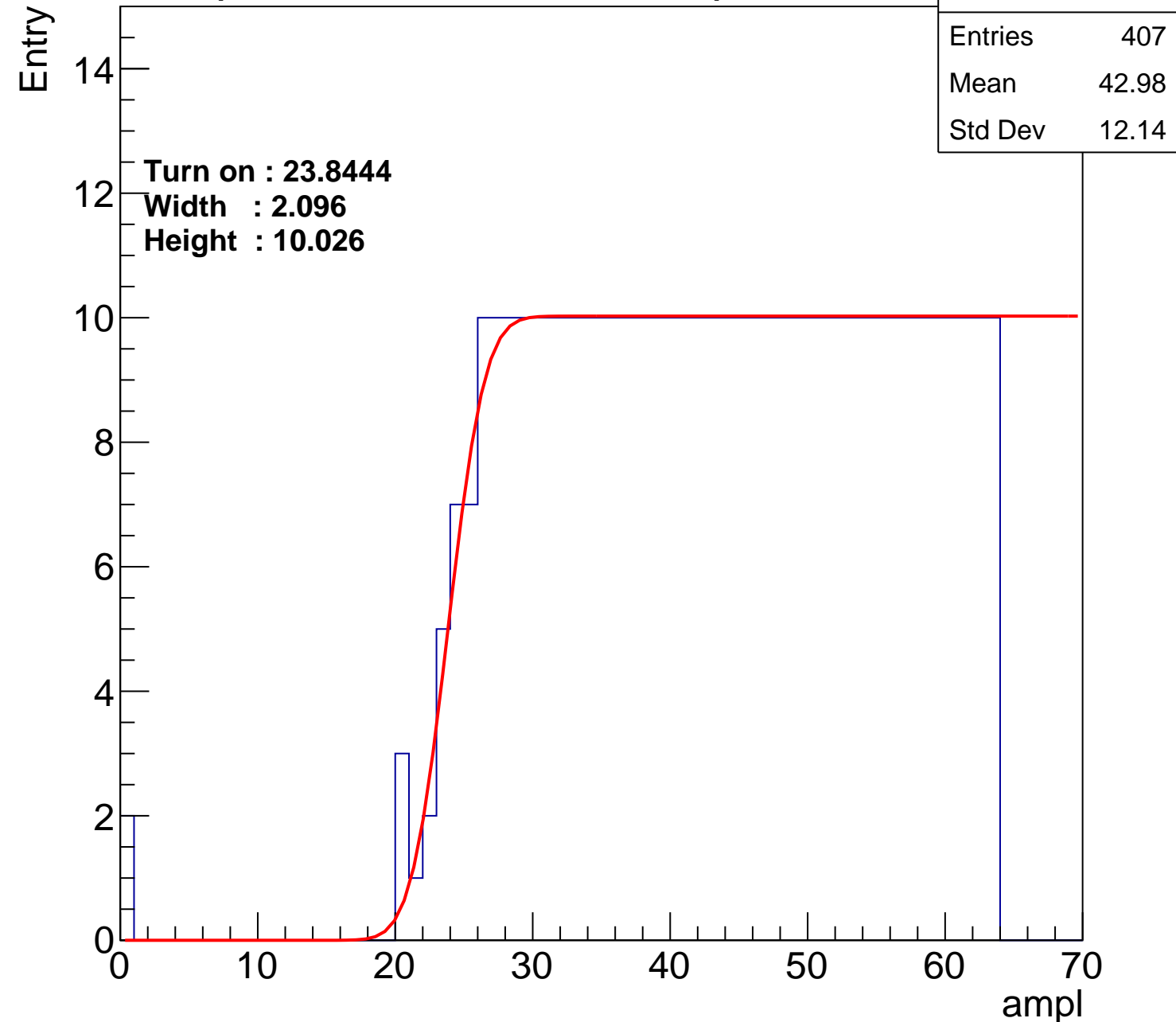
Width : 2.096

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch48

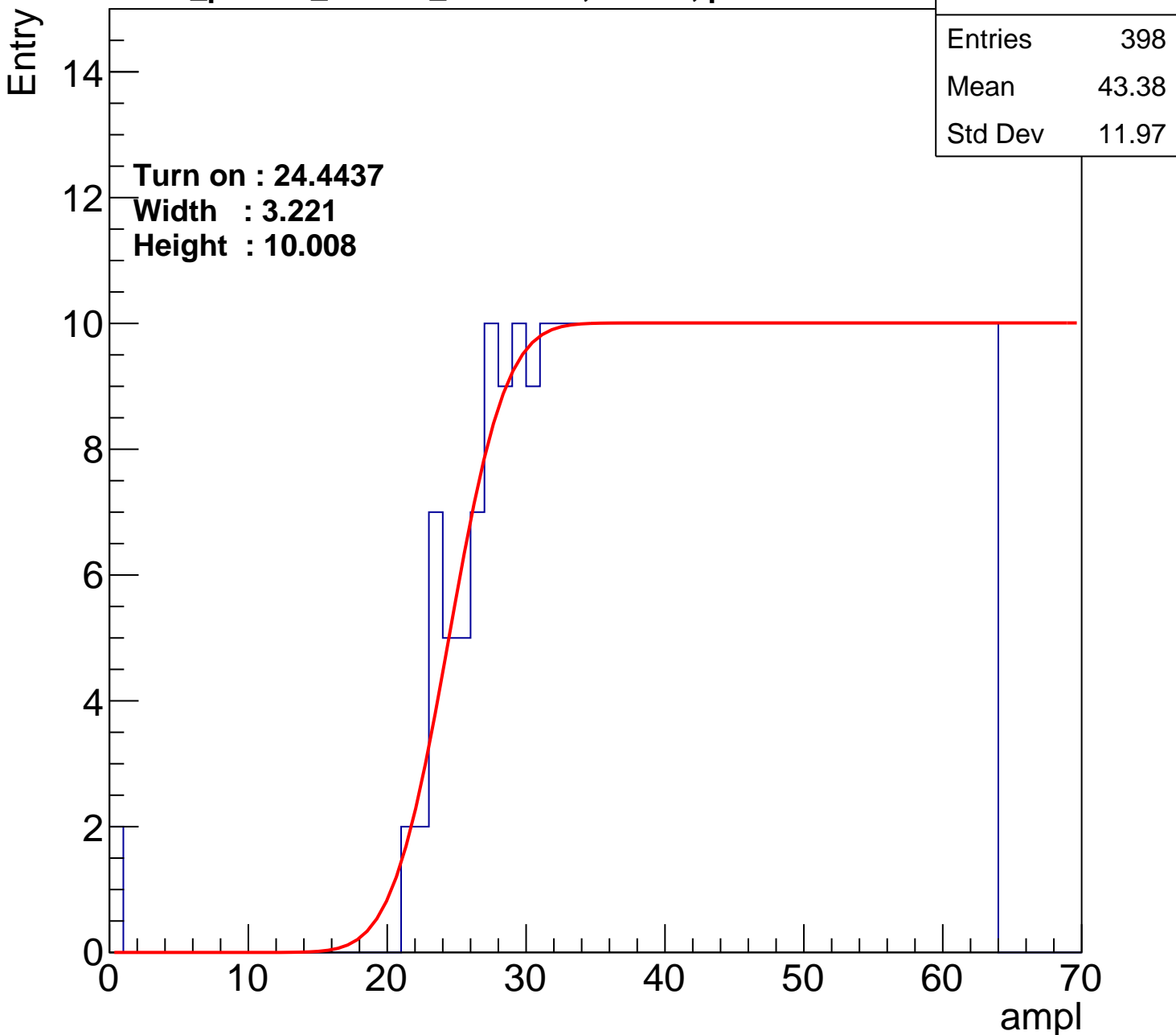
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 24.4437

Width : 3.221

Height : 10.008

Entries	398
Mean	43.38
Std Dev	11.97



B1L002S, U1-ch49

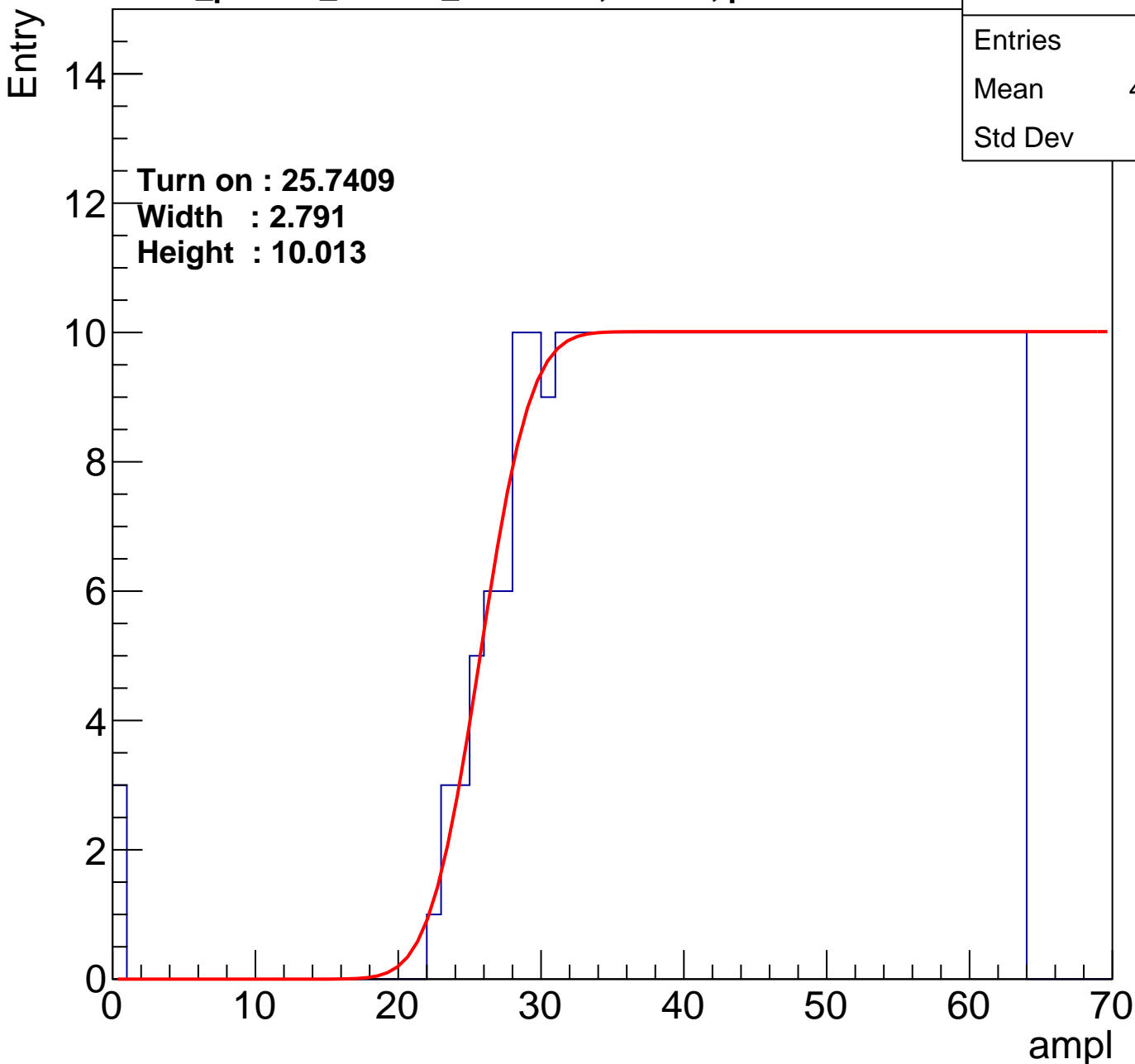
calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.93
Std Dev	11.8

Turn on : 25.7409

Width : 2.791

Height : 10.013



B1L002S, U1-ch50

calib_packv5_042523_0143.root, FC#10, port B3

Entries	416
Mean	42.34
Std Dev	12.83

Turn on : 22.0979

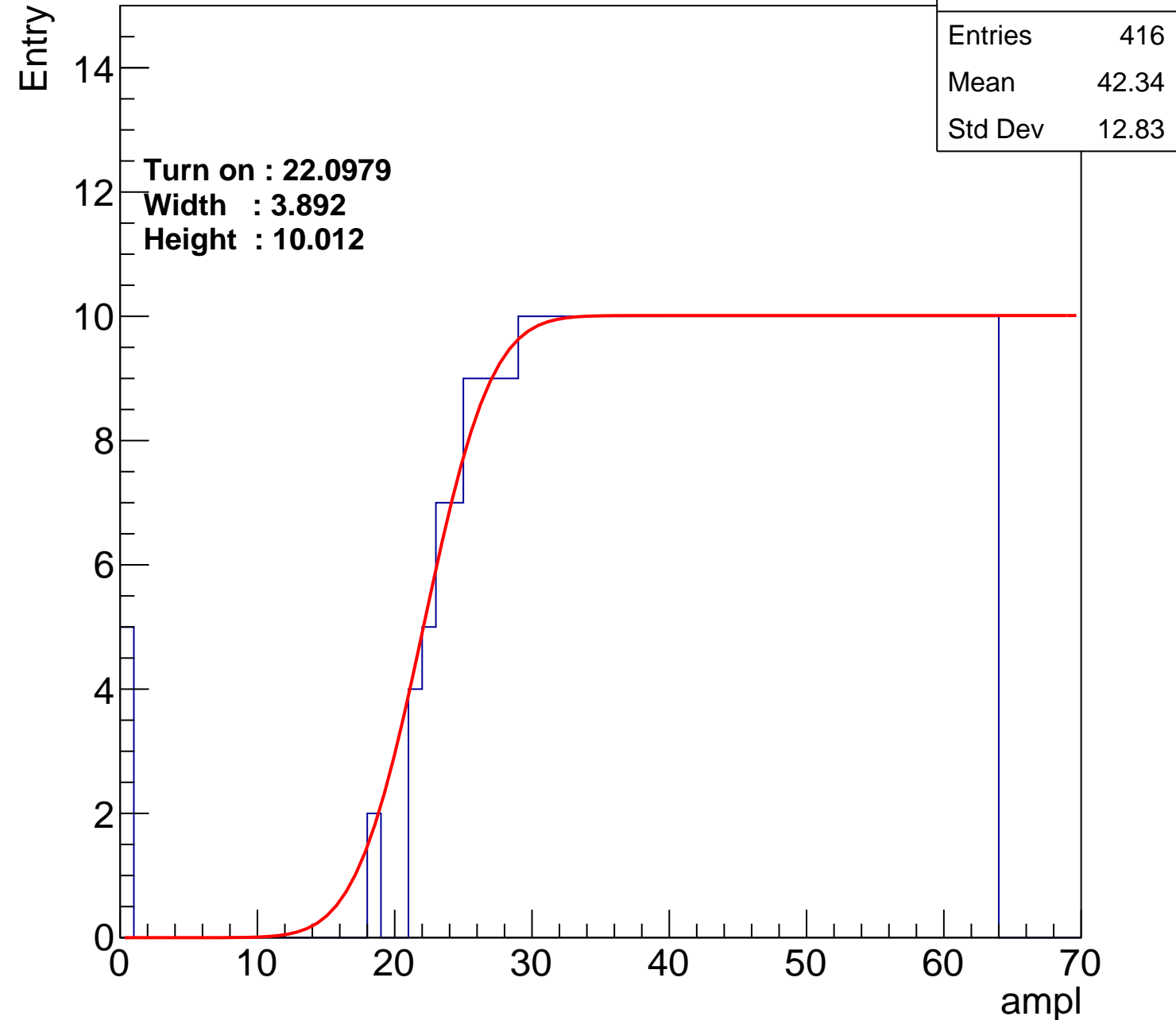
Width : 3.892

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.9
Std Dev	12.32

Turn on : 23.6801

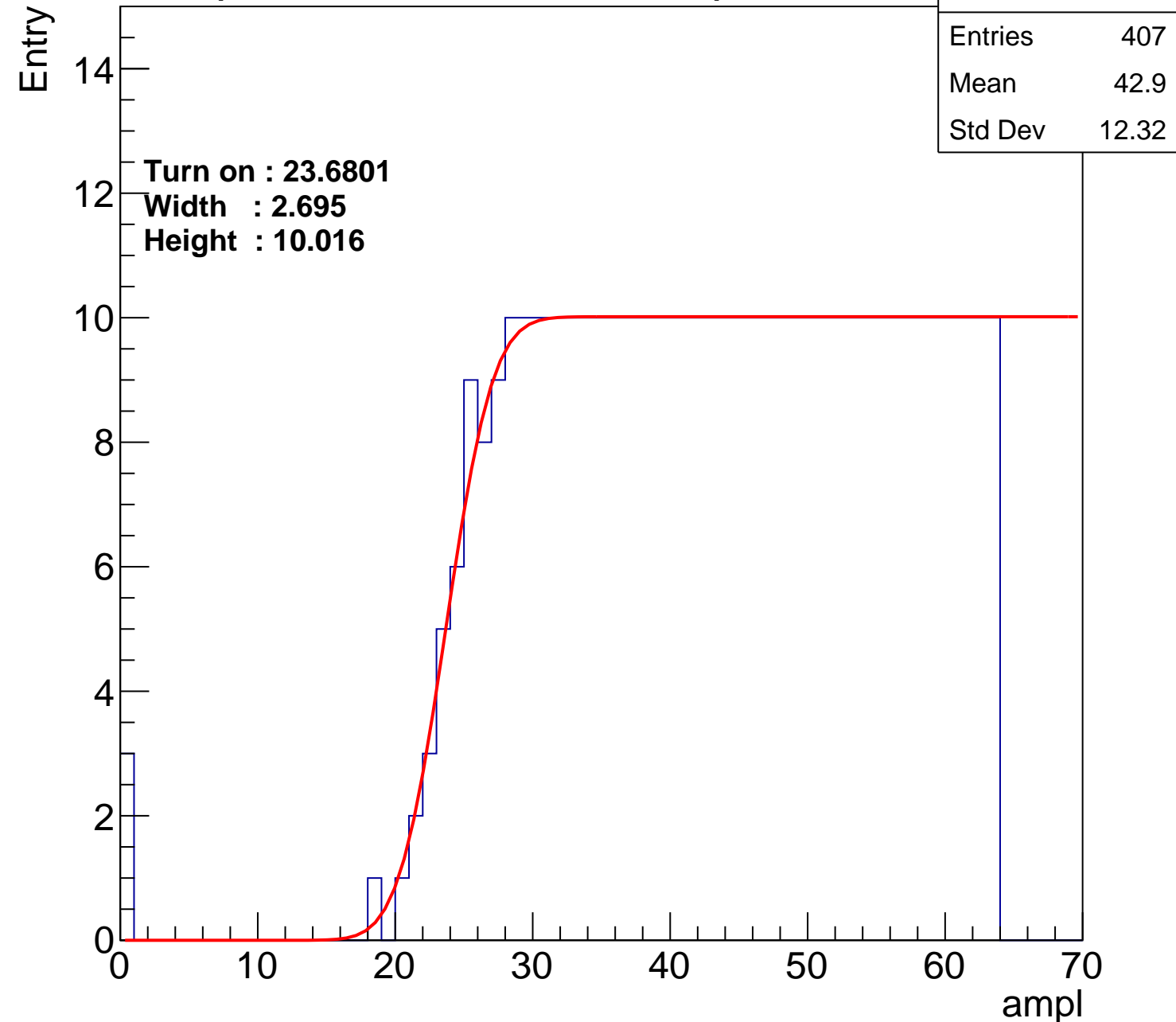
Width : 2.695

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch52

calib_packv5_042523_0143.root, FC#10, port B3

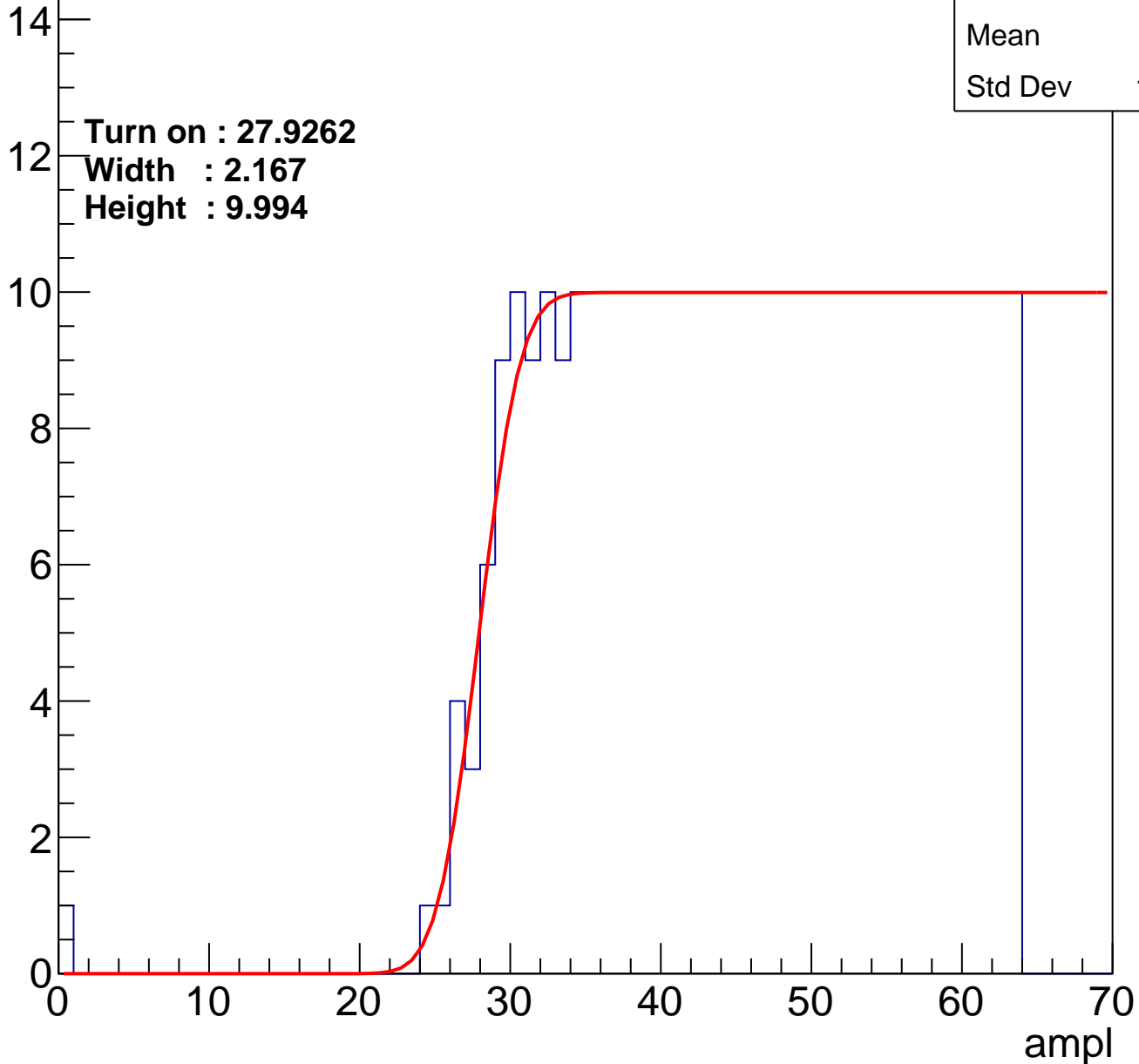
Entries	363
Mean	45.2
Std Dev	10.81

Turn on : 27.9262

Width : 2.167

Height : 9.994

Entry



B1L002S, U1-ch53

calib_packv5_042523_0143.root, FC#10, port B3

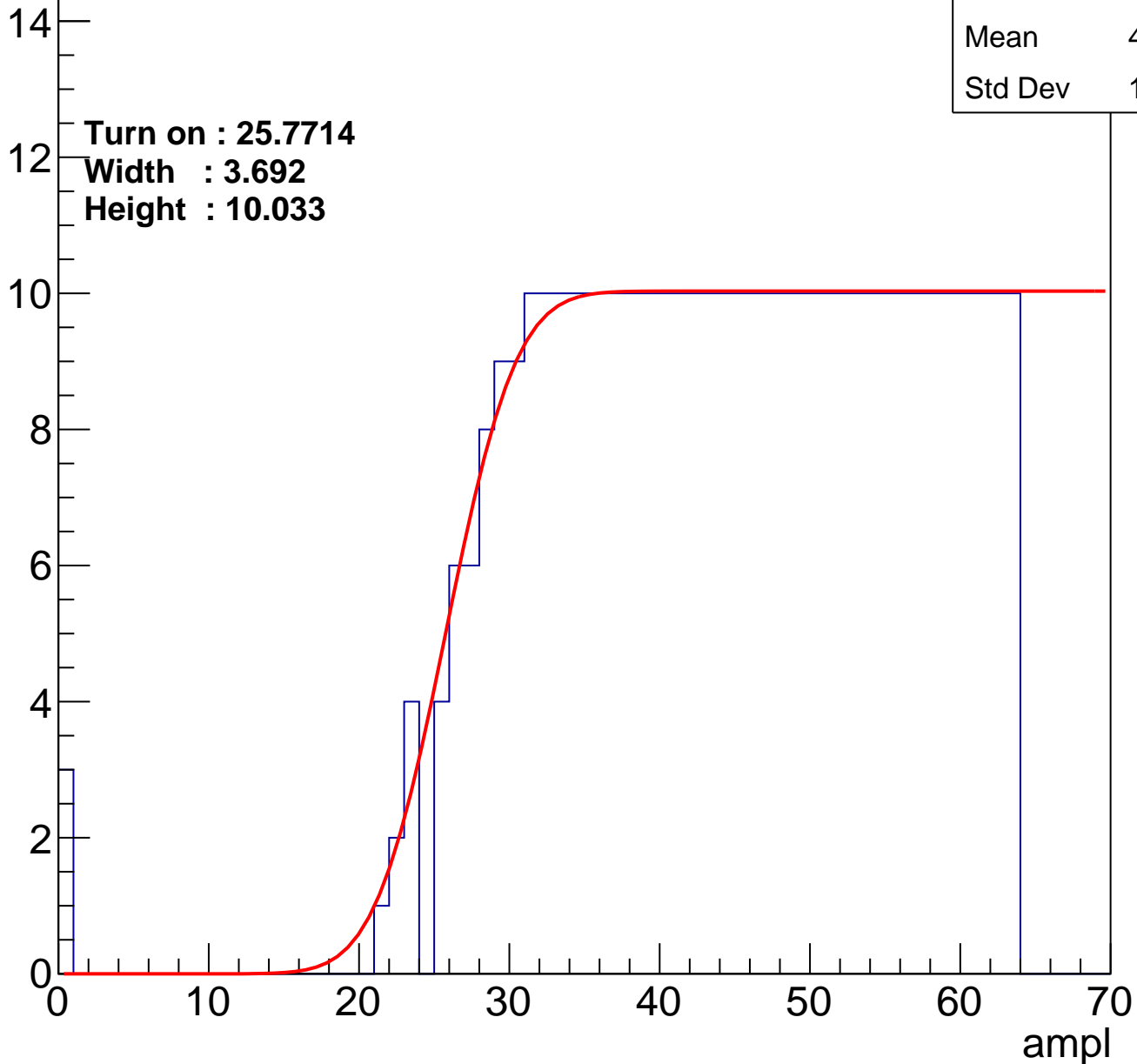
Entries	382
Mean	44.08
Std Dev	11.77

Turn on : 25.7714

Width : 3.692

Height : 10.033

Entry



B1L002S, U1-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.88
Std Dev	12.4

Turn on : 23.8843

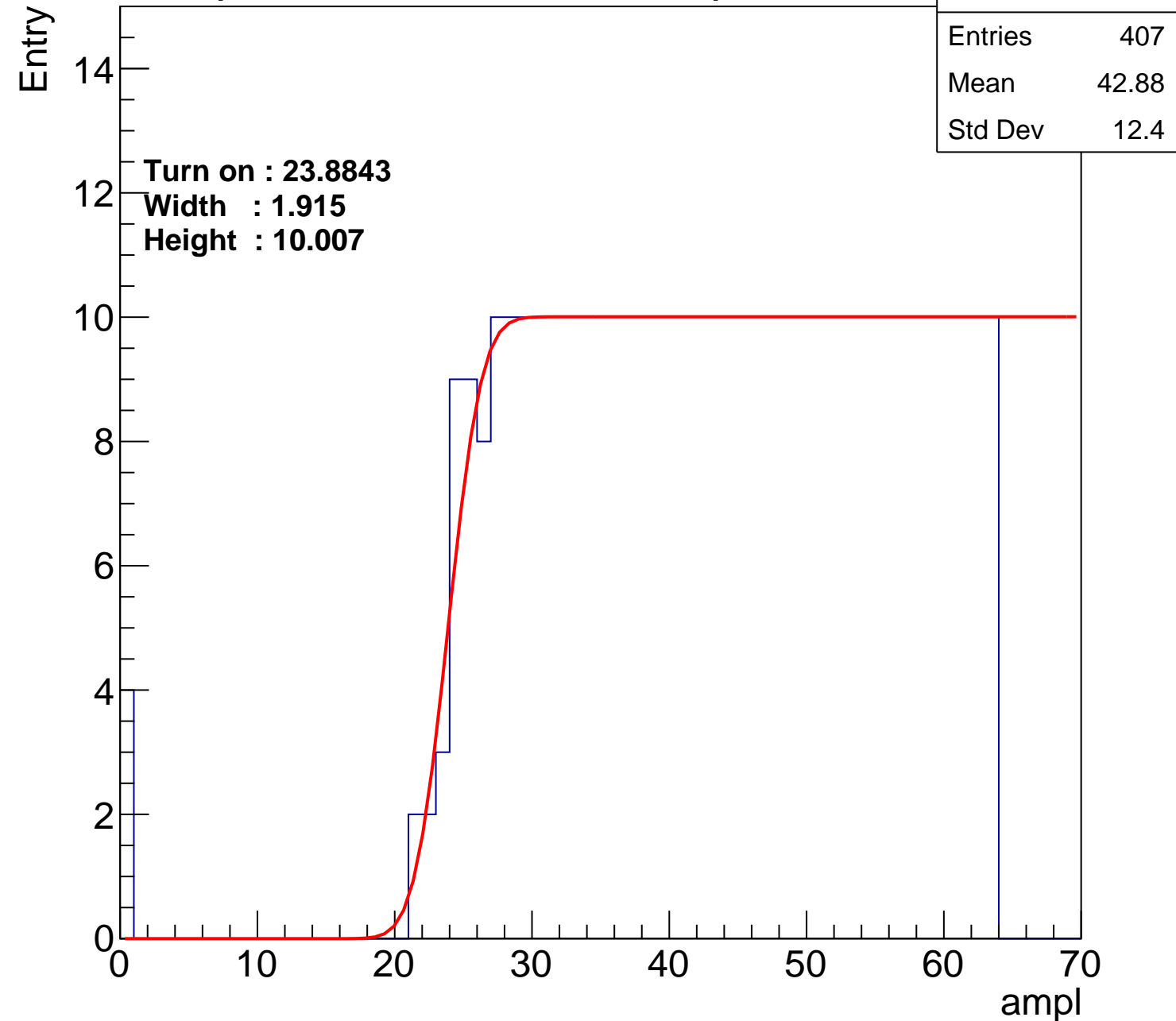
Width : 1.915

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.24
Std Dev	11.67

Turn on : 26.0441

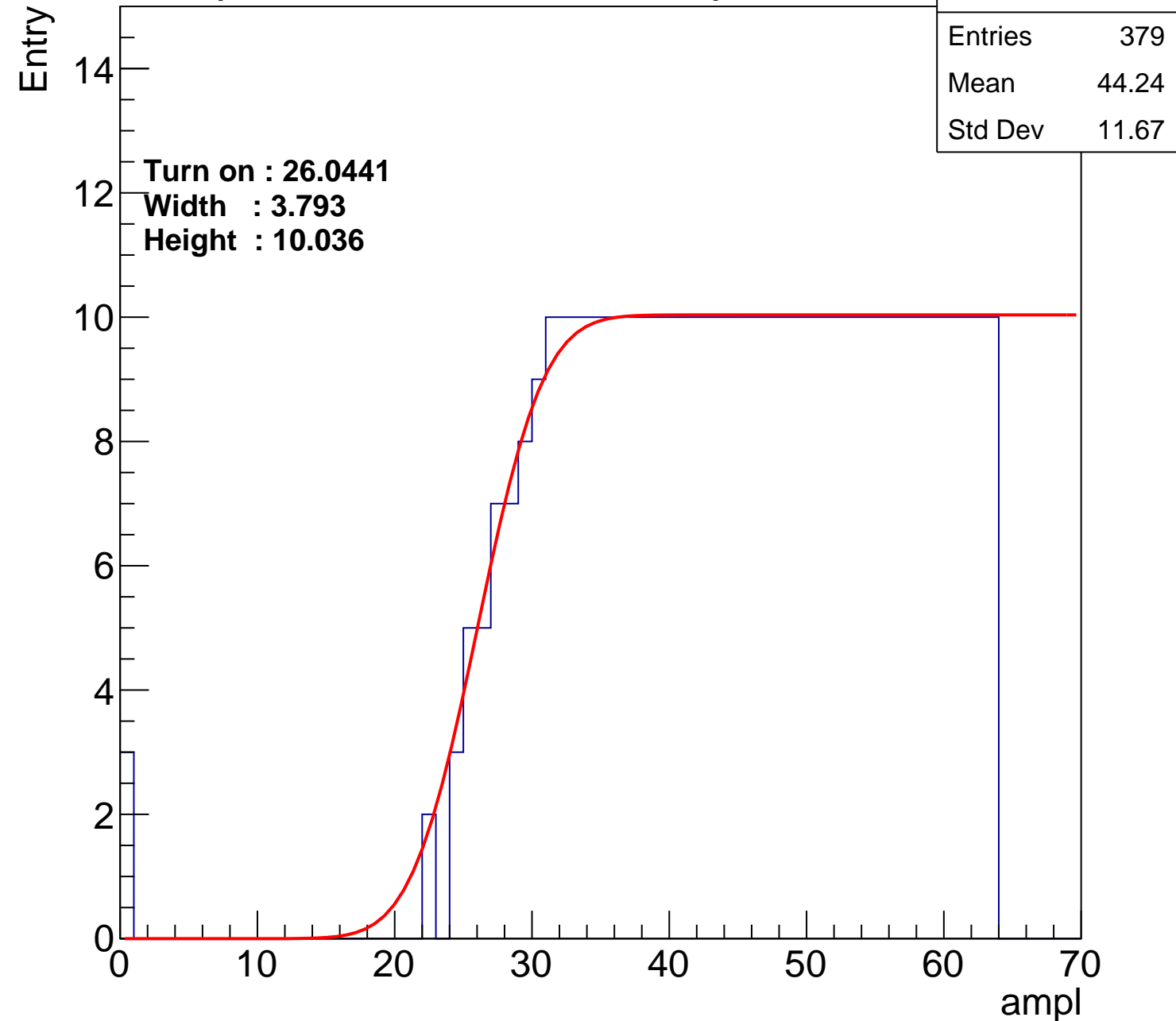
Width : 3.793

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.75
Std Dev	12.04

Turn on : 25.4642

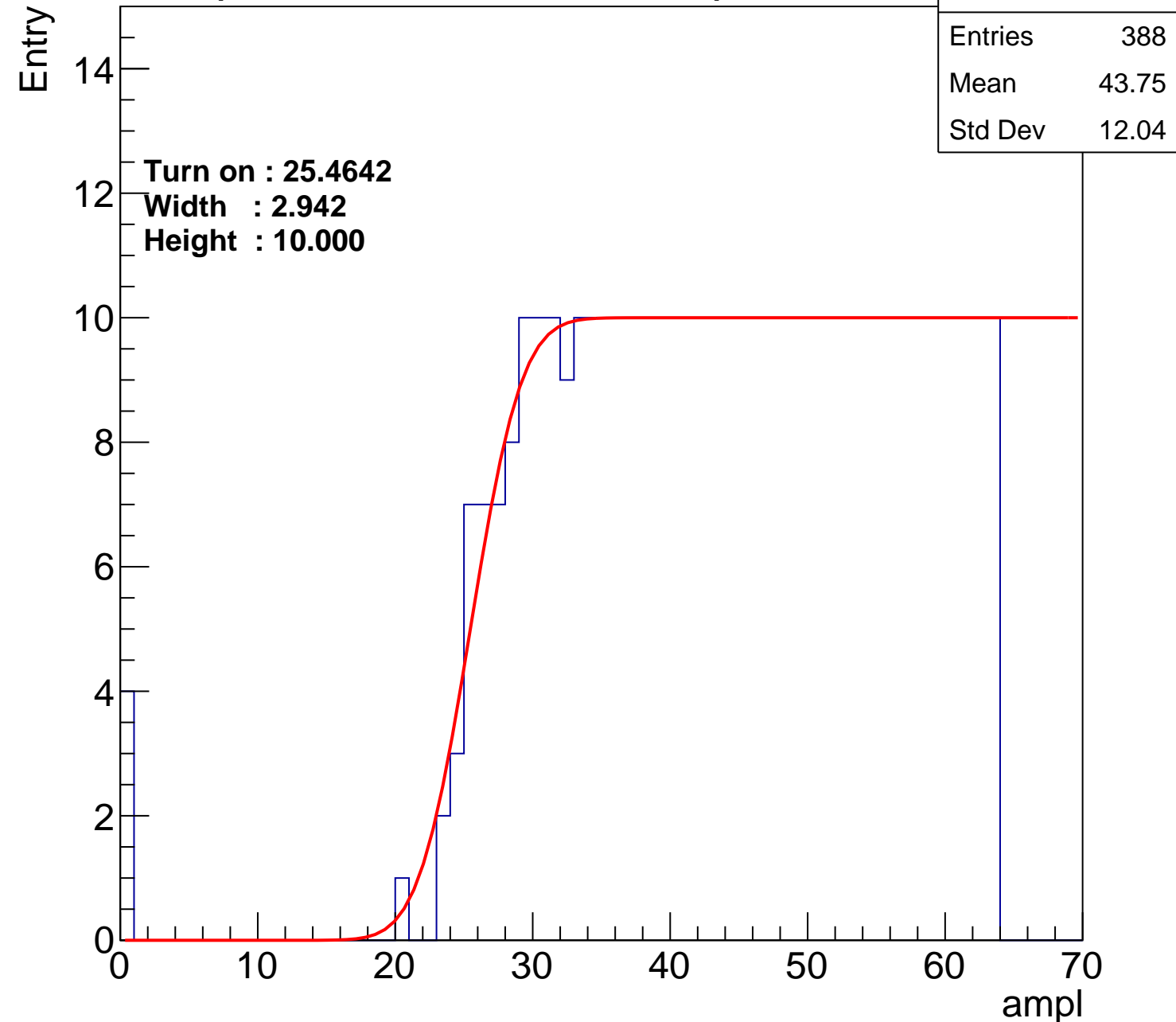
Width : 2.942

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.37
Std Dev	11.33

Turn on : 26.1901

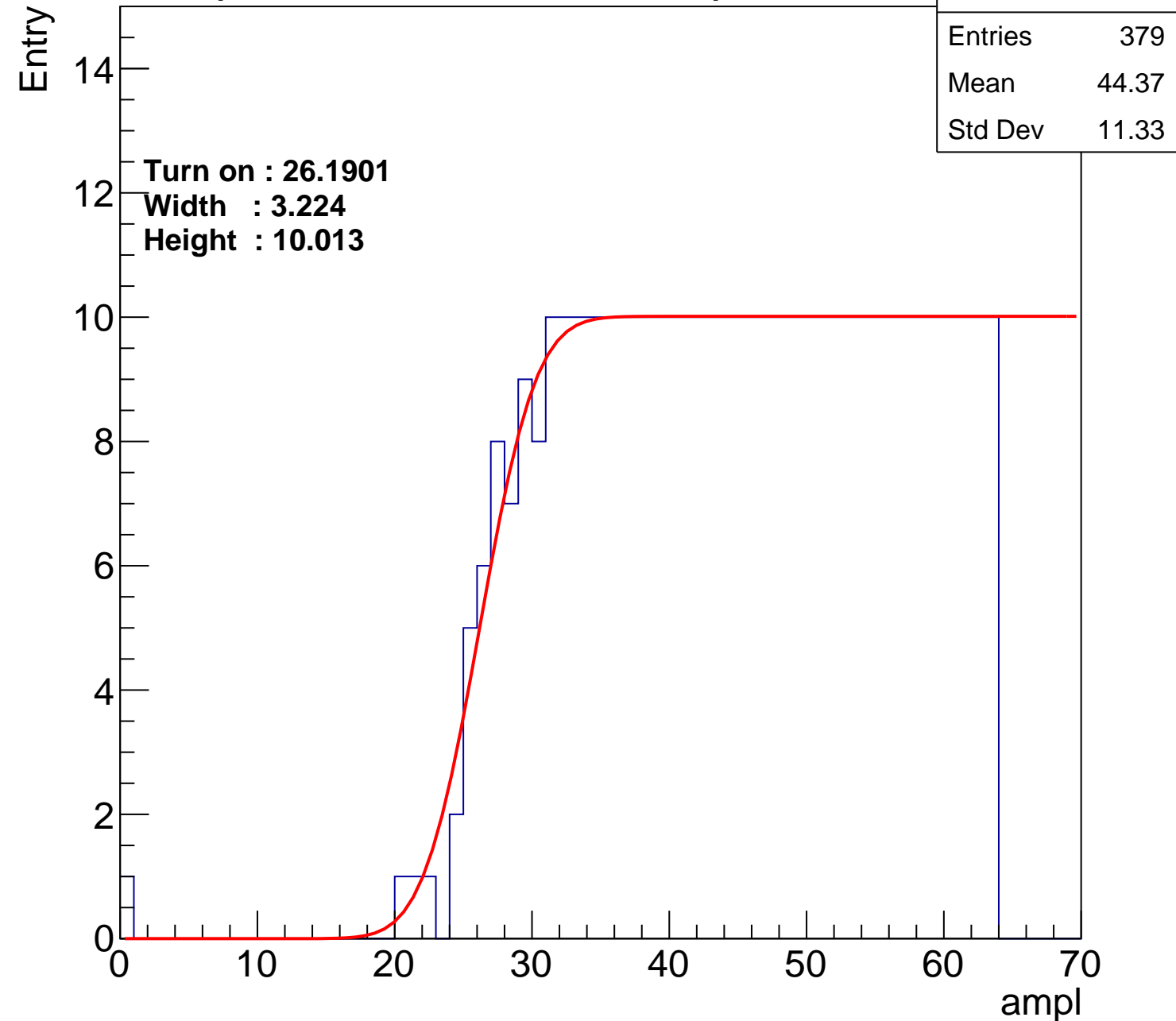
Width : 3.224

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch58

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.19
Std Dev	11.39

Turn on : 25.6645

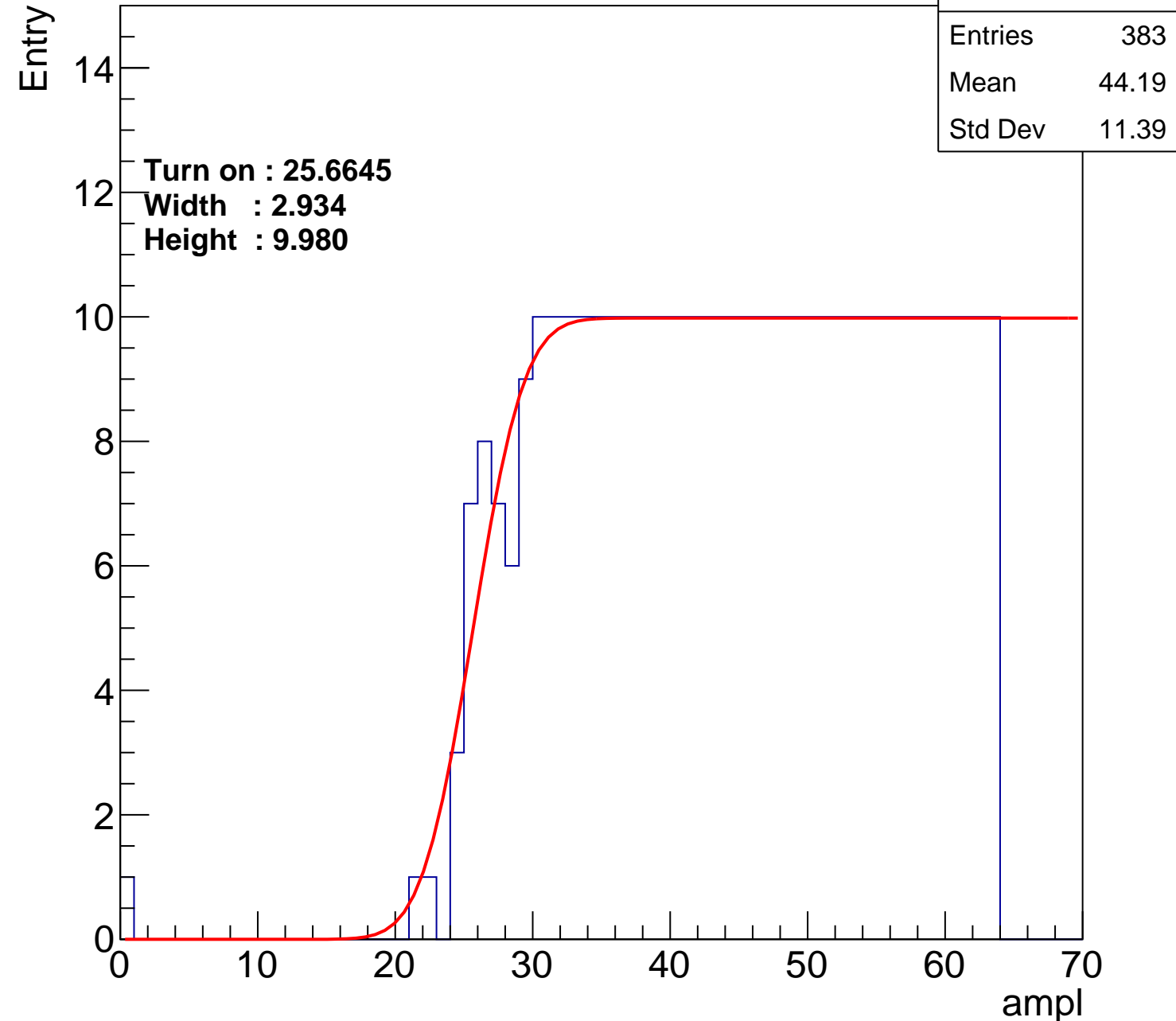
Width : 2.934

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	416
Mean	42.54
Std Dev	12.37

Turn on : 22.5364

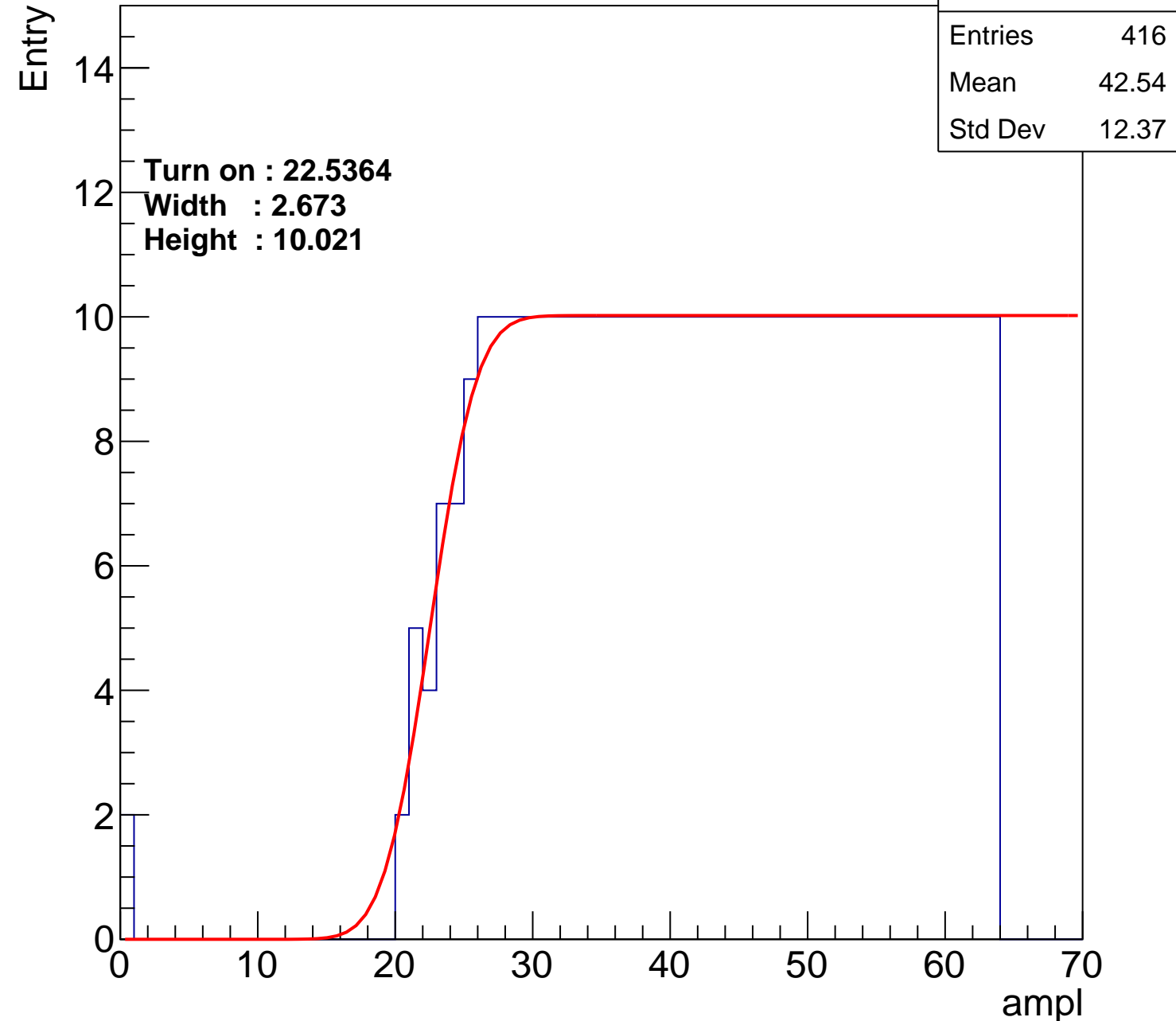
Width : 2.673

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.31
Std Dev	11.88

Turn on : 24.7730

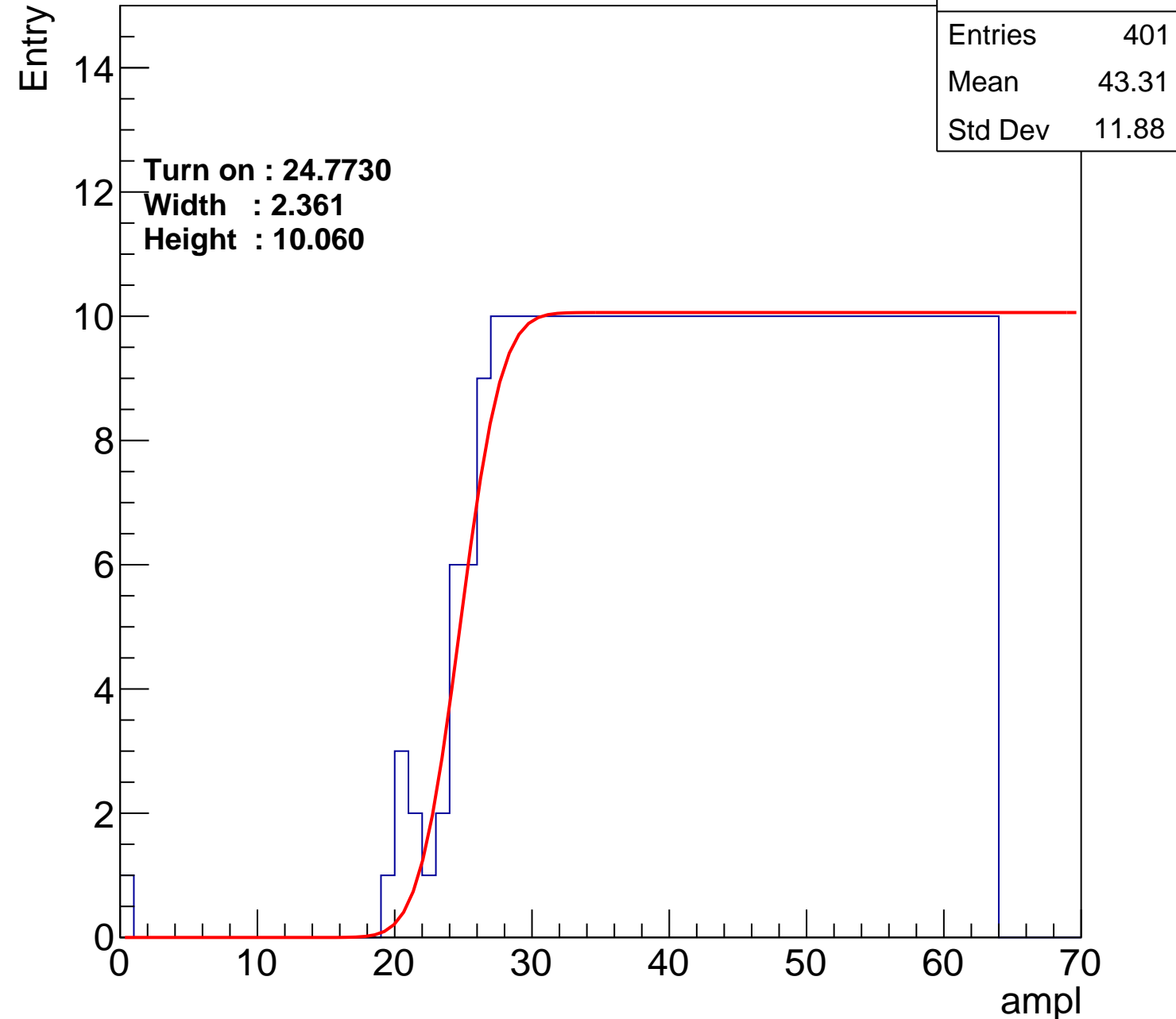
Width : 2.361

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.6003

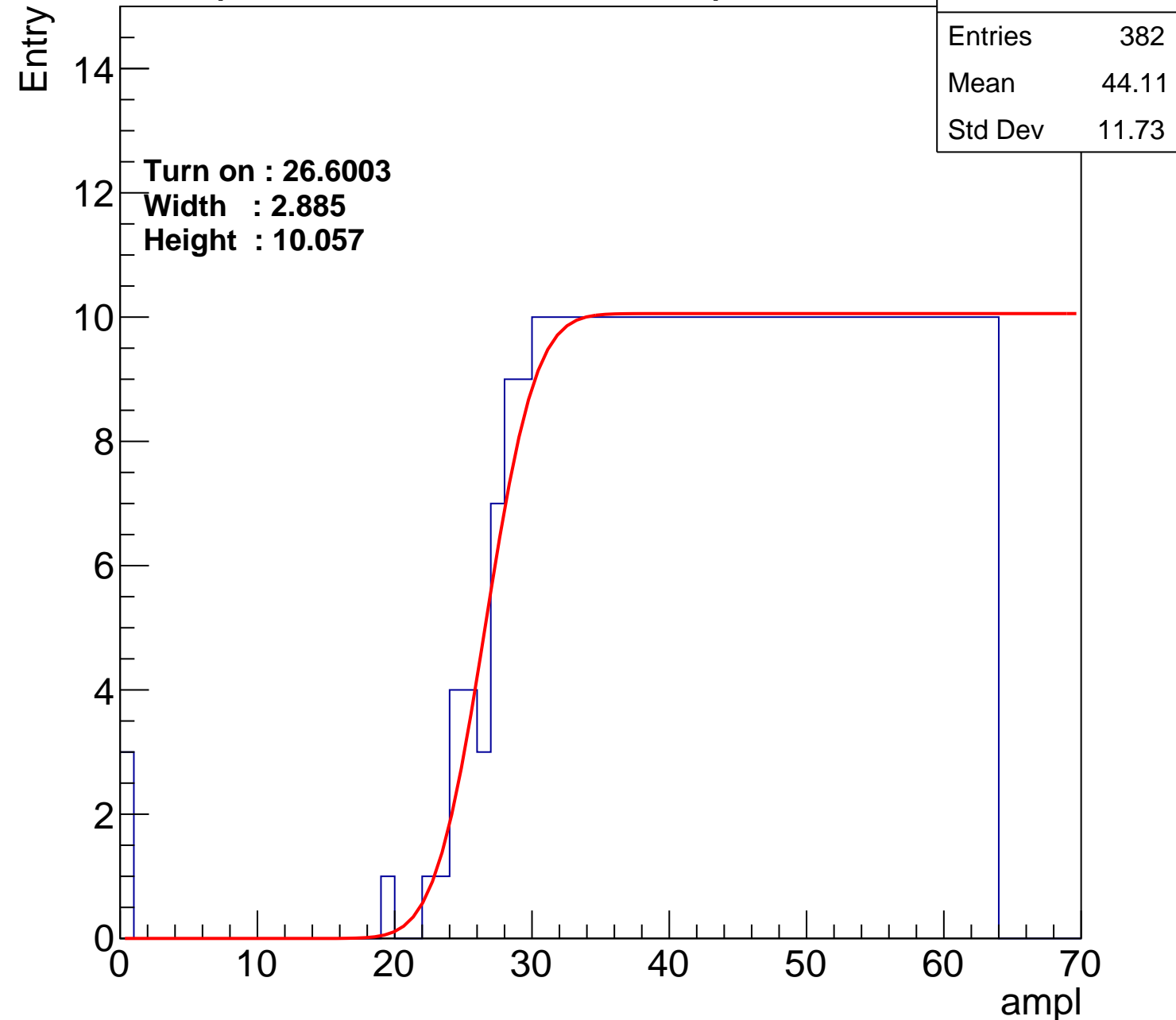
Width : 2.885

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch62

calib_packv5_042523_0143.root, FC#10, port B3

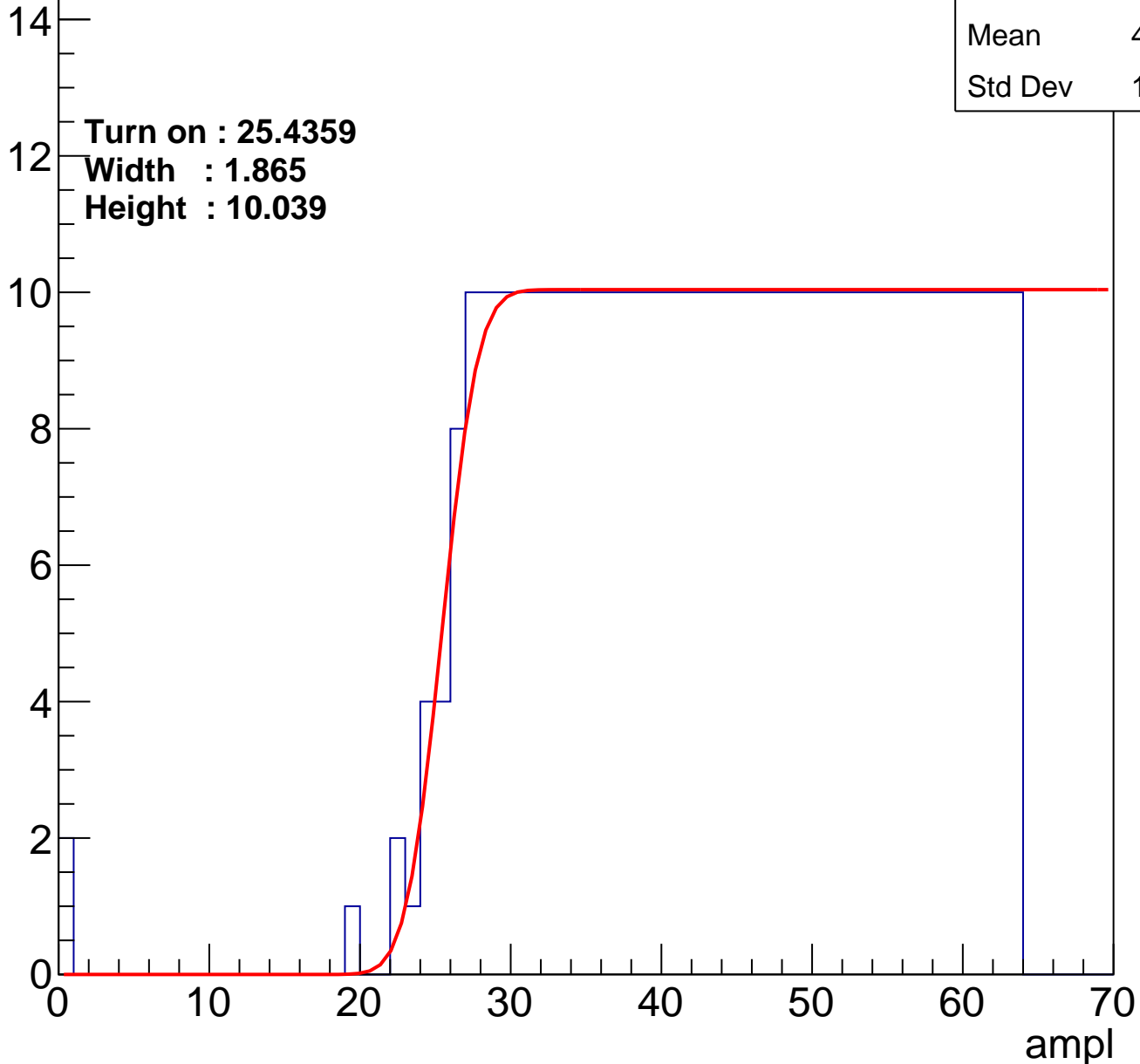
Entries	392
Mean	43.72
Std Dev	11.74

Turn on : 25.4359

Width : 1.865

Height : 10.039

Entry



B1L002S, U1-ch63

calib_packv5_042523_0143.root, FC#10, port B3

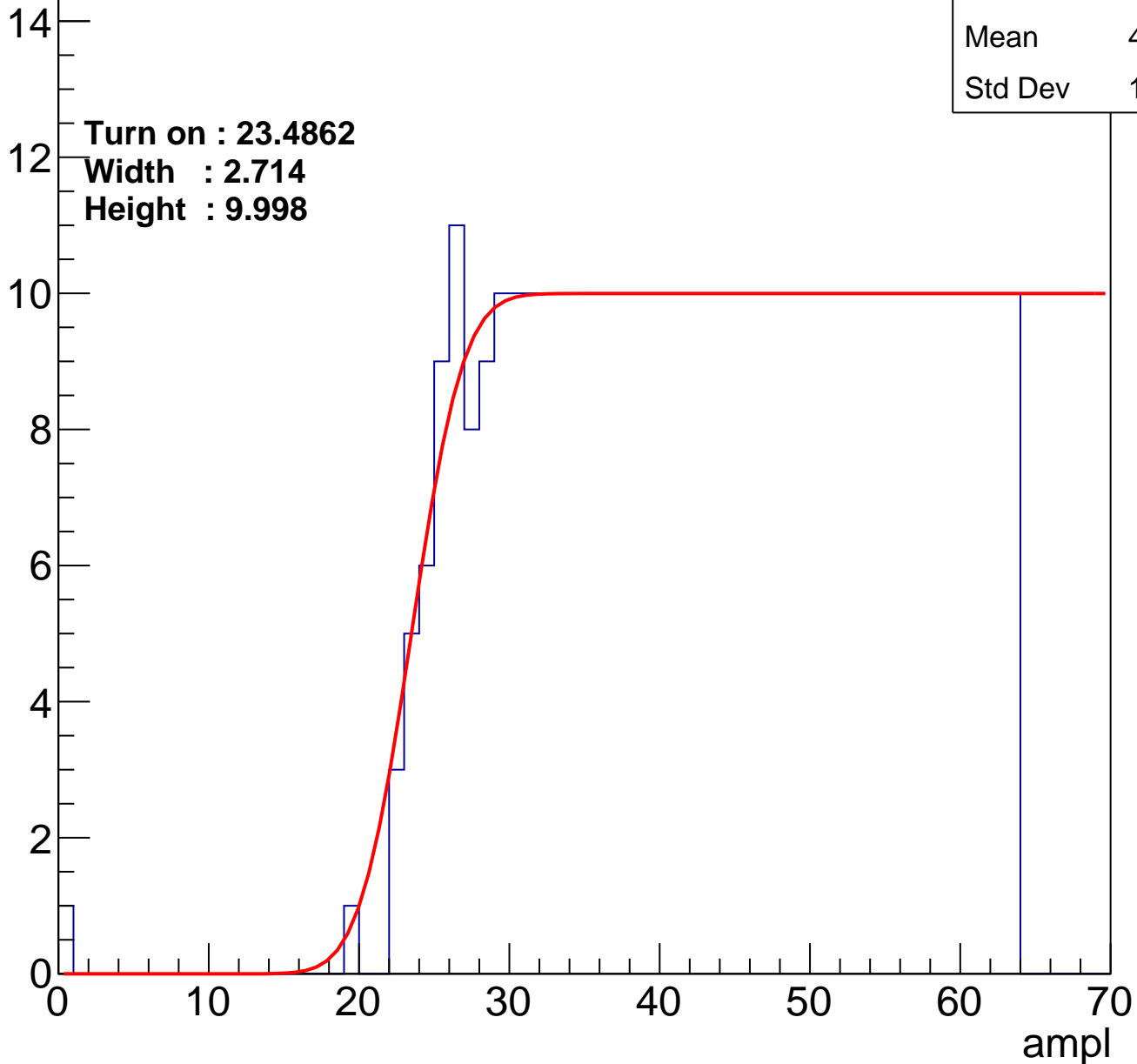
Entries	403
Mean	43.23
Std Dev	11.88

Turn on : 23.4862

Width : 2.714

Height : 9.998

Entry



B1L002S, U1-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.74
Std Dev	11.77

Turn on : 25.2519

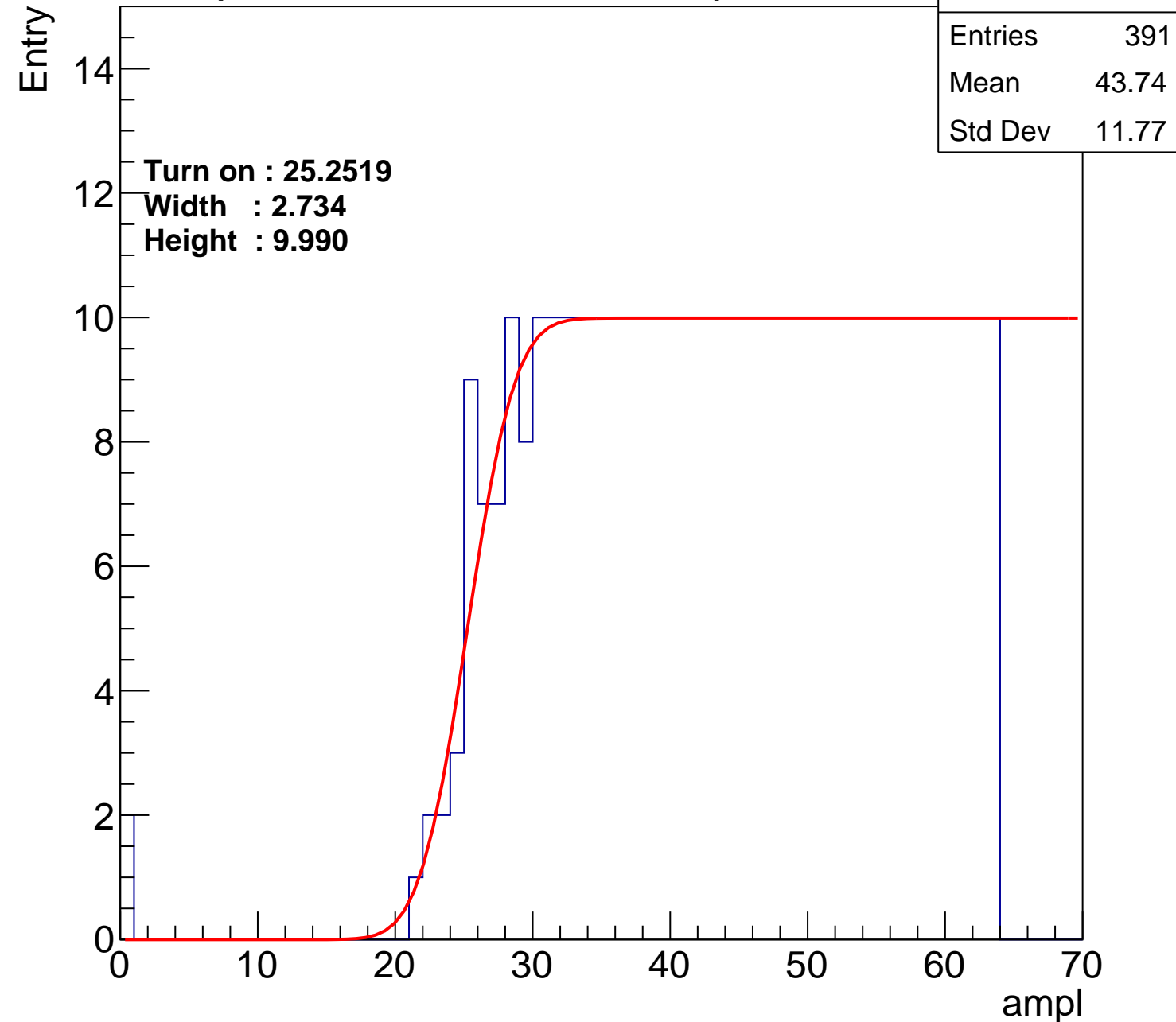
Width : 2.734

Height : 9.990

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.27
Std Dev	12.1

Turn on : 24.1126

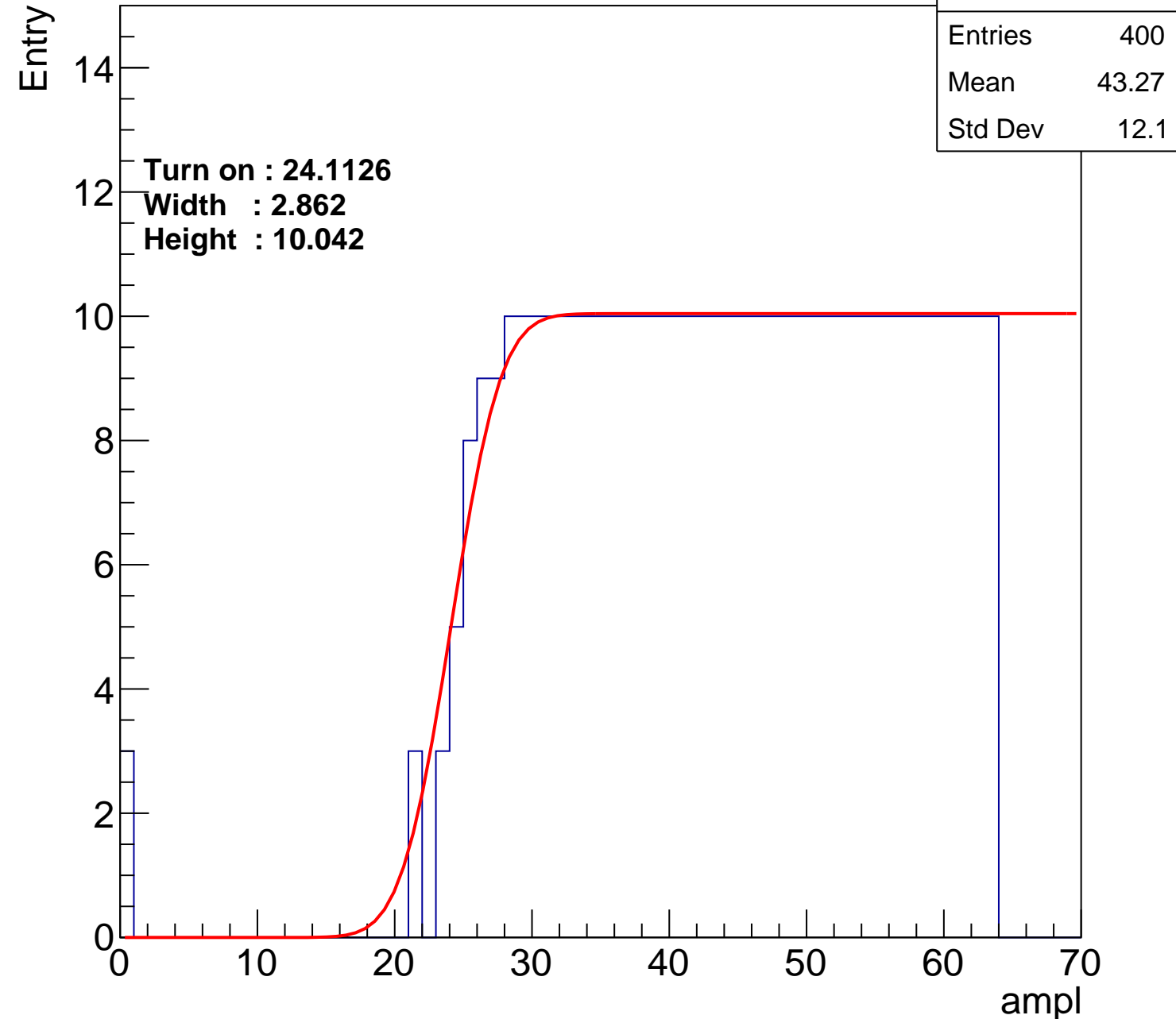
Width : 2.862

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.1
Std Dev	11.92

Turn on : 26.7649

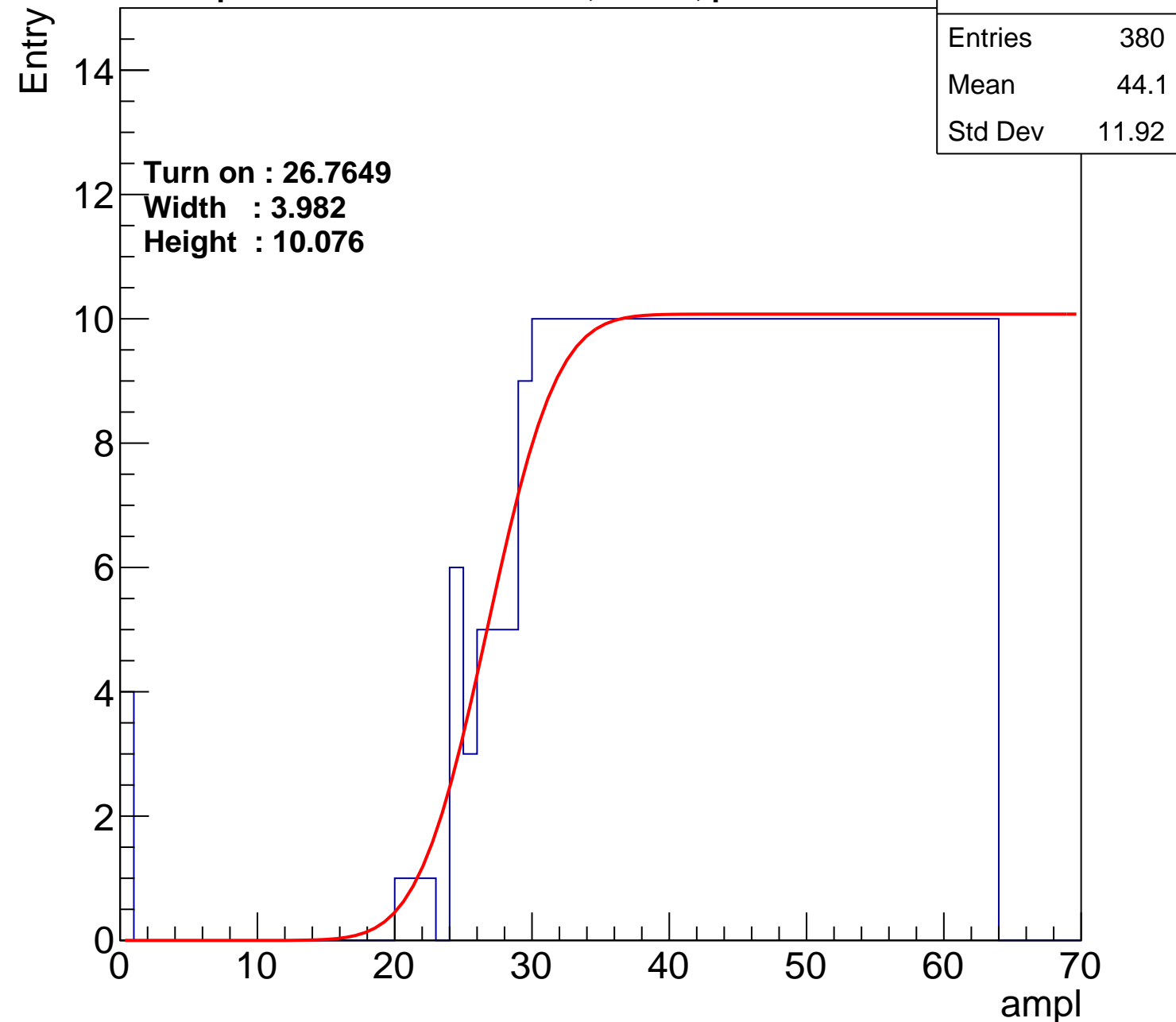
Width : 3.982

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.6
Std Dev	11.35

Turn on : 27.5625

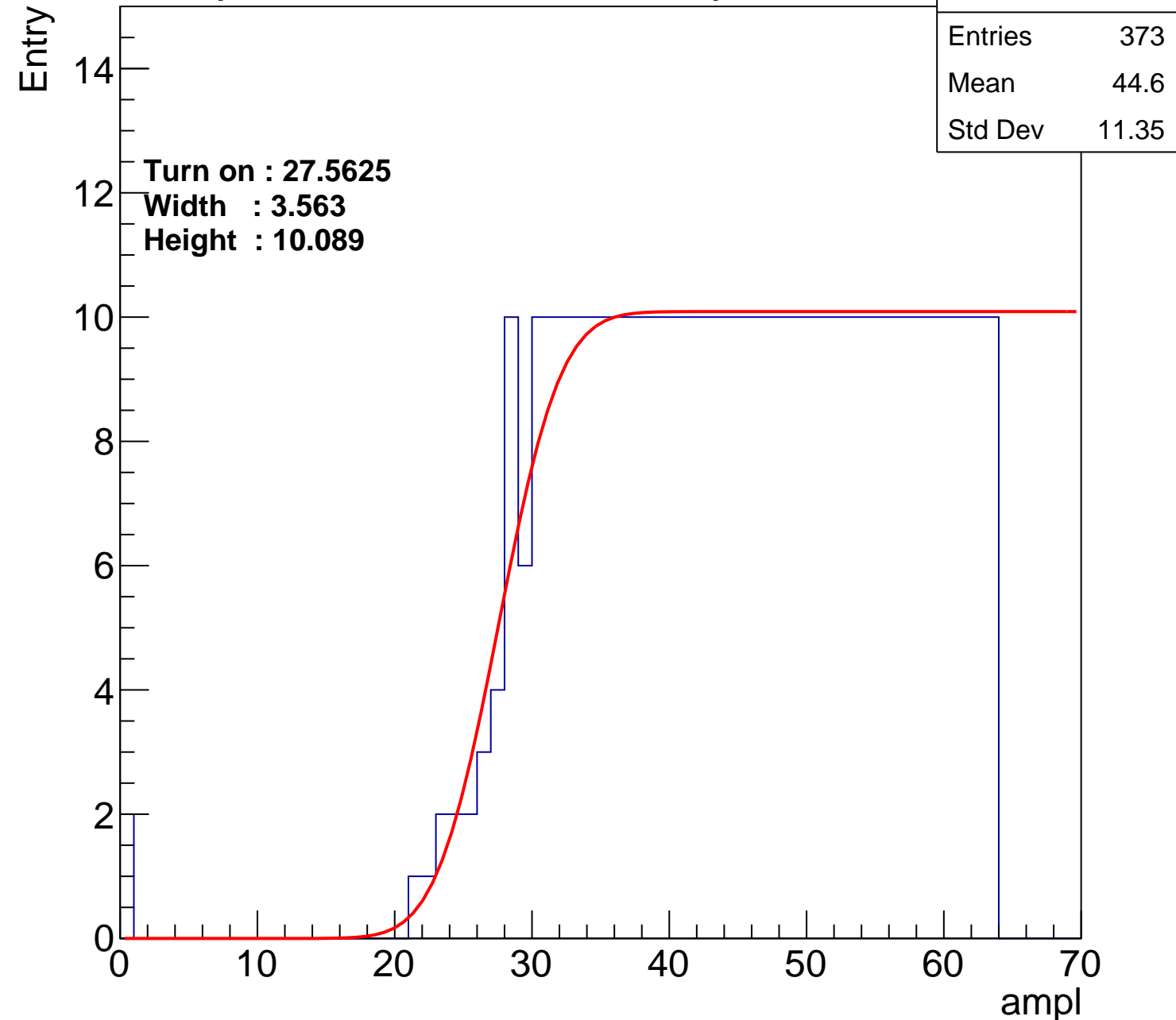
Width : 3.563

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.6
Std Dev	12.74

Turn on : 24.1253

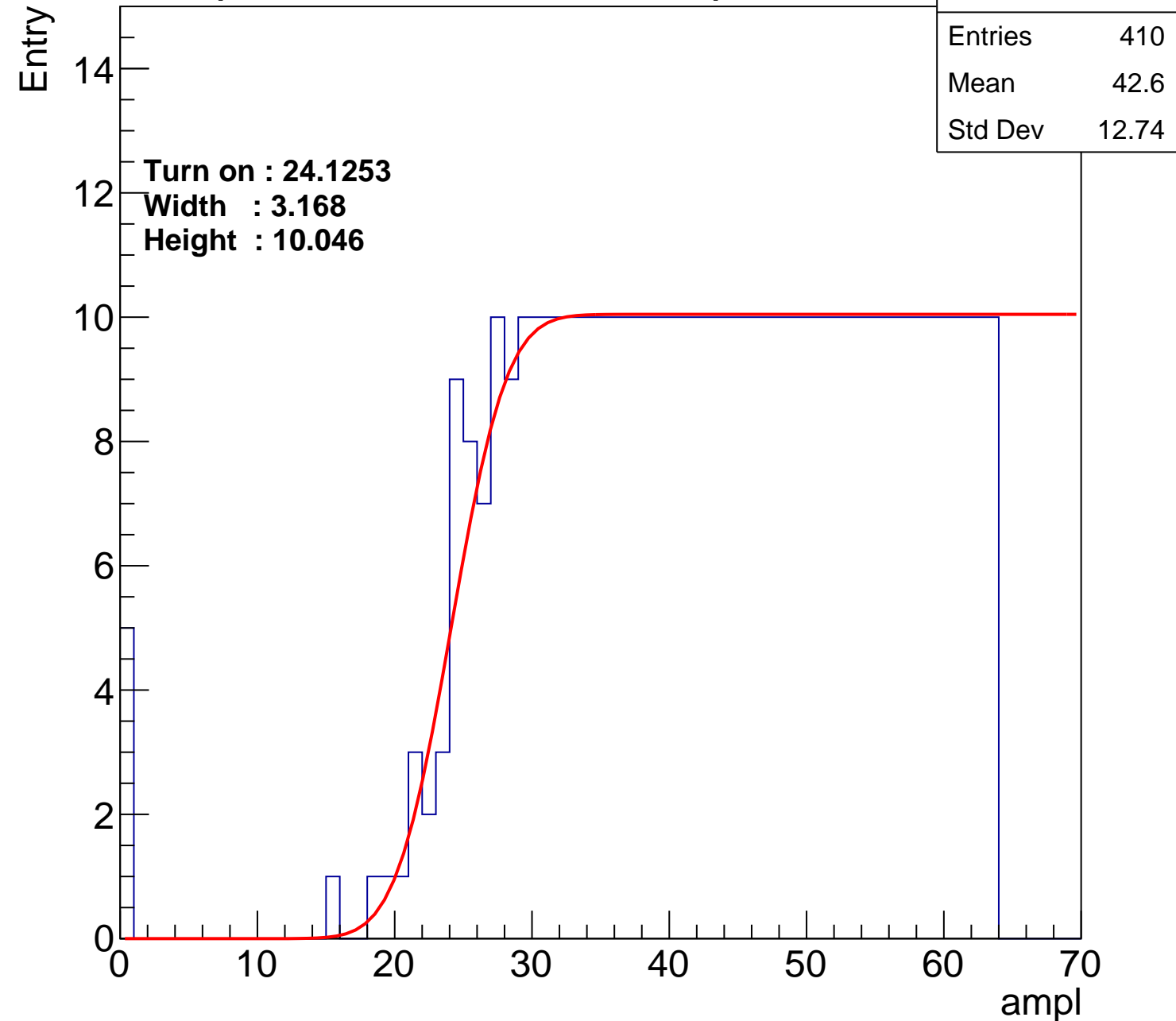
Width : 3.168

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.58
Std Dev	11.7

Turn on : 24.8681

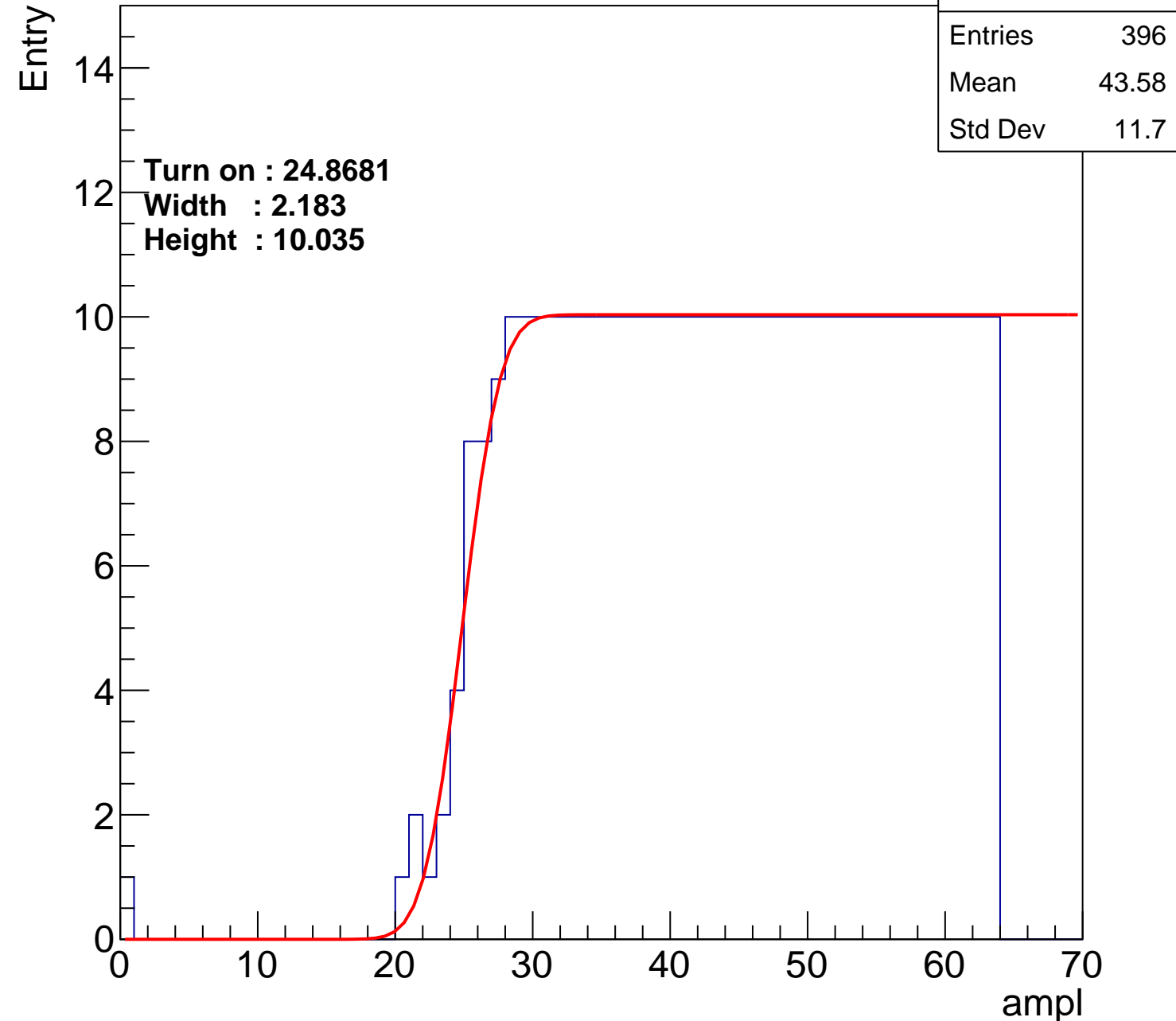
Width : 2.183

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.52
Std Dev	12.55

Turn on : 22.4365

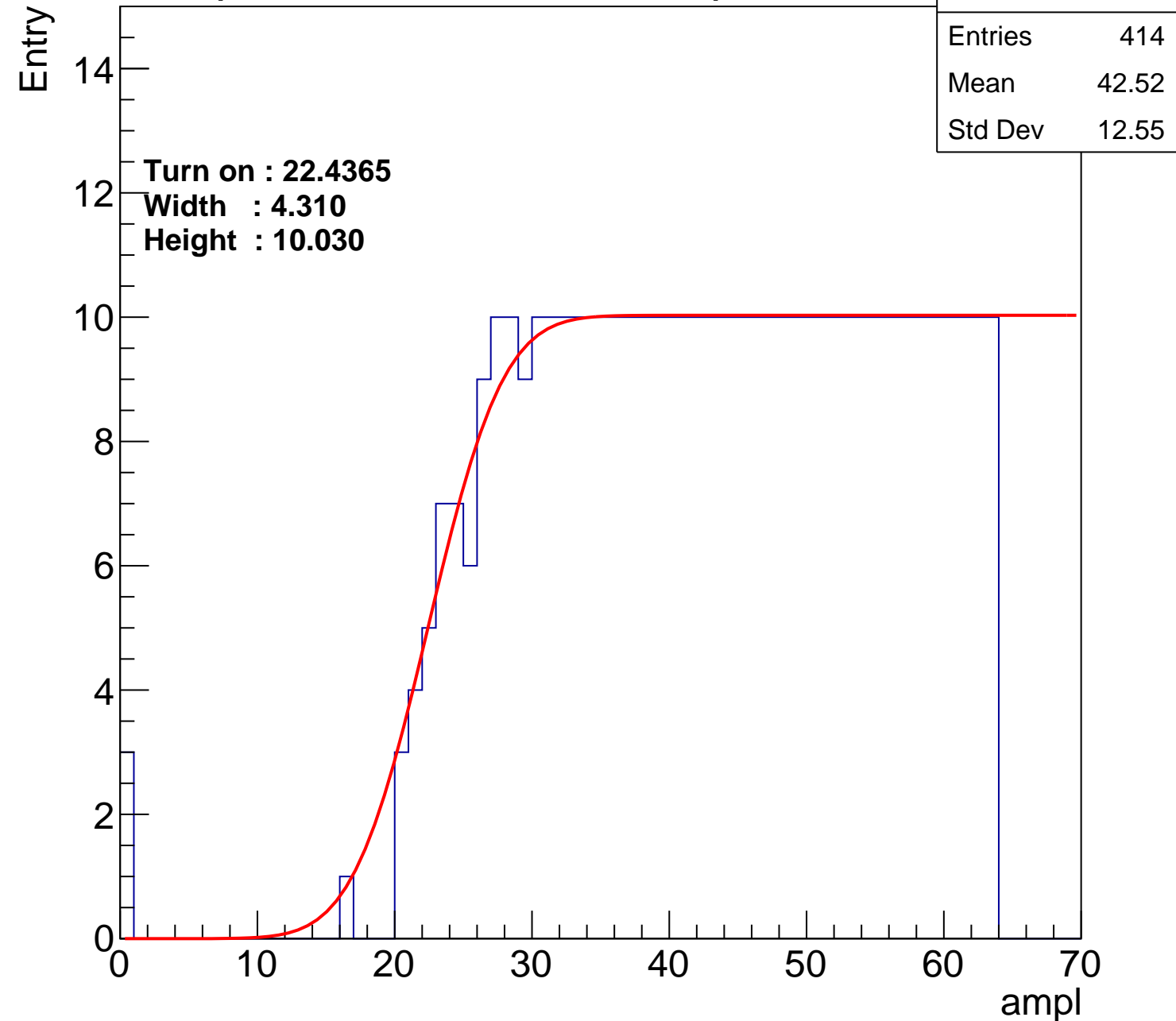
Width : 4.310

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43
Std Dev	12.38

Turn on : 23.6445

Width : 2.720

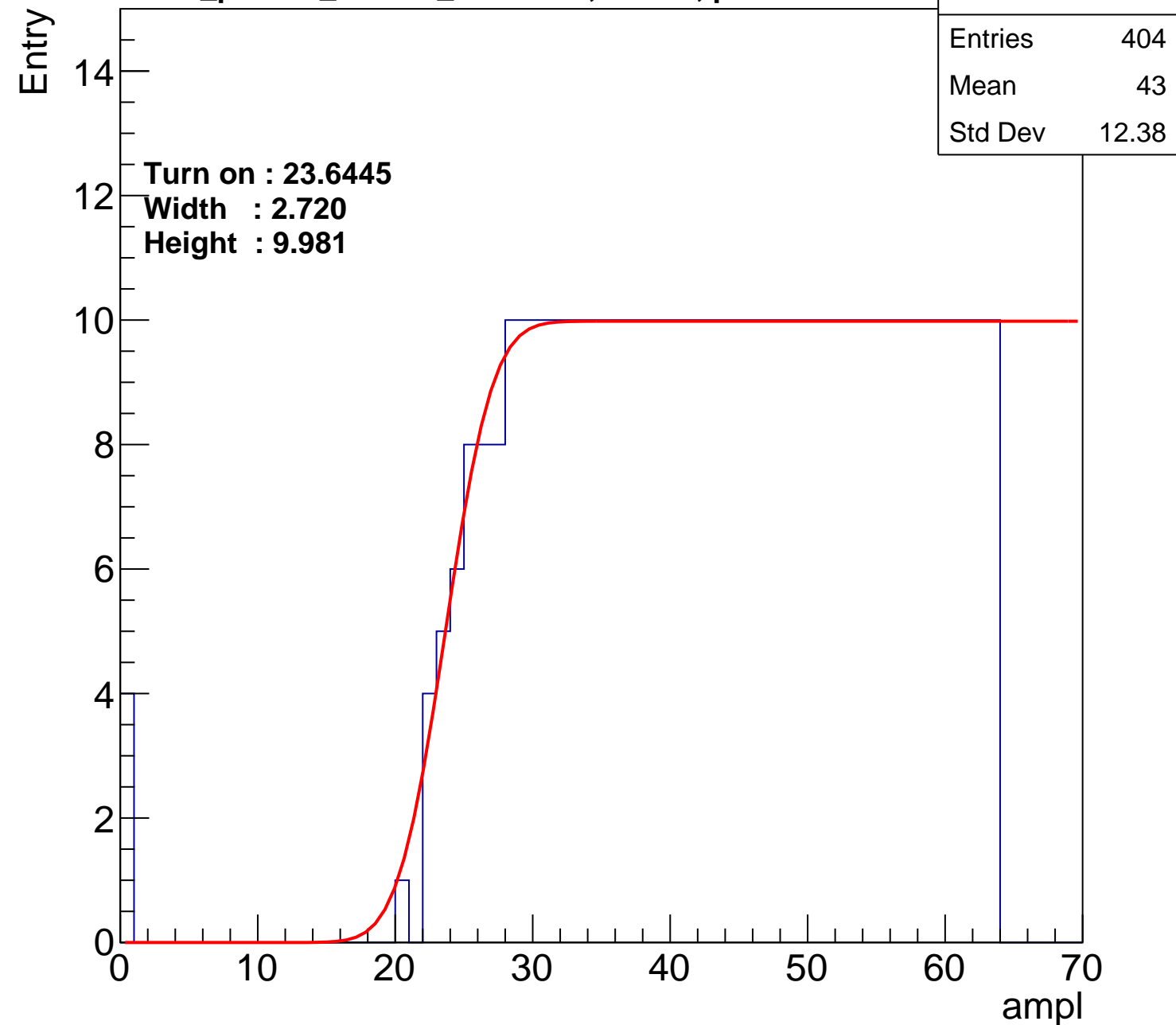
Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.53
Std Dev	12.12

Turn on : 24.7732

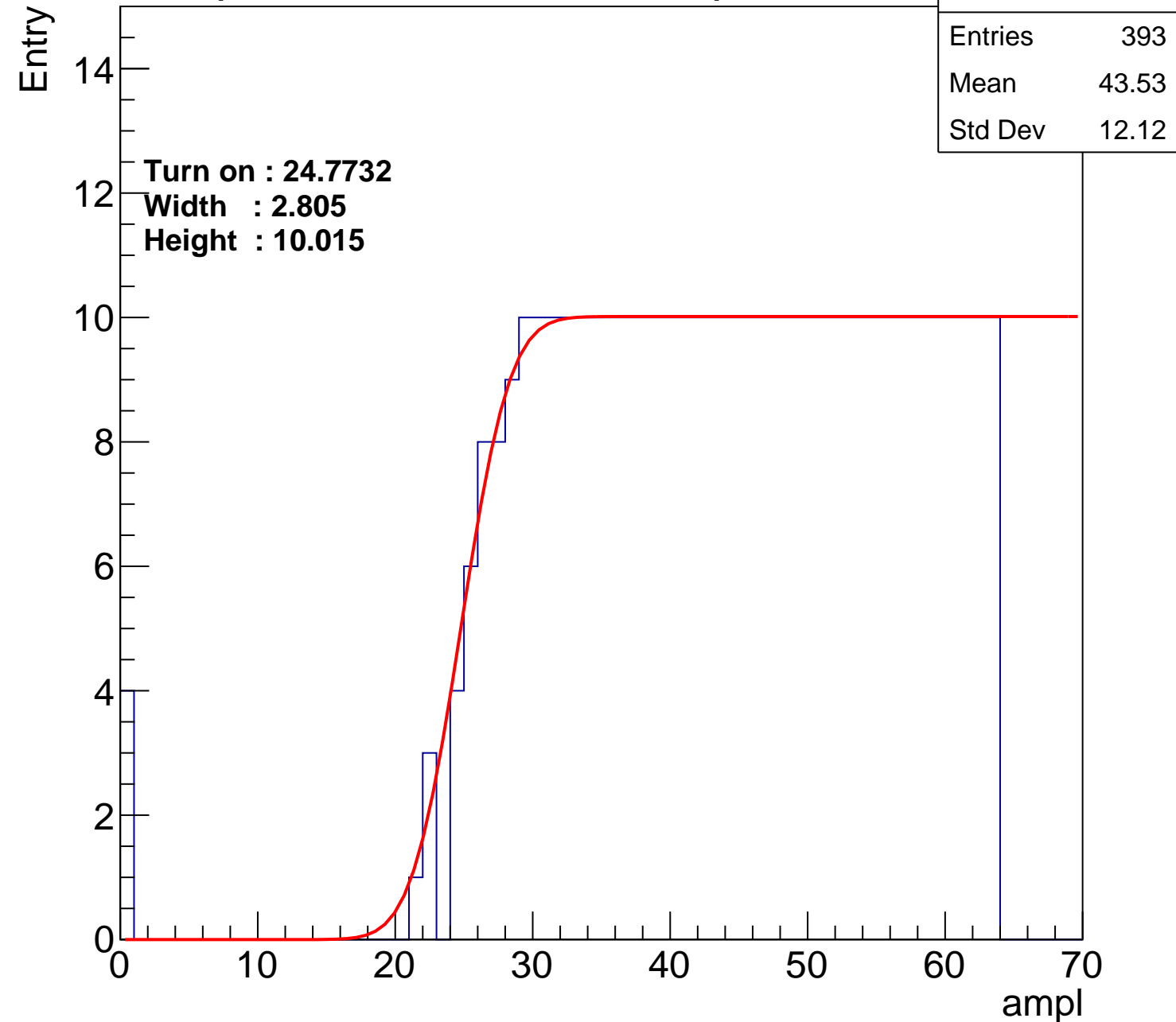
Width : 2.805

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.96
Std Dev	12.34

Turn on : 24.0364

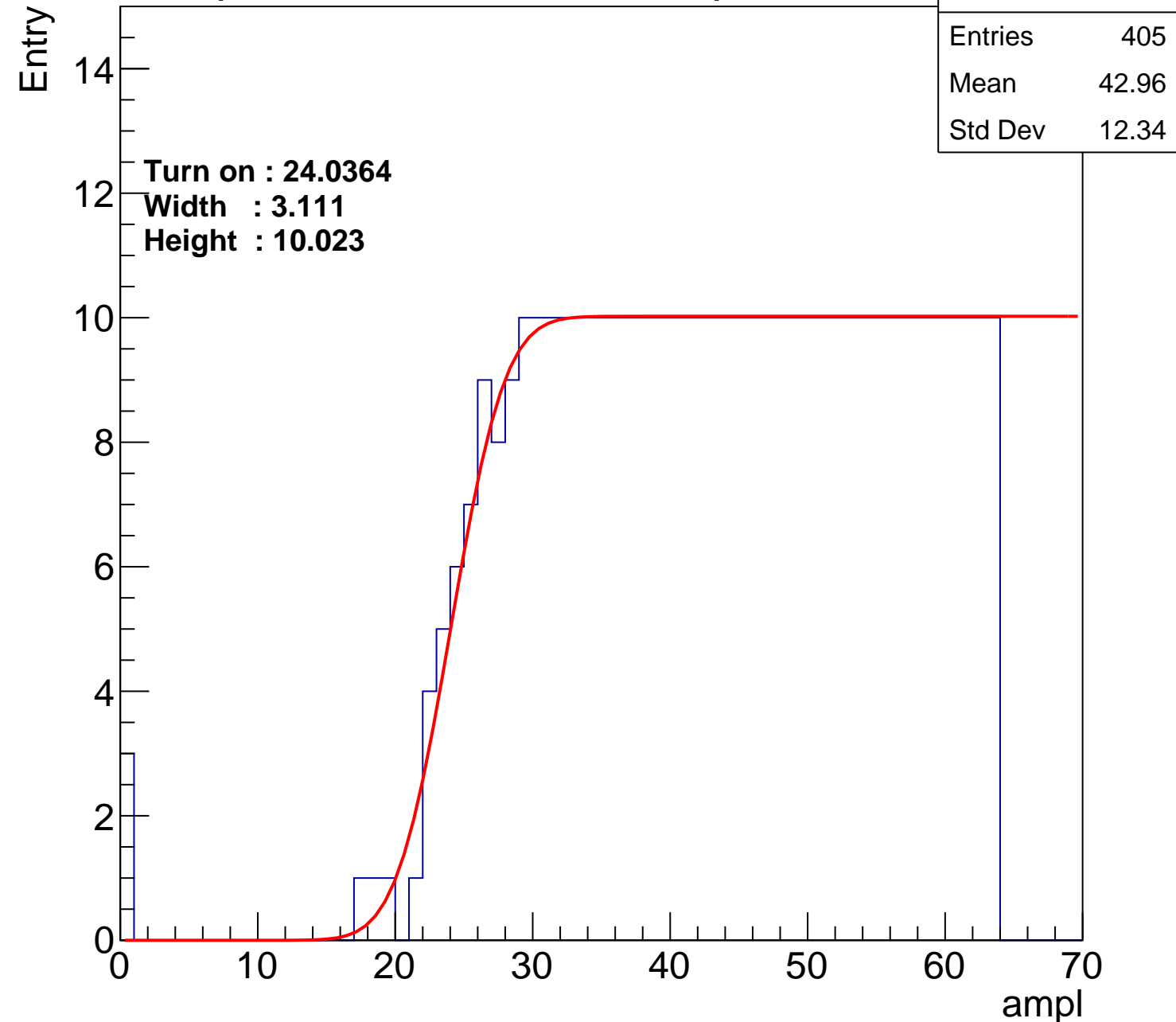
Width : 3.111

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.33
Std Dev	12.22

Turn on : 24.3852

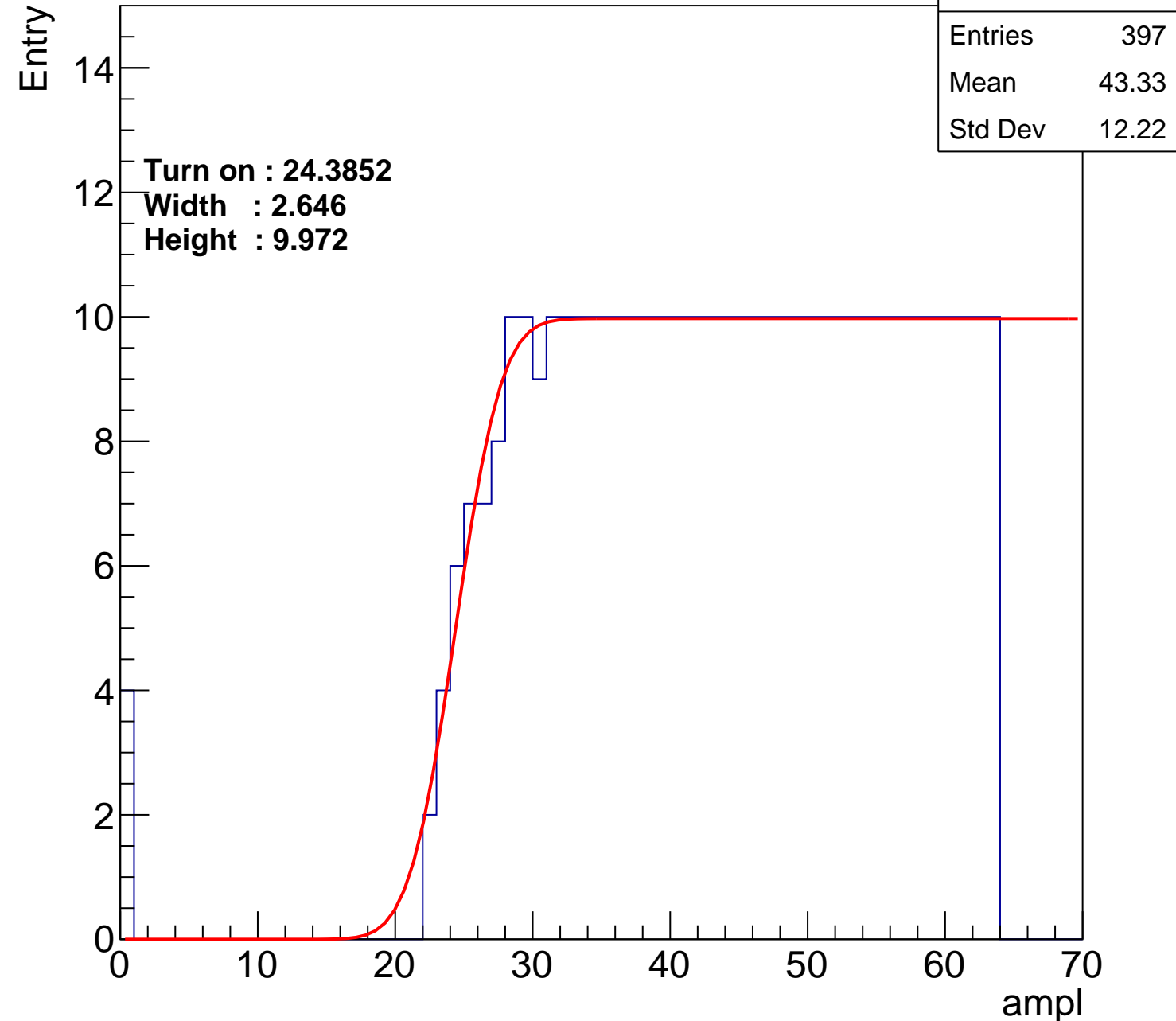
Width : 2.646

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.03
Std Dev	12.33

Turn on : 24.0428

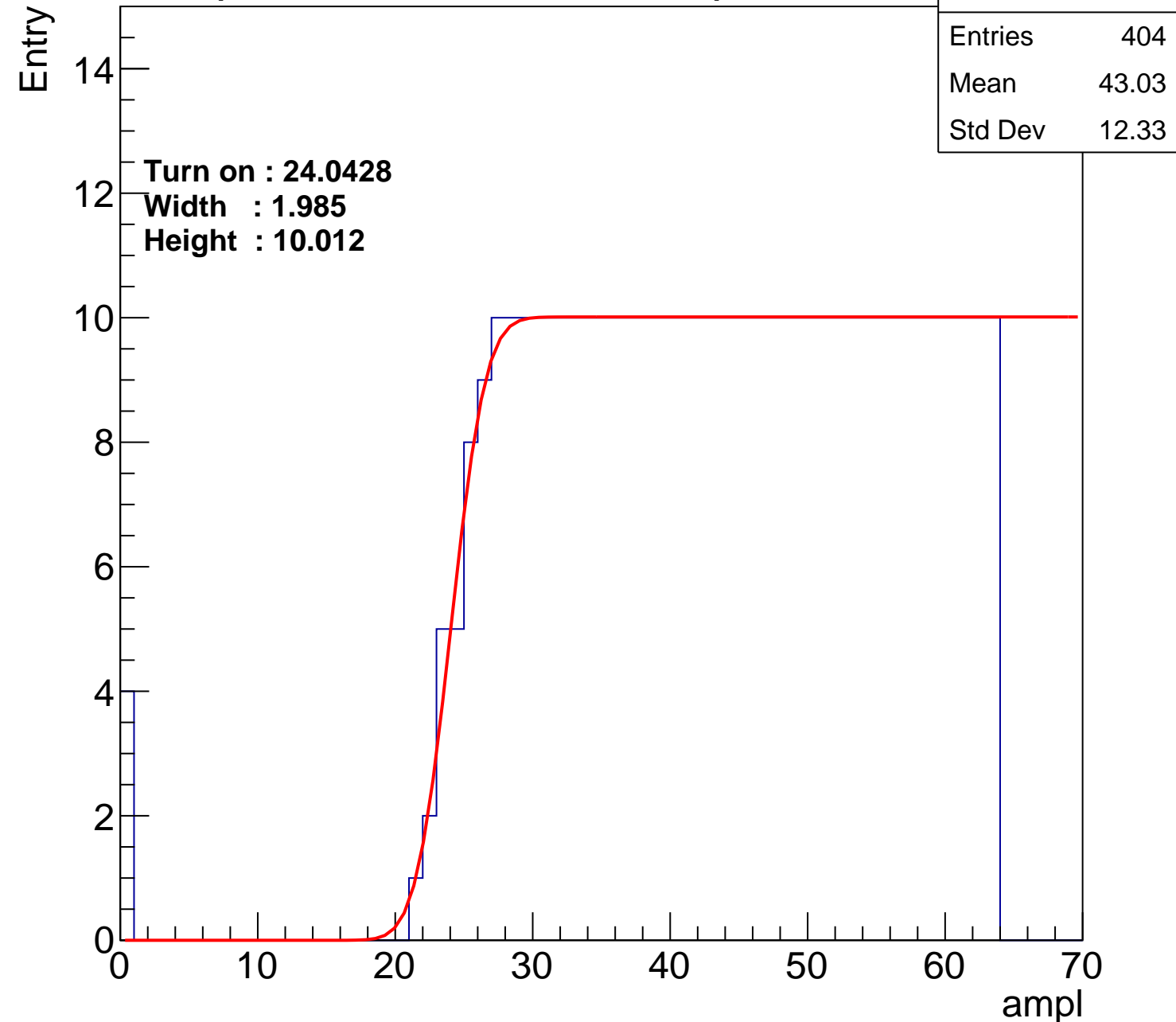
Width : 1.985

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.56
Std Dev	12.08

Turn on : 25.5546

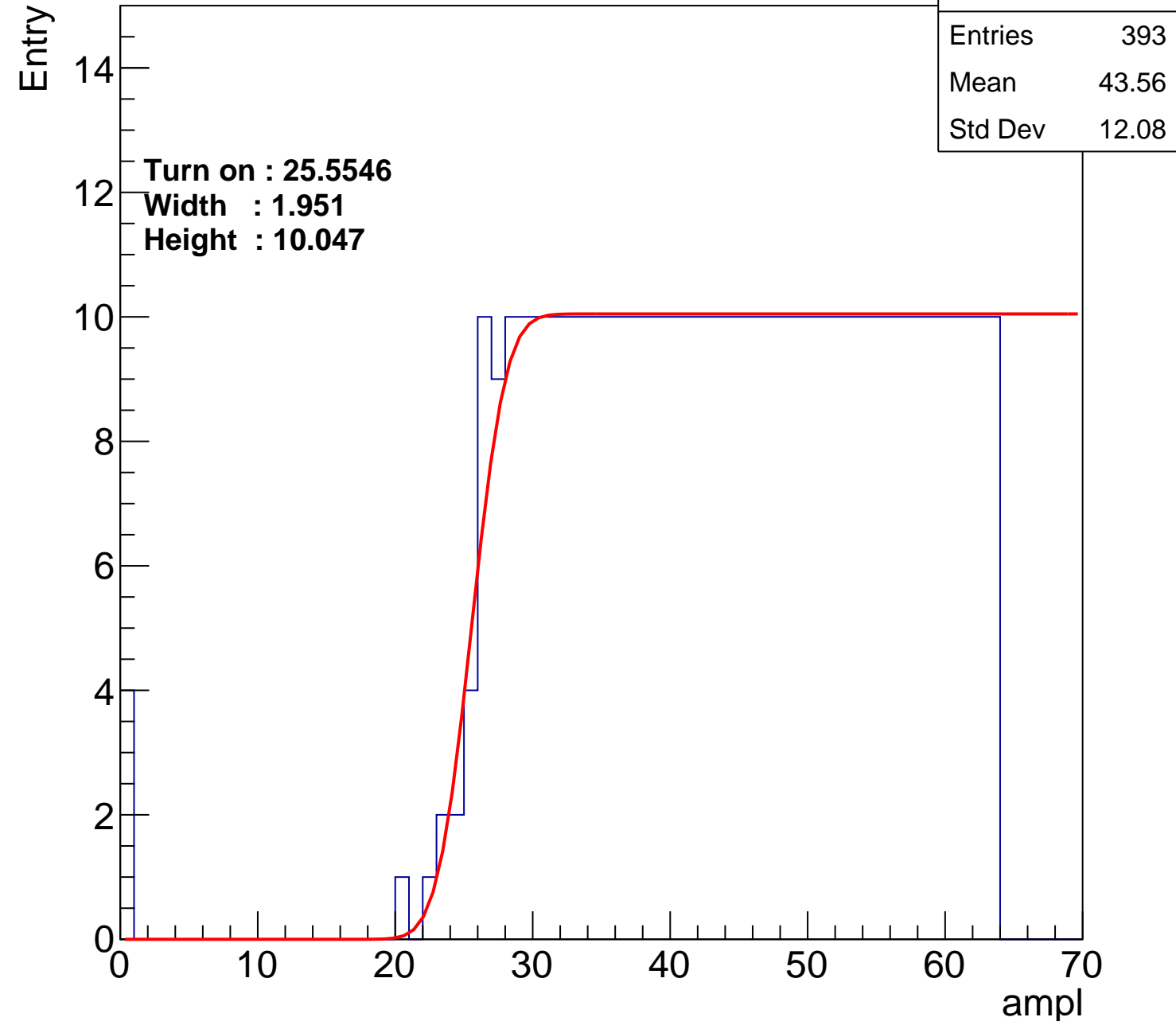
Width : 1.951

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.74
Std Dev	11.89

Turn on : 25.2593

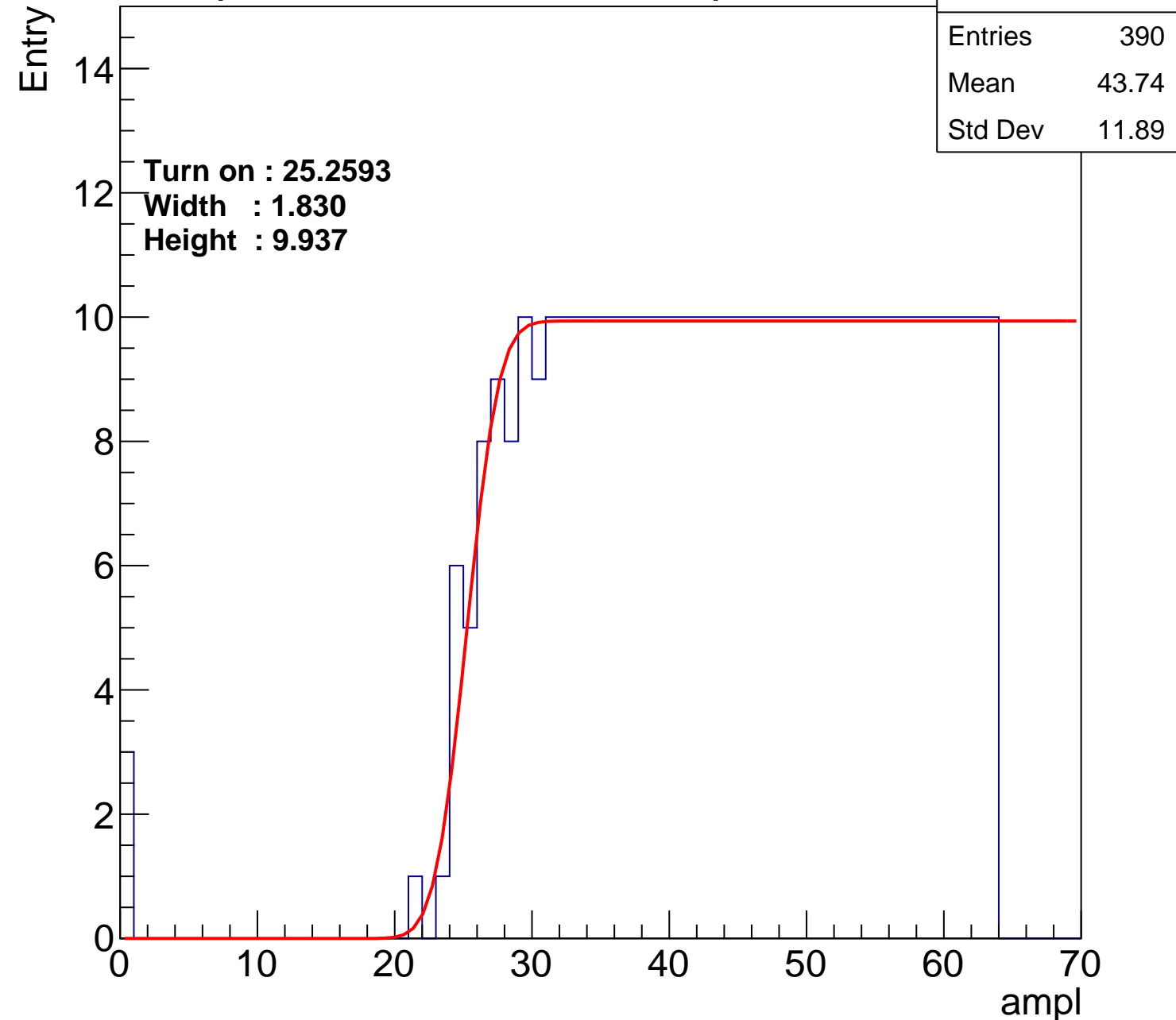
Width : 1.830

Height : 9.937

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch78

calib_packv5_042523_0143.root, FC#10, port B3

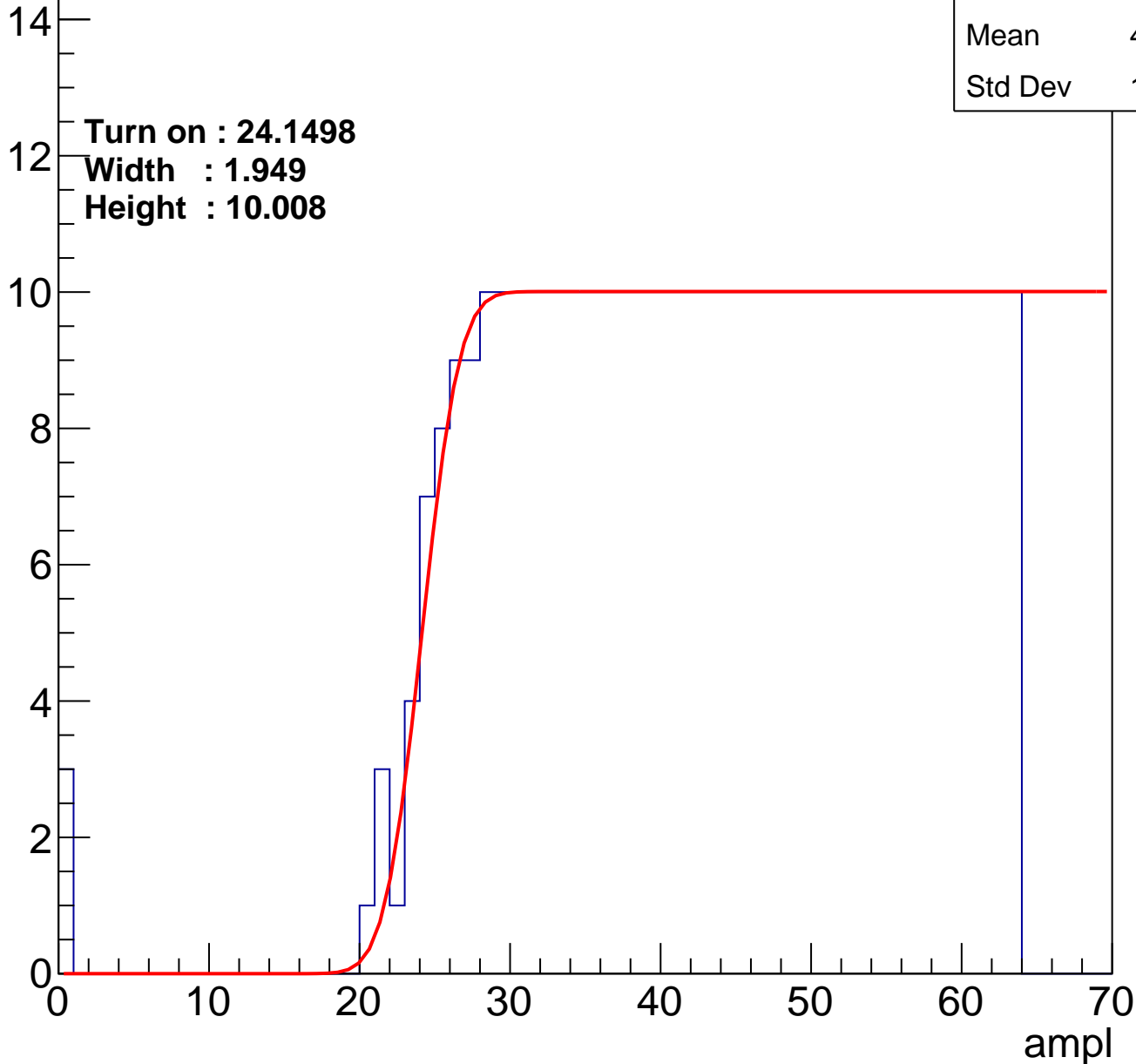
Entry

Entries	405
Mean	43.02
Std Dev	12.24

Turn on : 24.1498

Width : 1.949

Height : 10.008



B1L002S, U1-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.44
Std Dev	12.22

Turn on : 25.6185

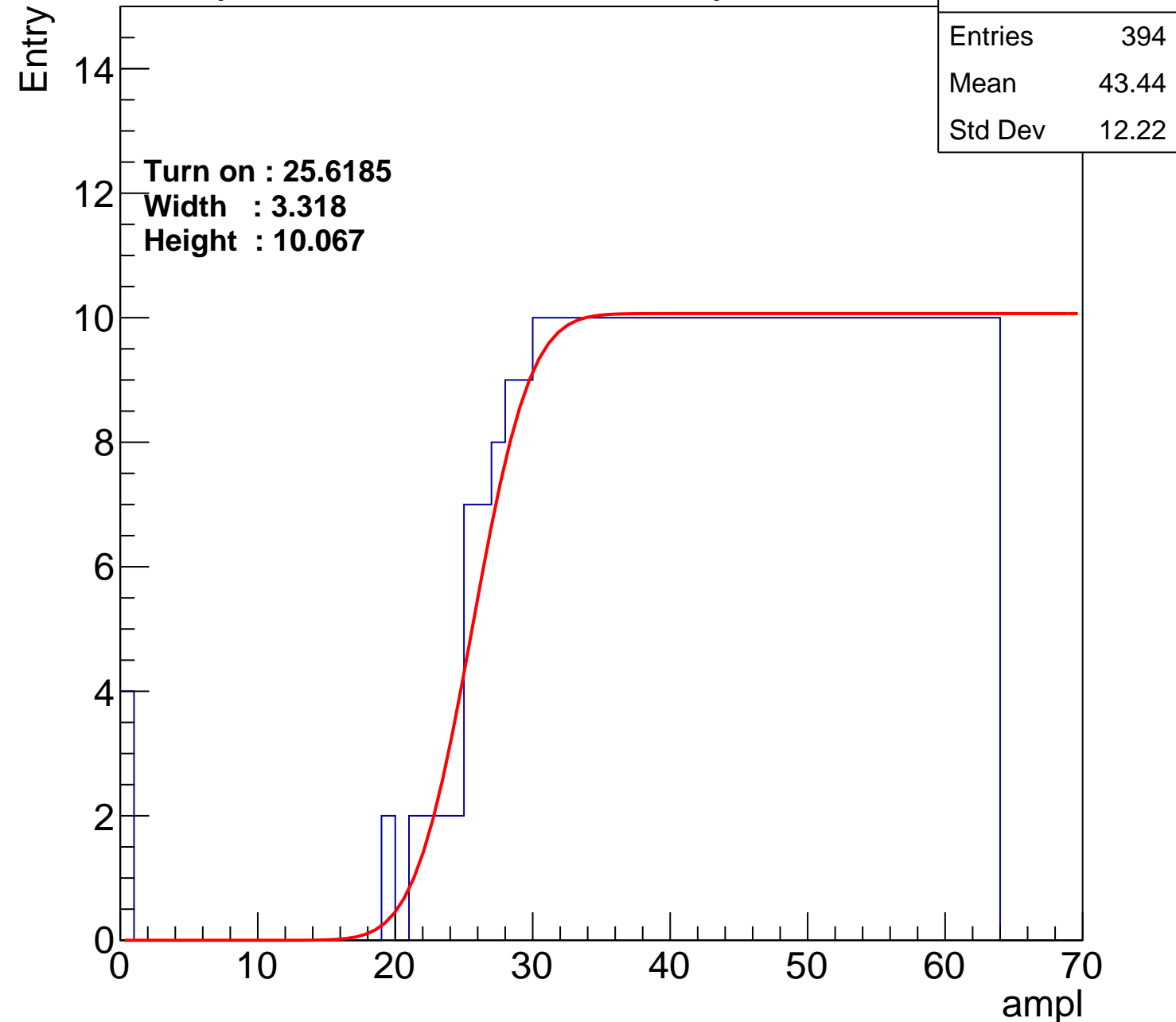
Width : 3.318

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch80

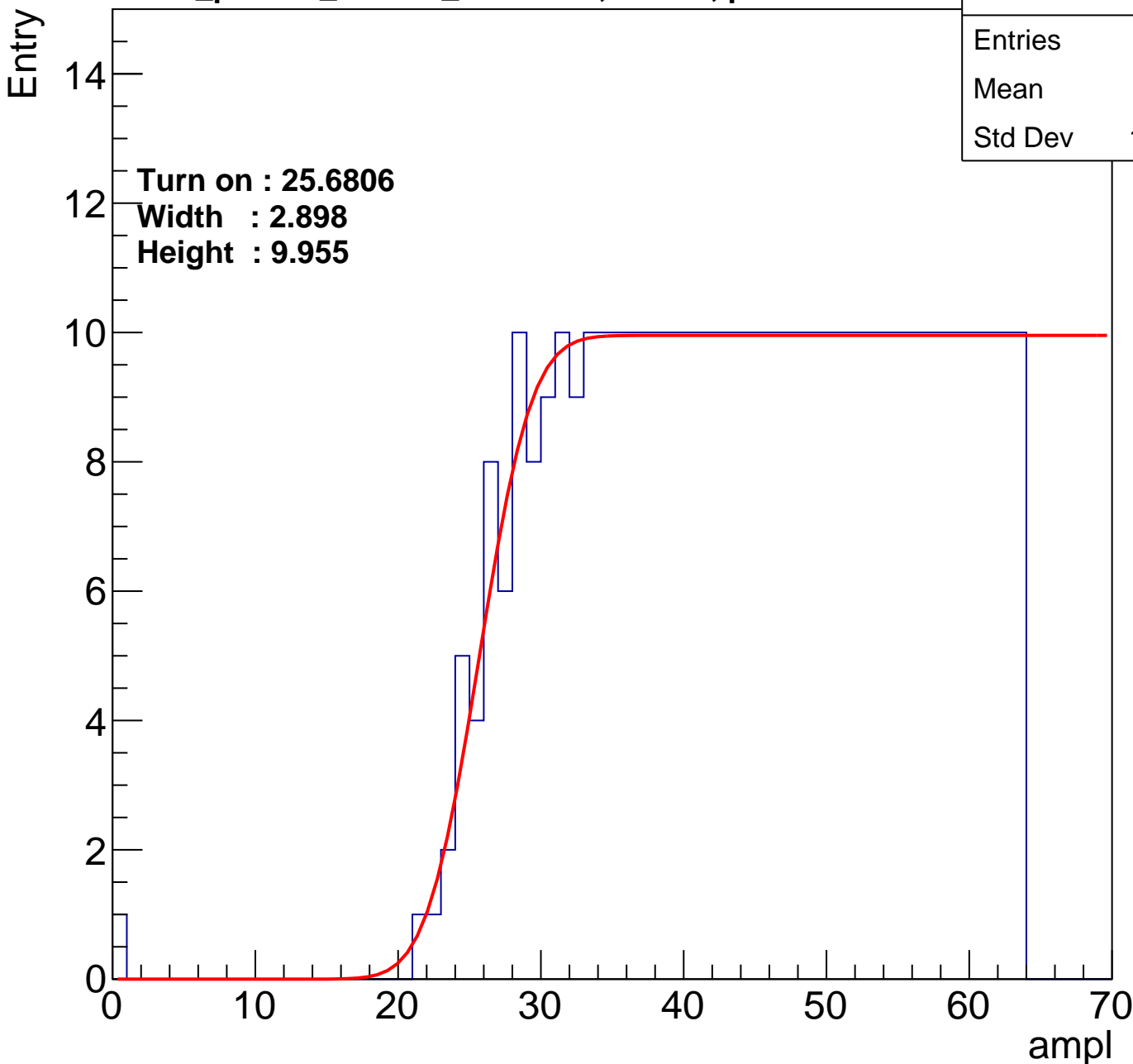
calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.11
Std Dev	11.46

Turn on : 25.6806

Width : 2.898

Height : 9.955



B1L002S, U1-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.64
Std Dev	11.83

Turn on : 25.1433

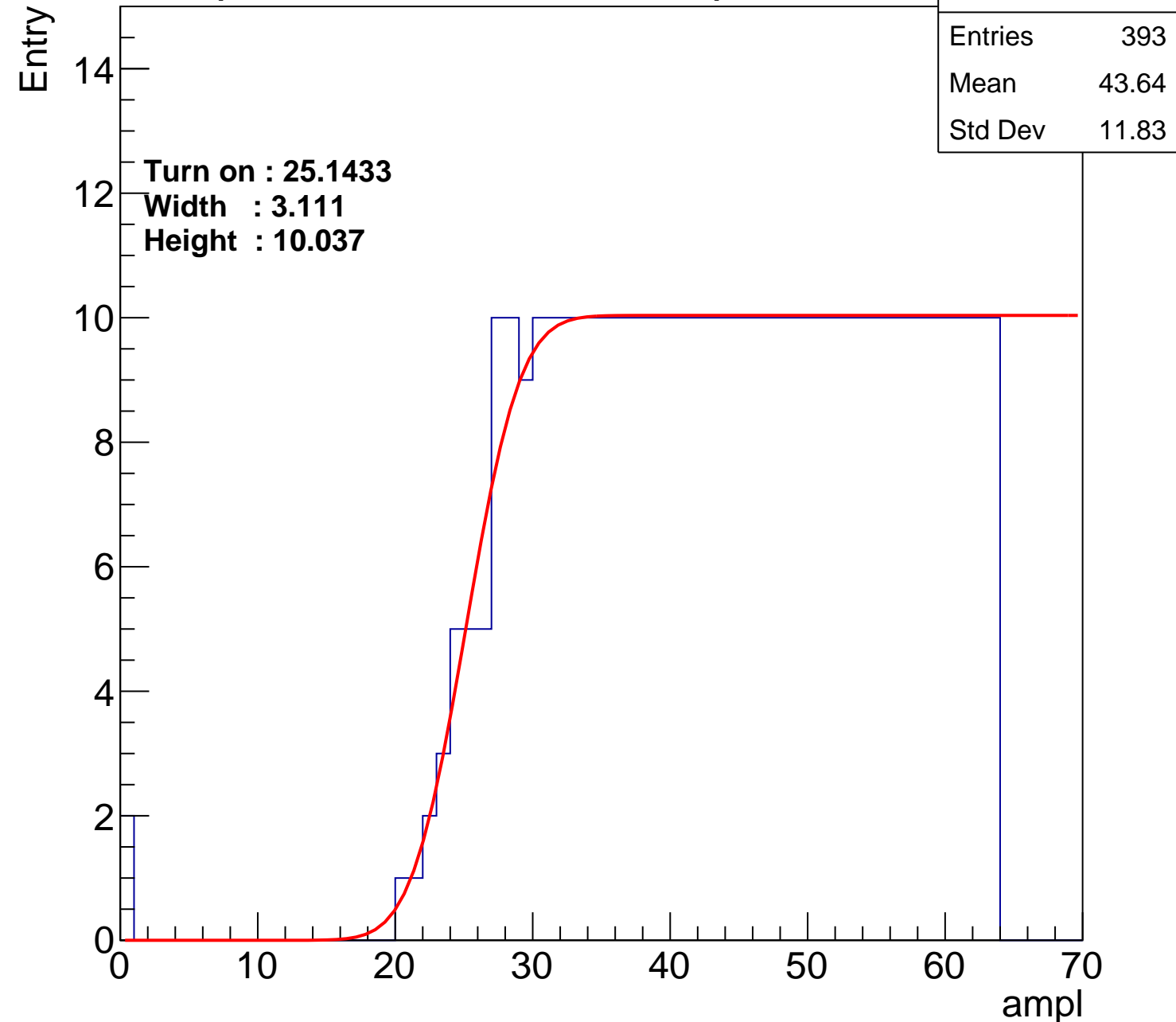
Width : 3.111

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.05
Std Dev	11.71

Turn on : 25.9869

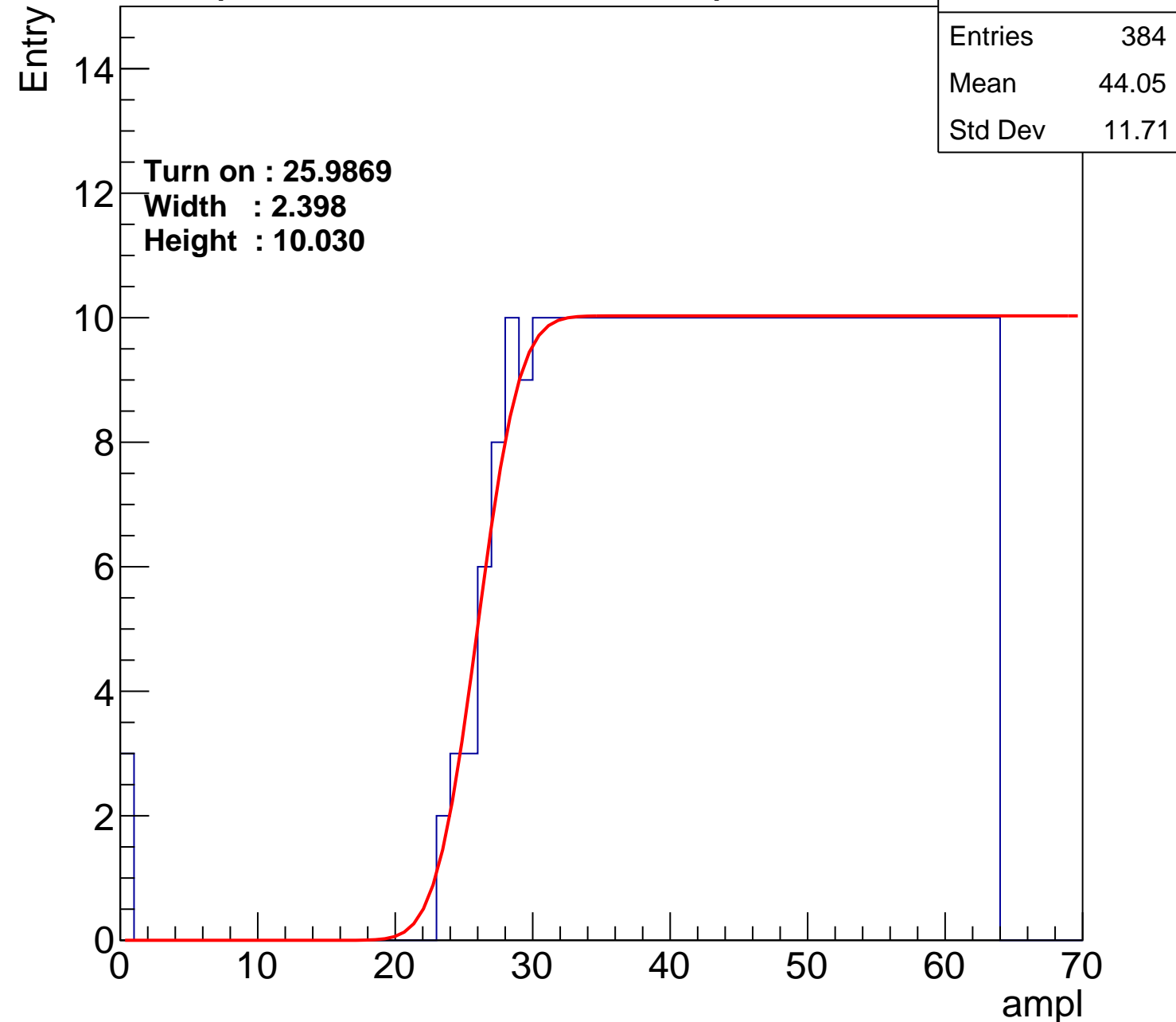
Width : 2.398

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	409
Mean	42.9
Std Dev	12.11

Turn on : 23.5685

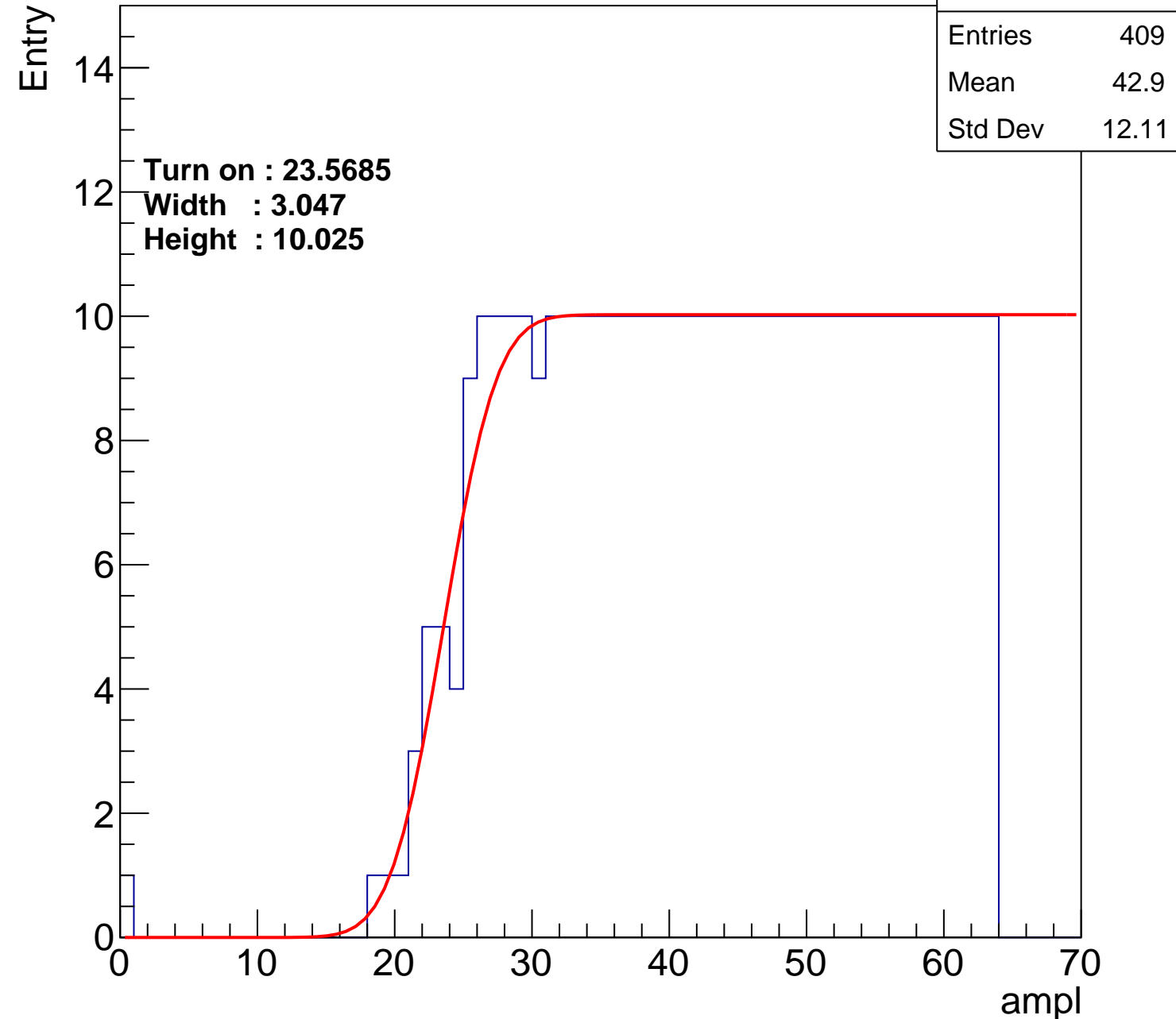
Width : 3.047

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.83
Std Dev	12.64

Turn on : 24.7728

Width : 3.962

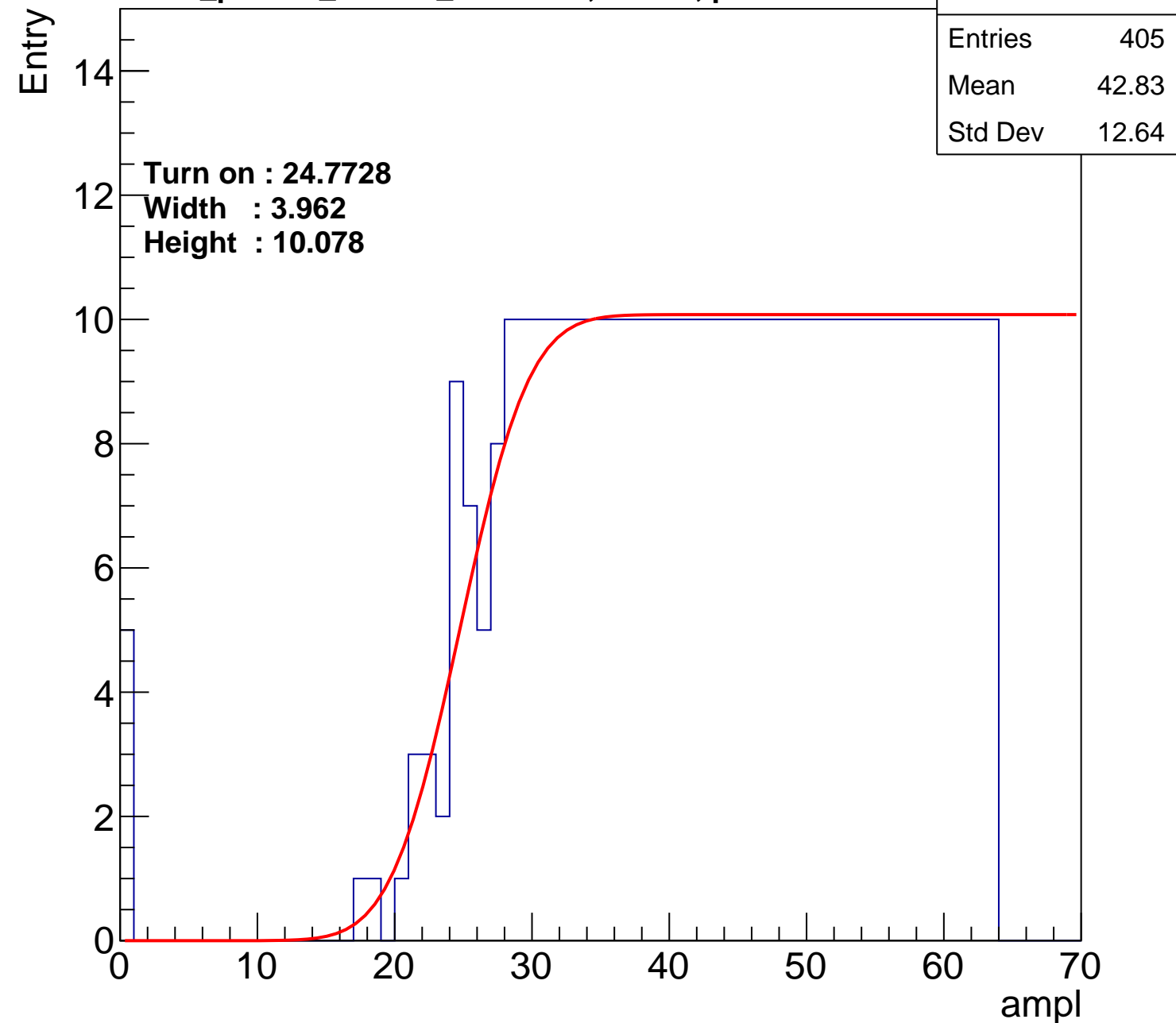
Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.32
Std Dev	11.61

Turn on : 26.8363

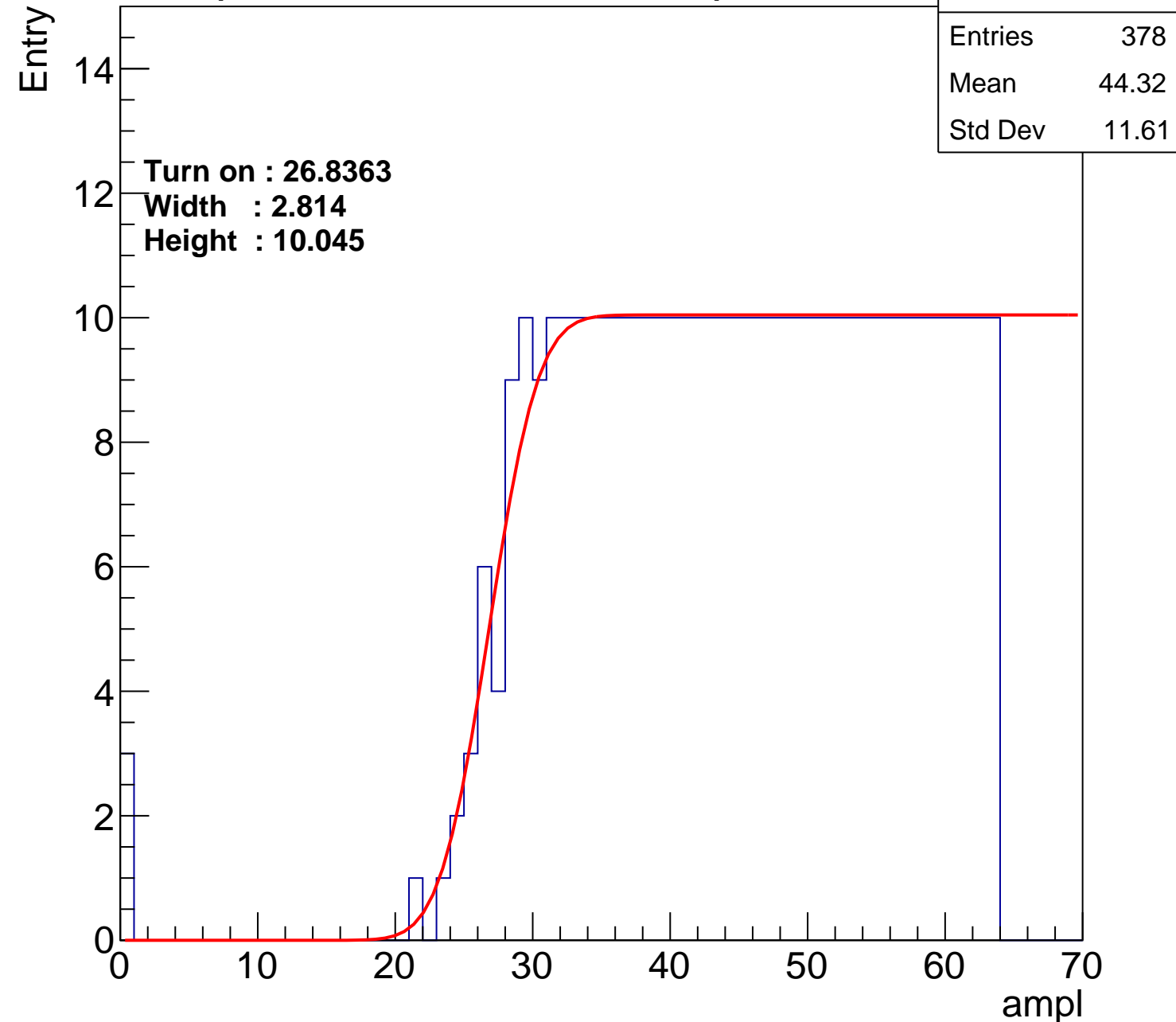
Width : 2.814

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.22
Std Dev	12.29

Turn on : 24.5817

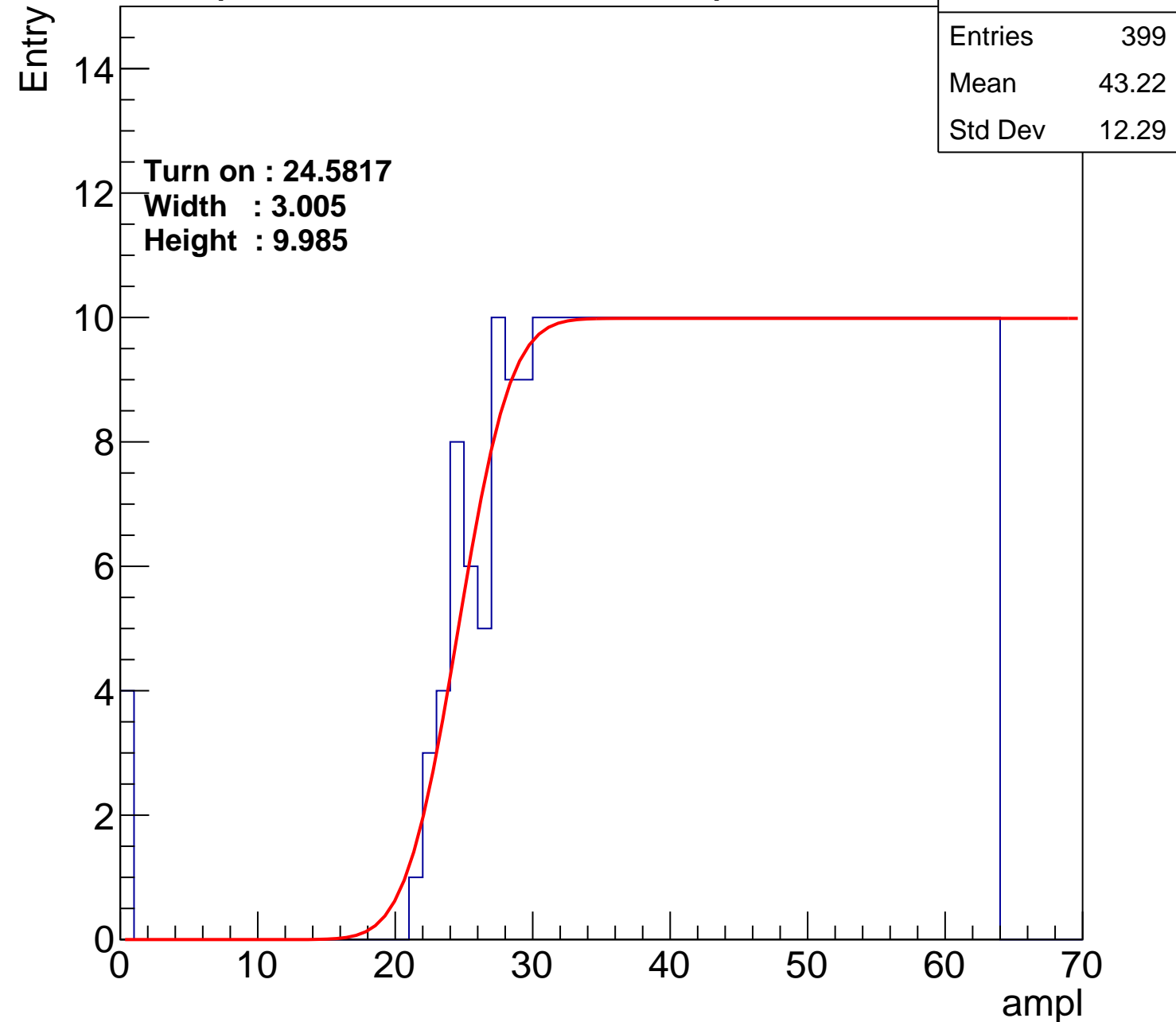
Width : 3.005

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch87

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.1264

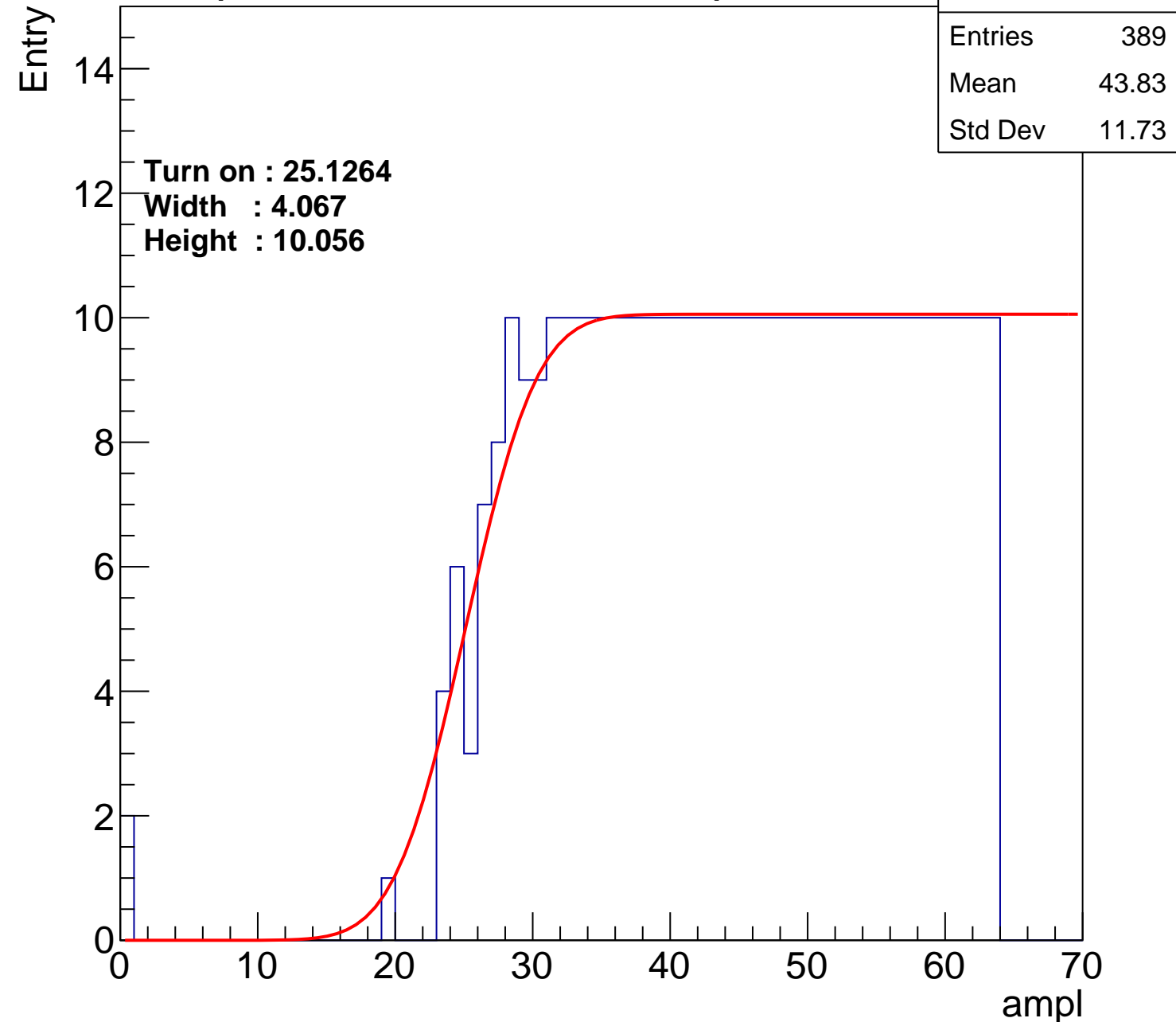
Width : 4.067

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	42.99
Std Dev	12.29

Turn on : 24.0108

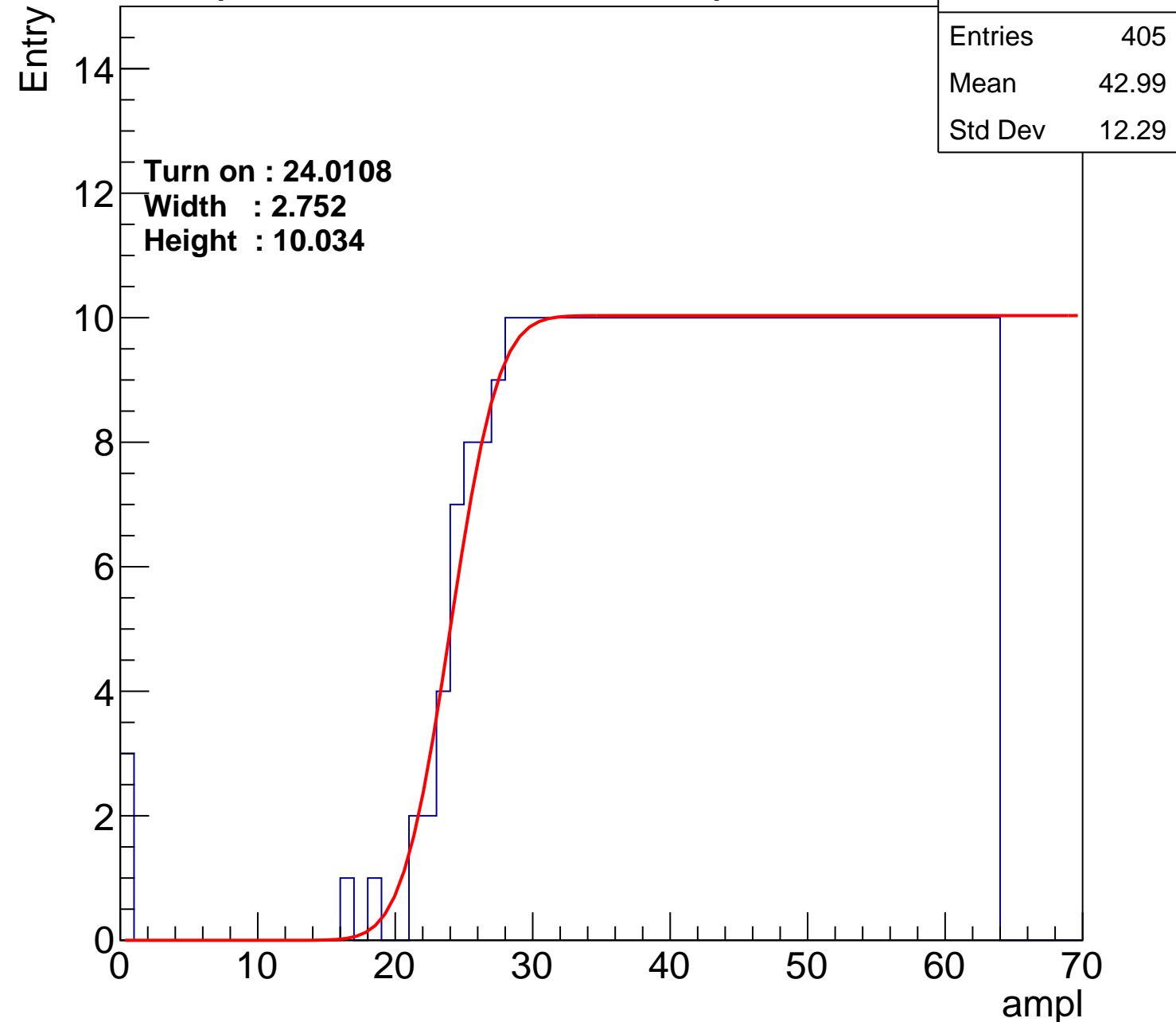
Width : 2.752

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch89

calib_packv5_042523_0143.root, FC#10, port B3

Entries	364
Mean	44.98
Std Dev	11.31

Turn on : 28.3028

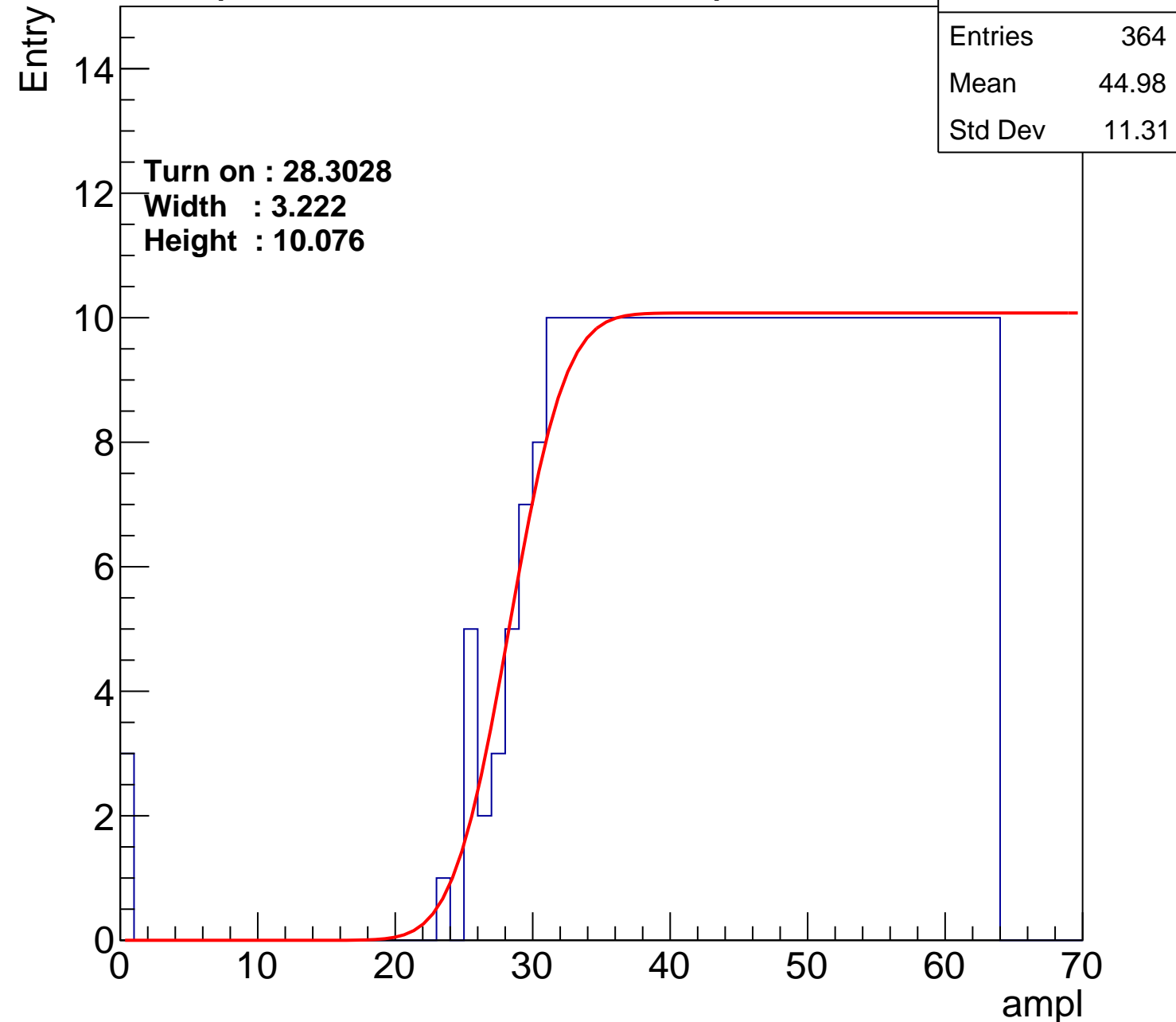
Width : 3.222

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch90

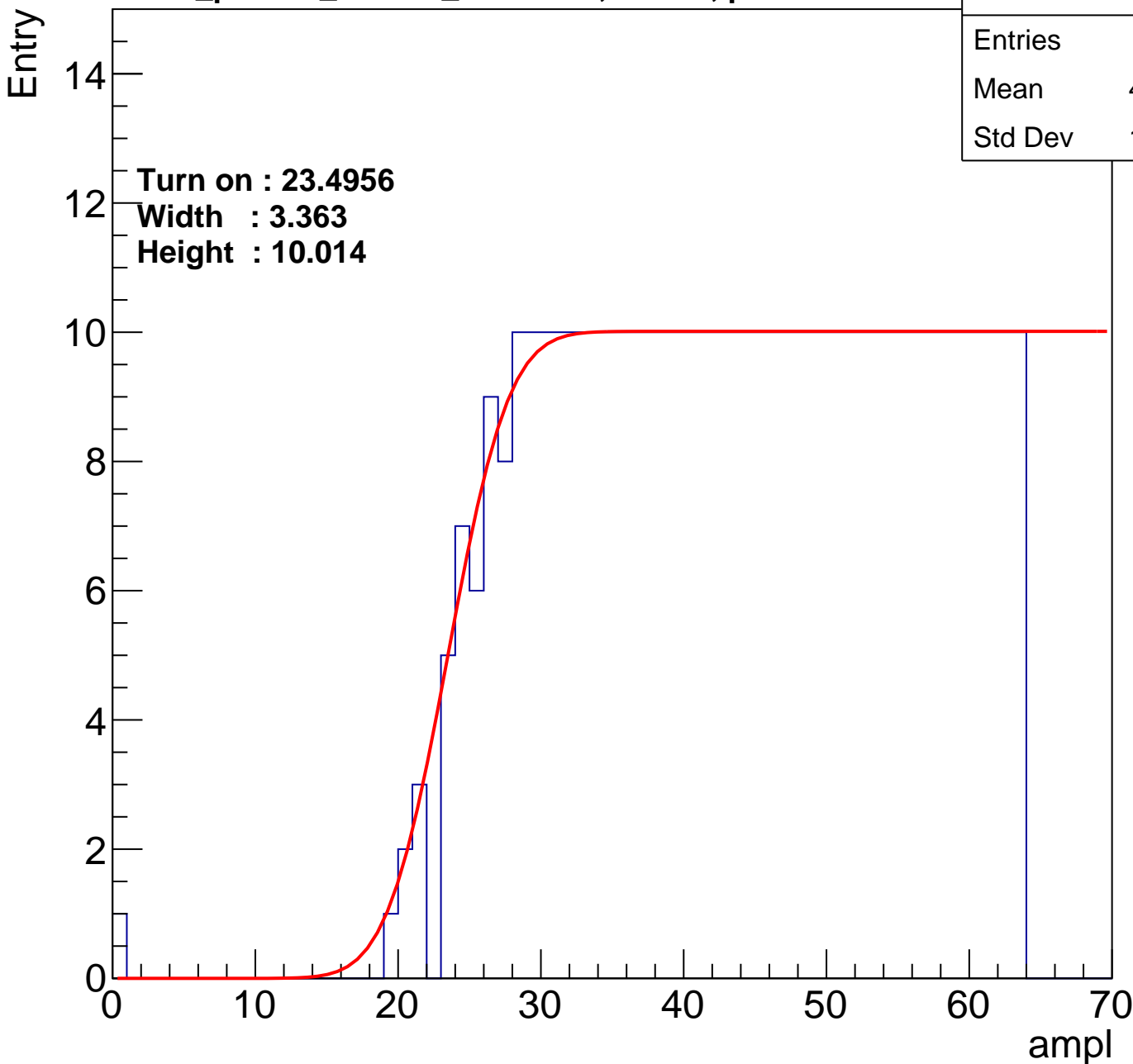
calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.25
Std Dev	11.92

Turn on : 23.4956

Width : 3.363

Height : 10.014



B1L002S, U1-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.96
Std Dev	11.51

Turn on : 25.1832

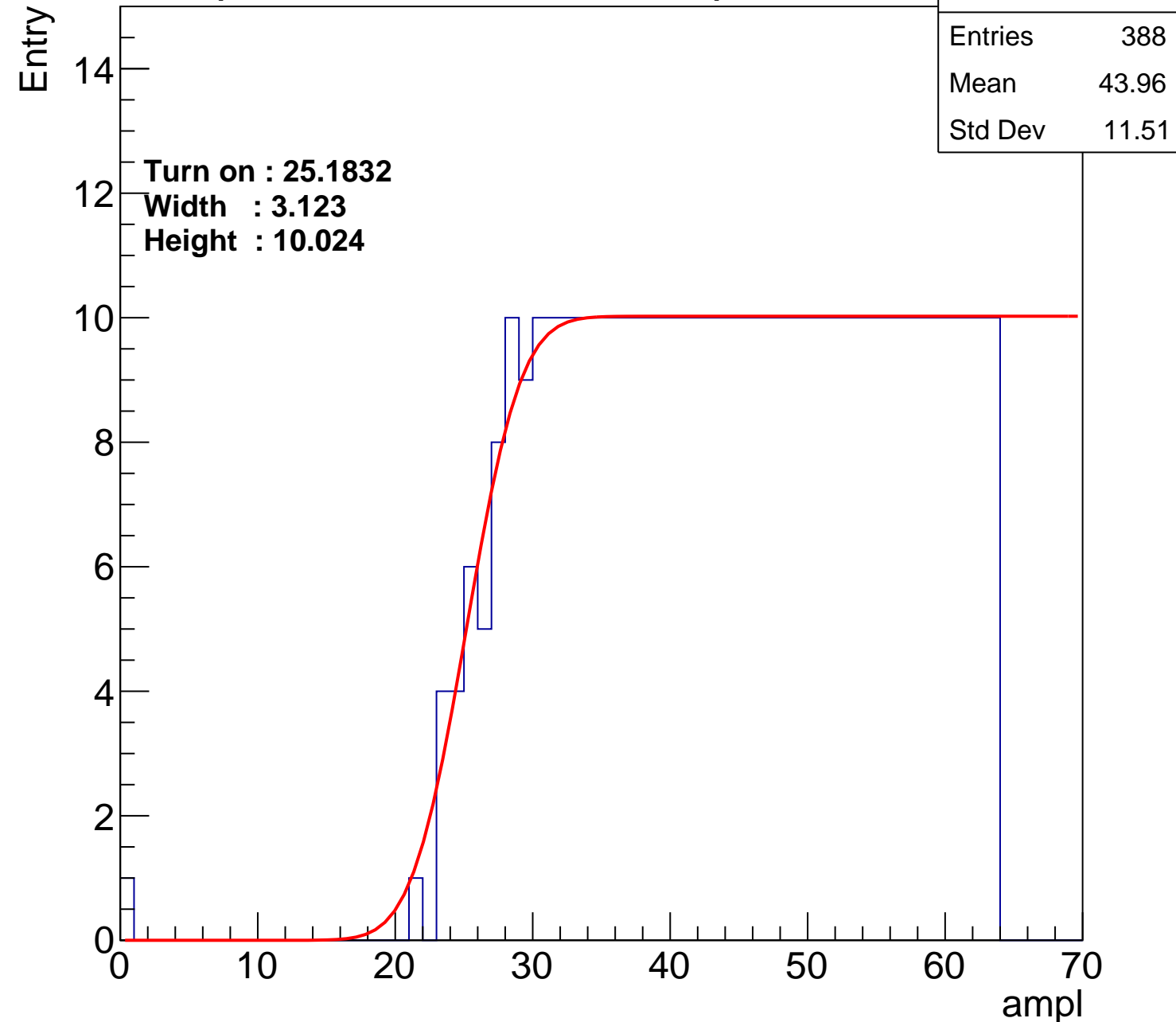
Width : 3.123

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch92

calib_packv5_042523_0143.root, FC#10, port B3

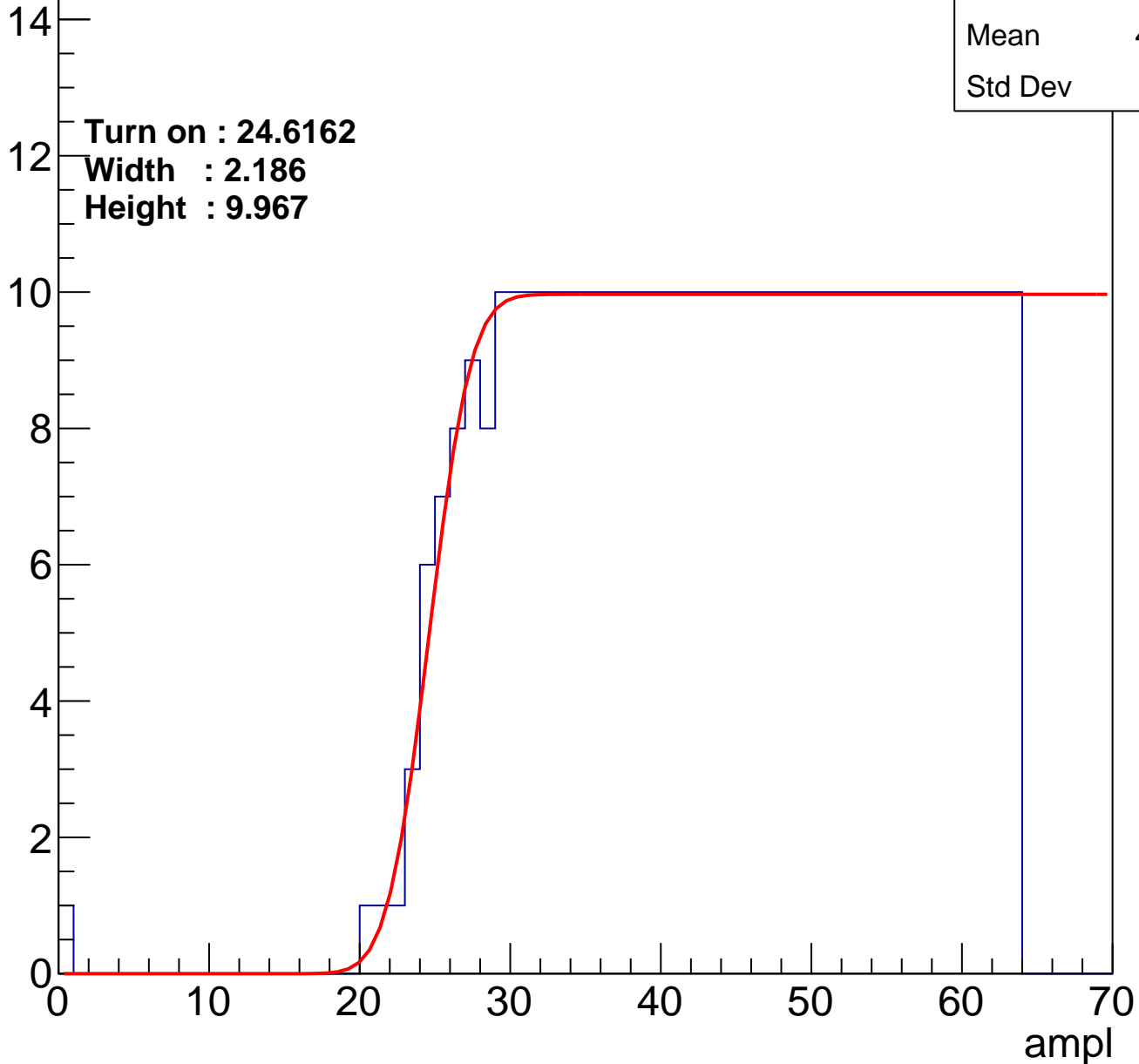
Entries	395
Mean	43.61
Std Dev	11.7

Turn on : 24.6162

Width : 2.186

Height : 9.967

Entry



B1L002S, U1-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.15
Std Dev	11.41

Turn on : 25.4317

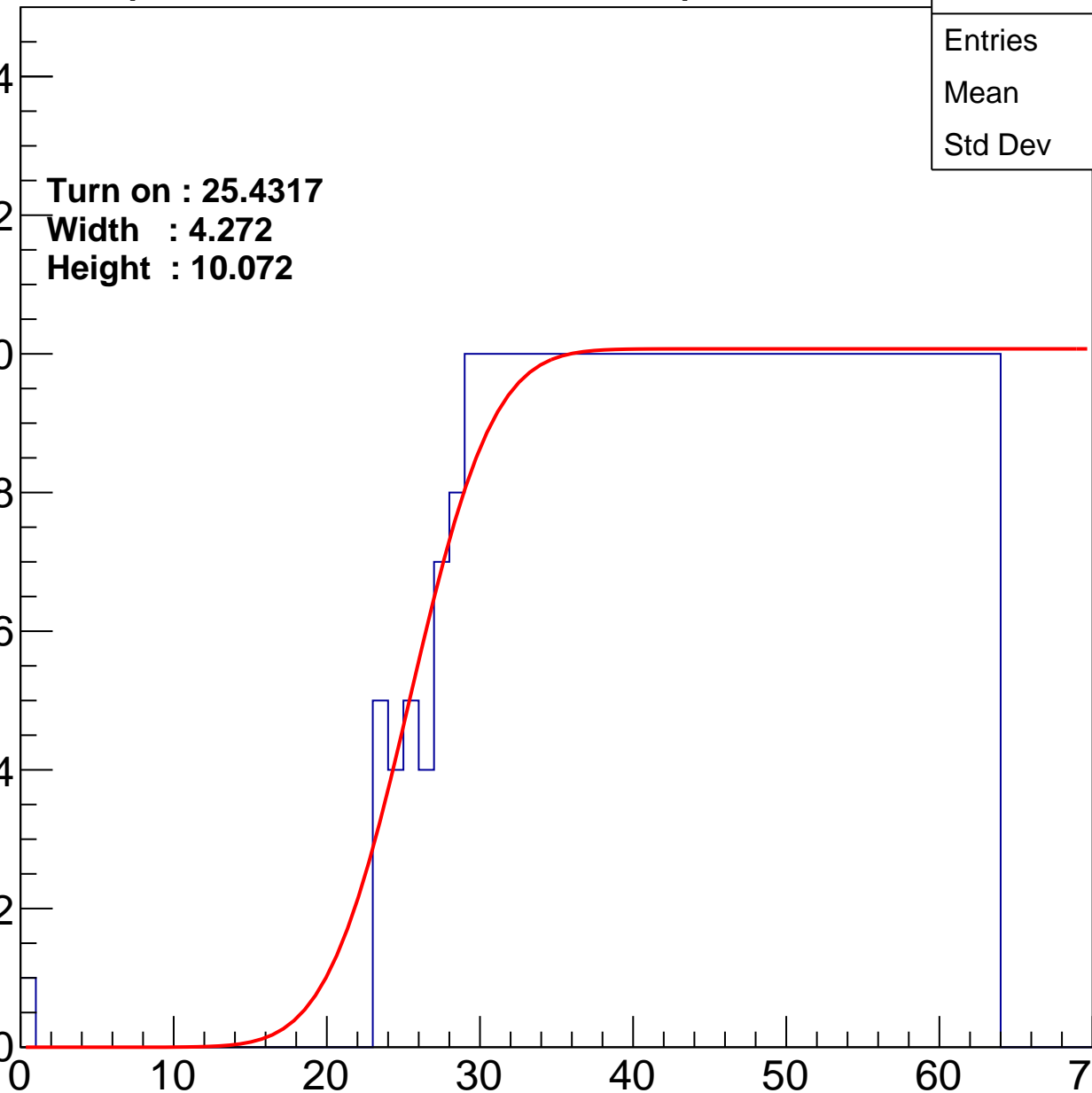
Width : 4.272

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.39
Std Dev	12.9

Turn on : 23.3630

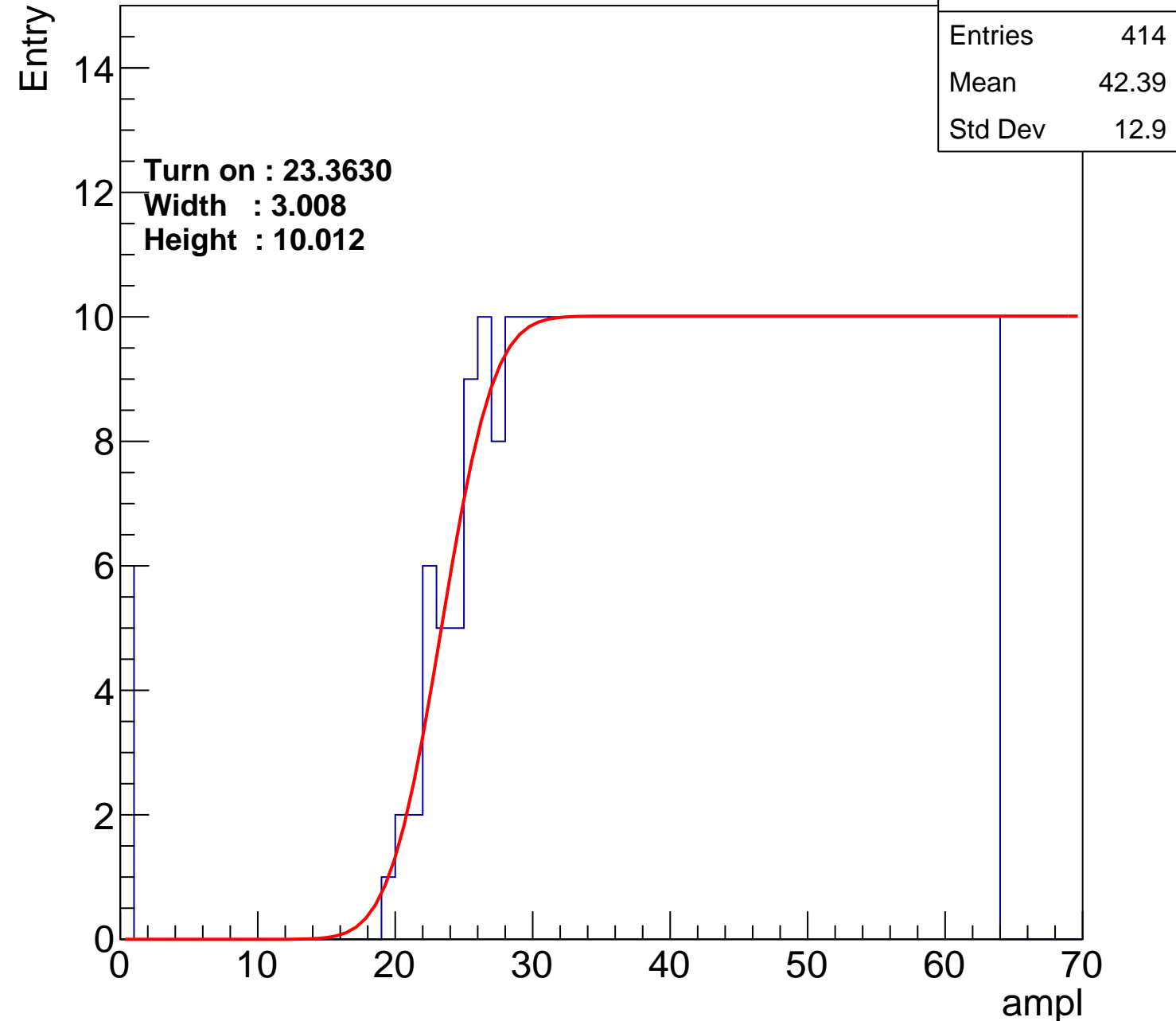
Width : 3.008

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch95

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 25.7395

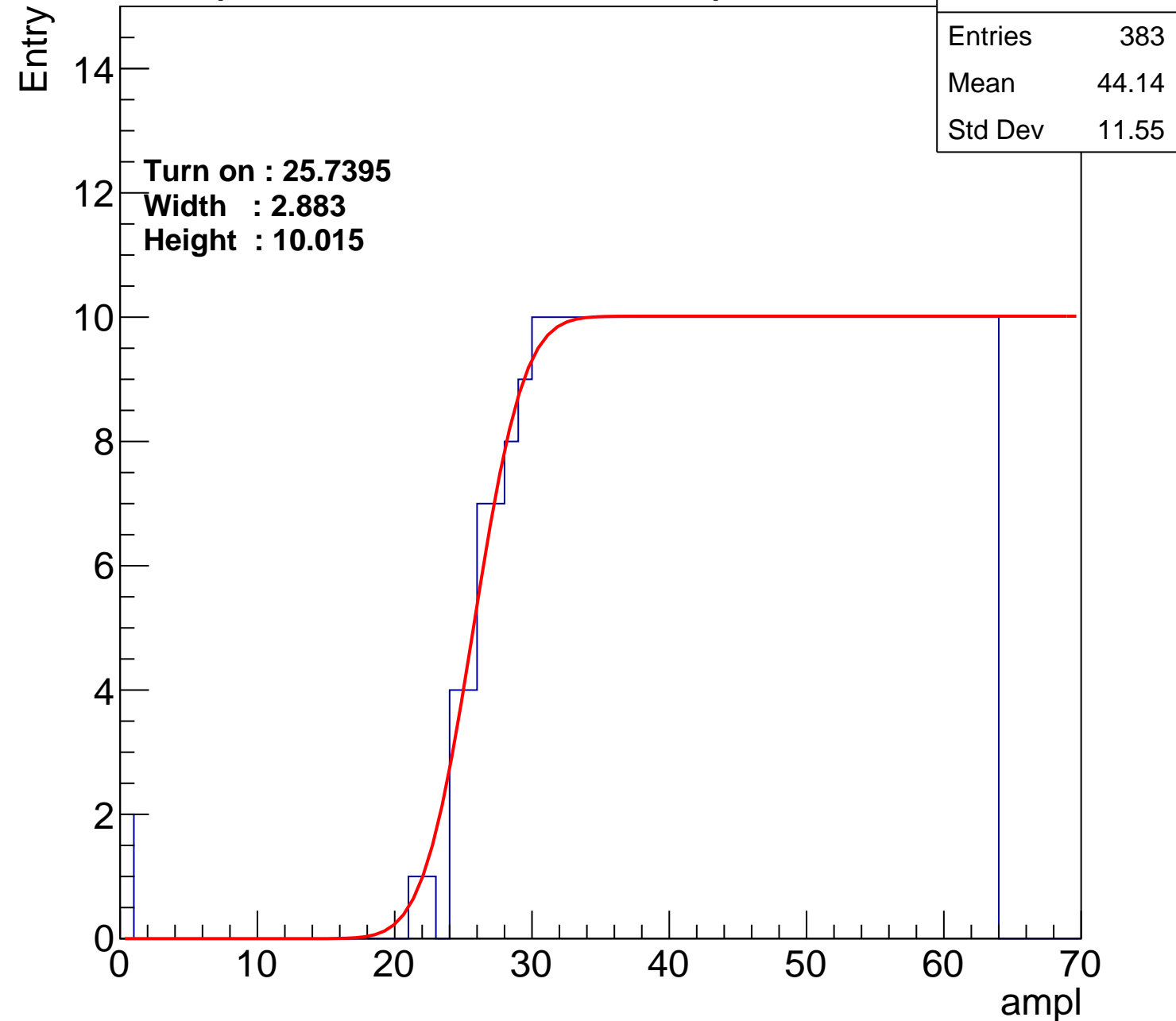
Width : 2.883

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	406
Mean	42.95
Std Dev	12.3

Turn on : 24.0473

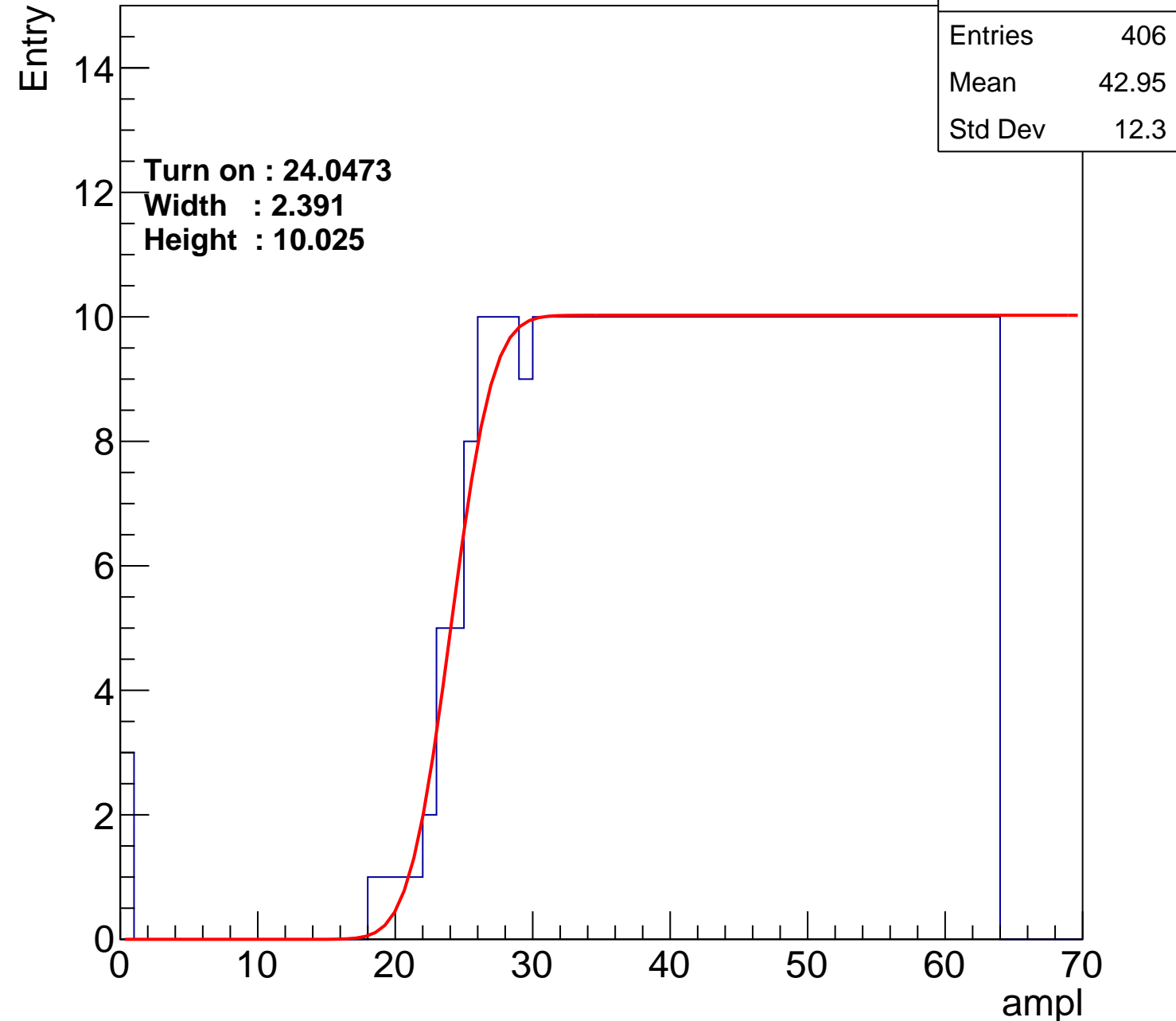
Width : 2.391

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.47
Std Dev	12.06

Turn on : 25.4596

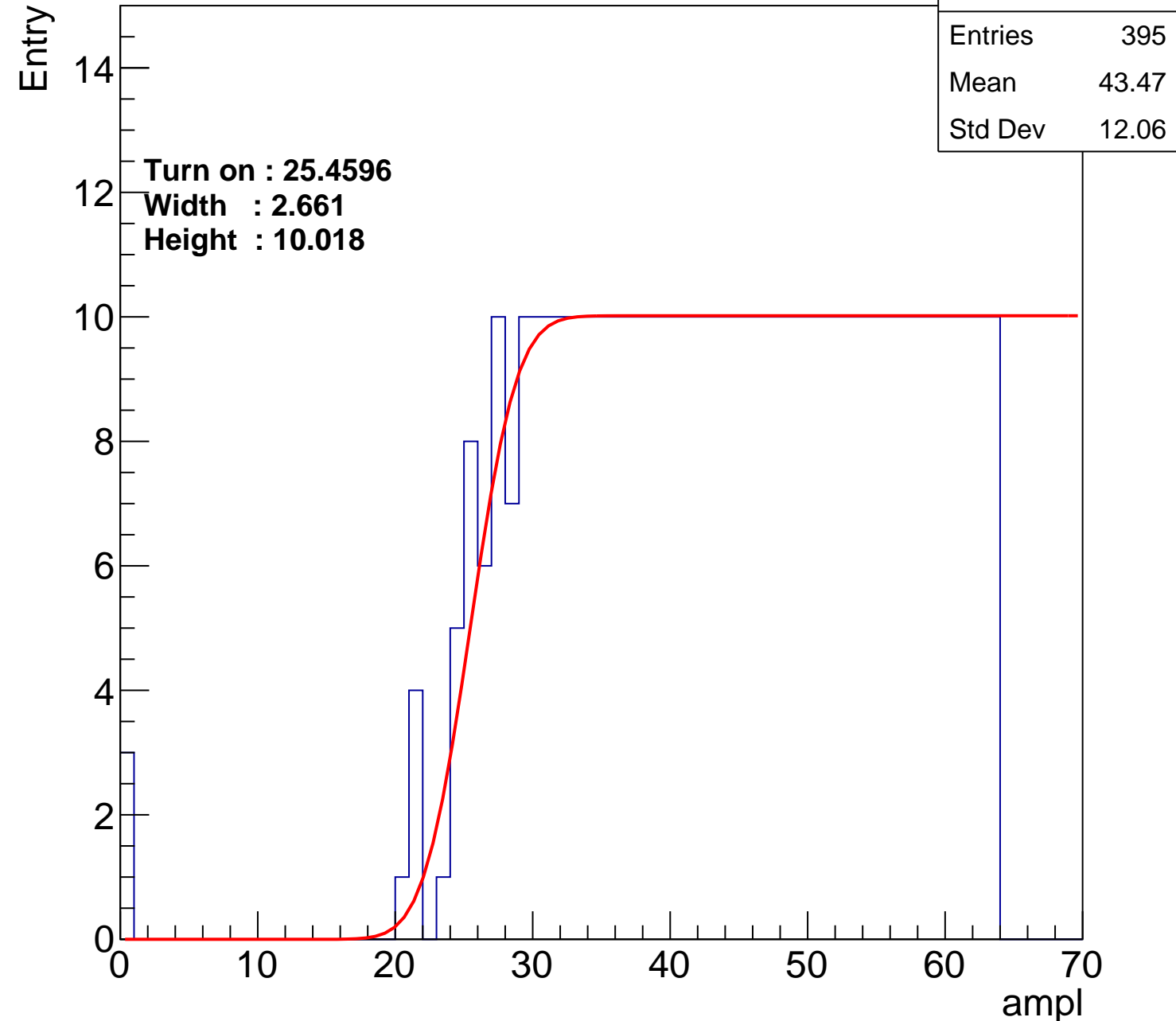
Width : 2.661

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.13
Std Dev	12.33

Turn on : 24.5081

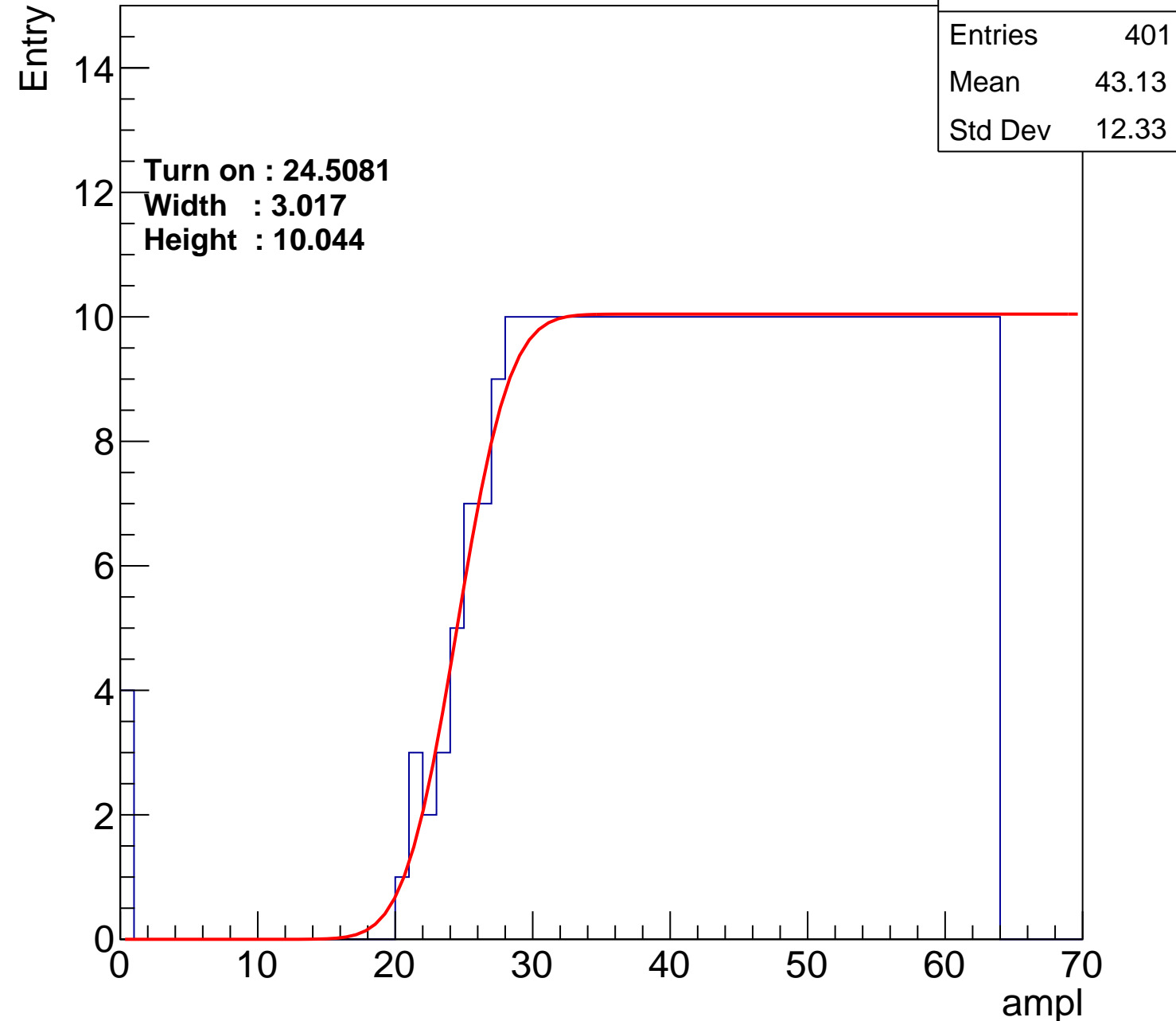
Width : 3.017

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch99

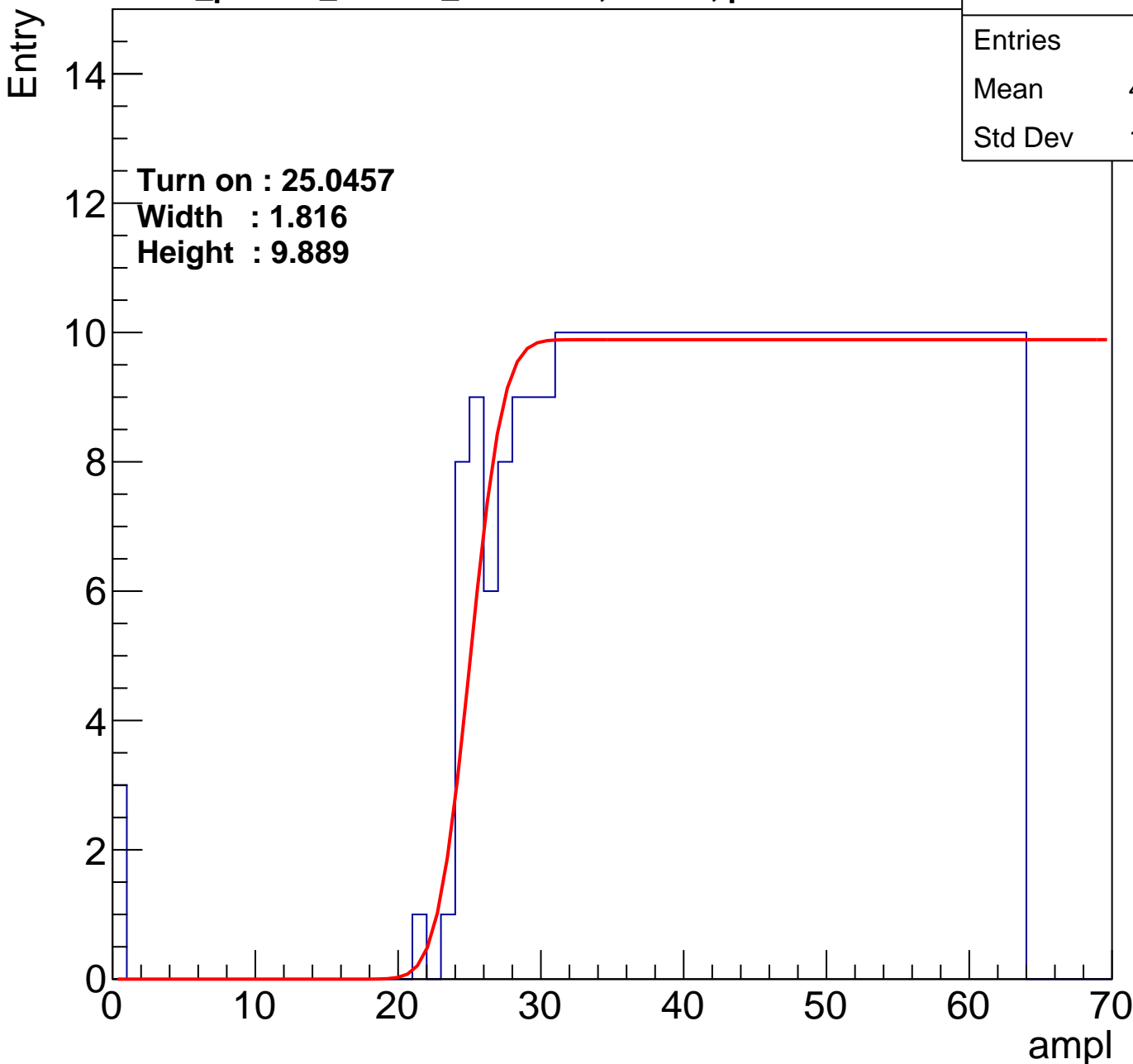
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.0457

Width : 1.816

Height : 9.889

Entries	393
Mean	43.58
Std Dev	11.98



B1L002S, U1-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.6
Std Dev	11.84

Turn on : 25.0407

Width : 2.594

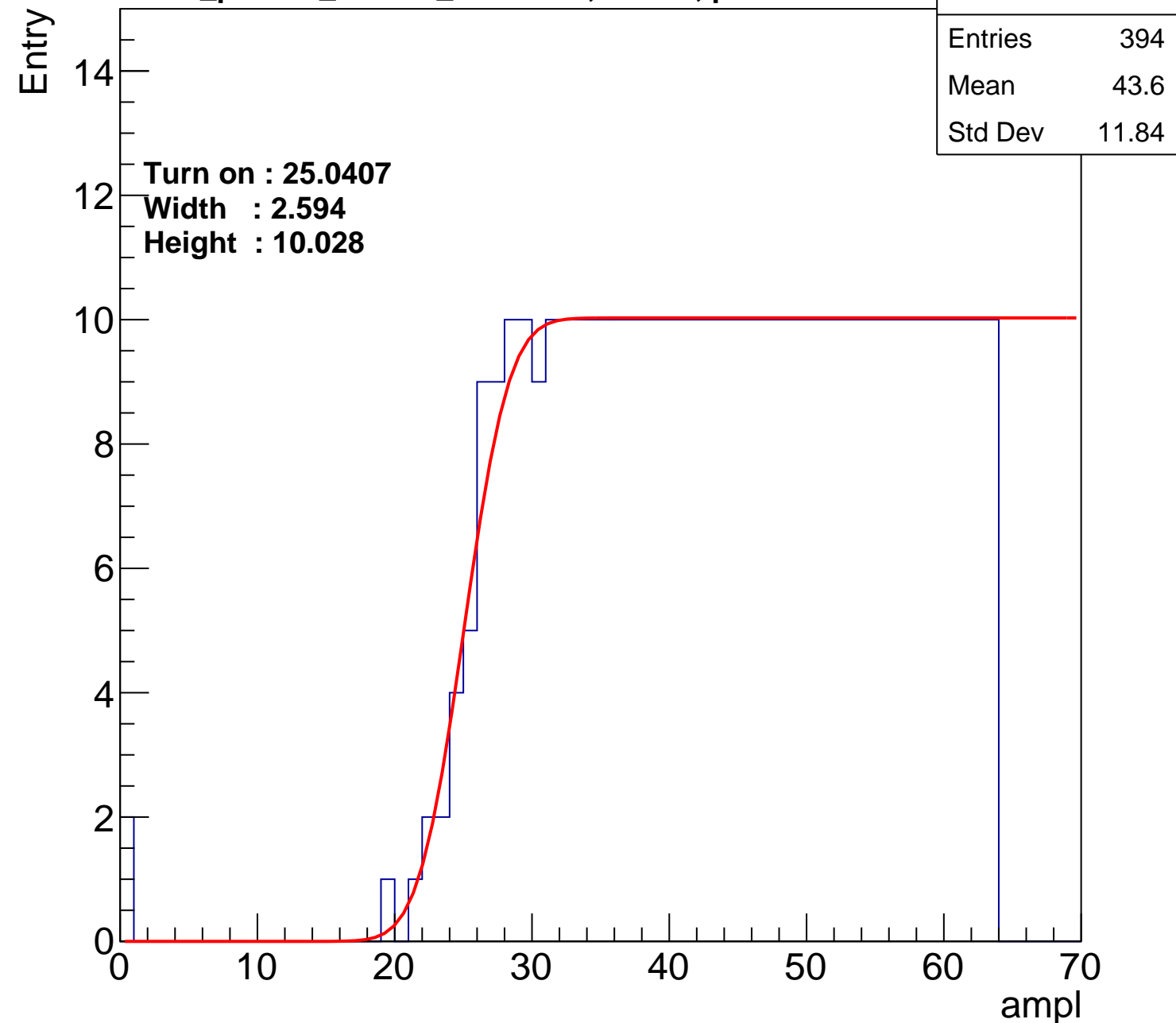
Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch101

calib_packv5_042523_0143.root, FC#10, port B3

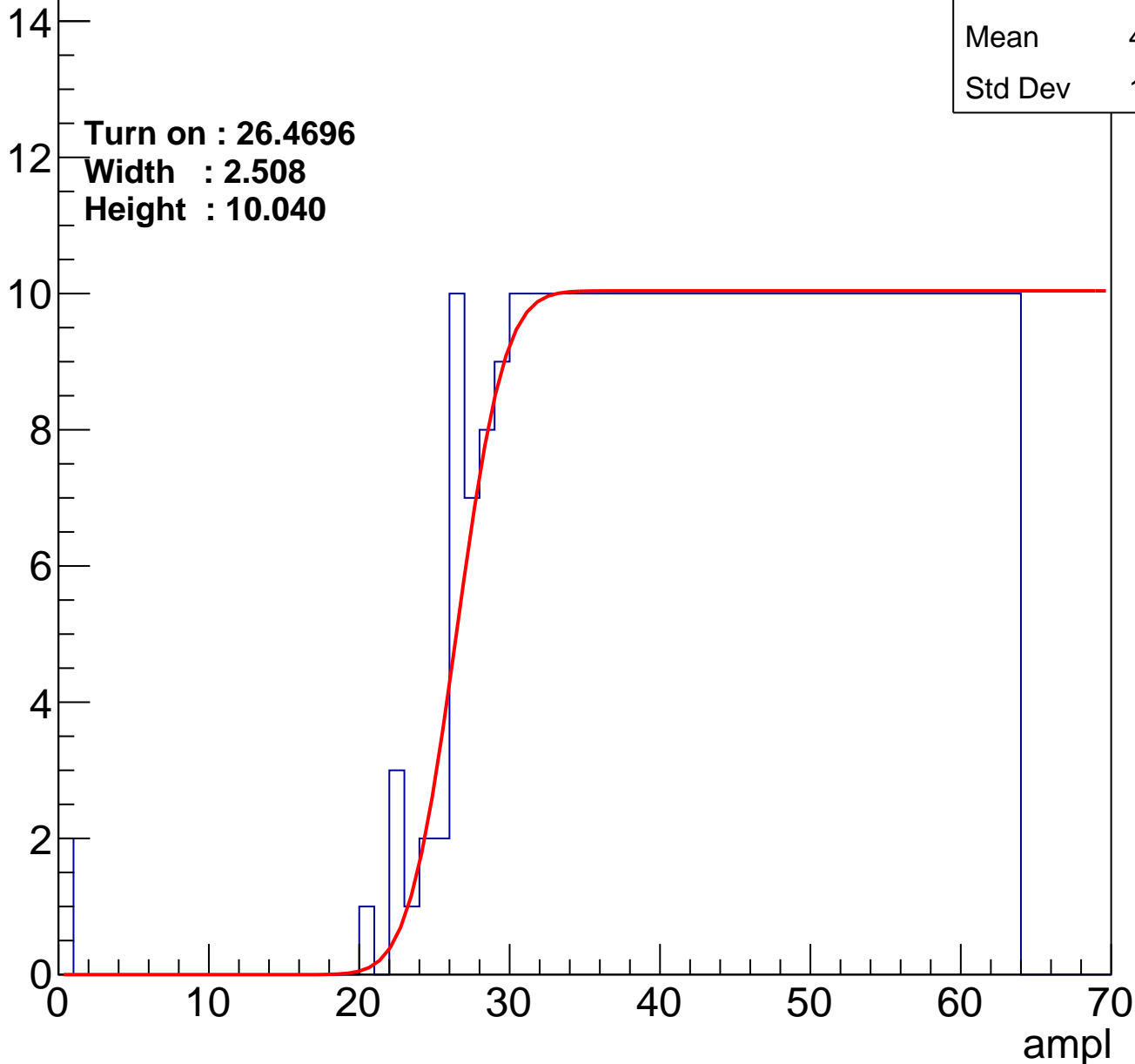
Entry

Entries	385
Mean	44.03
Std Dev	11.63

Turn on : 26.4696

Width : 2.508

Height : 10.040



B1L002S, U1-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	417
Mean	42.46
Std Dev	12.45

Turn on : 22.6323

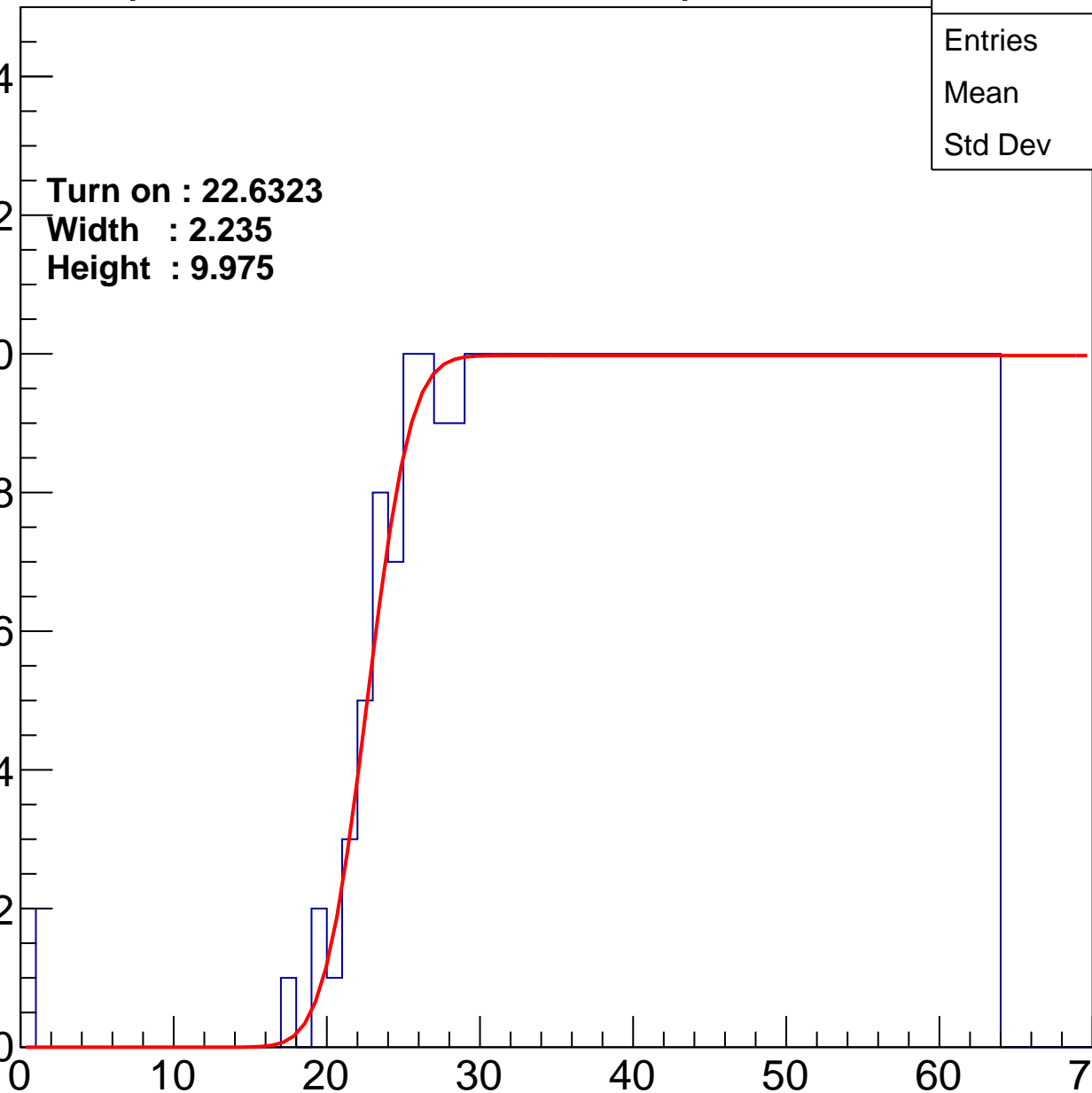
Width : 2.235

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.32
Std Dev	12.36

Turn on : 25.1077

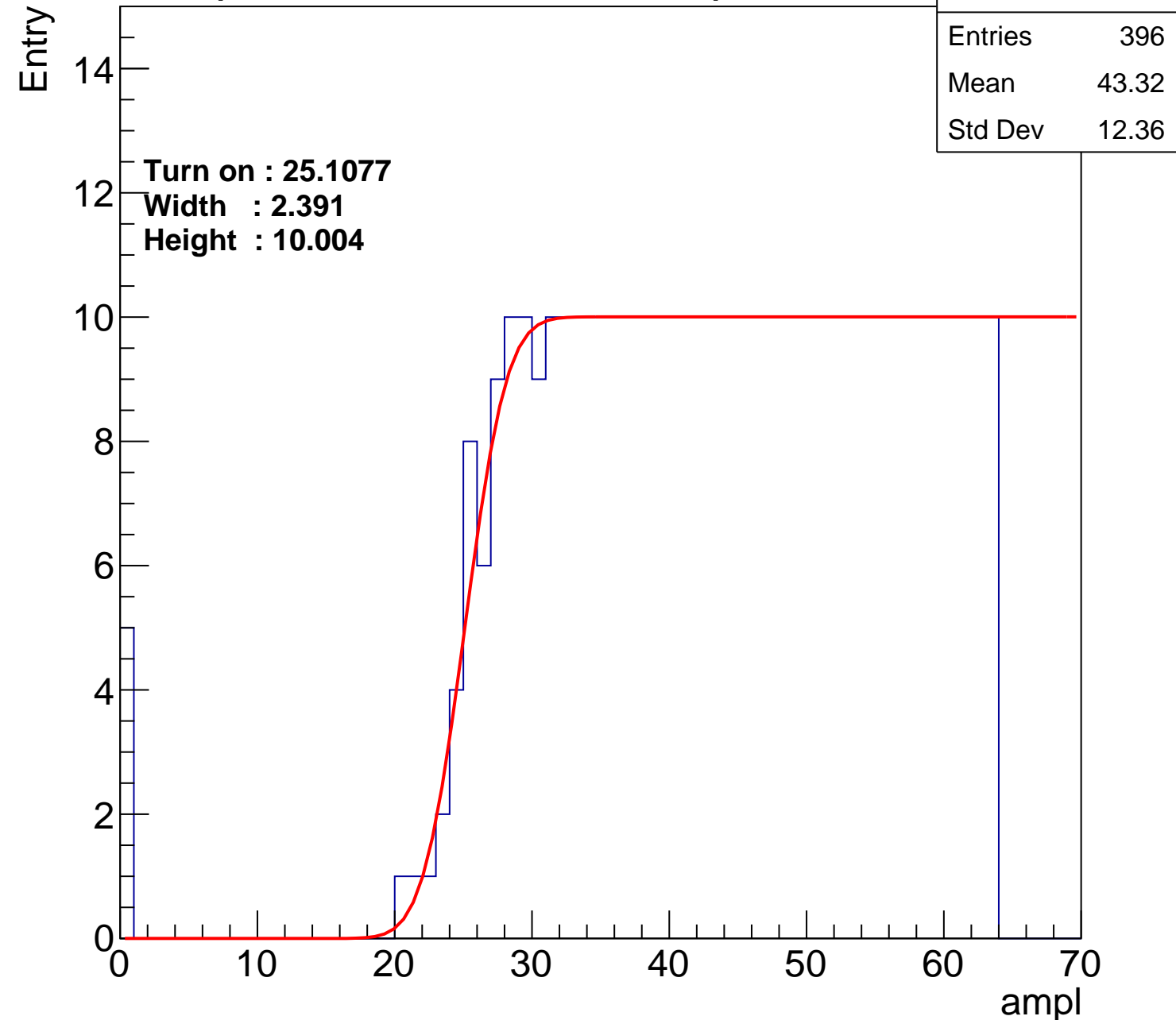
Width : 2.391

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch104

calib_packv5_042523_0143.root, FC#10, port B3

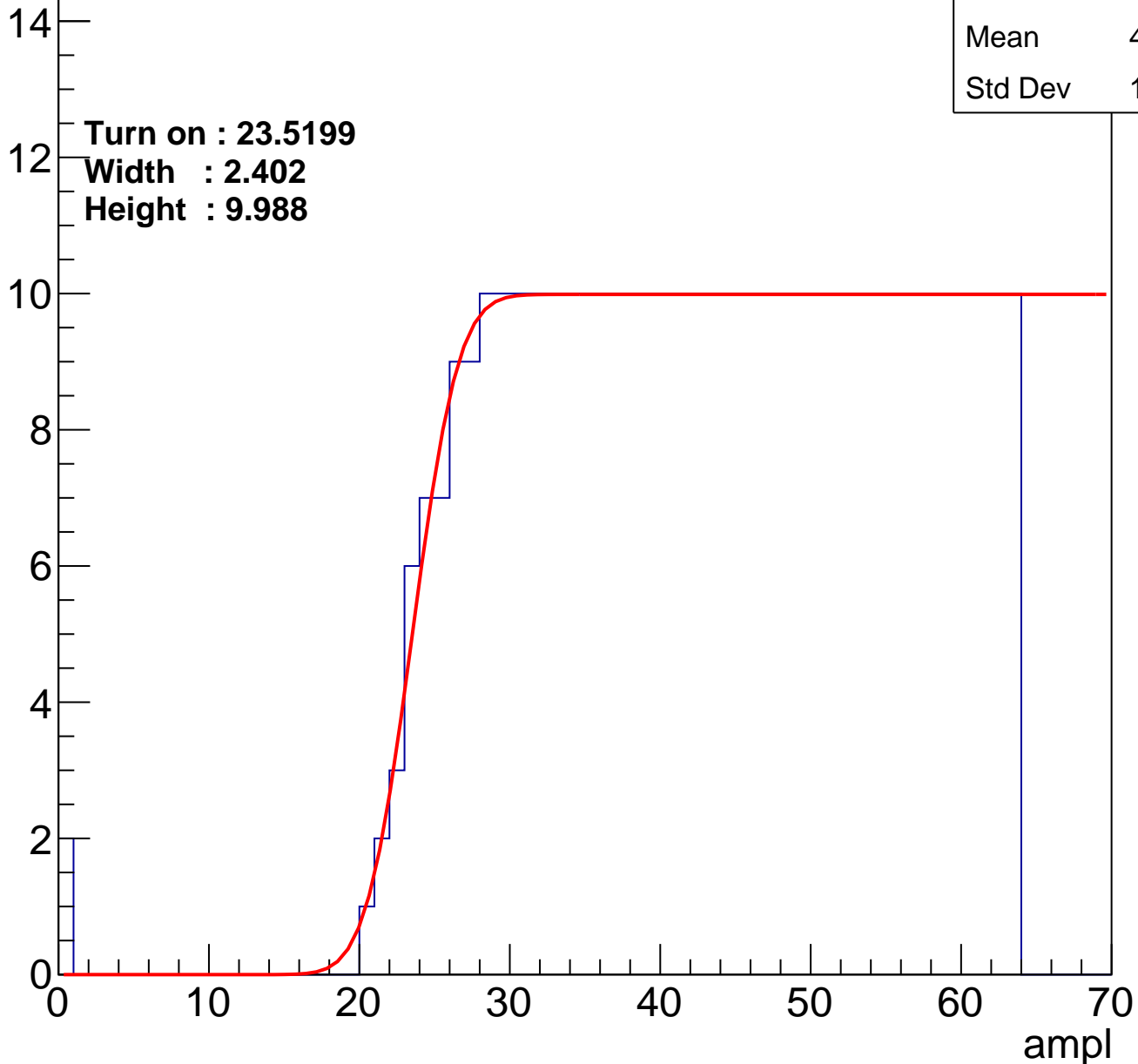
Entries	406
Mean	43.02
Std Dev	12.13

Turn on : 23.5199

Width : 2.402

Height : 9.988

Entry



B1L002S, U1-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.7
Std Dev	11.77

Turn on : 24.8213

Width : 2.204

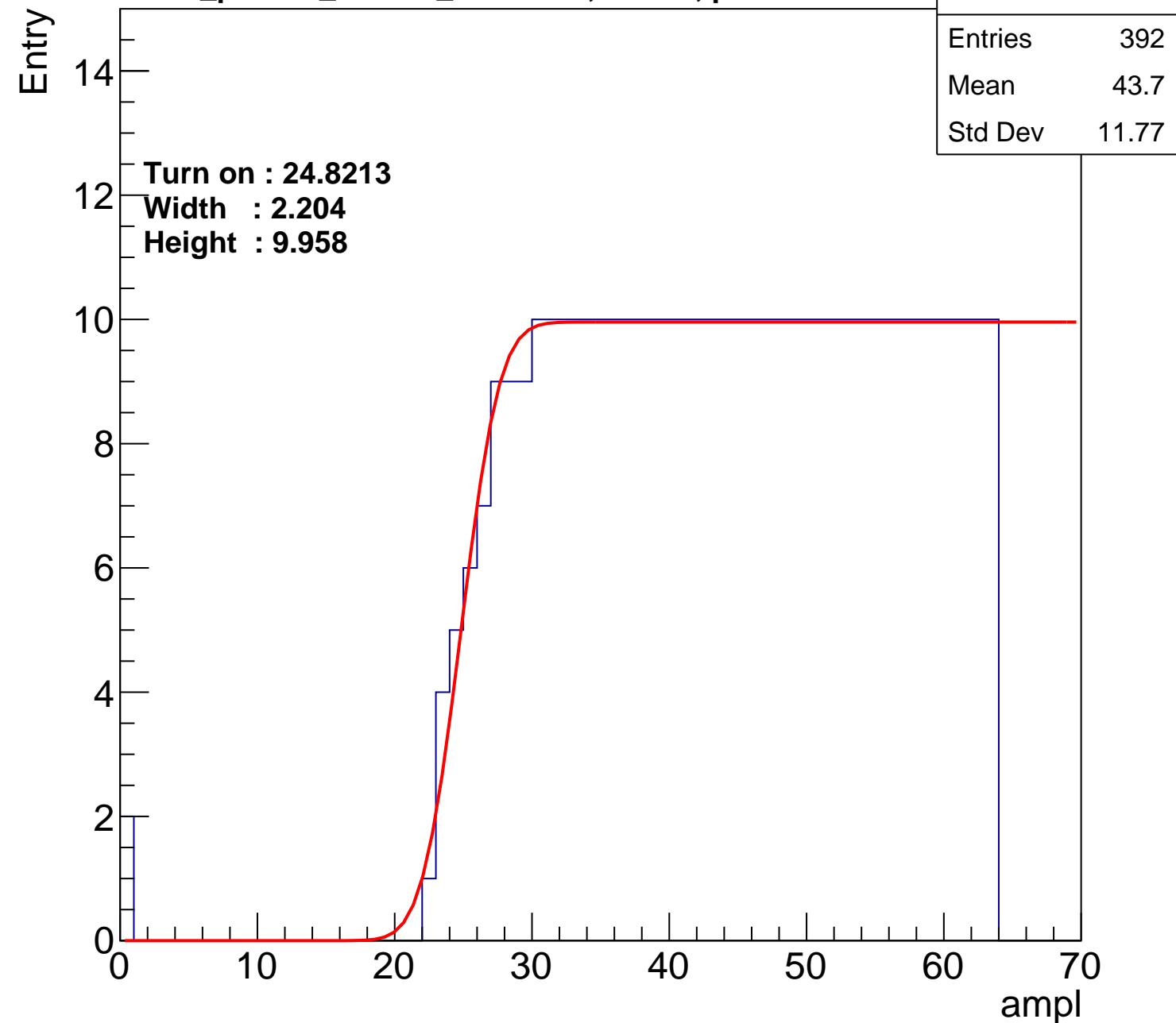
Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch106

calib_packv5_042523_0143.root, FC#10, port B3

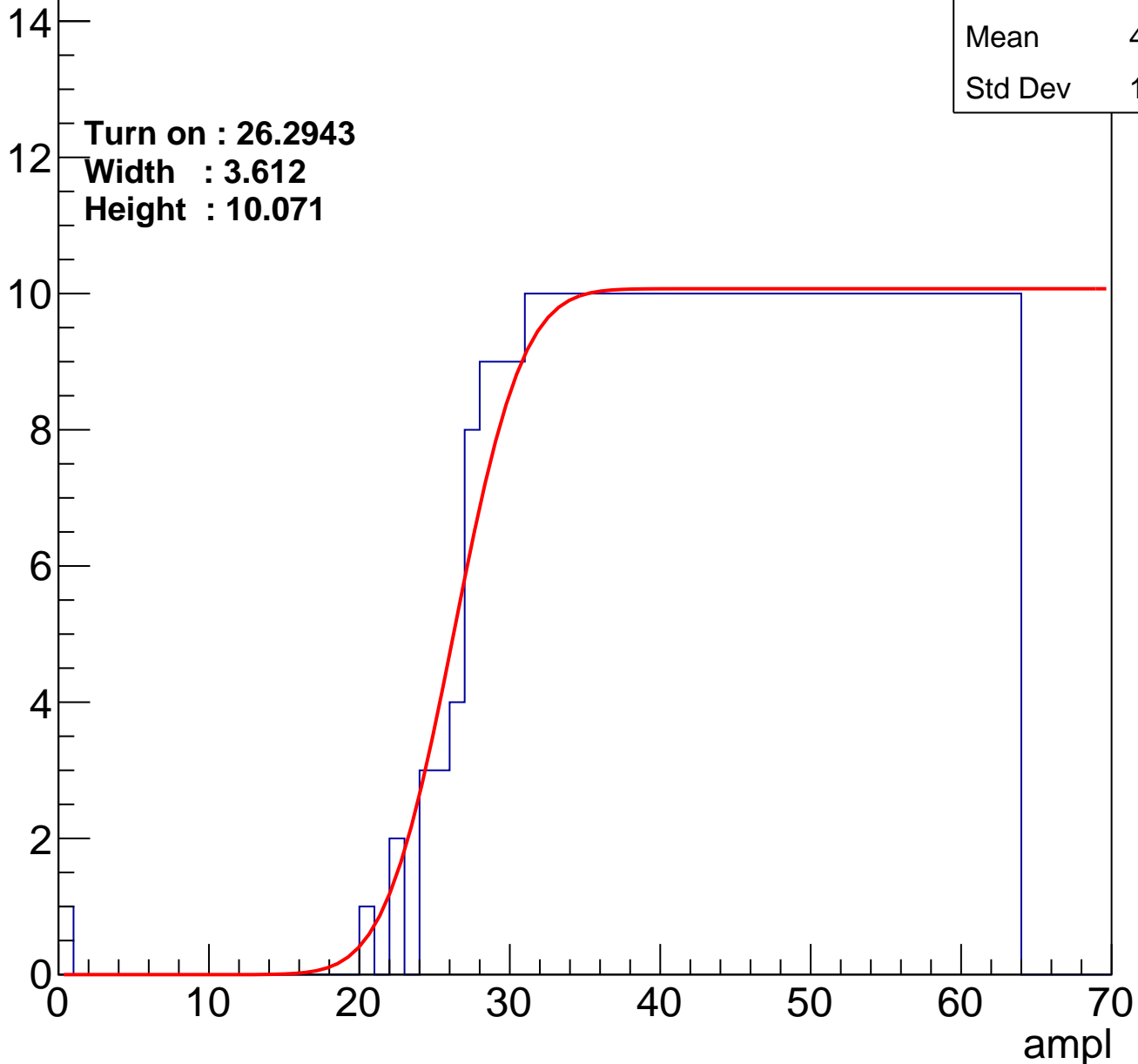
Entries	379
Mean	44.39
Std Dev	11.29

Turn on : 26.2943

Width : 3.612

Height : 10.071

Entry



B1L002S, U1-ch107

calib_packv5_042523_0143.root, FC#10, port B3

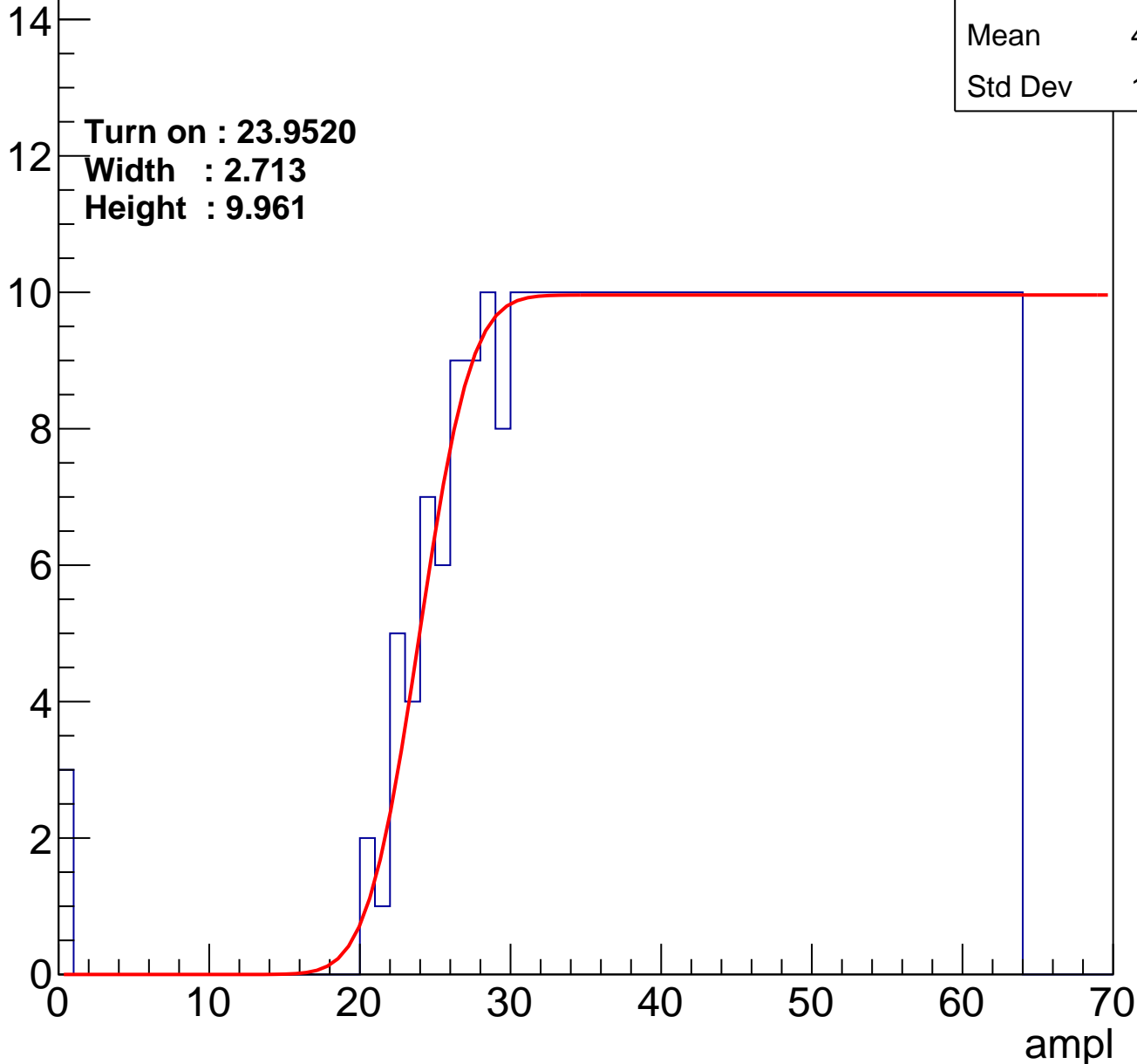
Entries	404
Mean	43.02
Std Dev	12.29

Turn on : 23.9520

Width : 2.713

Height : 9.961

Entry



B1L002S, U1-ch108

calib_packv5_042523_0143.root, FC#10, port B3

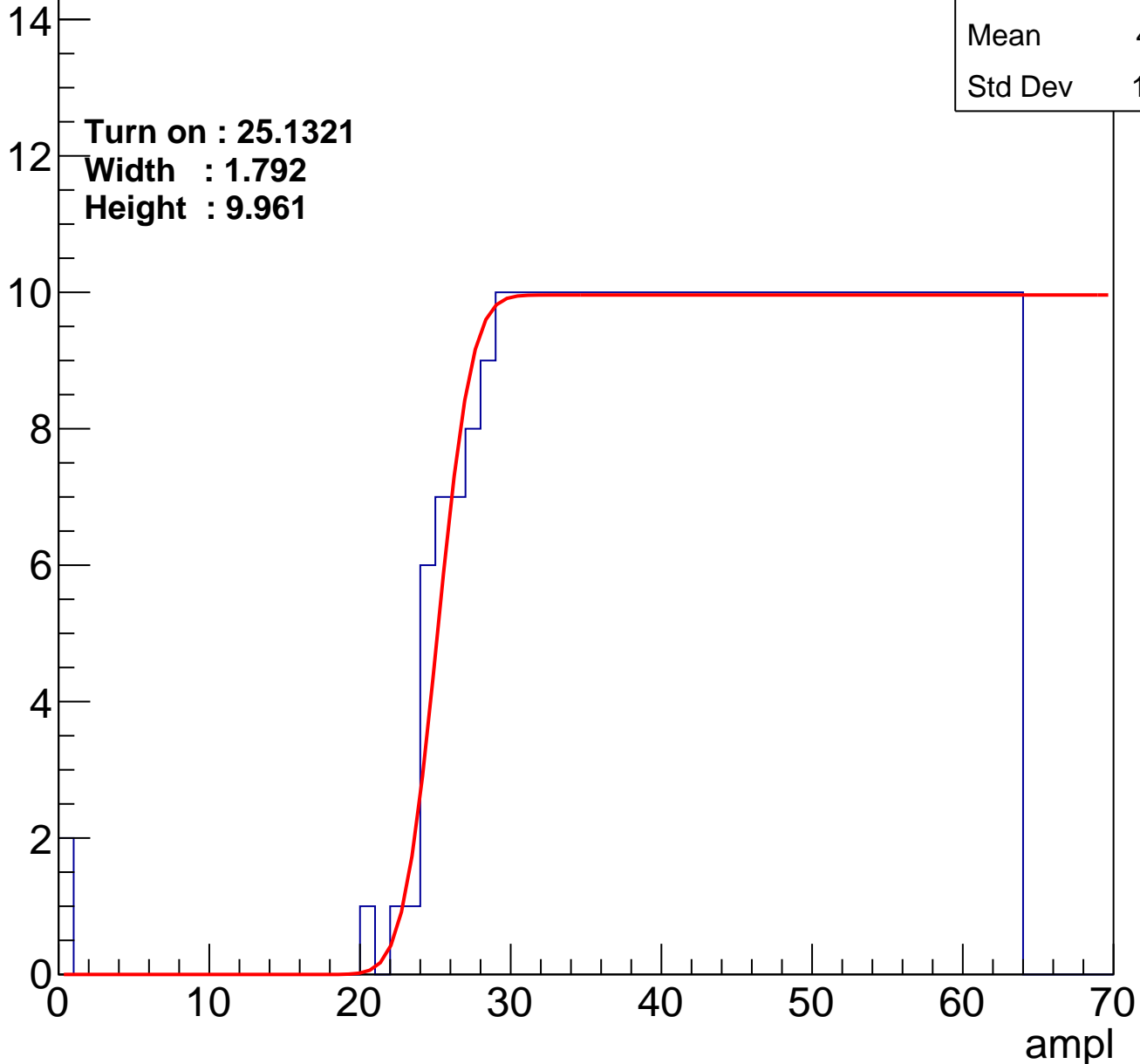
Entries	392
Mean	43.71
Std Dev	11.76

Turn on : 25.1321

Width : 1.792

Height : 9.961

Entry



B1L002S, U1-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.89
Std Dev	11.69

Turn on : 25.3680

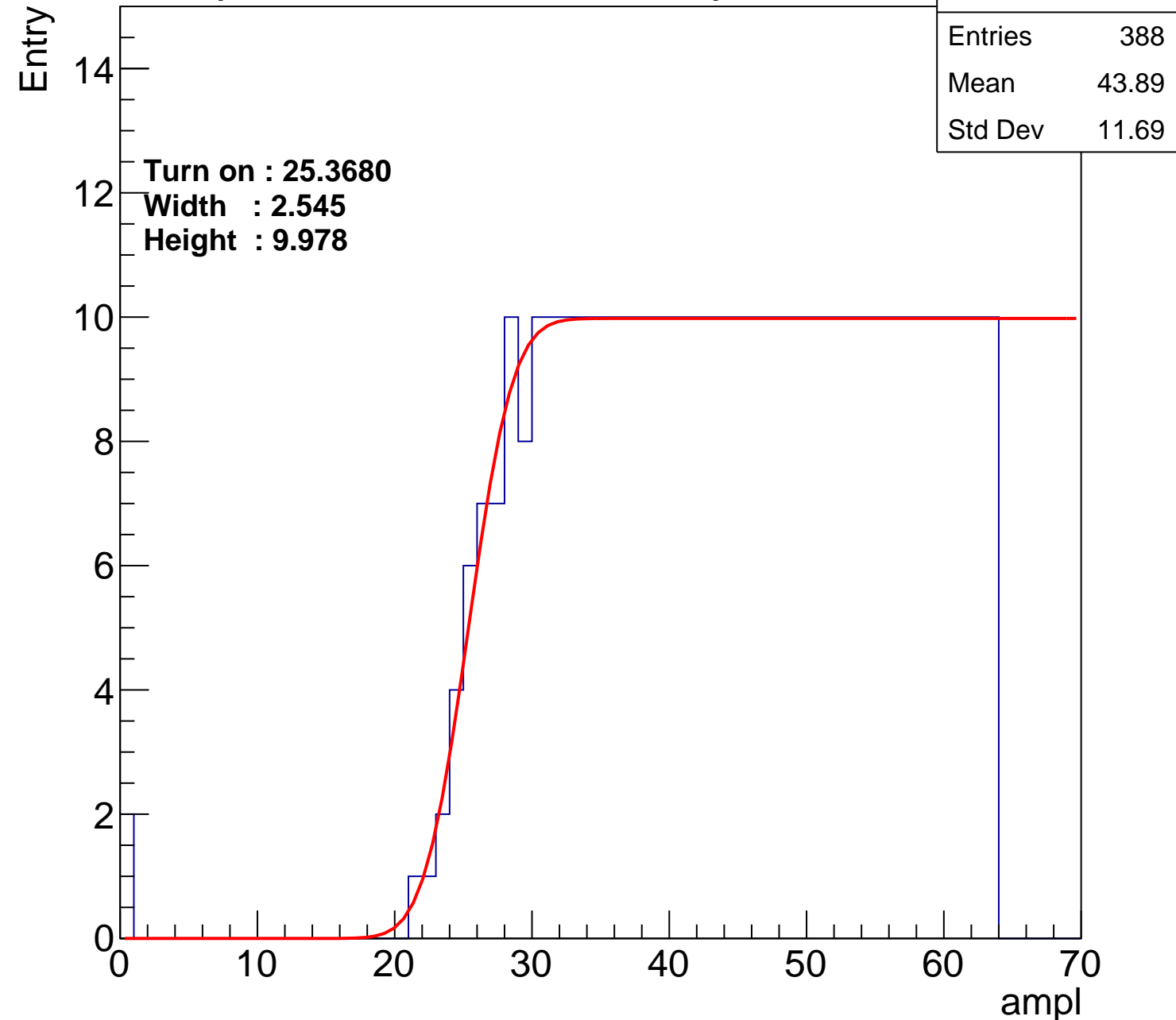
Width : 2.545

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.31
Std Dev	11.48

Turn on : 25.7557

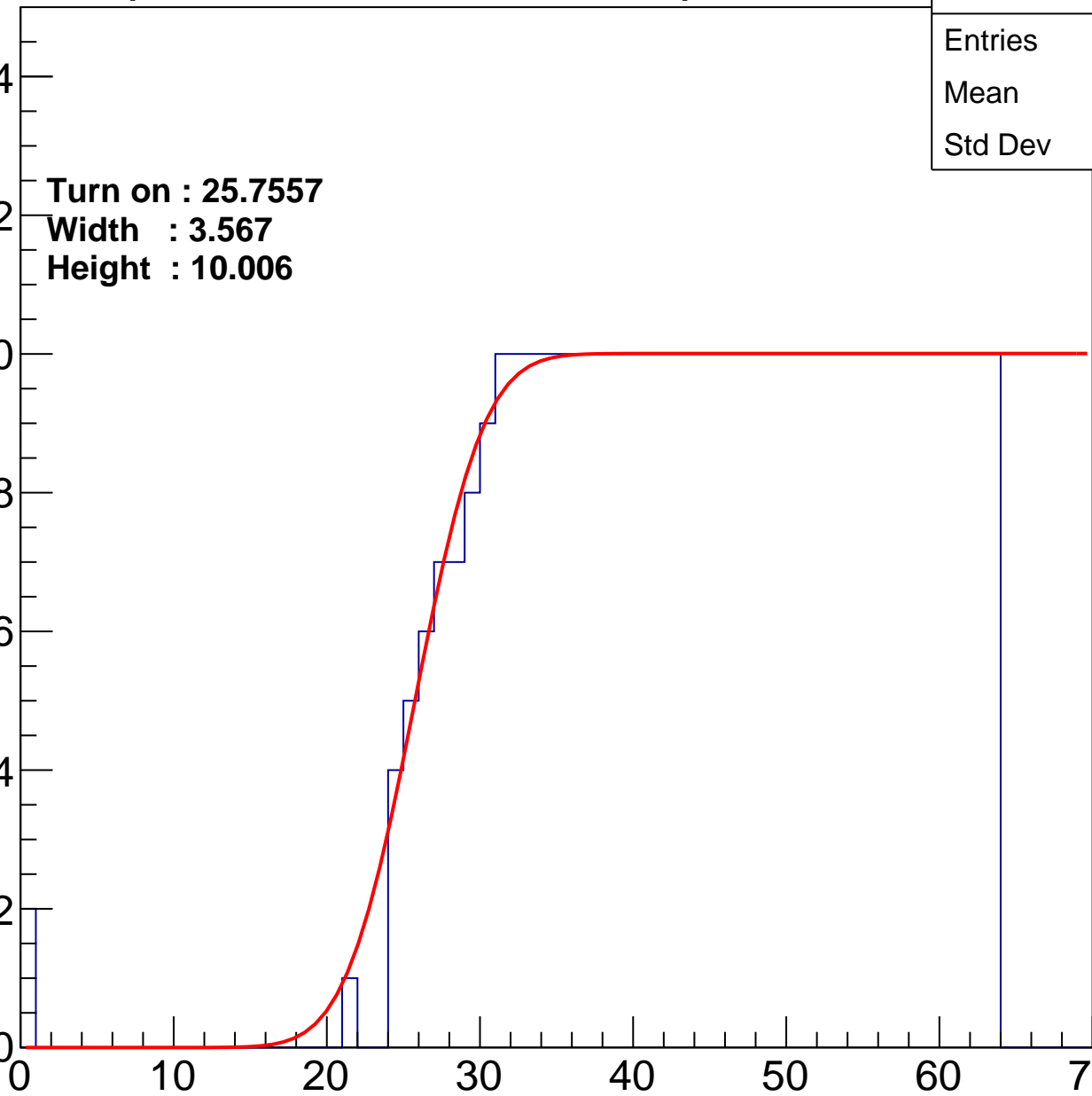
Width : 3.567

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch111

calib_packv5_042523_0143.root, FC#10, port B3

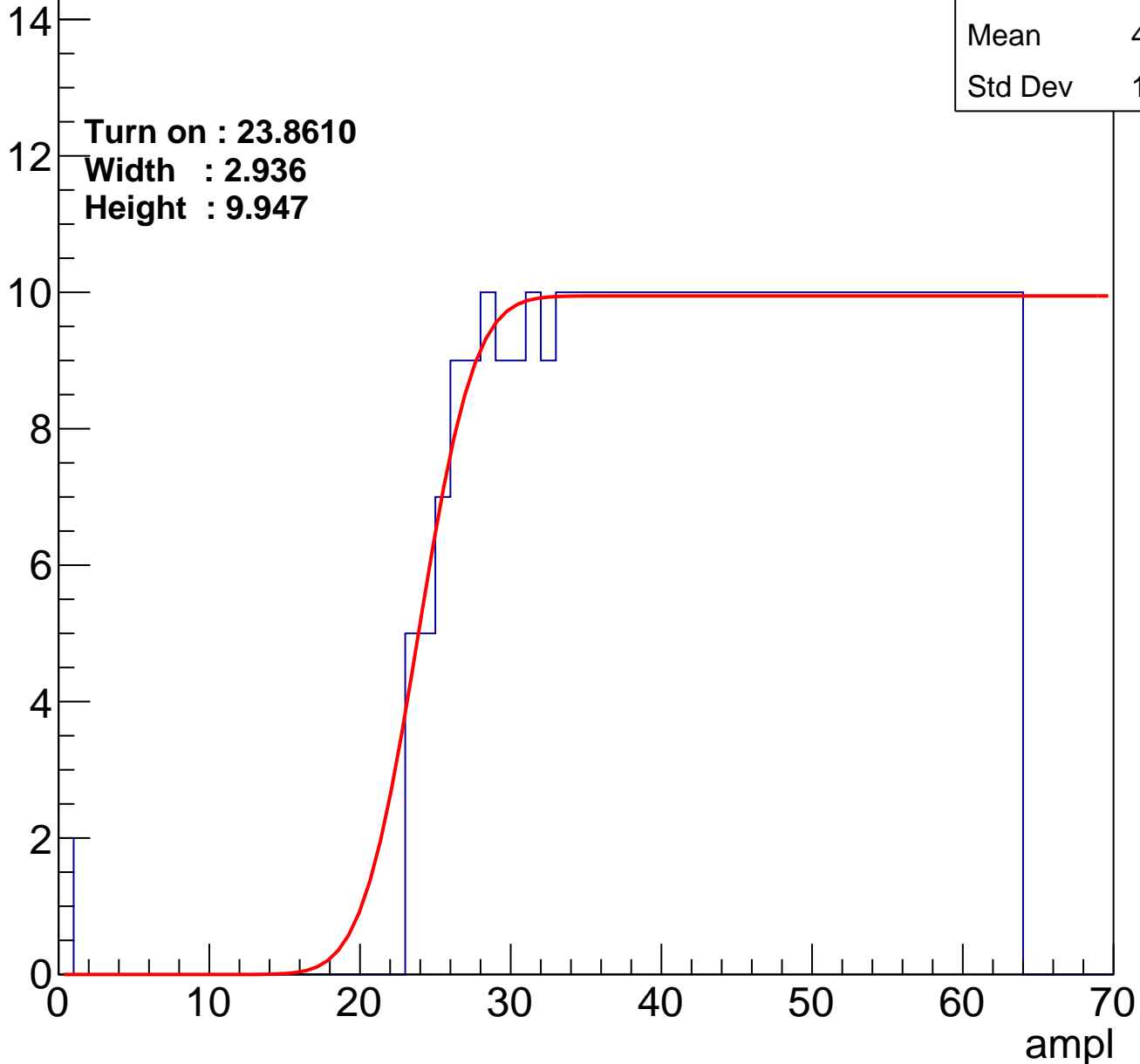
Entries	394
Mean	43.59
Std Dev	11.83

Turn on : 23.8610

Width : 2.936

Height : 9.947

Entry



B1L002S, U1-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.81
Std Dev	11.7

Turn on : 25.3522

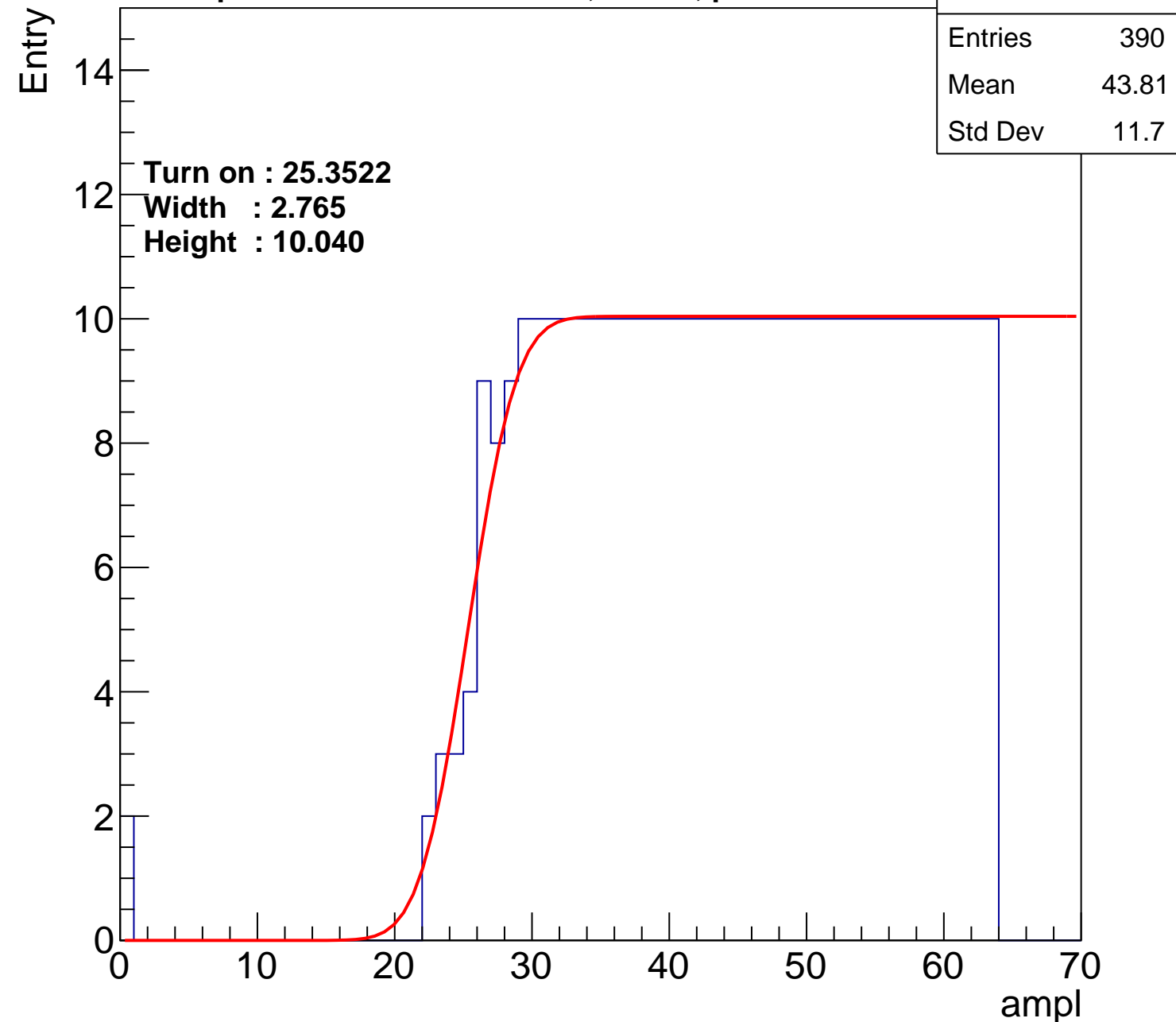
Width : 2.765

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.56

Turn on : 26.4313

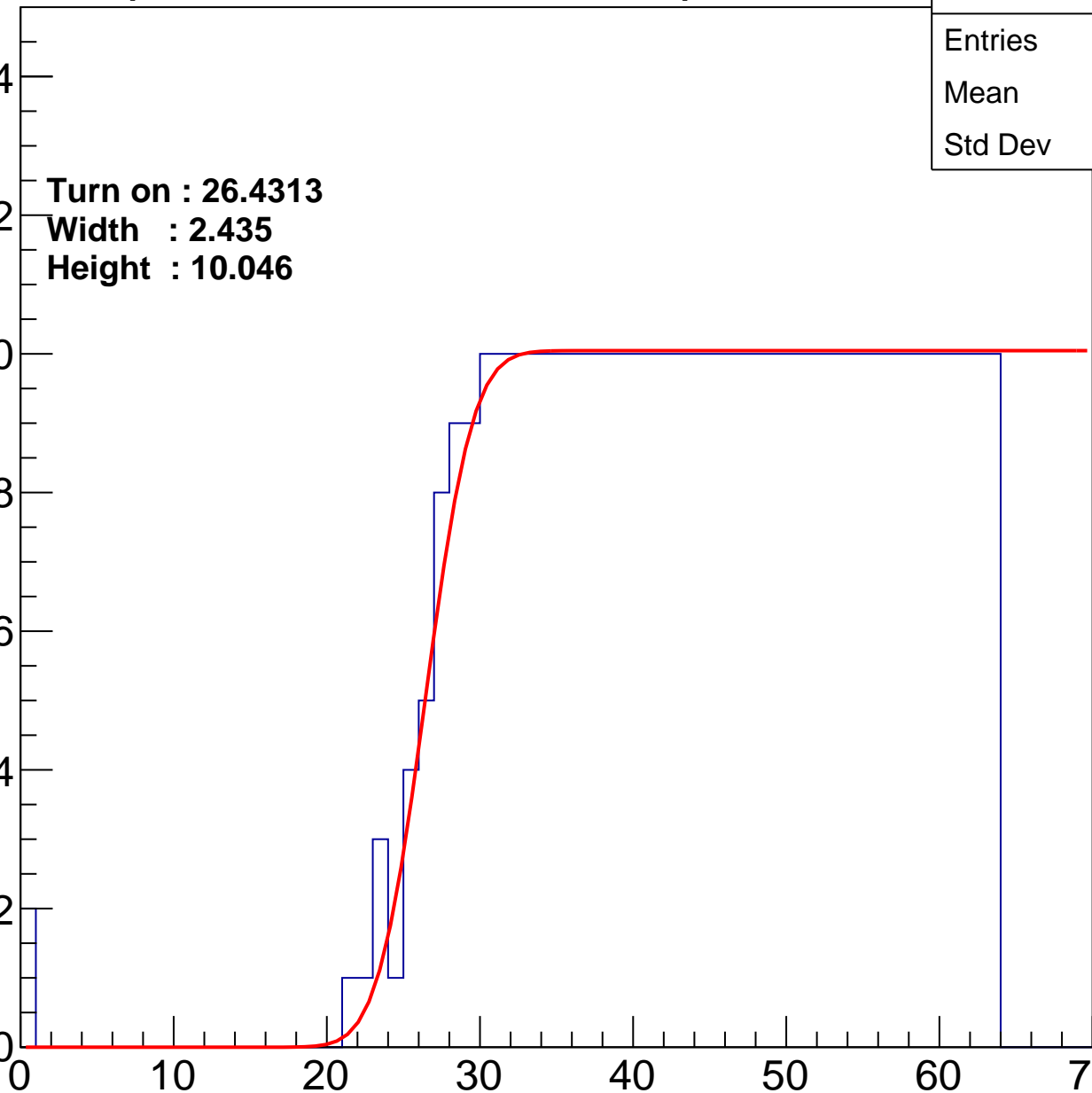
Width : 2.435

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.1016

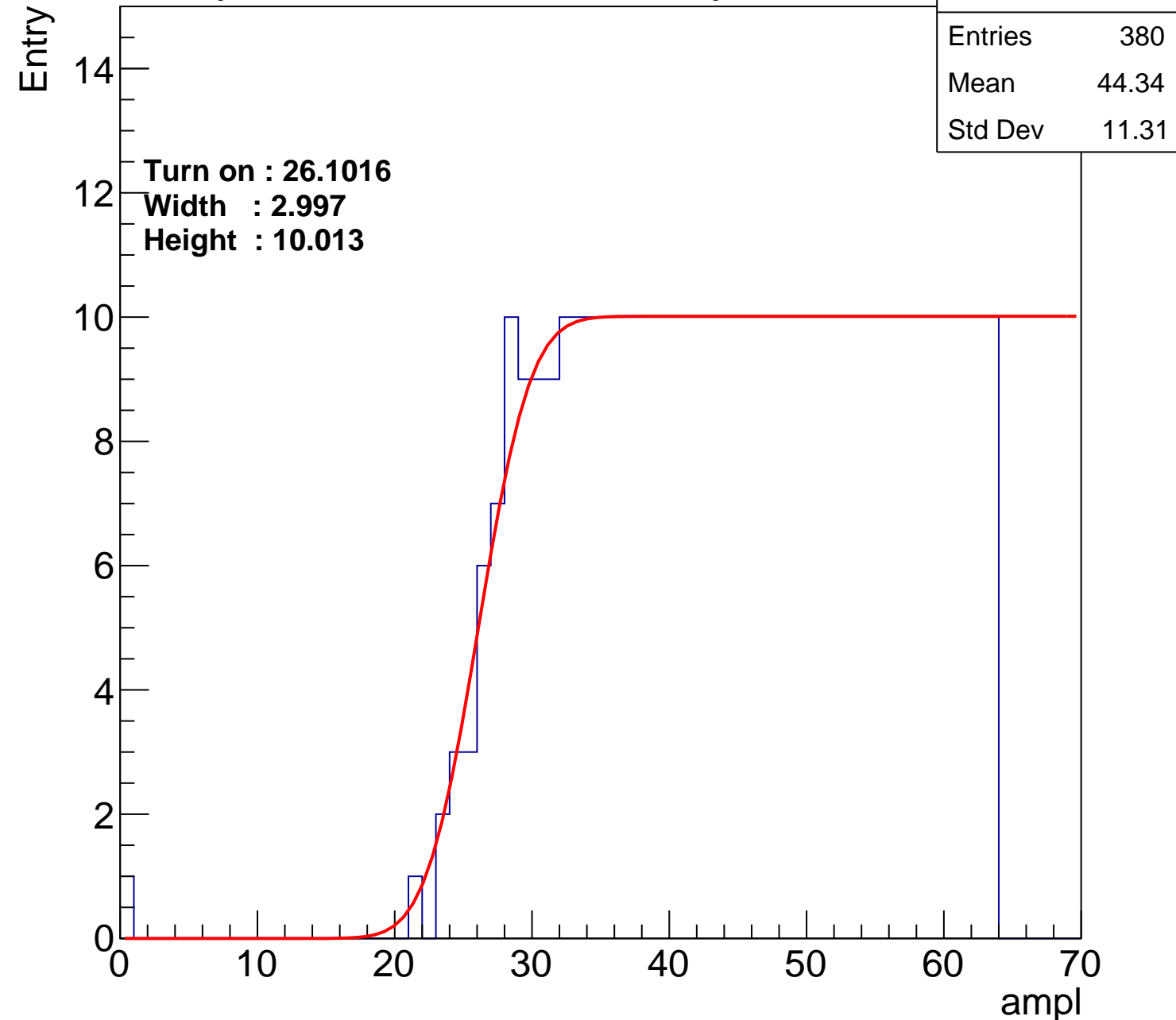
Width : 2.997

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.72
Std Dev	11.91

Turn on : 26.1538

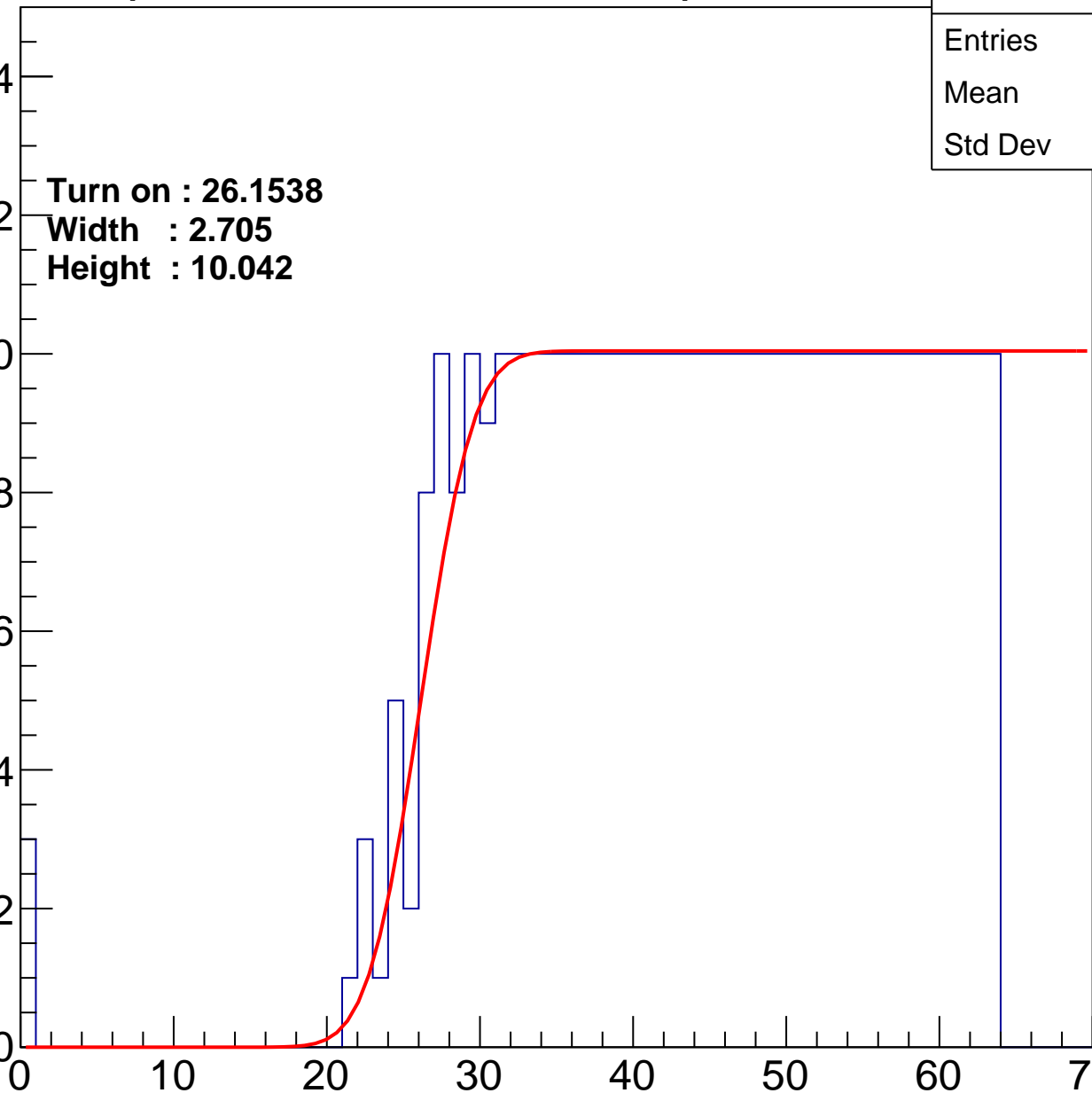
Width : 2.705

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch116

calib_packv5_042523_0143.root, FC#10, port B3

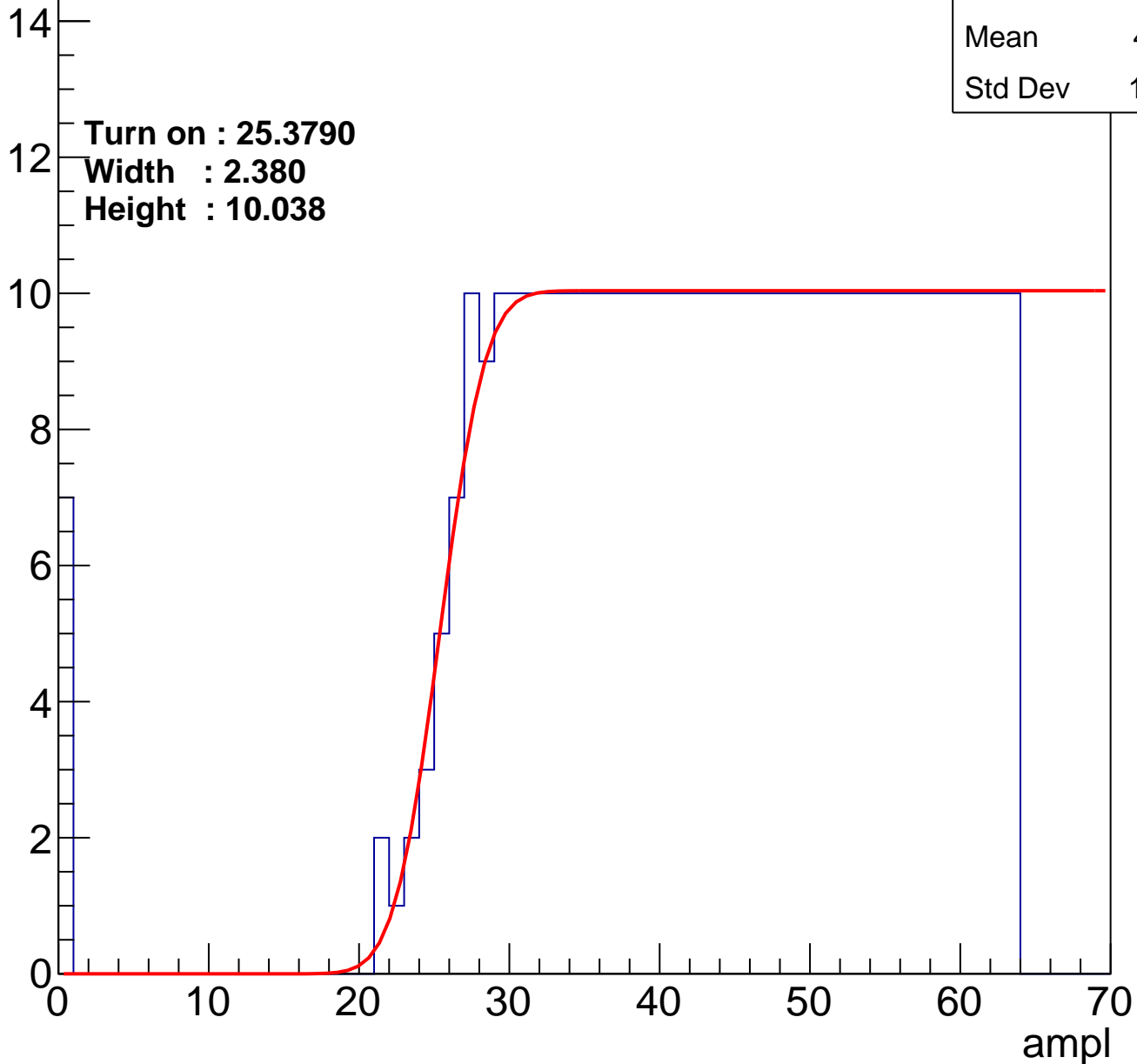
Entries	396
Mean	43.21
Std Dev	12.64

Turn on : 25.3790

Width : 2.380

Height : 10.038

Entry



B1L002S, U1-ch117

calib_packv5_042523_0143.root, FC#10, port B3

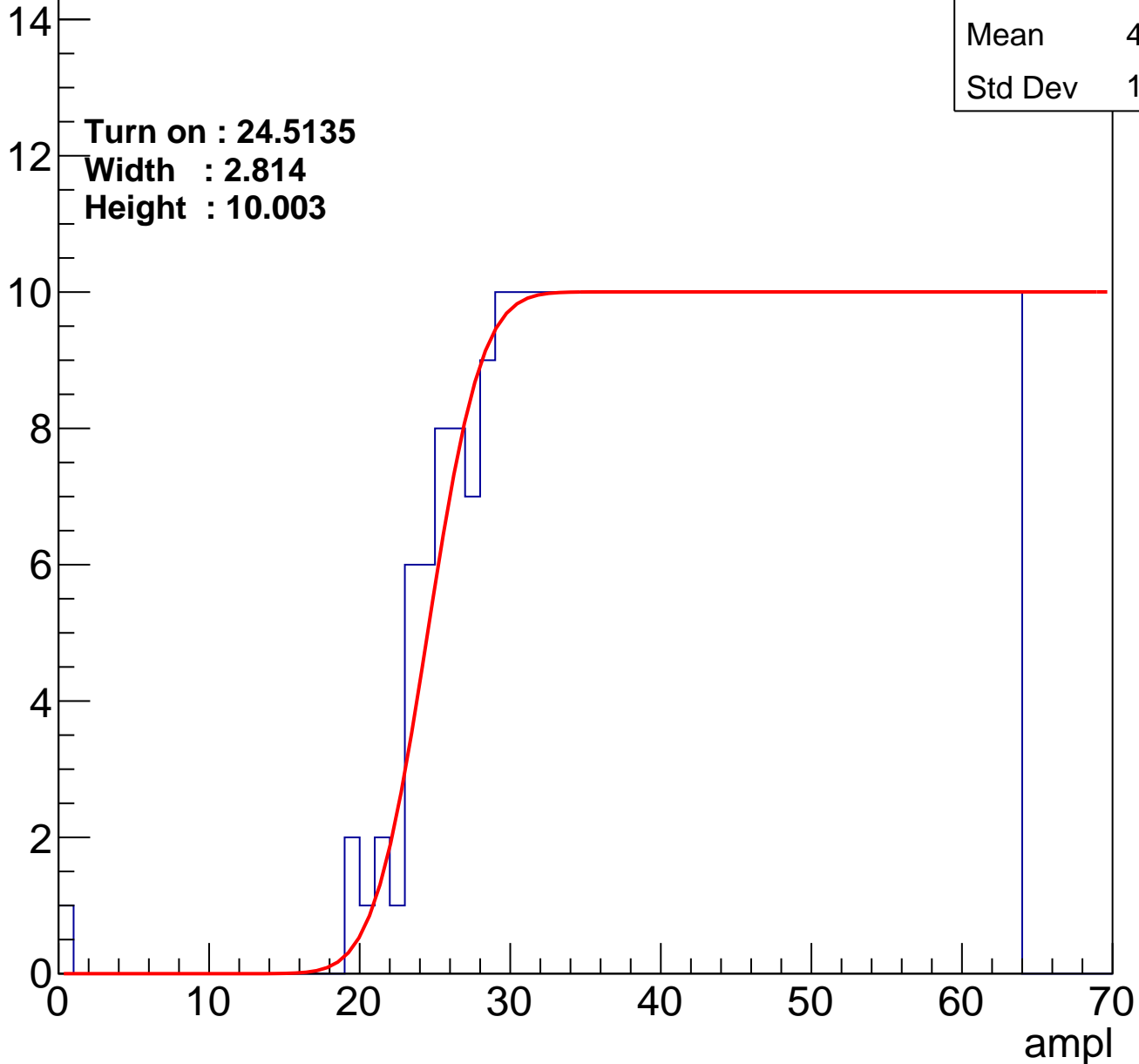
Entries	401
Mean	43.27
Std Dev	11.93

Turn on : 24.5135

Width : 2.814

Height : 10.003

Entry



B1L002S, U1-ch118

calib_packv5_042523_0143.root, FC#10, port B3

Entries	415
Mean	42.44
Std Dev	12.68

Turn on : 22.8810

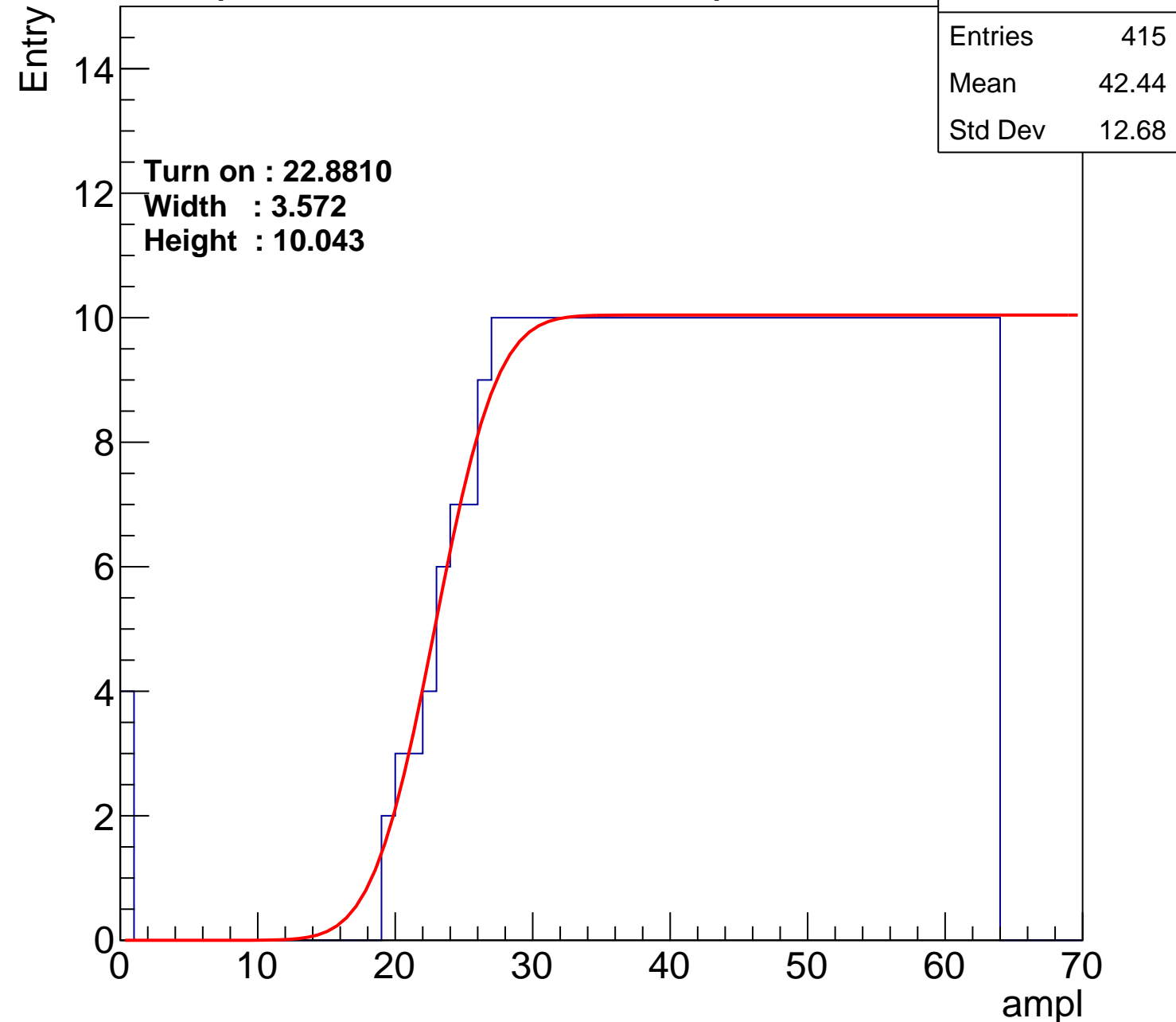
Width : 3.572

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.88
Std Dev	11.7

Turn on : 25.4206

Width : 2.084

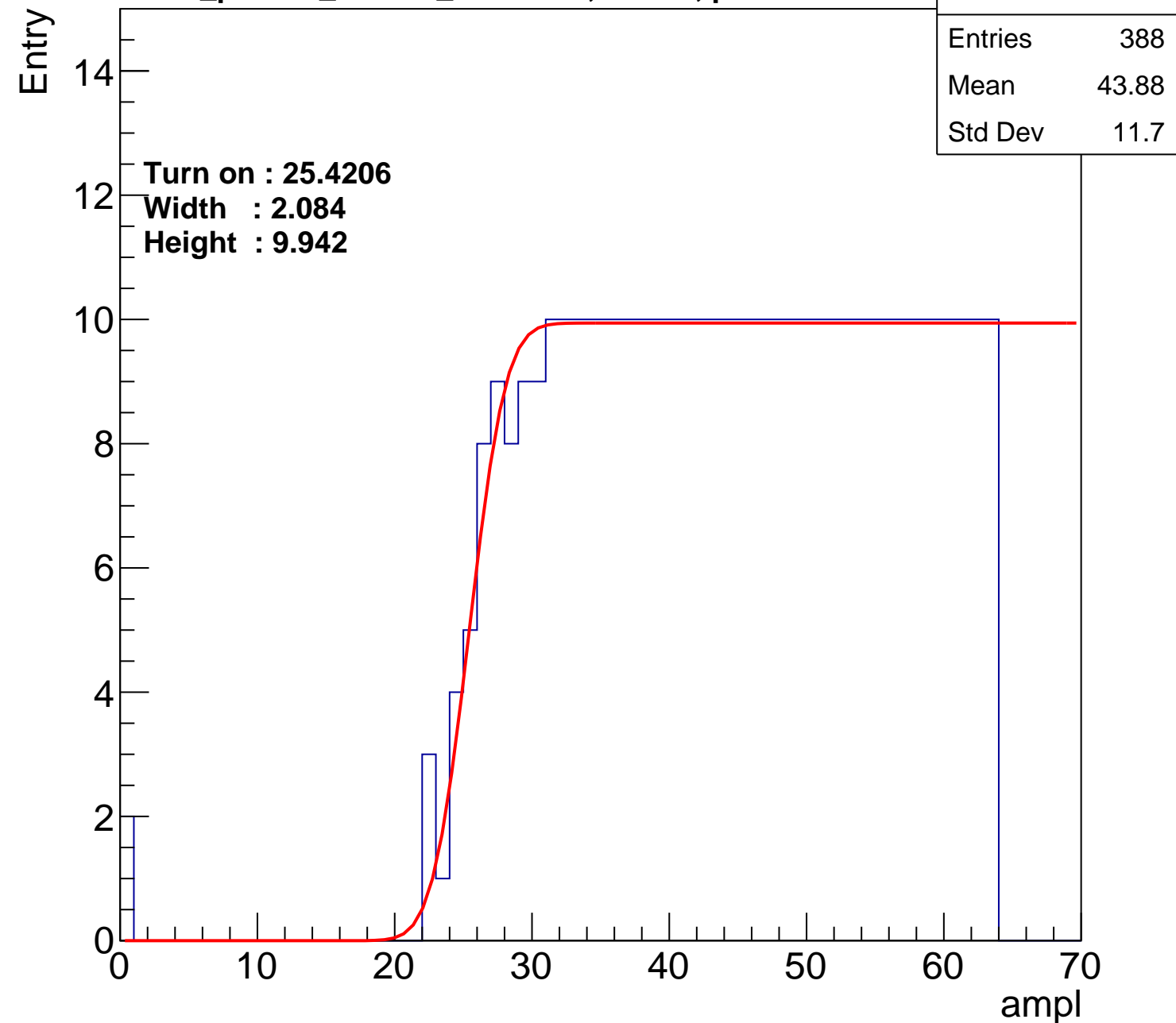
Height : 9.942

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U1-ch120

calib_packv5_042523_0143.root, FC#10, port B3

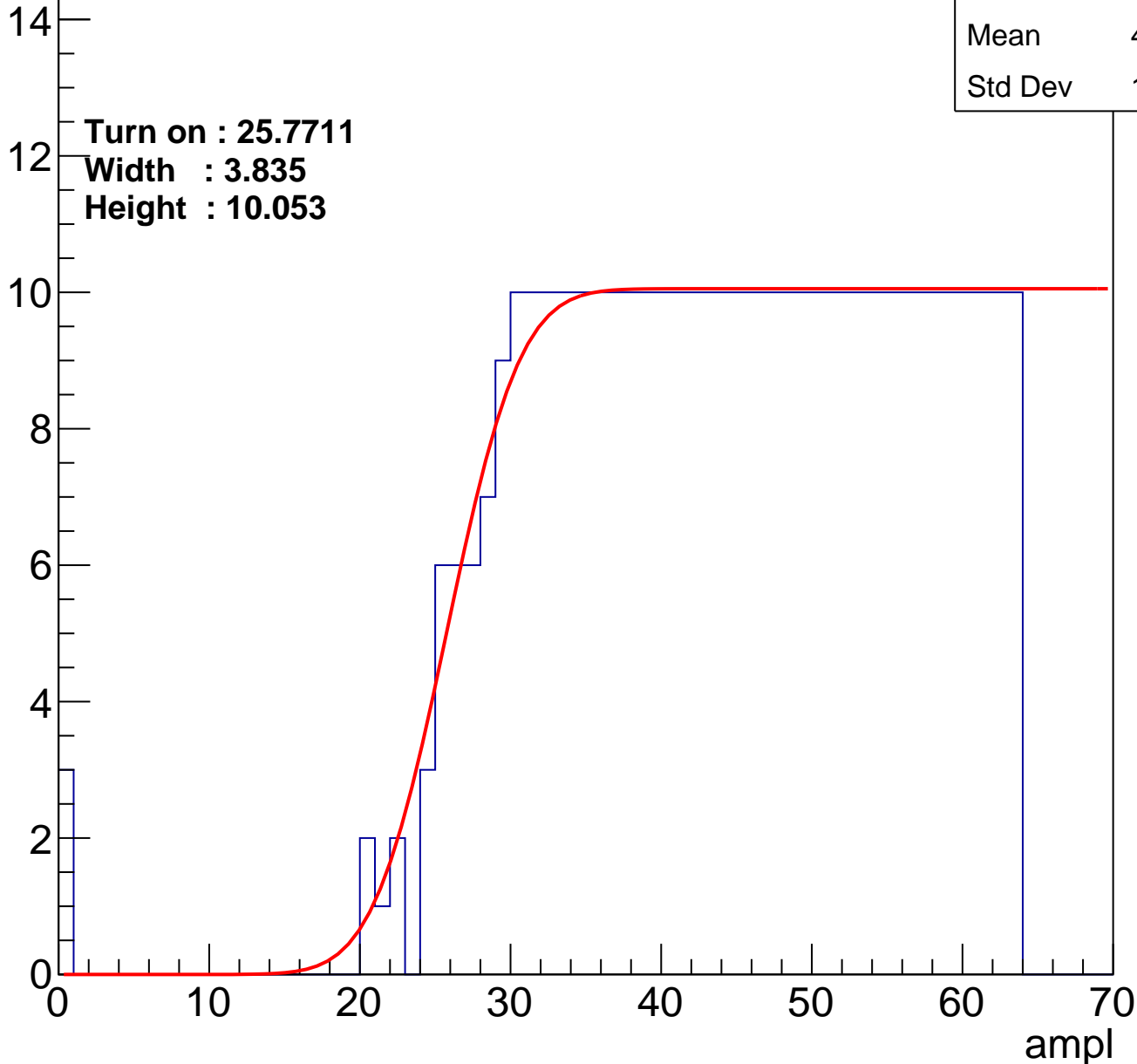
Entries	385
Mean	43.93
Std Dev	11.86

Turn on : 25.7711

Width : 3.835

Height : 10.053

Entry



B1L002S, U1-ch121

calib_packv5_042523_0143.root, FC#10, port B3

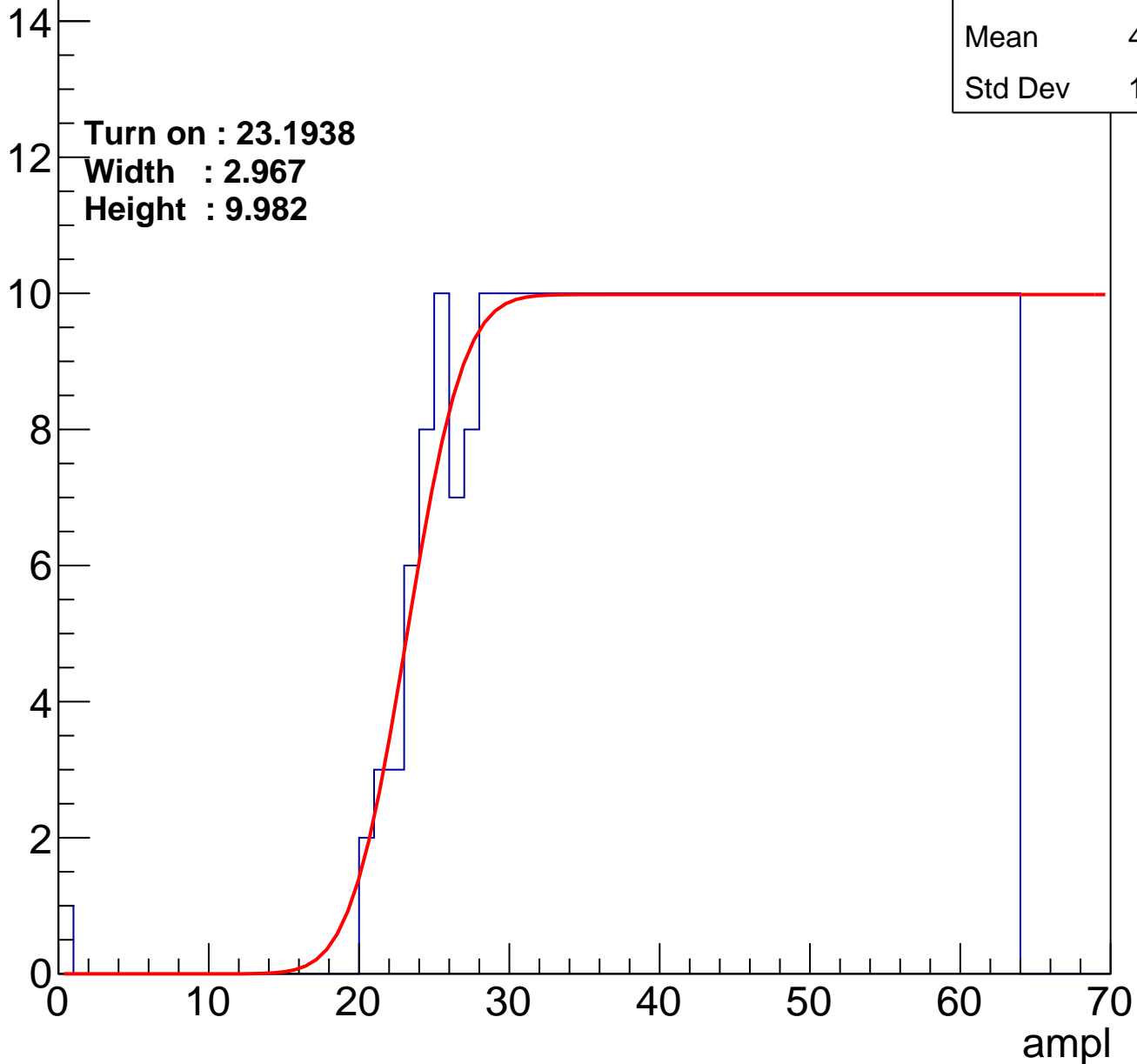
Entries	408
Mean	42.96
Std Dev	12.06

Turn on : 23.1938

Width : 2.967

Height : 9.982

Entry



B1L002S, U1-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.36
Std Dev	11.84

Turn on : 23.7945

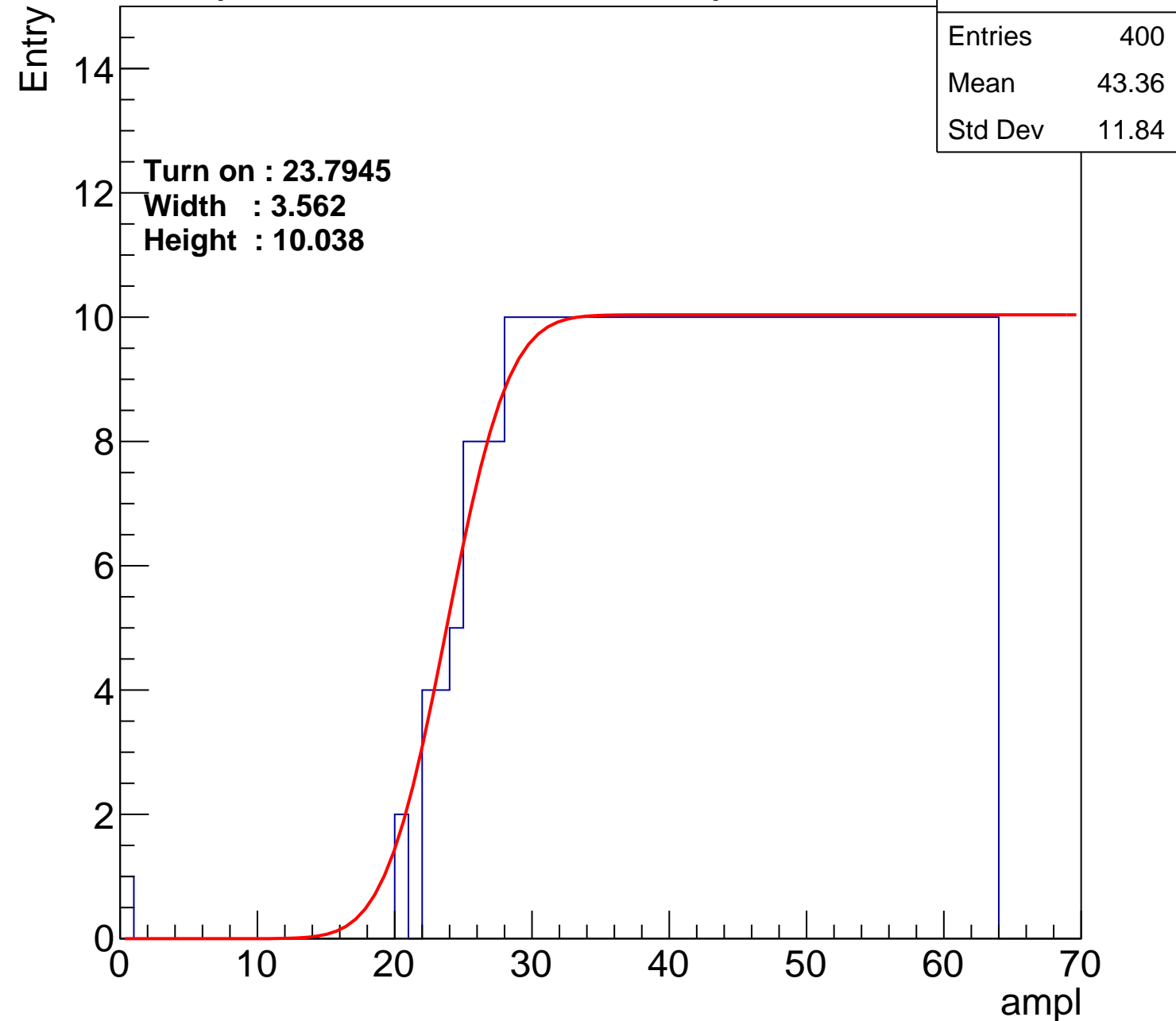
Width : 3.562

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.66
Std Dev	12.25

Turn on : 26.5857

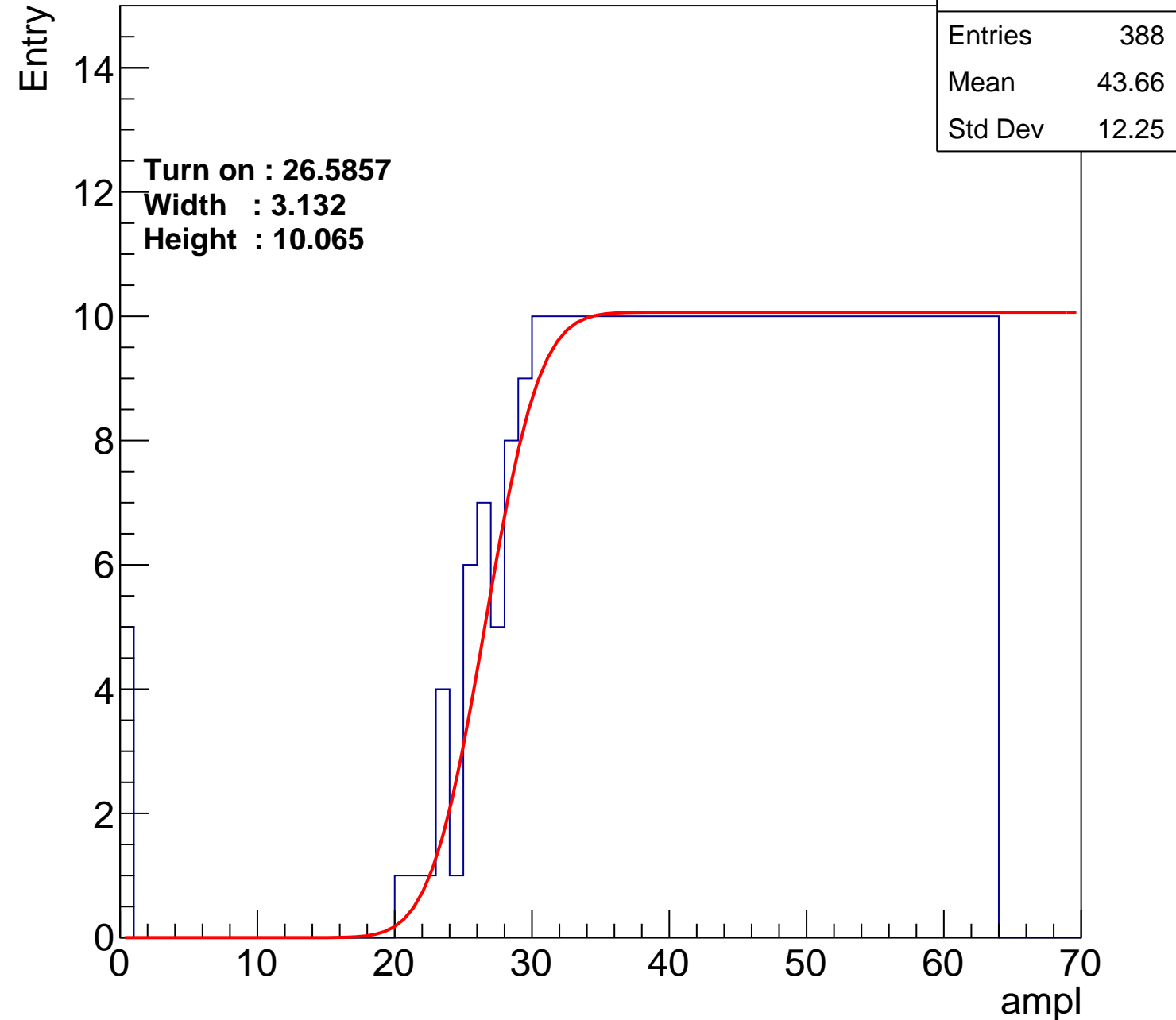
Width : 3.132

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	405
Mean	43.07
Std Dev	12.1

Turn on : 24.3234

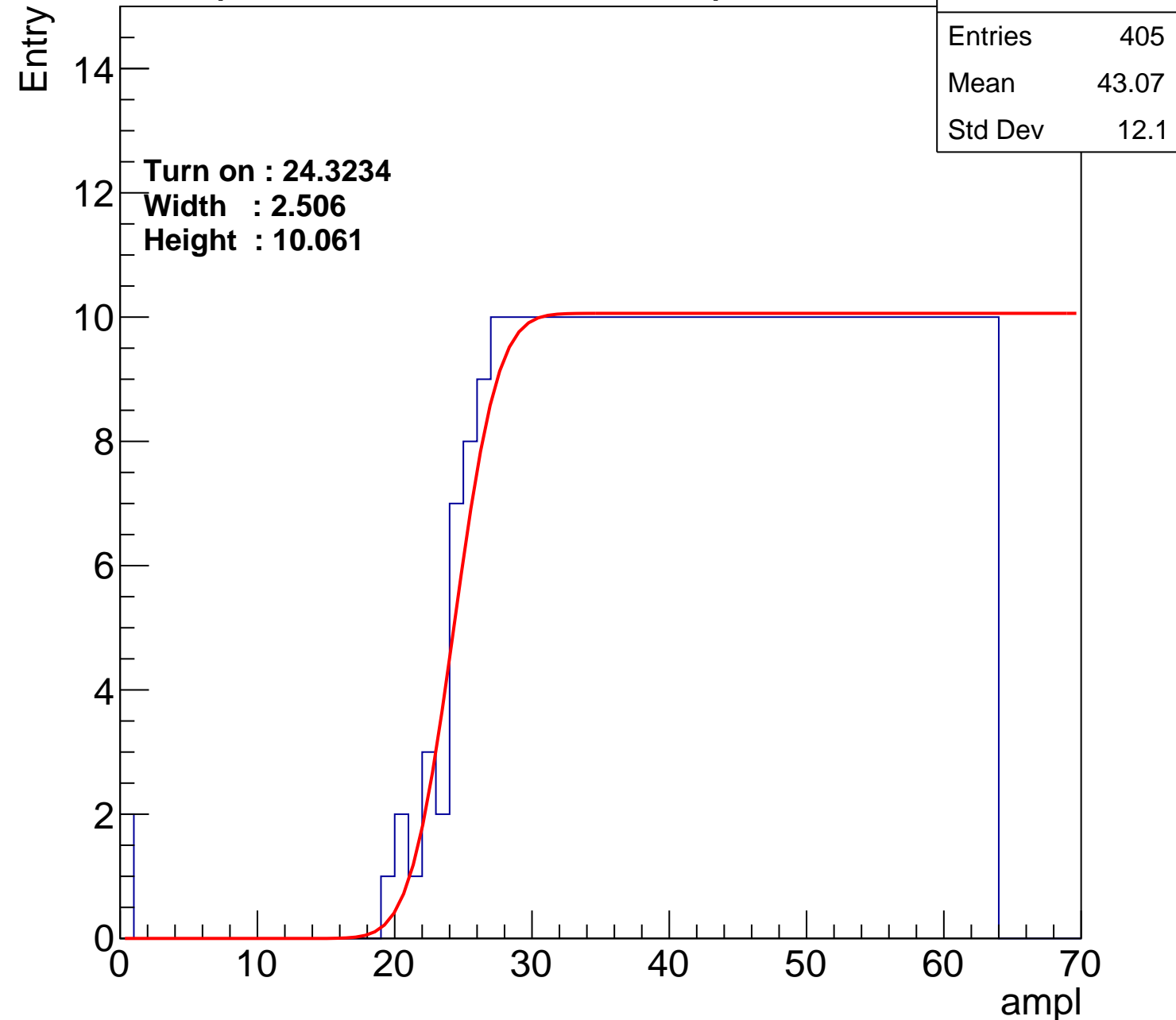
Width : 2.506

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch125

calib_packv5_042523_0143.root, FC#10, port B3

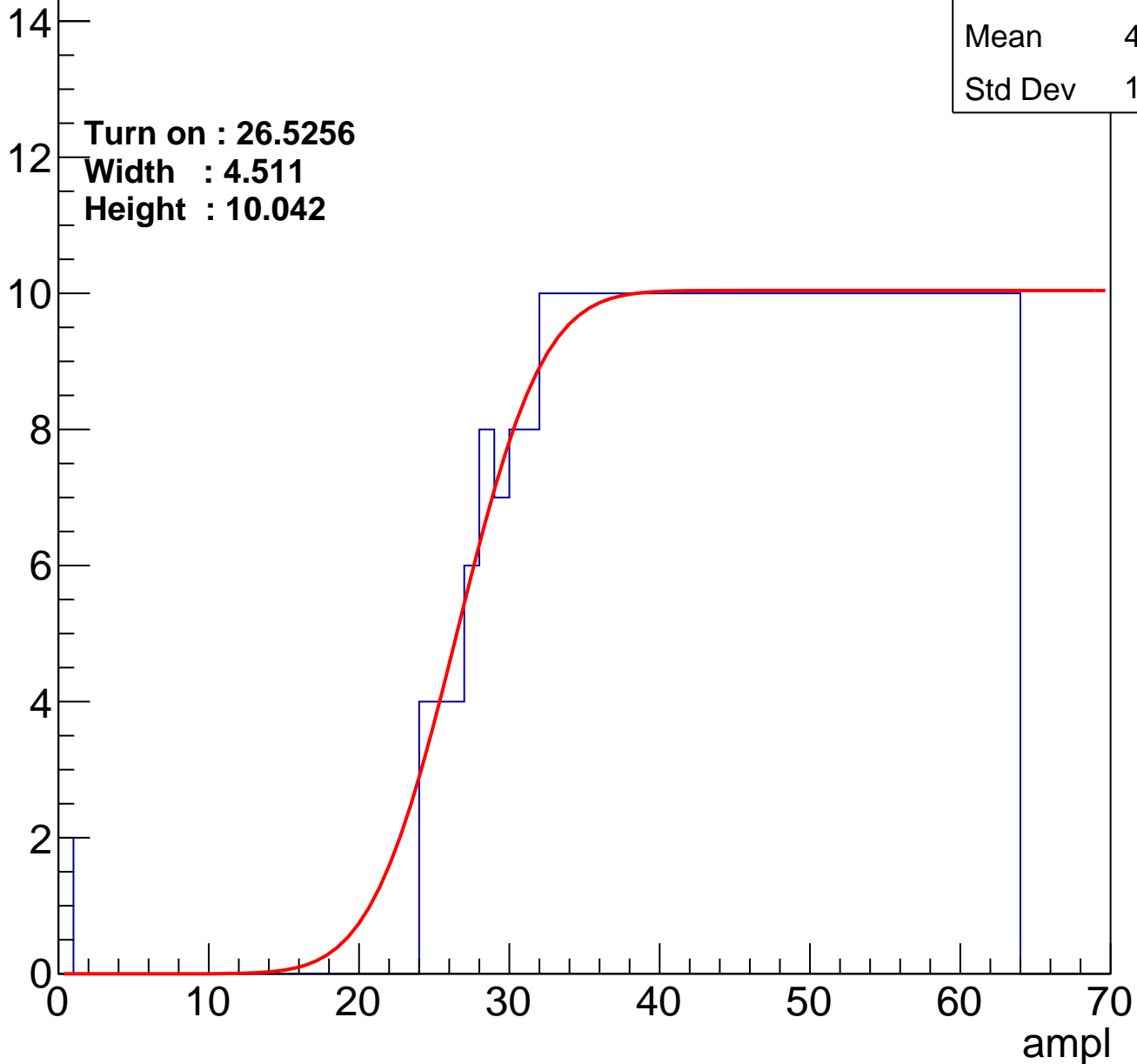
Entries	371
Mean	44.68
Std Dev	11.32

Turn on : 26.5256

Width : 4.511

Height : 10.042

Entry



B1L002S, U1-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.75
Std Dev	11.81

Turn on : 25.5131

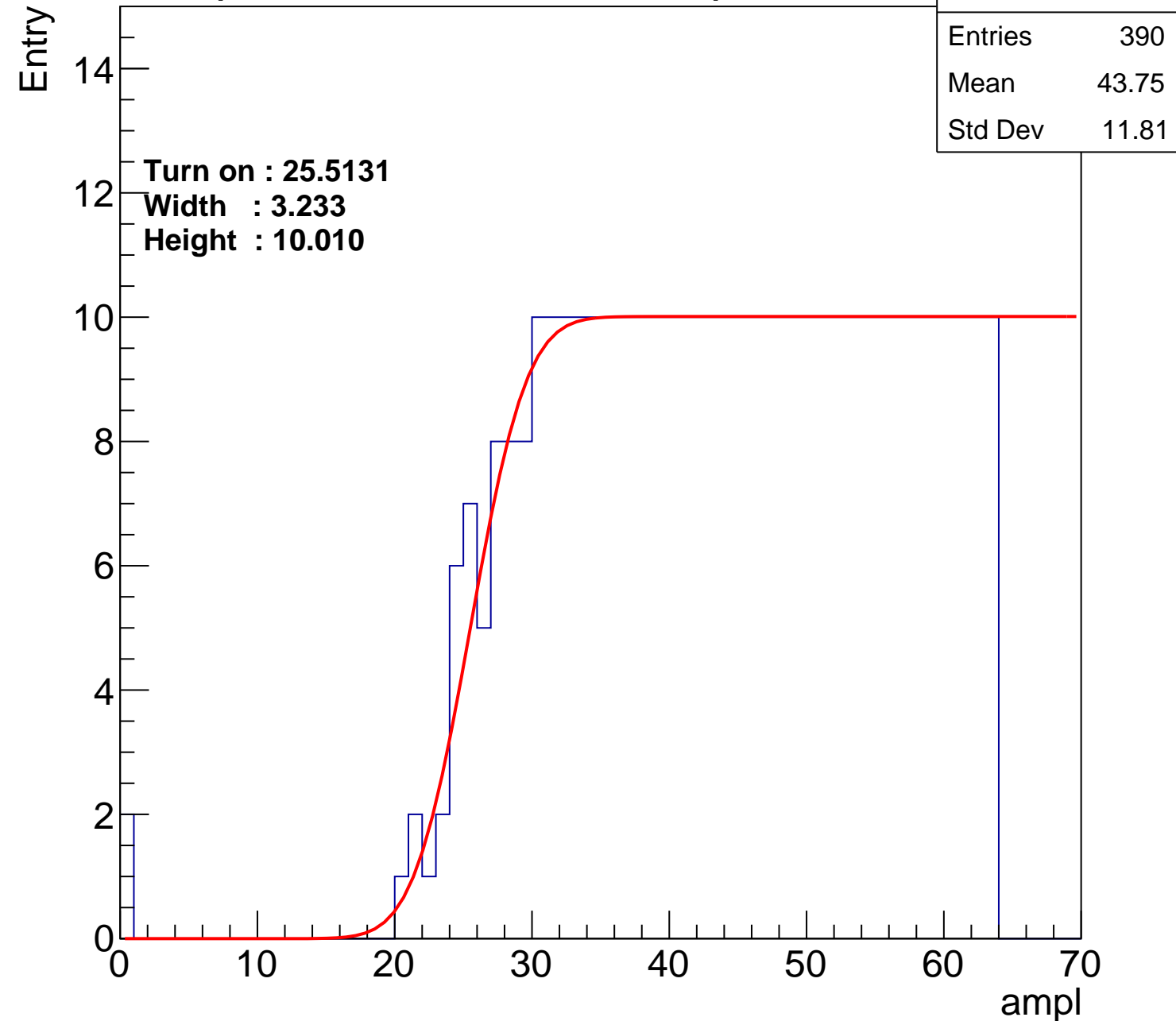
Width : 3.233

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.75
Std Dev	11.91

Turn on : 25.3093

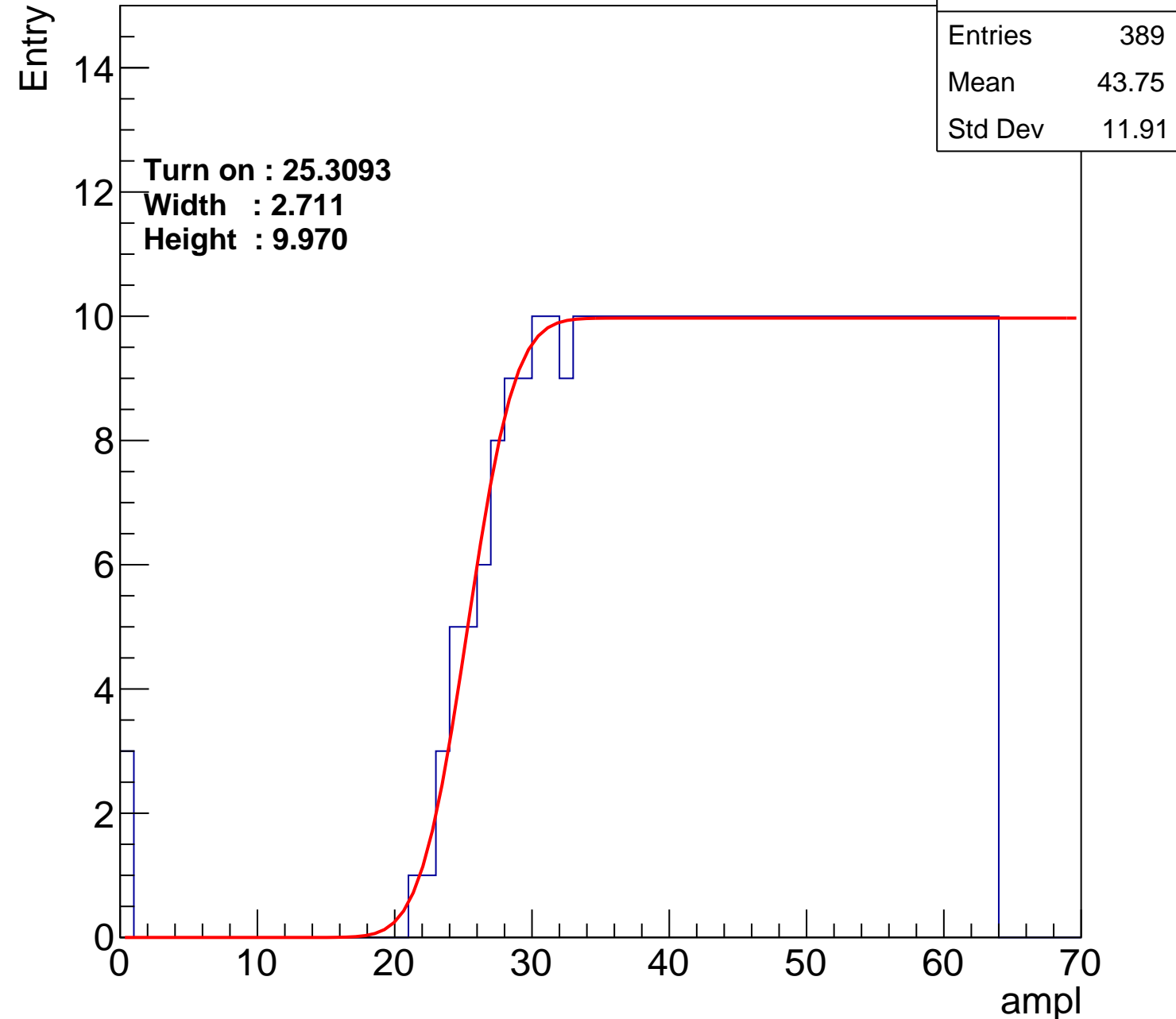
Width : 2.711

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U1-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.75
Std Dev	11.91

Turn on : 25.3093

Width : 2.711

Height : 9.970

Entry

