

B0L102S, U20-ch0

calib_packv5_042523_0143.root, FC#12, port B1

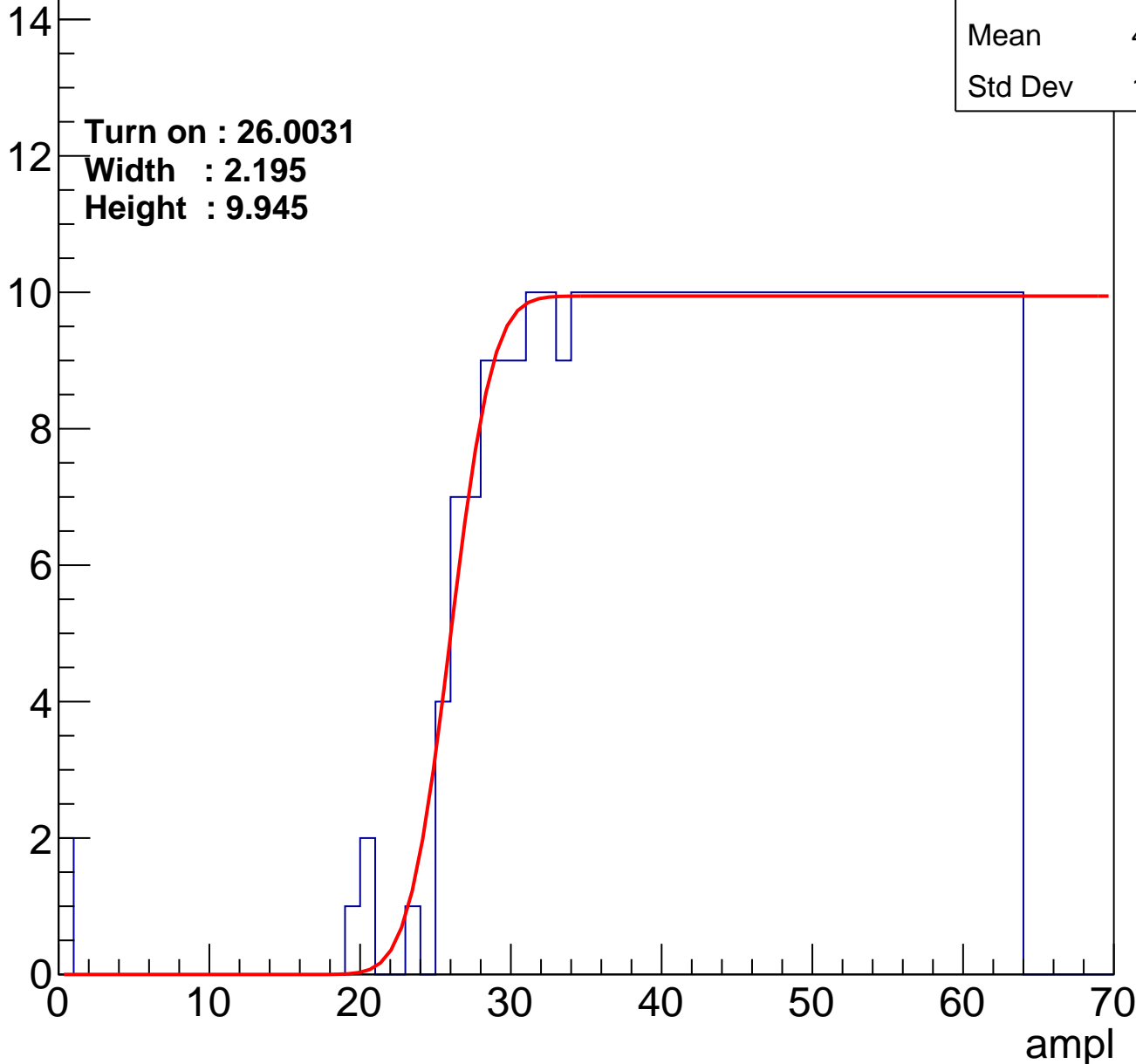
Entries	380
Mean	44.24
Std Dev	11.55

Turn on : 26.0031

Width : 2.195

Height : 9.945

Entry



B0L102S, U20-ch1

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.26
Std Dev	11.04

Turn on : 28.5947

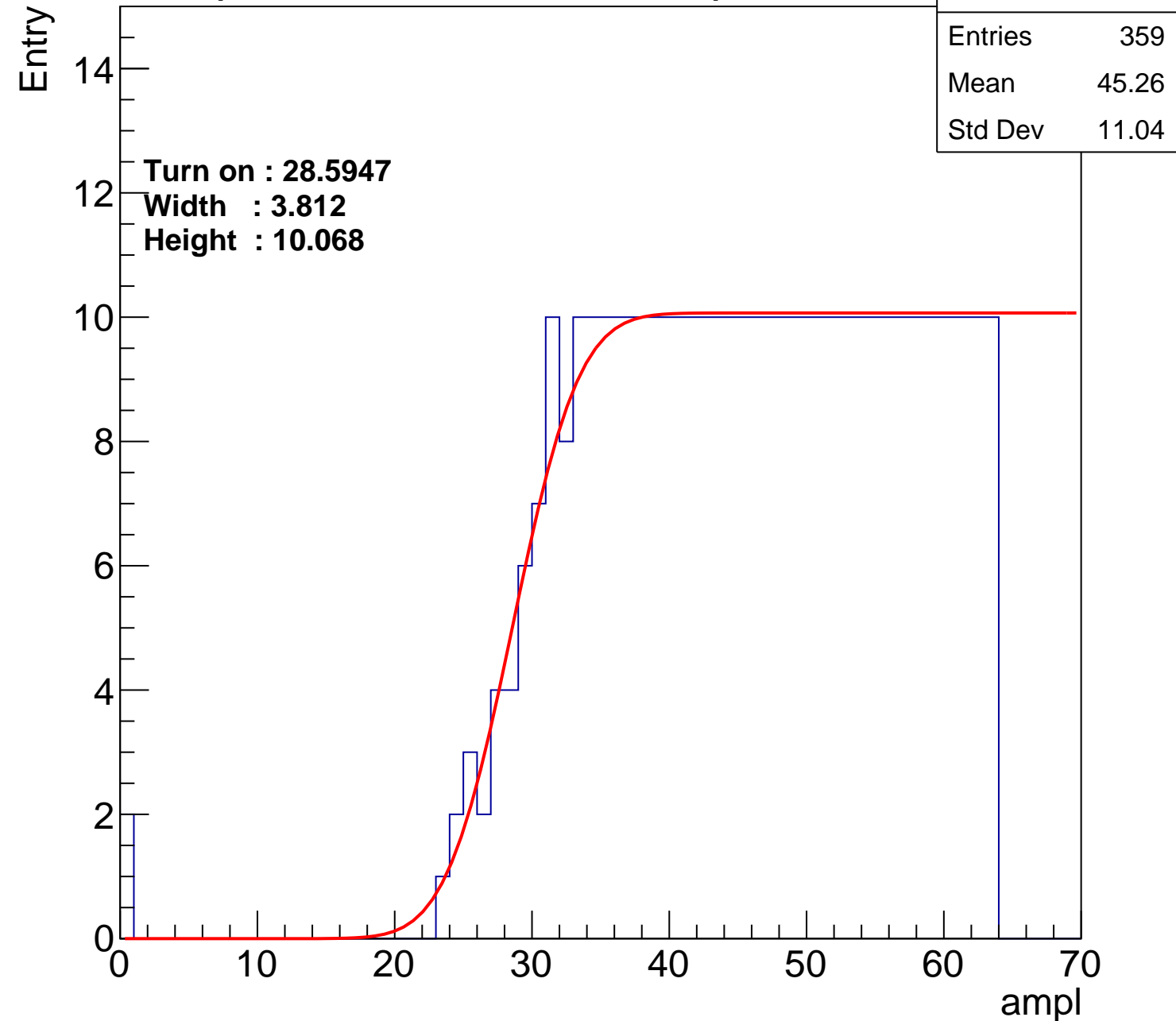
Width : 3.812

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch2

calib_packv5_042523_0143.root, FC#12, port B1

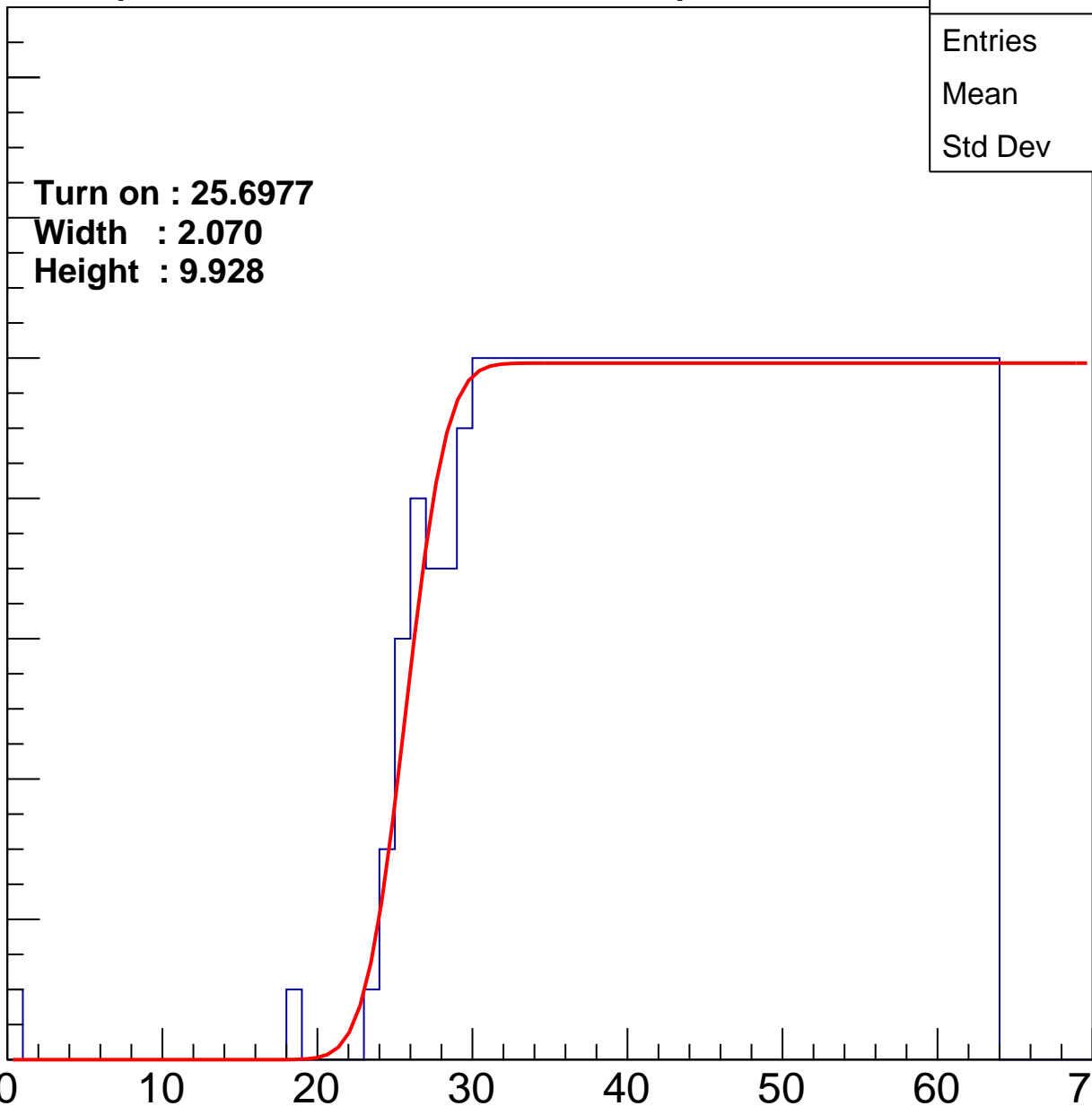
Entry

14
12
10
8
6
4
2
0

Turn on : 25.6977
Width : 2.070
Height : 9.928

Entries	383
Mean	44.2
Std Dev	11.39

ampl



B0L102S, U20-ch3

calib_packv5_042523_0143.root, FC#12, port B1

Entries	360
Mean	45.35
Std Dev	10.75

Turn on : 28.4615

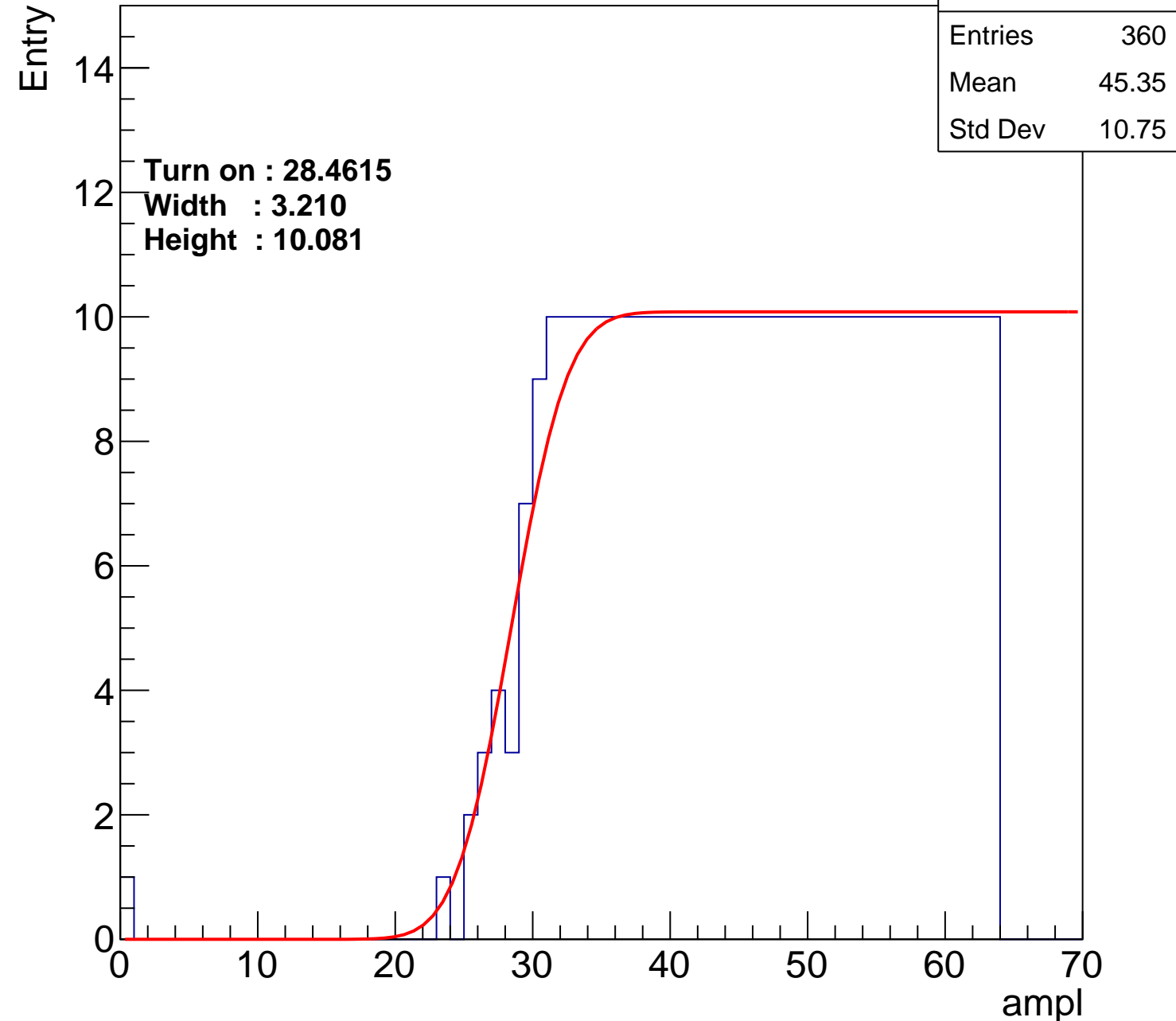
Width : 3.210

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch4

calib_packv5_042523_0143.root, FC#12, port B1

Entries	408
Mean	42.9
Std Dev	12.21

Turn on : 23.4107

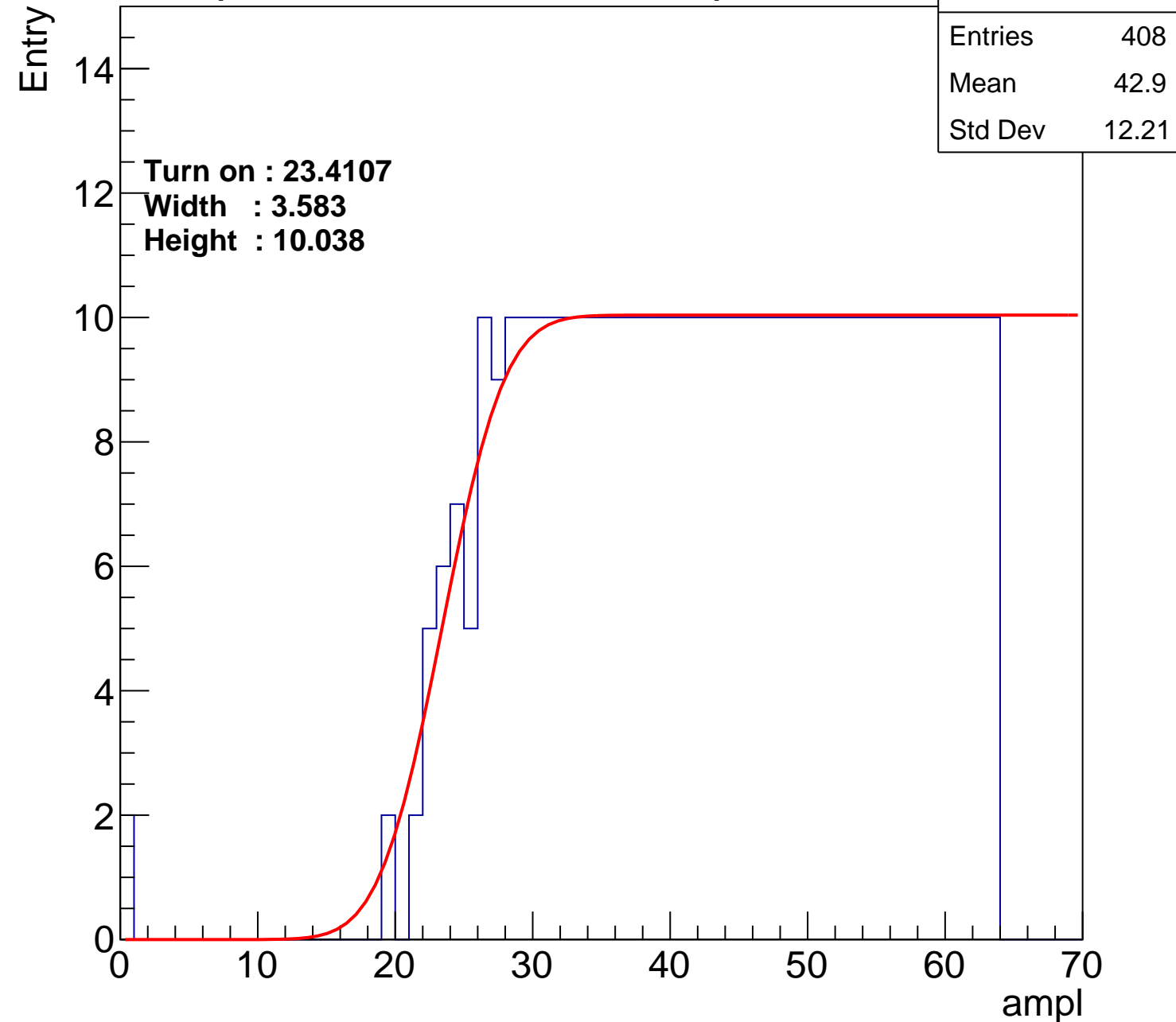
Width : 3.583

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch5

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	45.03
Std Dev	10.99

Turn on : 27.7714

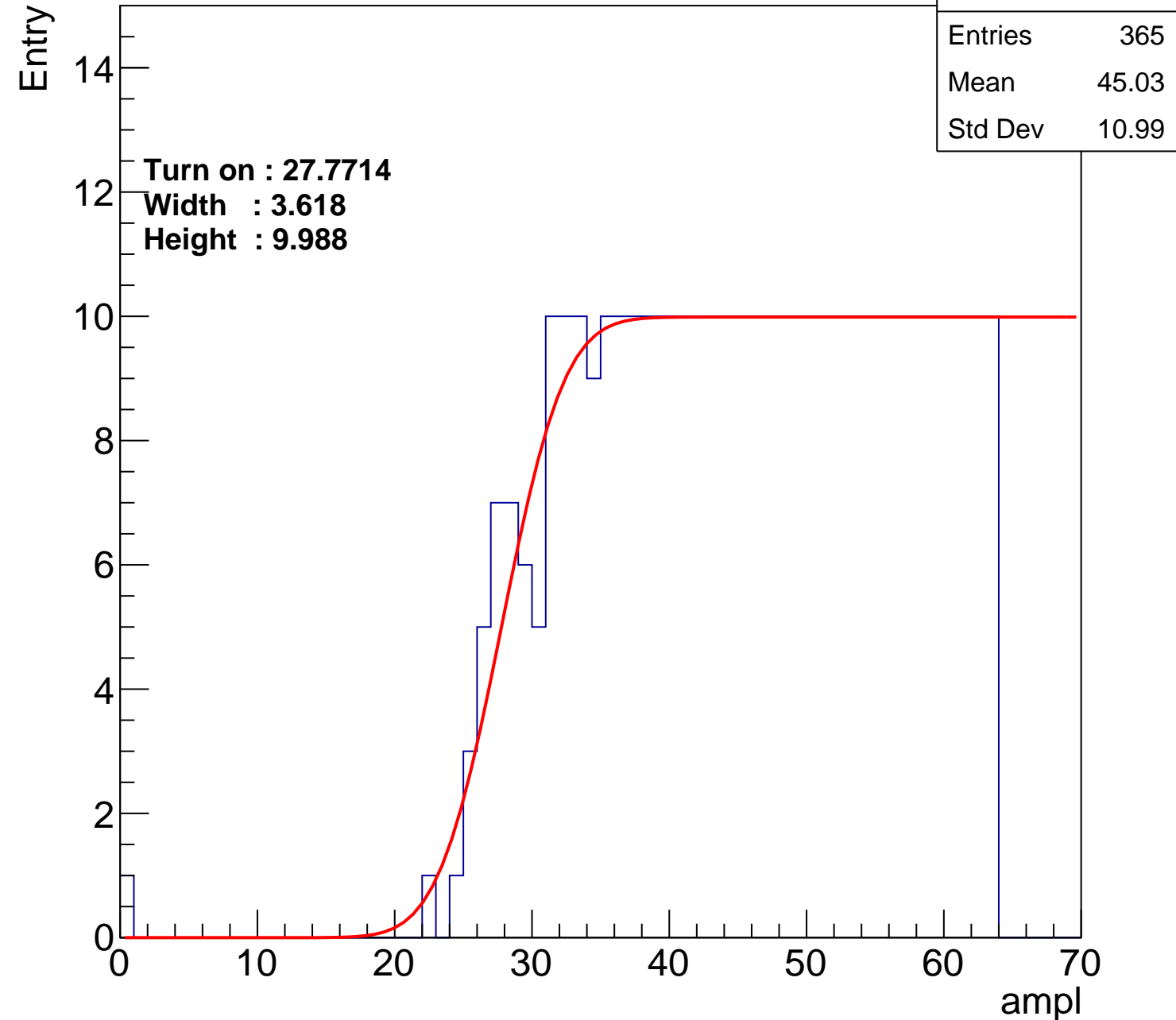
Width : 3.618

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch6

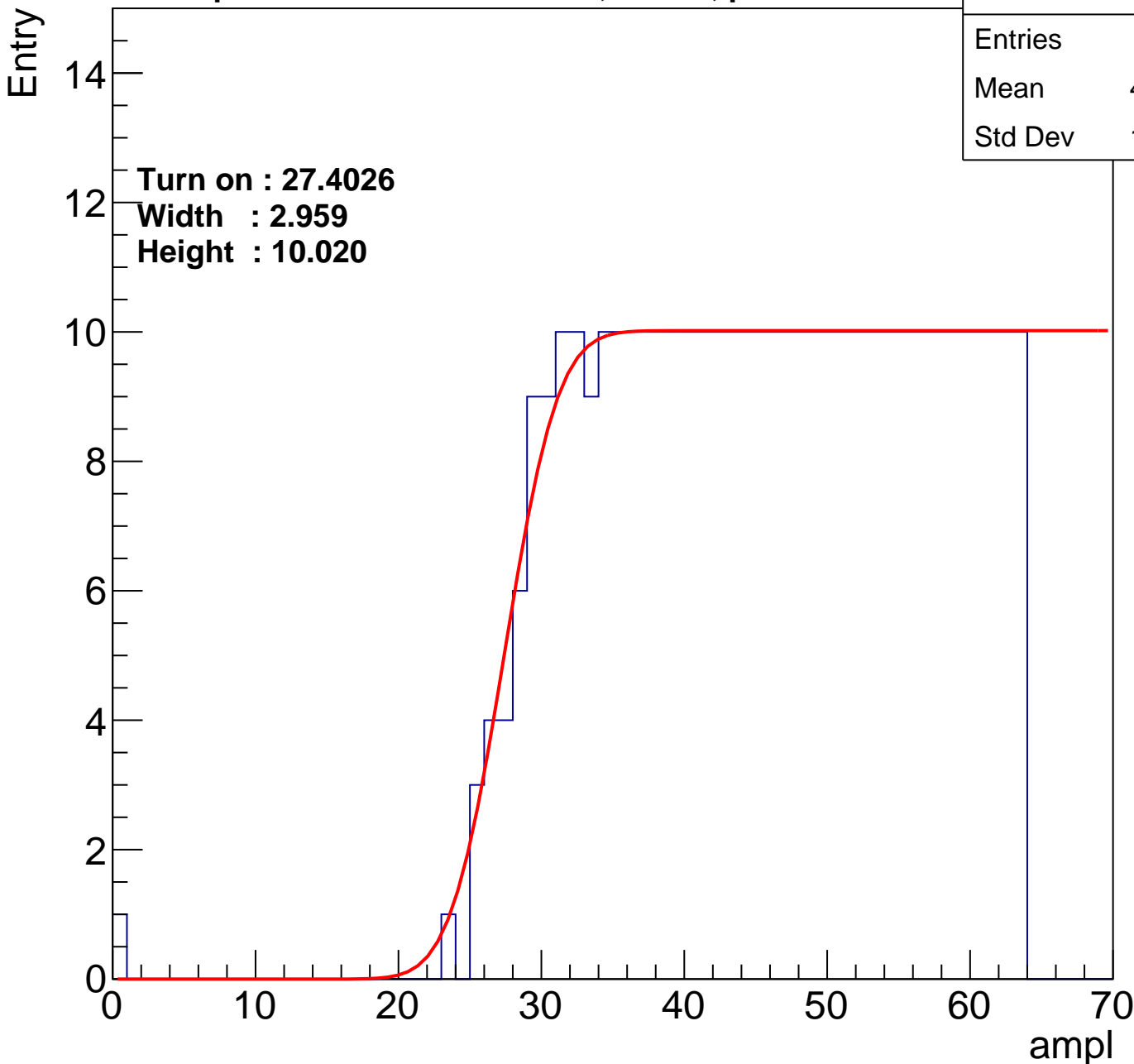
calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.04
Std Dev	10.92

Turn on : 27.4026

Width : 2.959

Height : 10.020



B0L102S, U20-ch7

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.17
Std Dev	11.37

Turn on : 25.7722

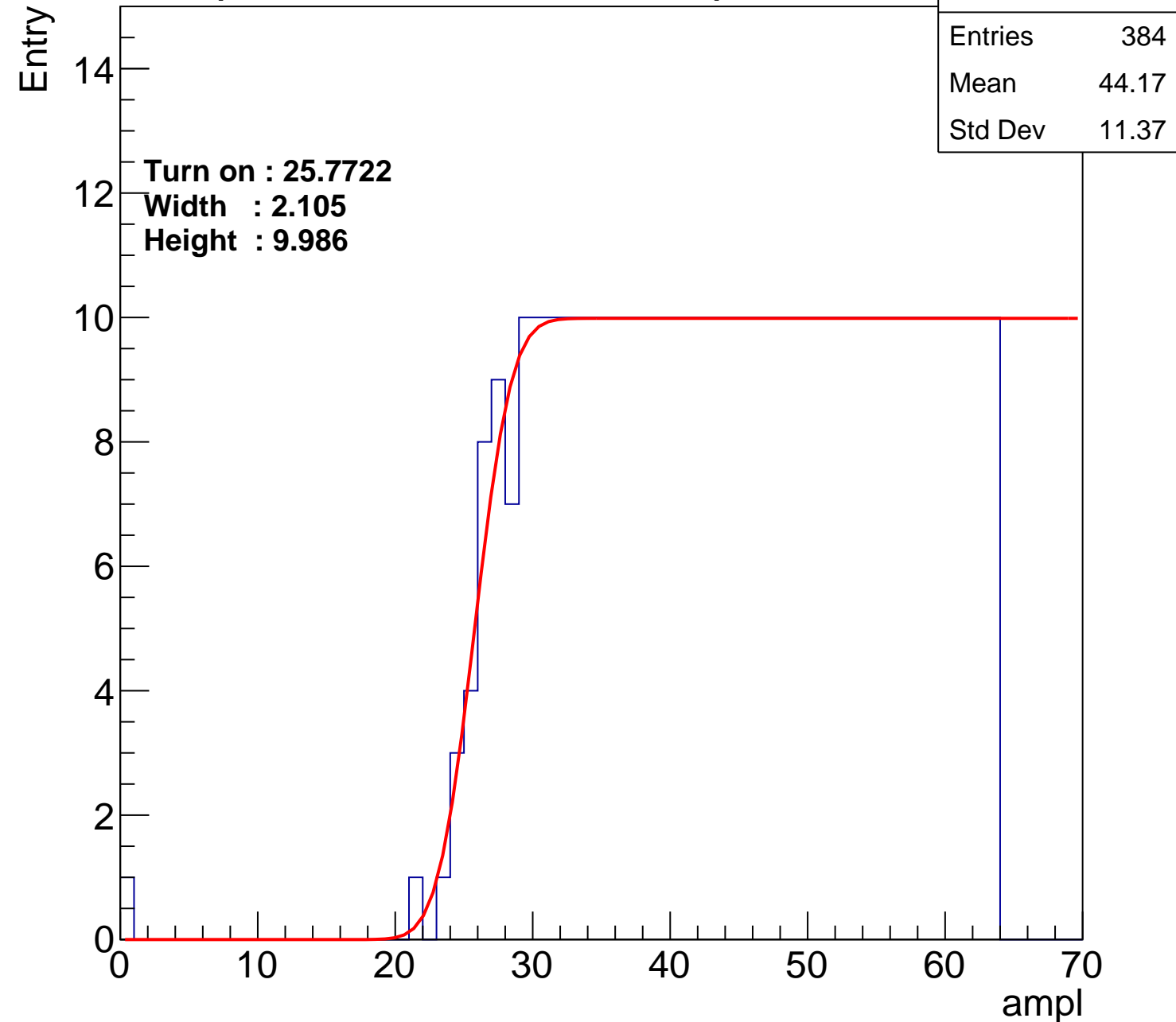
Width : 2.105

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch8

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.32
Std Dev	11.3

Turn on : 25.2881

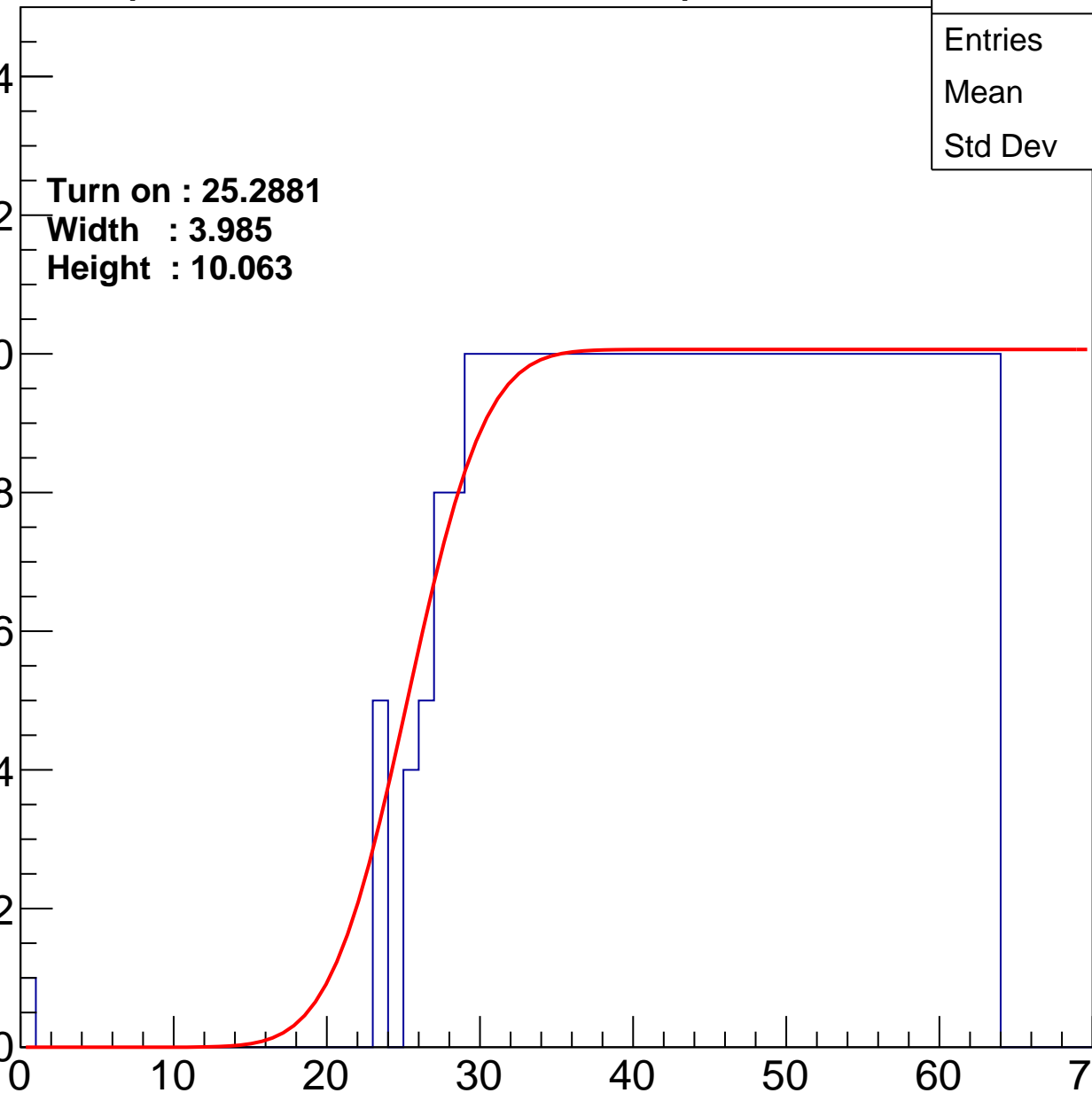
Width : 3.985

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch9

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.69
Std Dev	11.12

Turn on : 26.6728

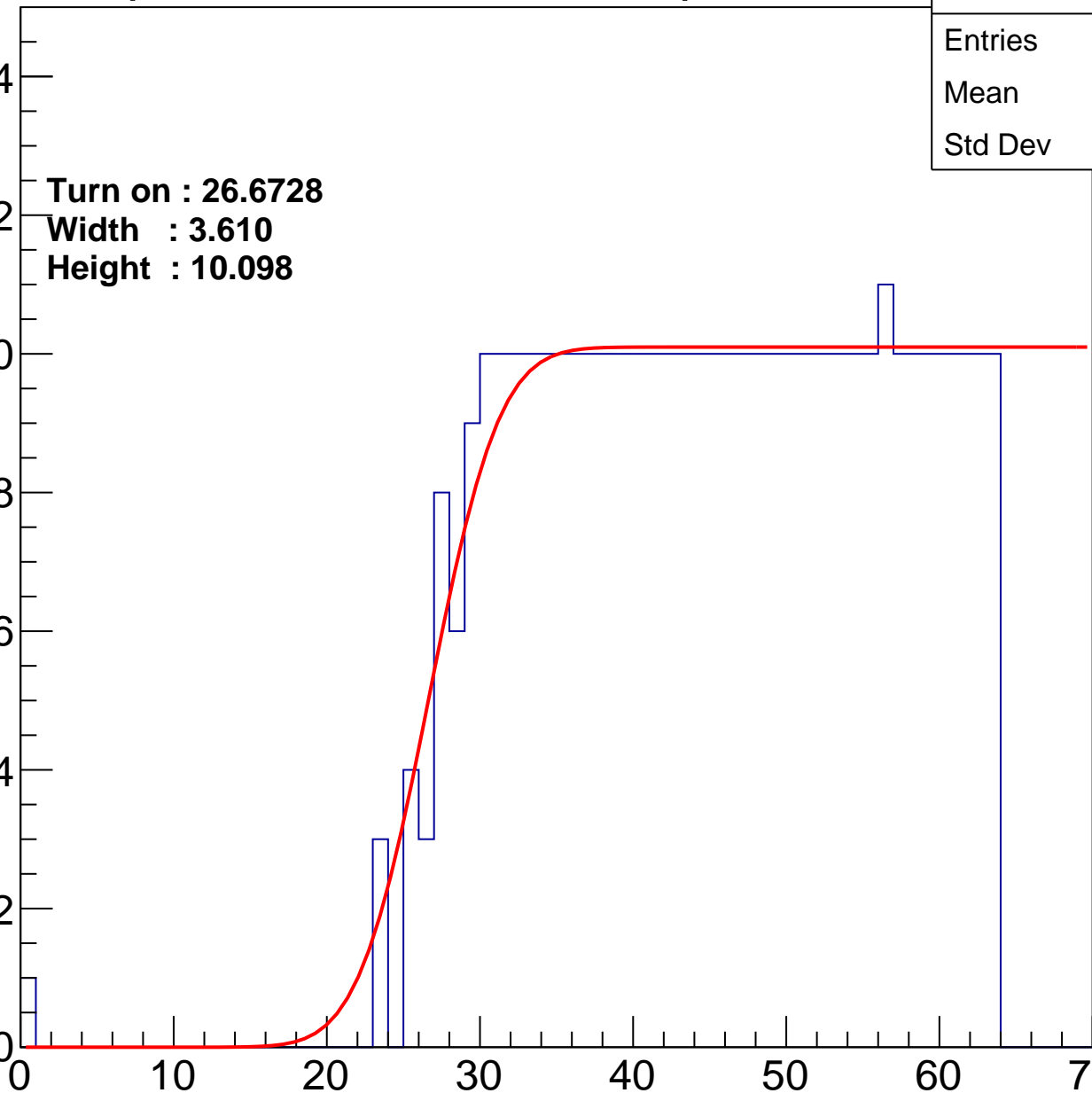
Width : 3.610

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch10

calib_packv5_042523_0143.root, FC#12, port B1

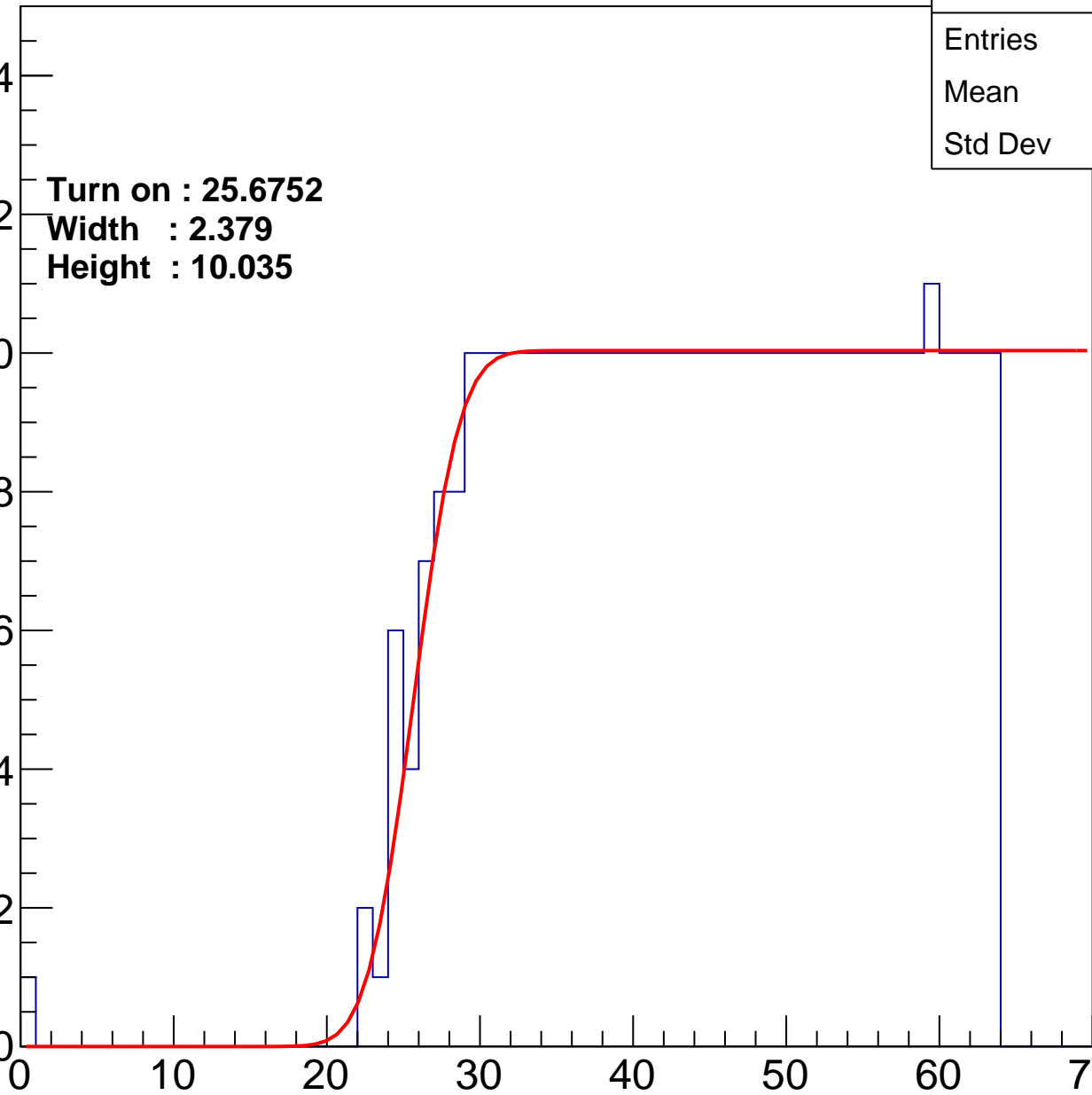
Entry

14
12
10
8
6
4
2
0

Turn on : 25.6752
Width : 2.379
Height : 10.035

Entries	388
Mean	44.05
Std Dev	11.48

ampl



BOL102S, U20-ch11

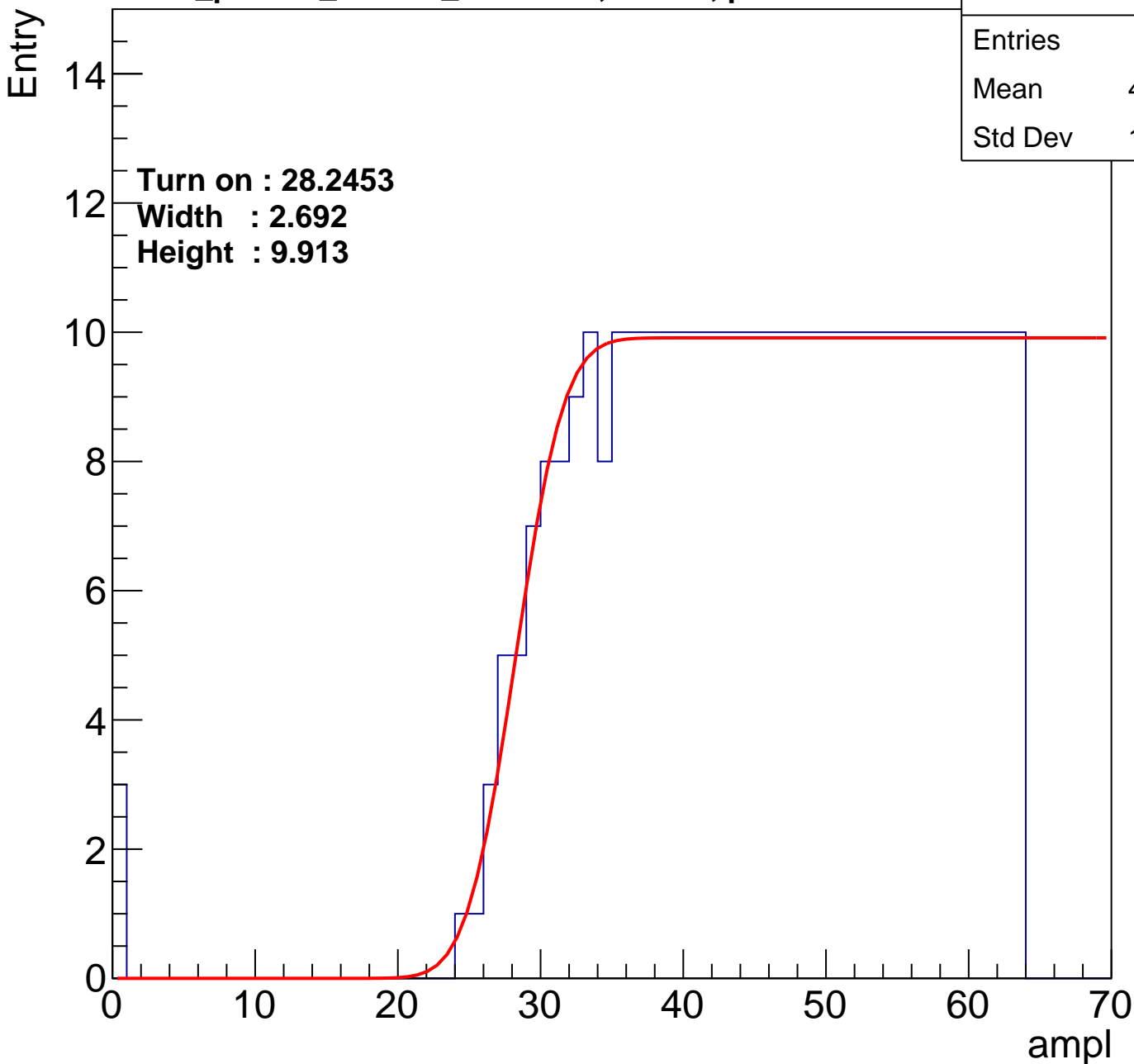
calib_packv5_042523_0143.root, FC#12, port B1

Turn on : 28.2453

Width : 2.692

Height : 9.913

Entries	358
Mean	45.23
Std Dev	11.22



B0L102S, U20-ch12

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.8
Std Dev	11.22

Turn on : 27.9003

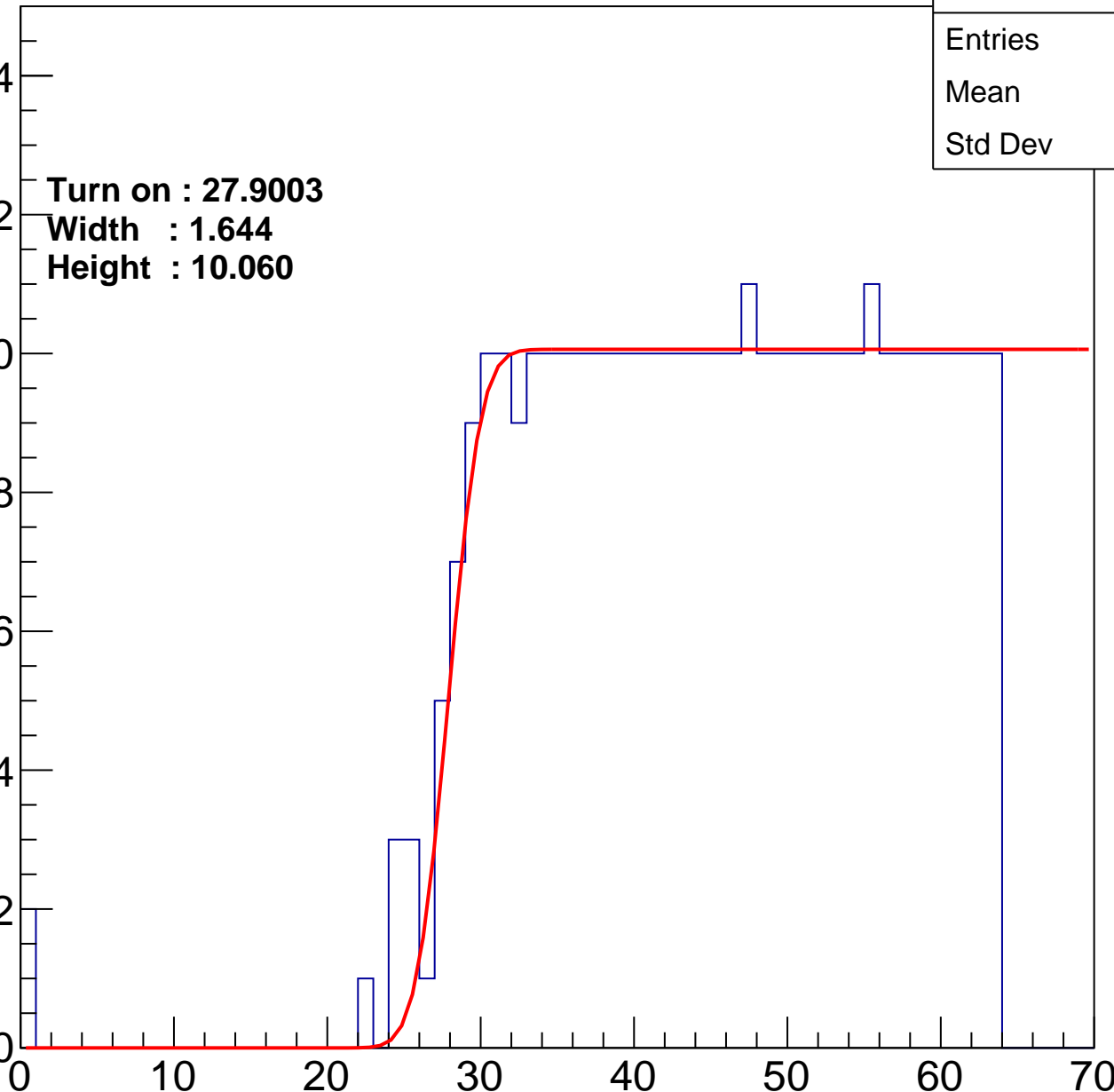
Width : 1.644

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch13

calib_packv5_042523_0143.root, FC#12, port B1

Entries	396
Mean	43.36
Std Dev	12.24

Turn on : 25.4657

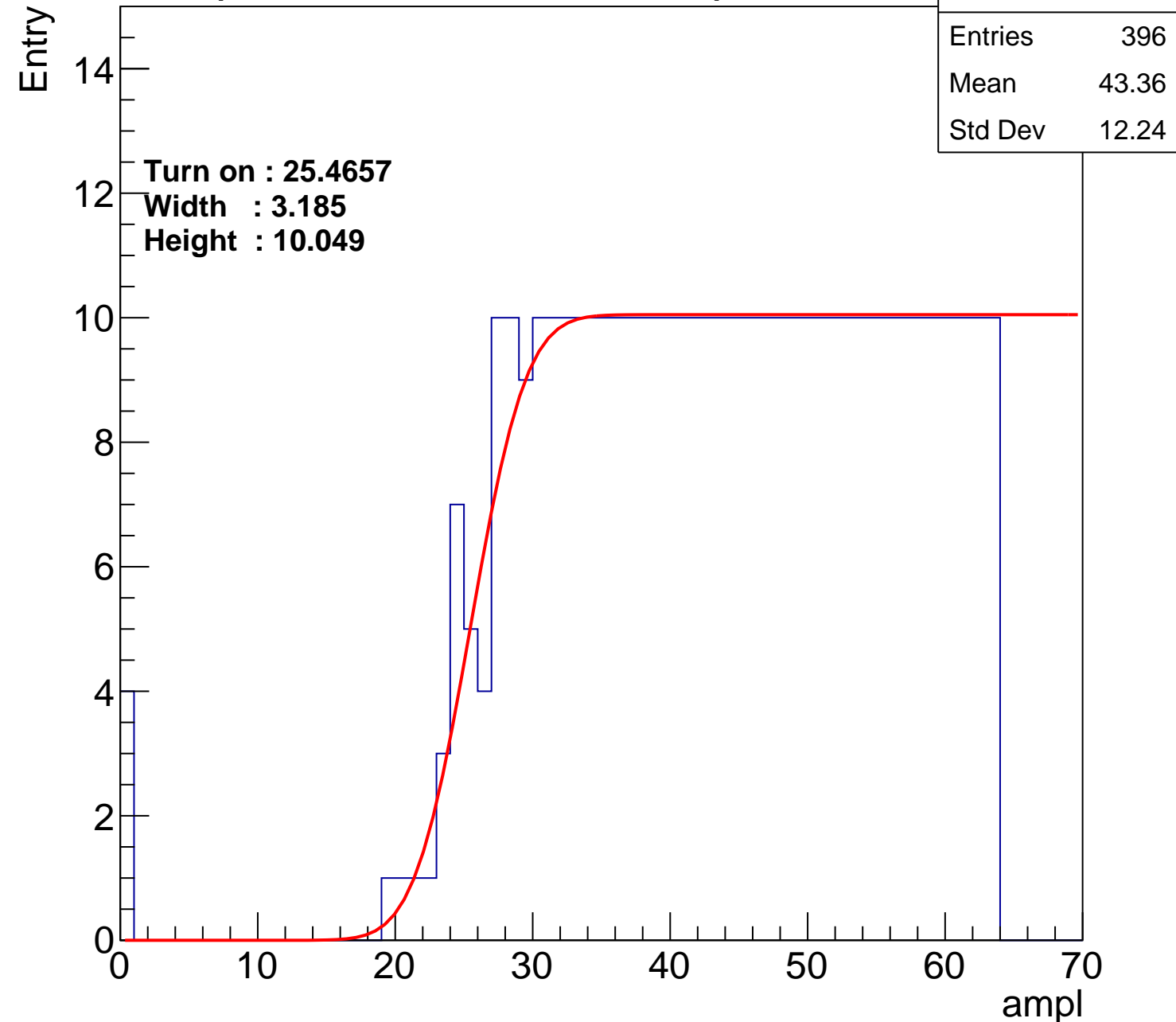
Width : 3.185

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch14

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.67
Std Dev	11.43

Turn on : 27.5954

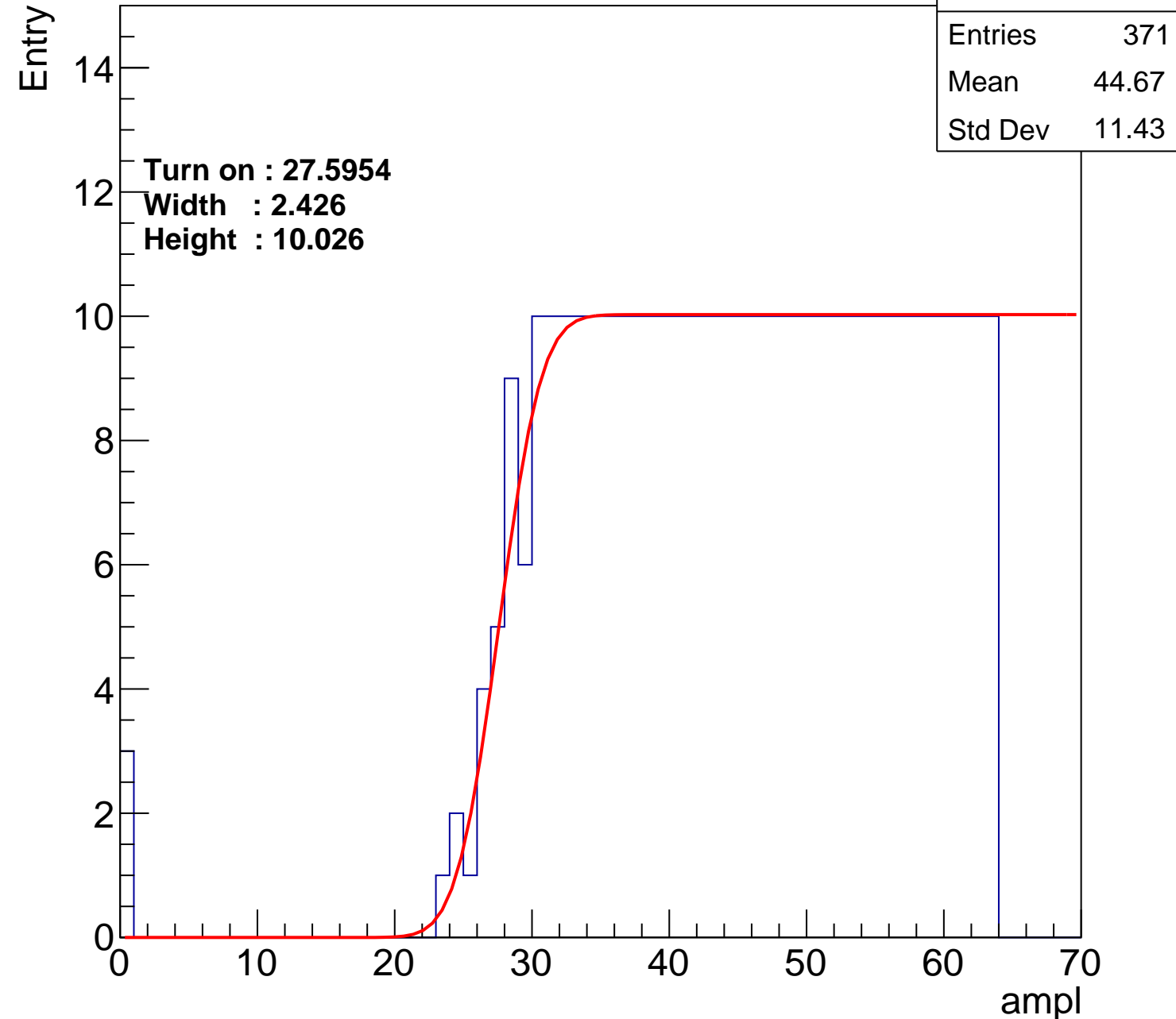
Width : 2.426

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch15

calib_packv5_042523_0143.root, FC#12, port B1

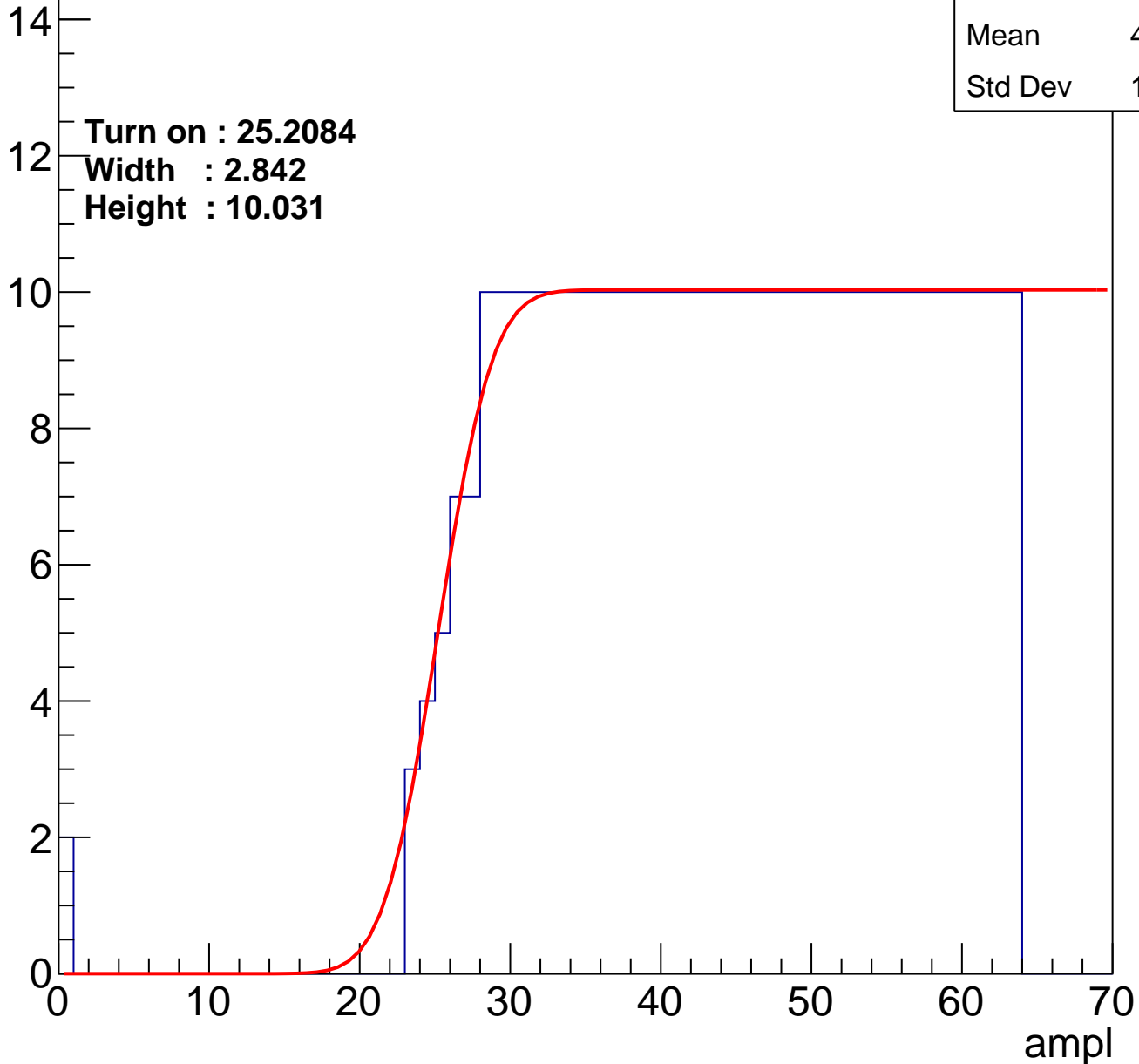
Entries	388
Mean	43.92
Std Dev	11.64

Turn on : 25.2084

Width : 2.842

Height : 10.031

Entry



B0L102S, U20-ch16

calib_packv5_042523_0143.root, FC#12, port B1

Entries	401
Mean	43.23
Std Dev	12.05

Turn on : 24.6438

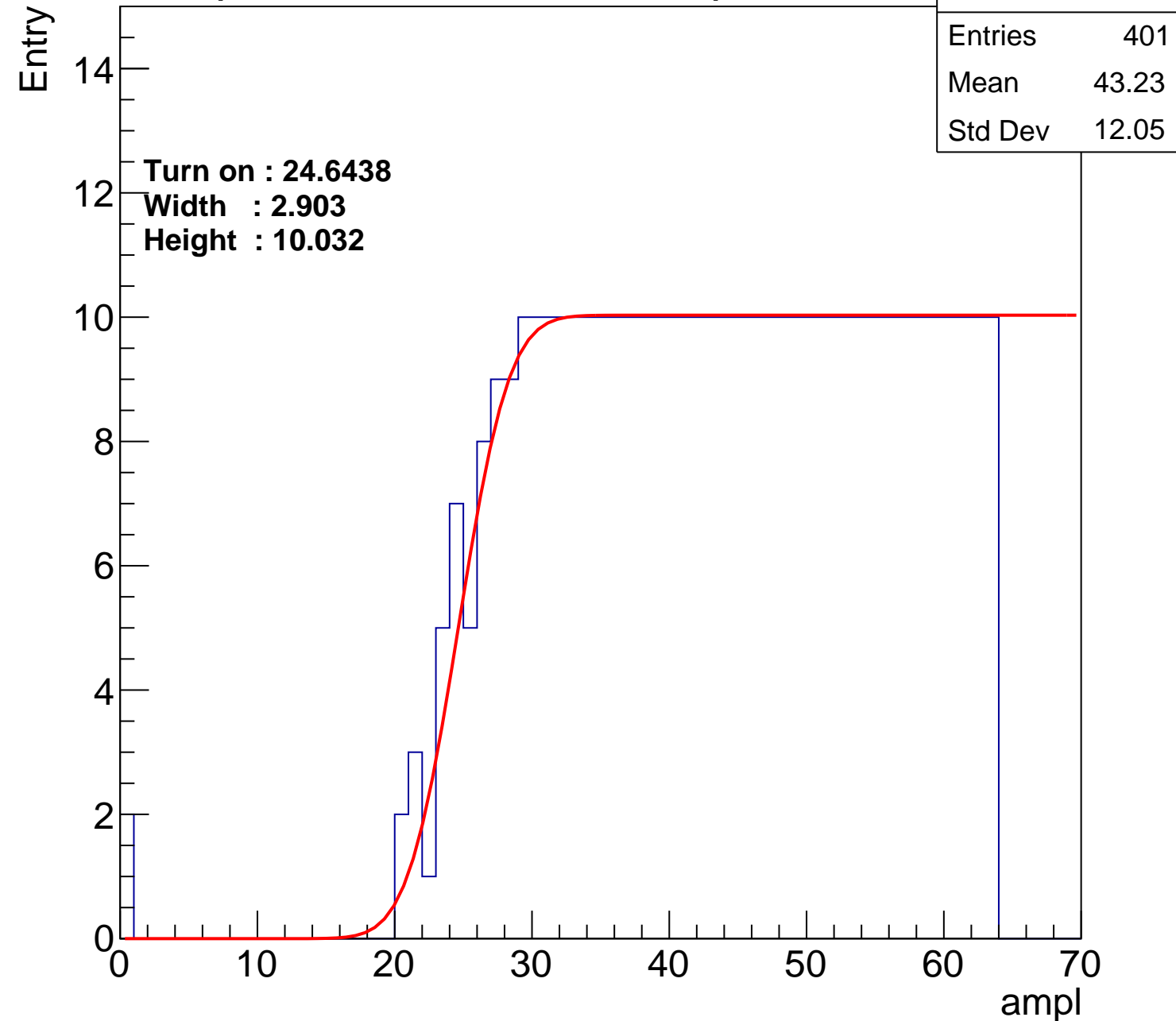
Width : 2.903

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch17

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.19
Std Dev	11.52

Turn on : 26.3631

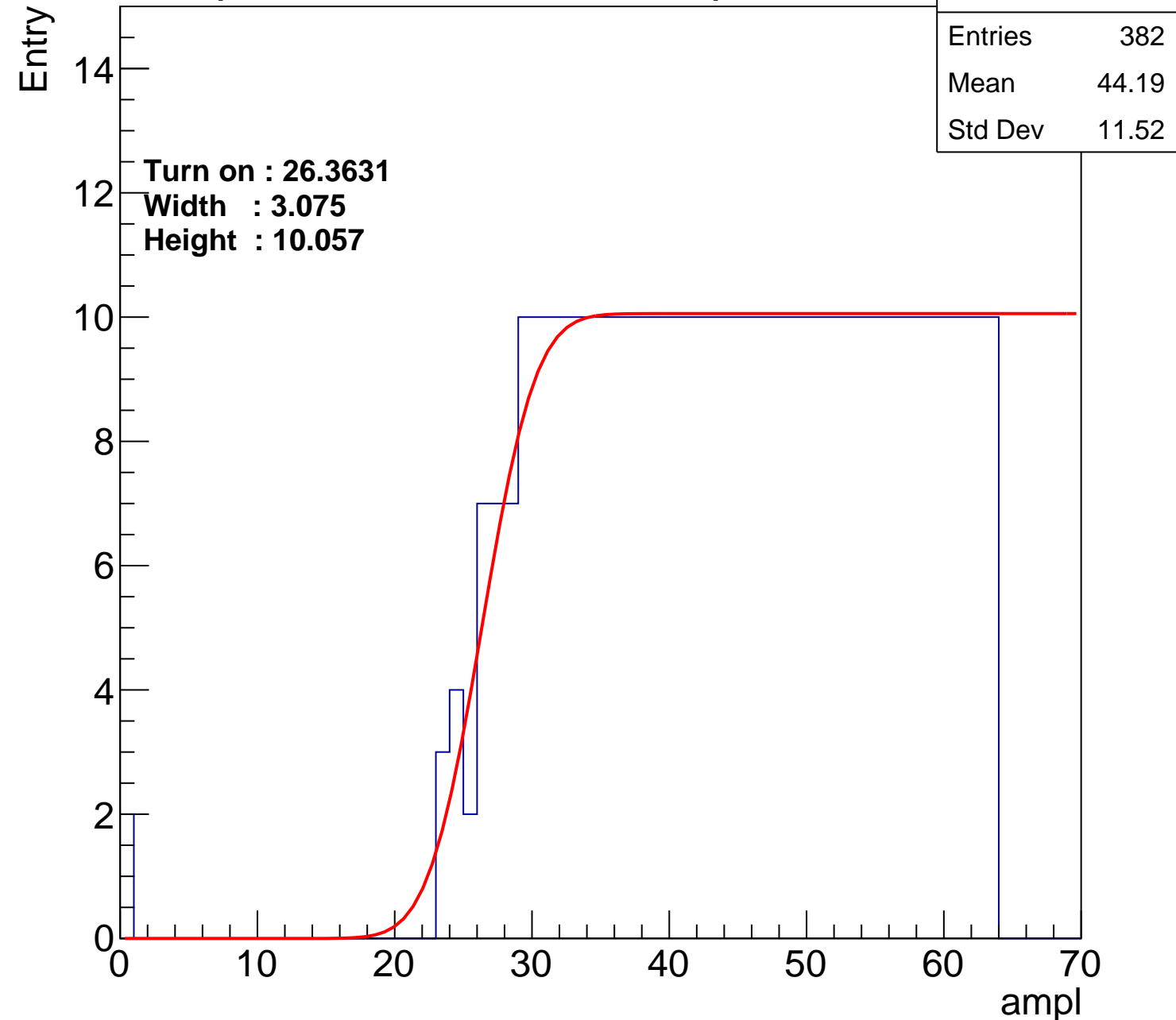
Width : 3.075

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch18

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.73
Std Dev	11.25

Turn on : 27.6140

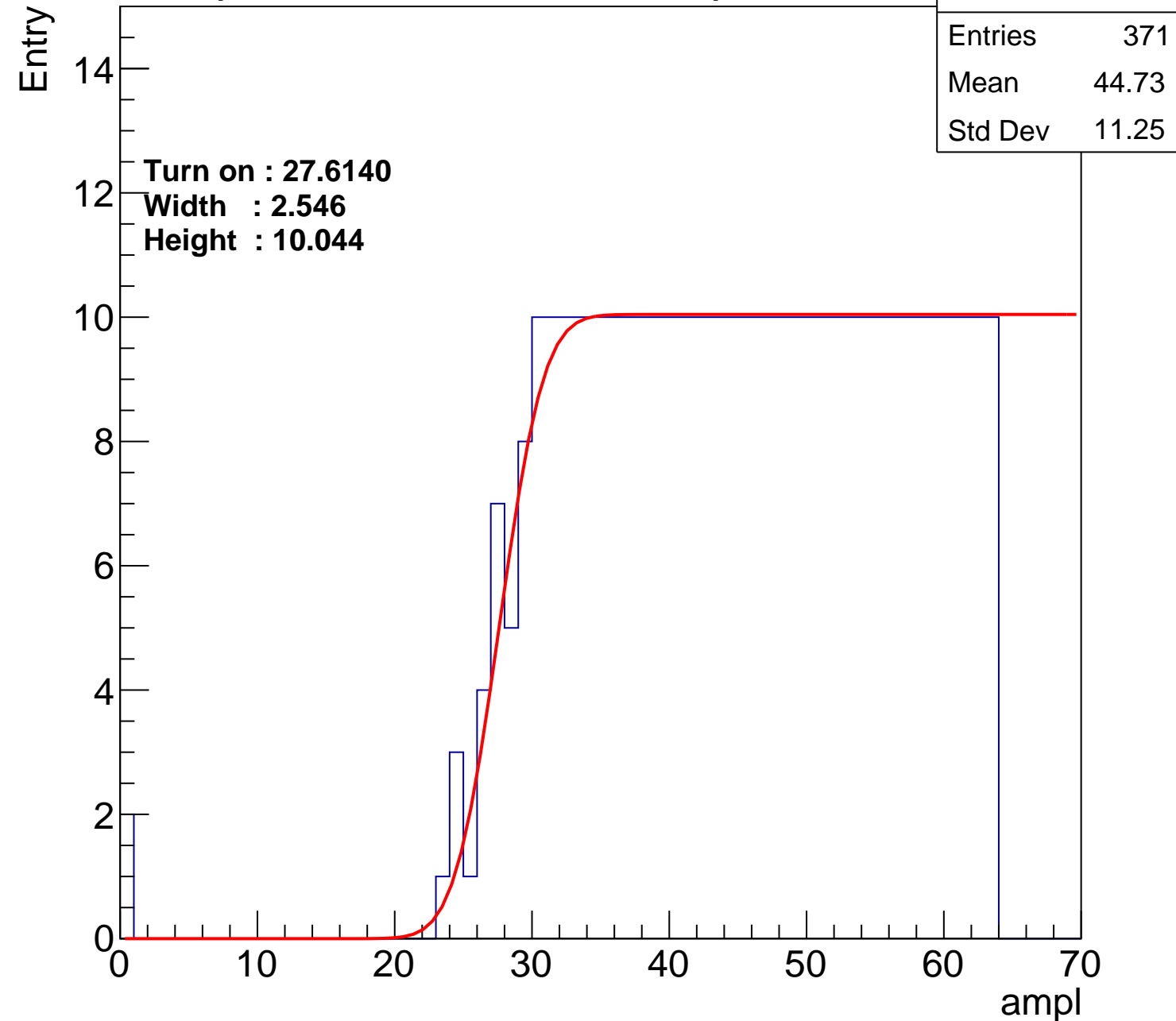
Width : 2.546

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch19

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.89
Std Dev	11.34

Turn on : 27.8472

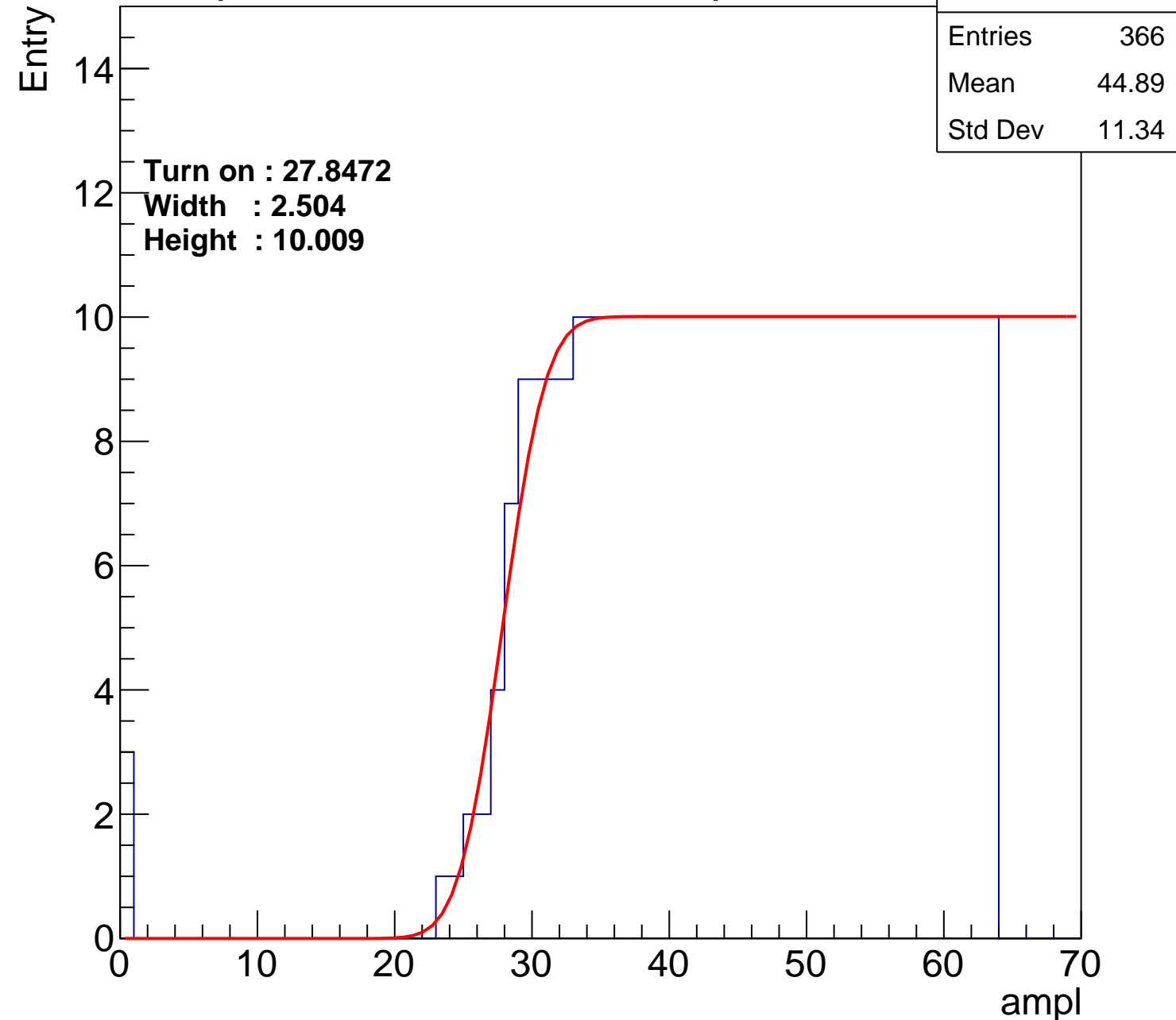
Width : 2.504

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch20

calib_packv5_042523_0143.root, FC#12, port B1

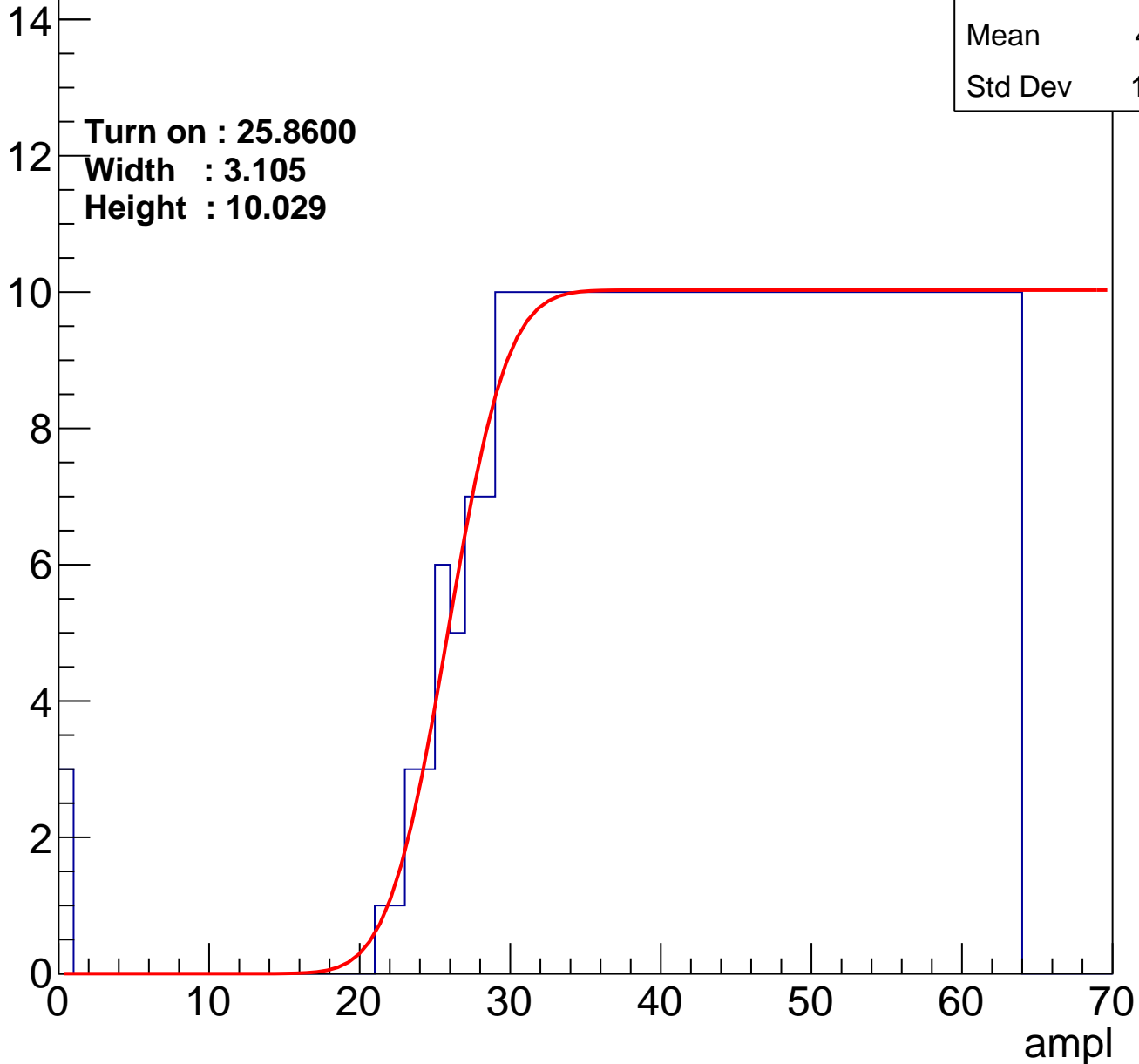
Entries	386
Mean	43.91
Std Dev	11.83

Turn on : 25.8600

Width : 3.105

Height : 10.029

Entry



B0L102S, U20-ch21

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.37
Std Dev	11.27

Turn on : 26.4477

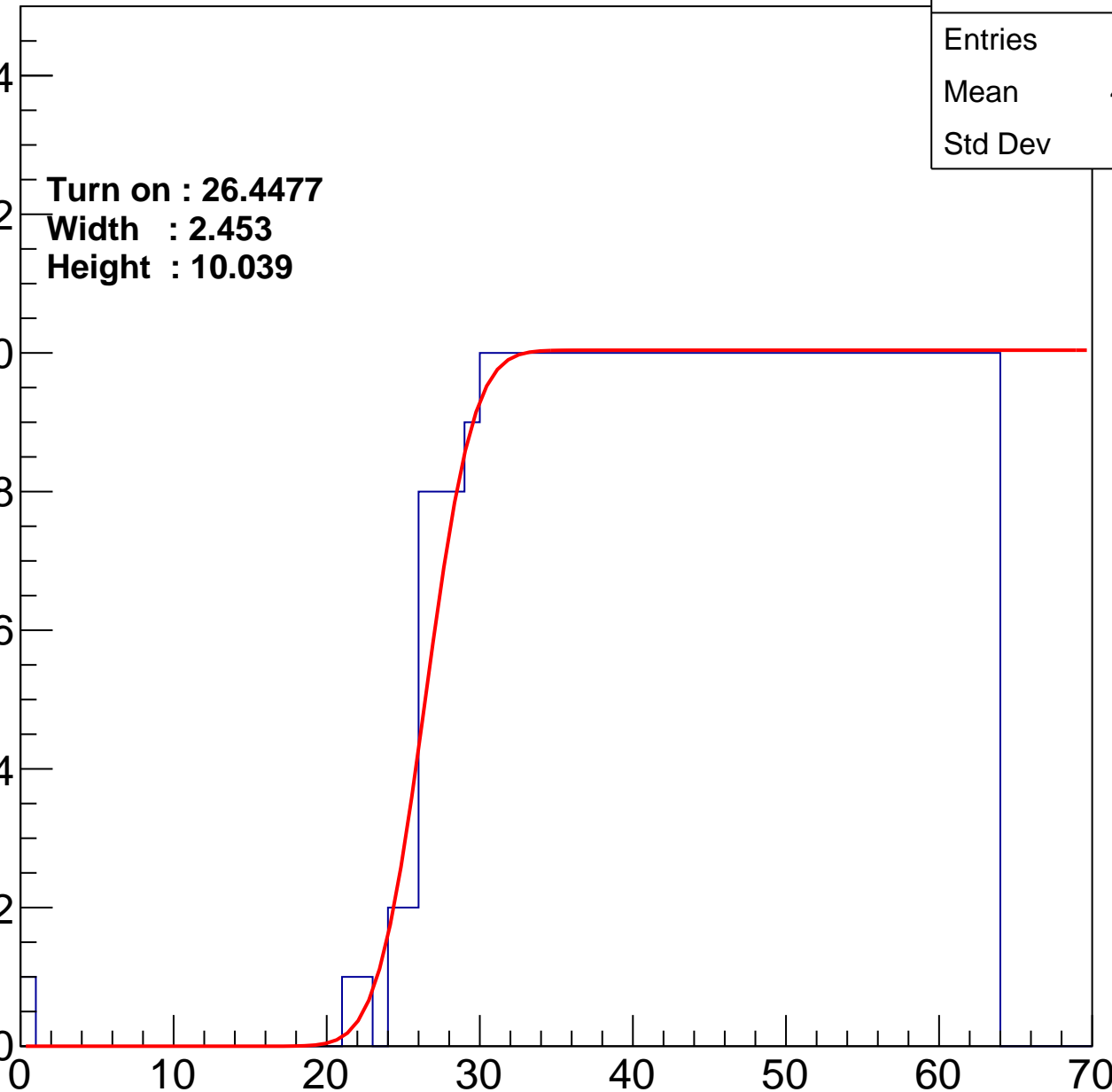
Width : 2.453

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch22

calib_packv5_042523_0143.root, FC#12, port B1

Entries	402
Mean	43.11
Std Dev	12.24

Turn on : 24.1162

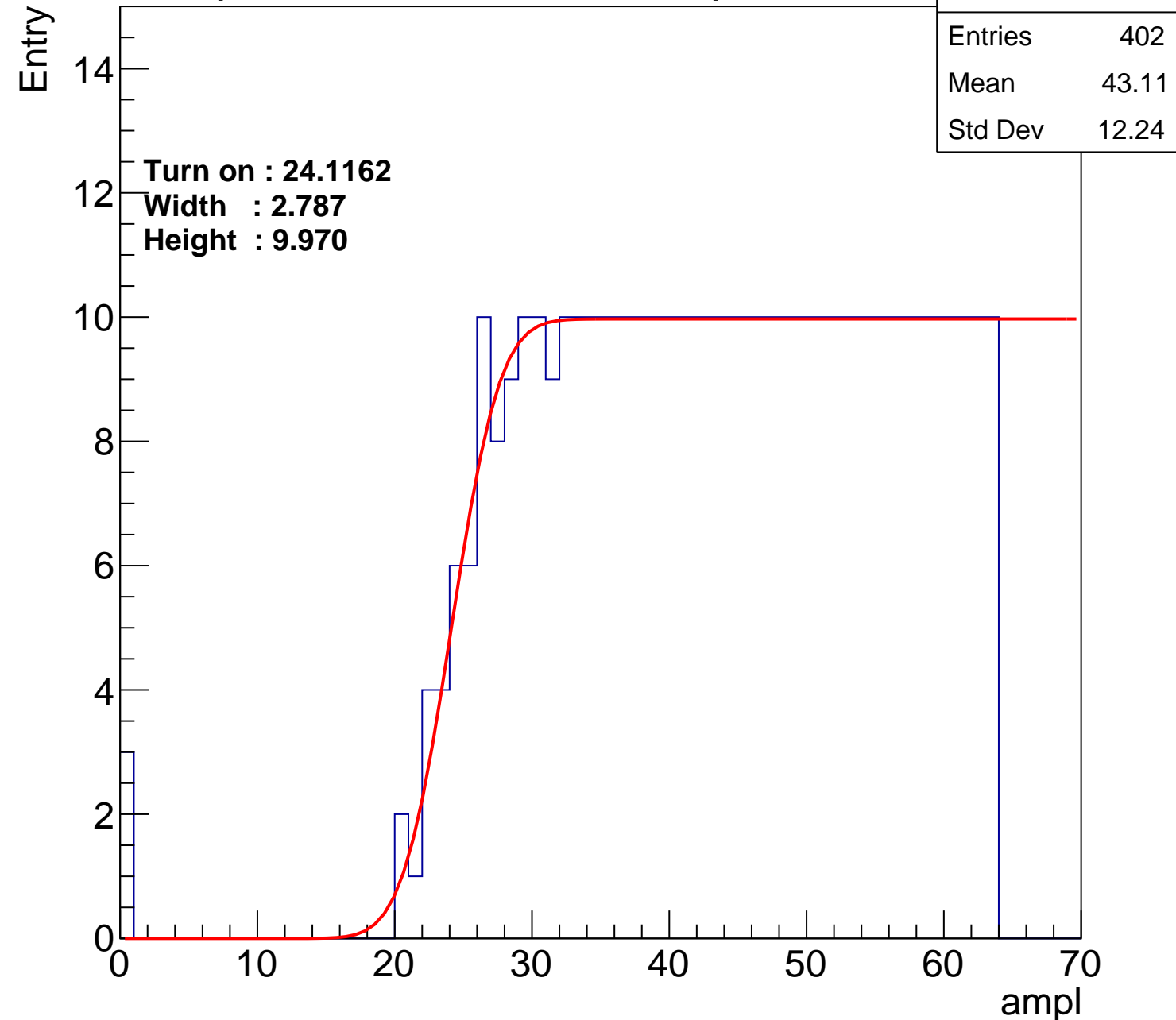
Width : 2.787

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch23

calib_packv5_042523_0143.root, FC#12, port B1

Entries	358
Mean	45.42
Std Dev	10.74

Turn on : 29.0971

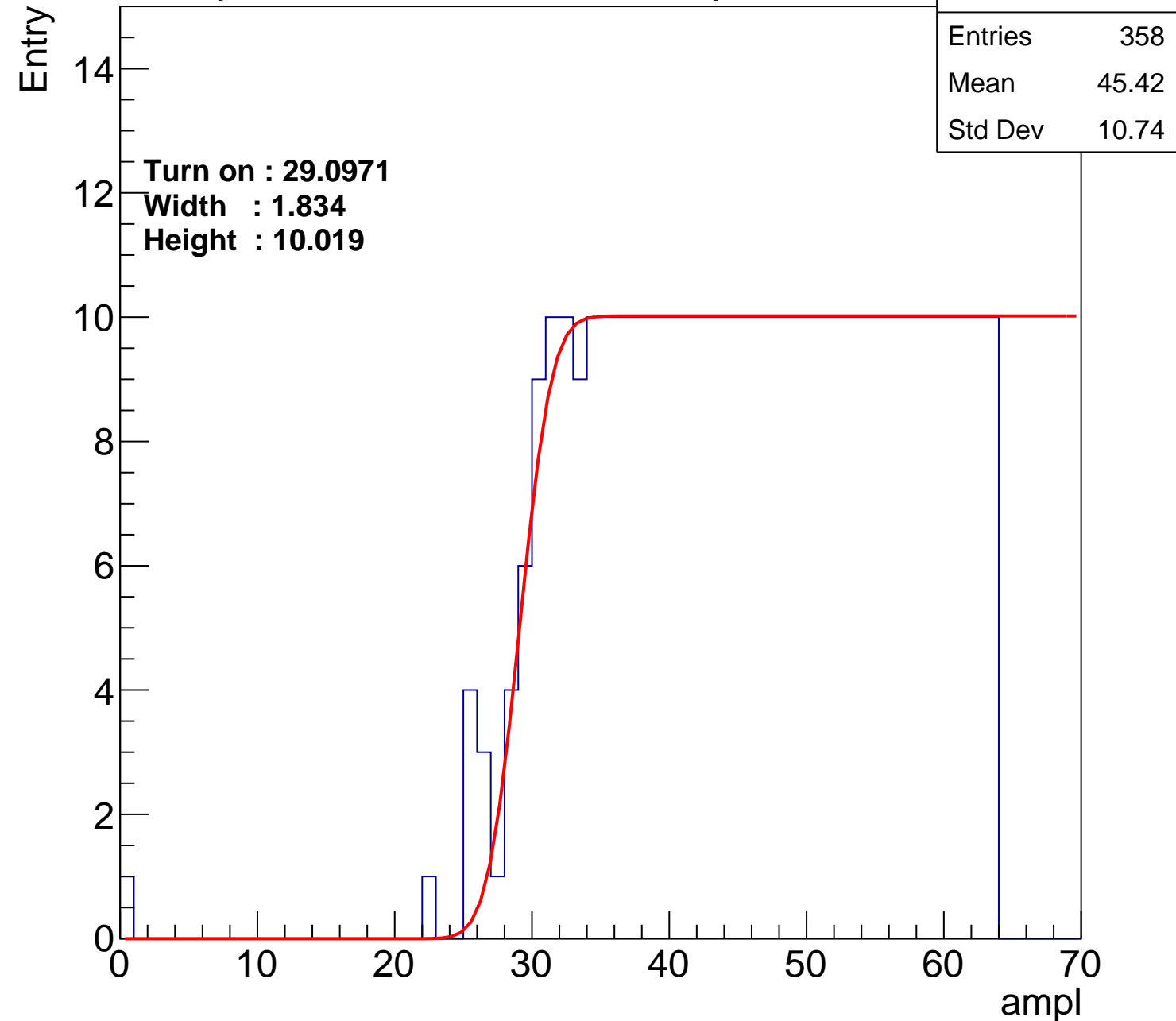
Width : 1.834

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch24

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.04
Std Dev	10.93

Turn on : 27.3407

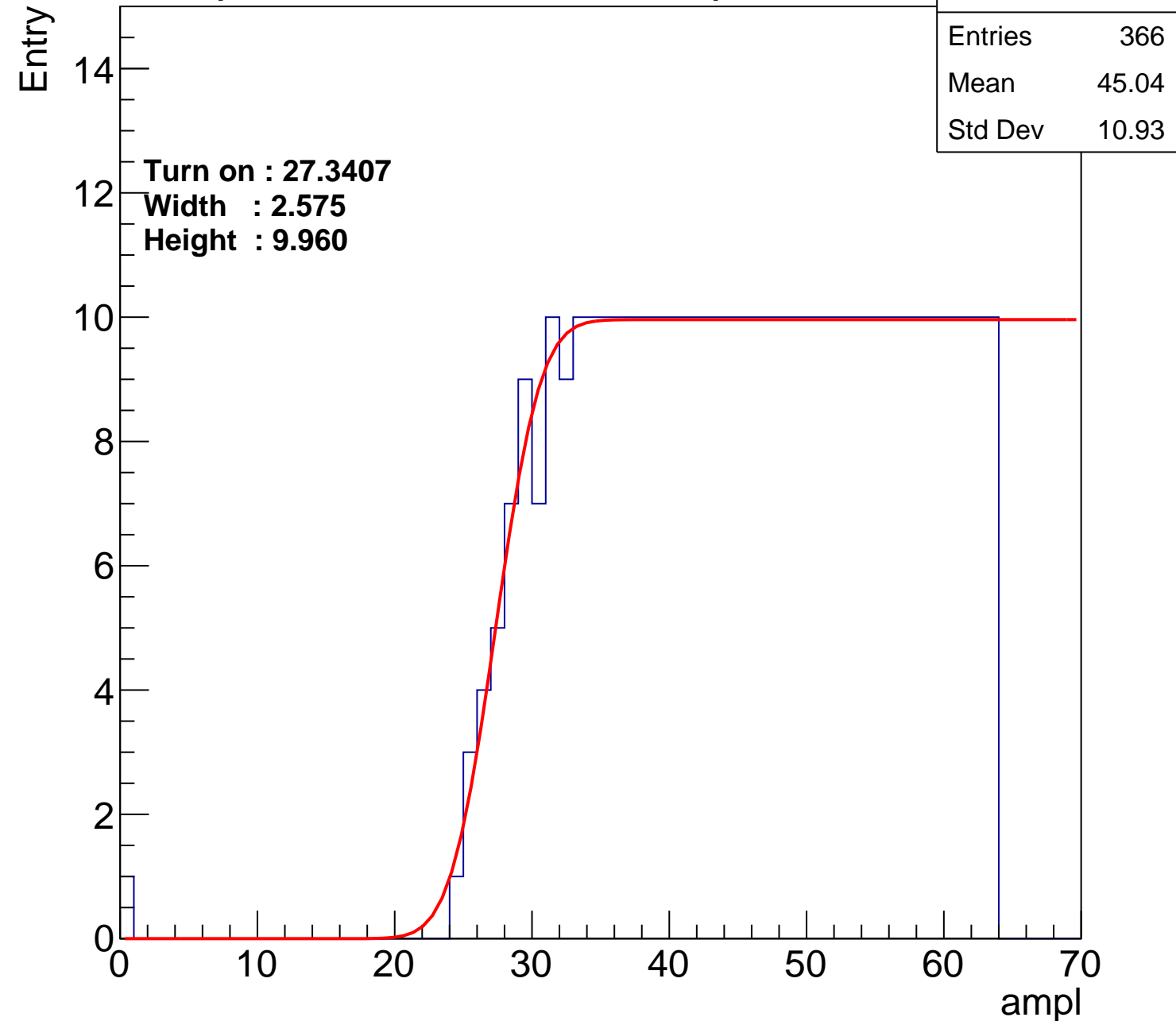
Width : 2.575

Height : 9.960

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch25

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.38
Std Dev	11.24

Turn on : 25.9781

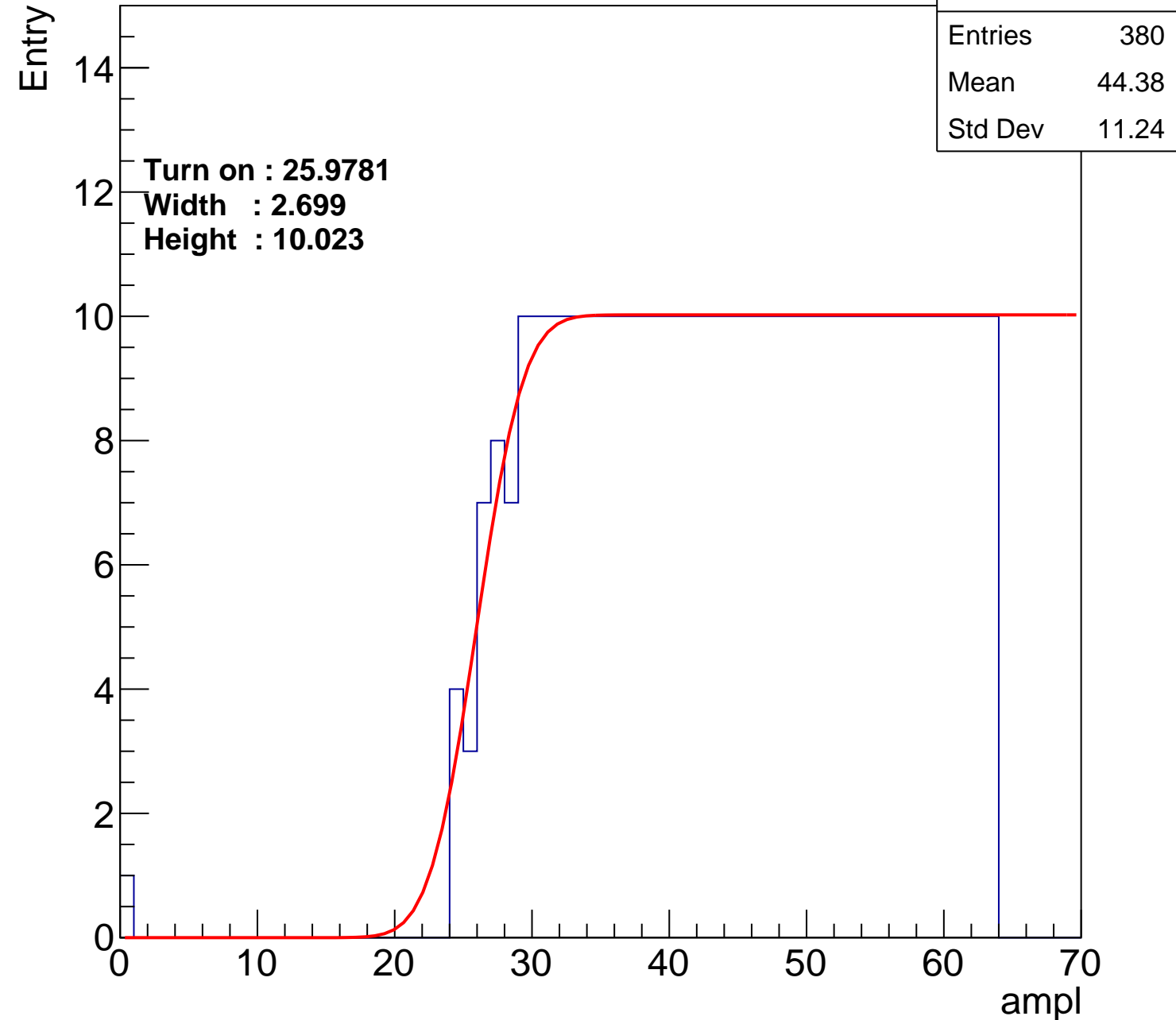
Width : 2.699

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch26

calib_packv5_042523_0143.root, FC#12, port B1

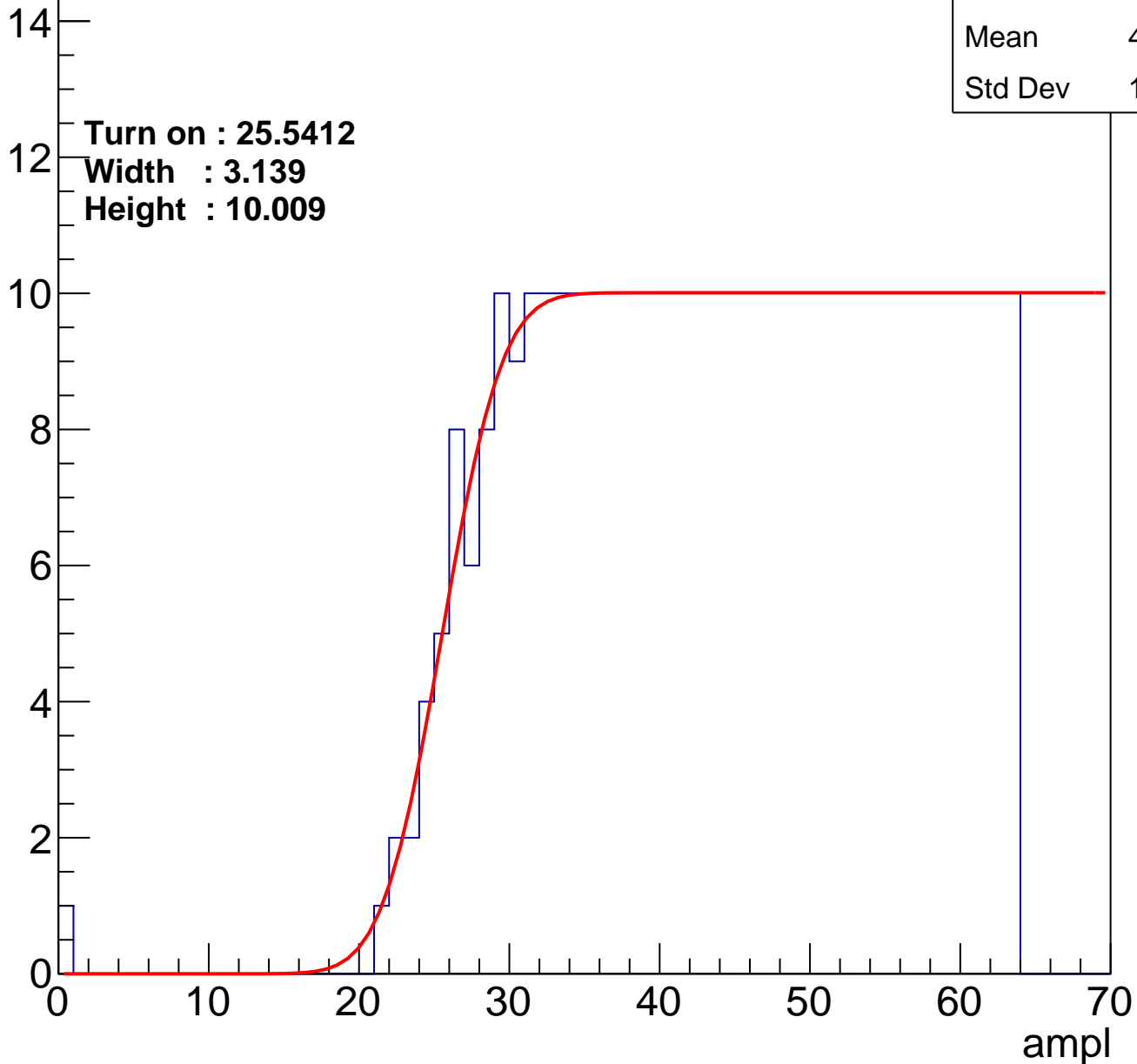
Entries	386
Mean	44.03
Std Dev	11.49

Turn on : 25.5412

Width : 3.139

Height : 10.009

Entry



B0L102S, U20-ch27

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	44.04
Std Dev	11.47

Turn on : 25.5667

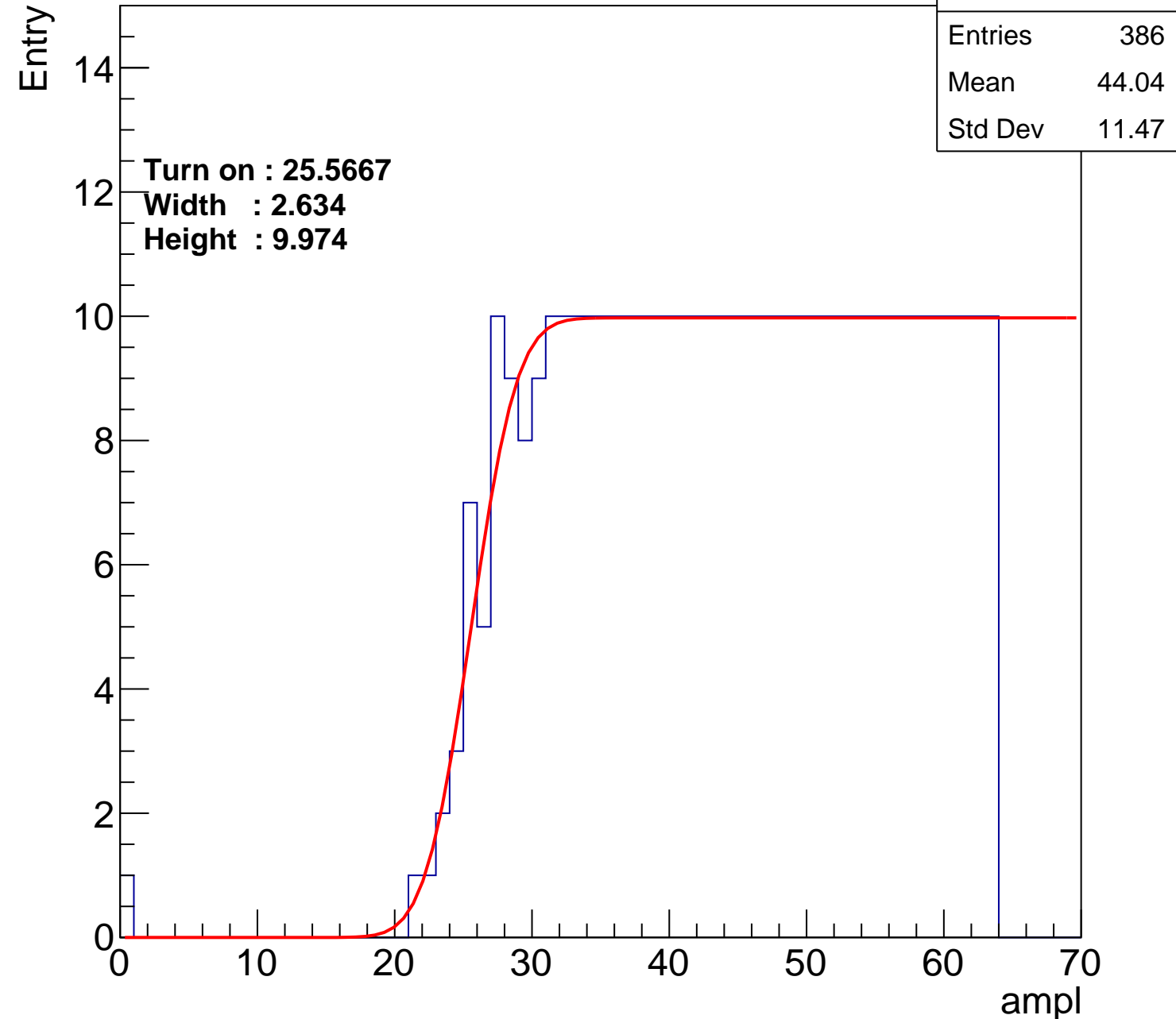
Width : 2.634

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch28

calib_packv5_042523_0143.root, FC#12, port B1

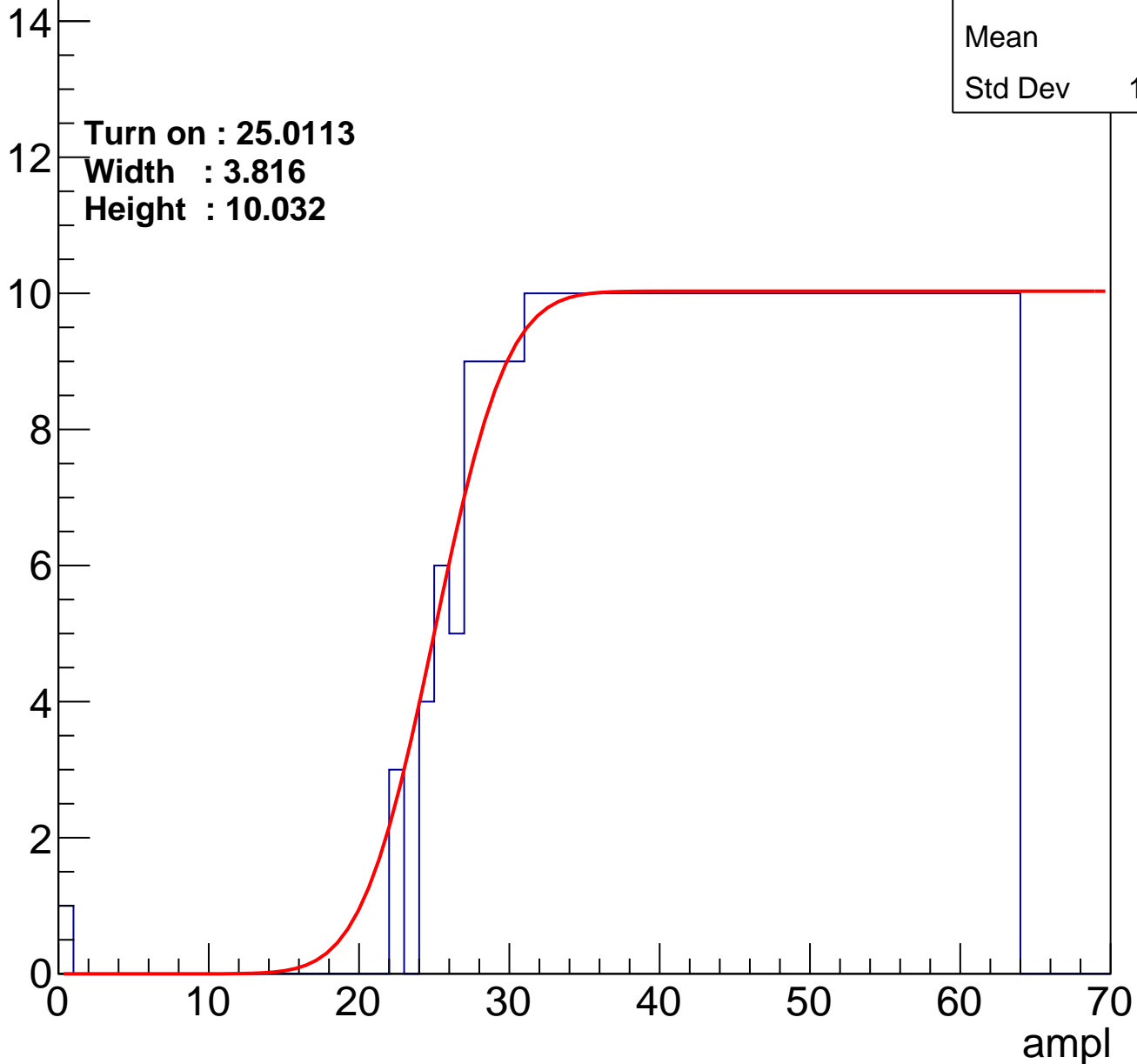
Entries	385
Mean	44.1
Std Dev	11.44

Turn on : 25.0113

Width : 3.816

Height : 10.032

Entry



B0L102S, U20-ch29

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.1
Std Dev	11.75

Turn on : 26.6296

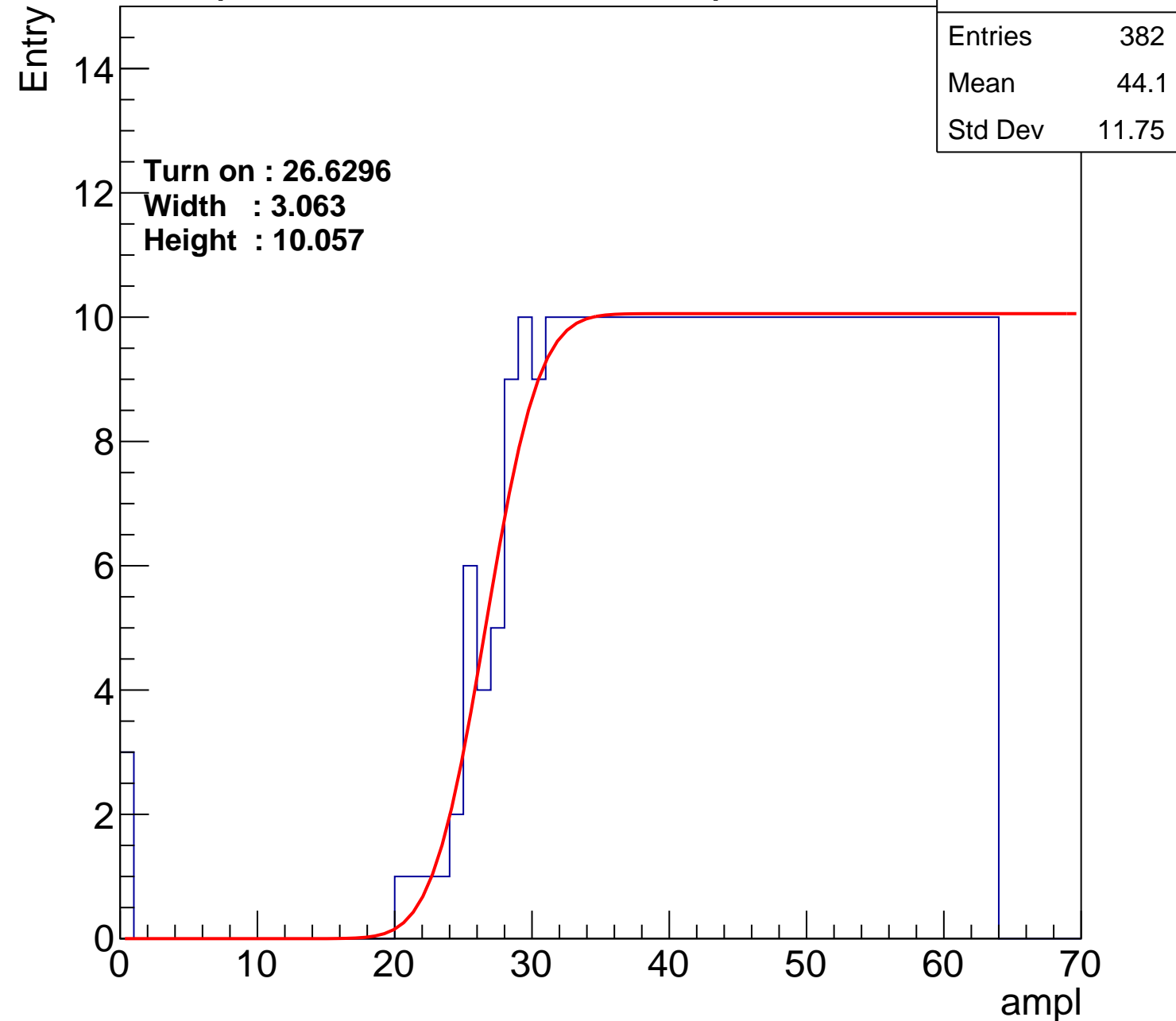
Width : 3.063

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch30

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.26
Std Dev	11.03

Turn on : 28.3946

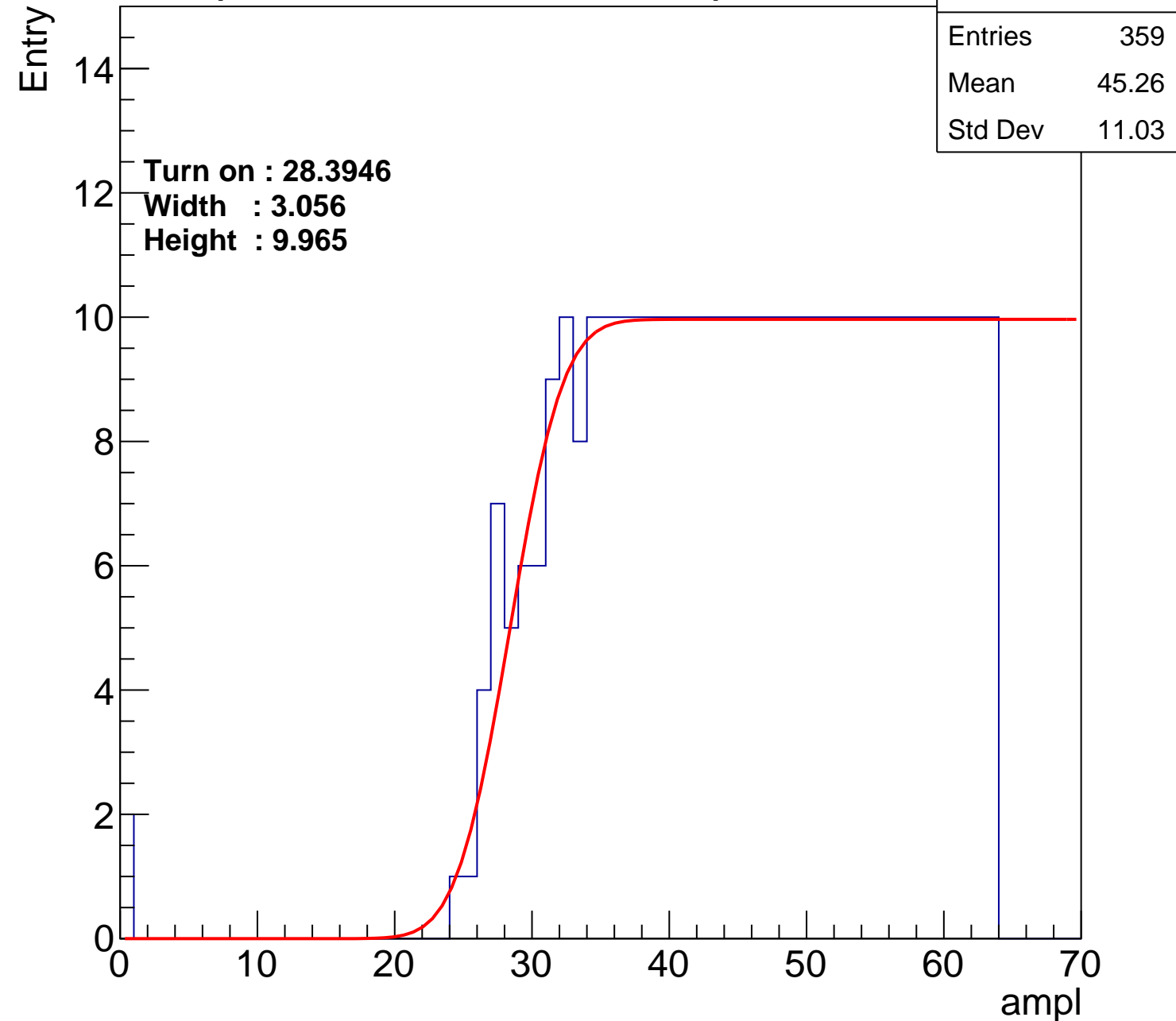
Width : 3.056

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch31

calib_packv5_042523_0143.root, FC#12, port B1

Entries	363
Mean	45.12
Std Dev	11.05

Turn on : 28.3524

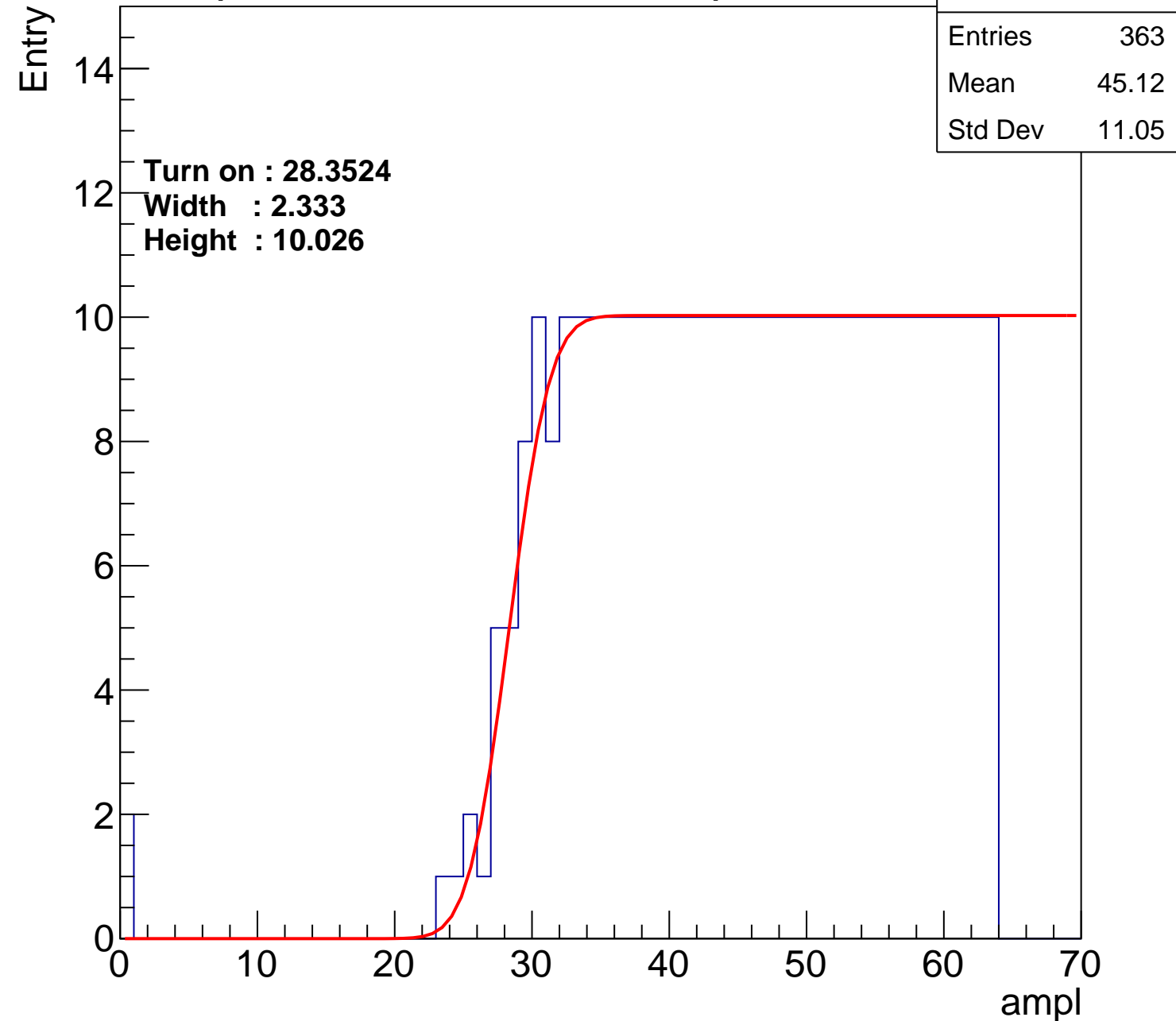
Width : 2.333

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch32

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 26.0062

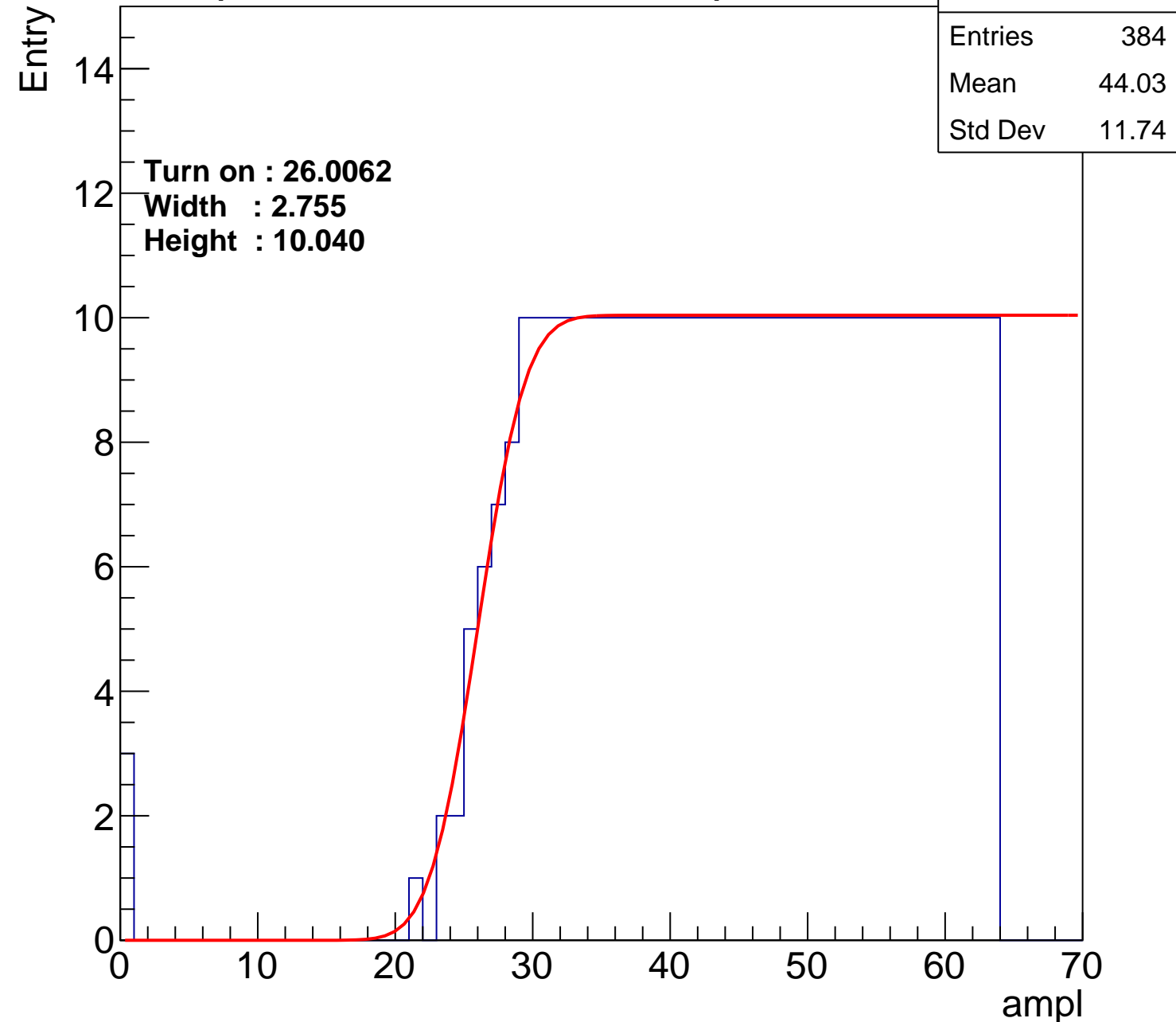
Width : 2.755

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch33

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.71
Std Dev	11.27

Turn on : 27.0134

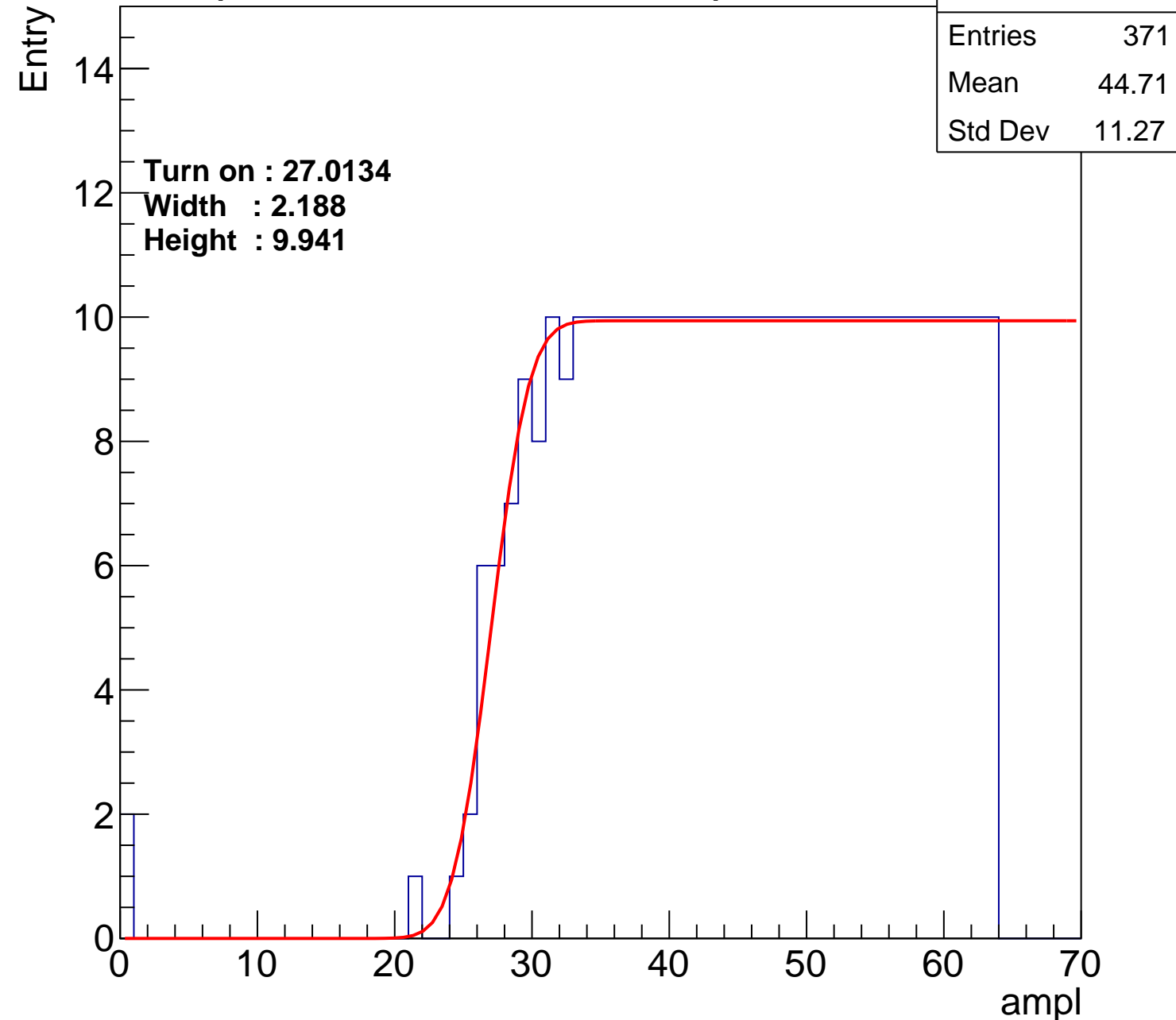
Width : 2.188

Height : 9.941

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch34

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.3
Std Dev	11.45

Turn on : 26.2716

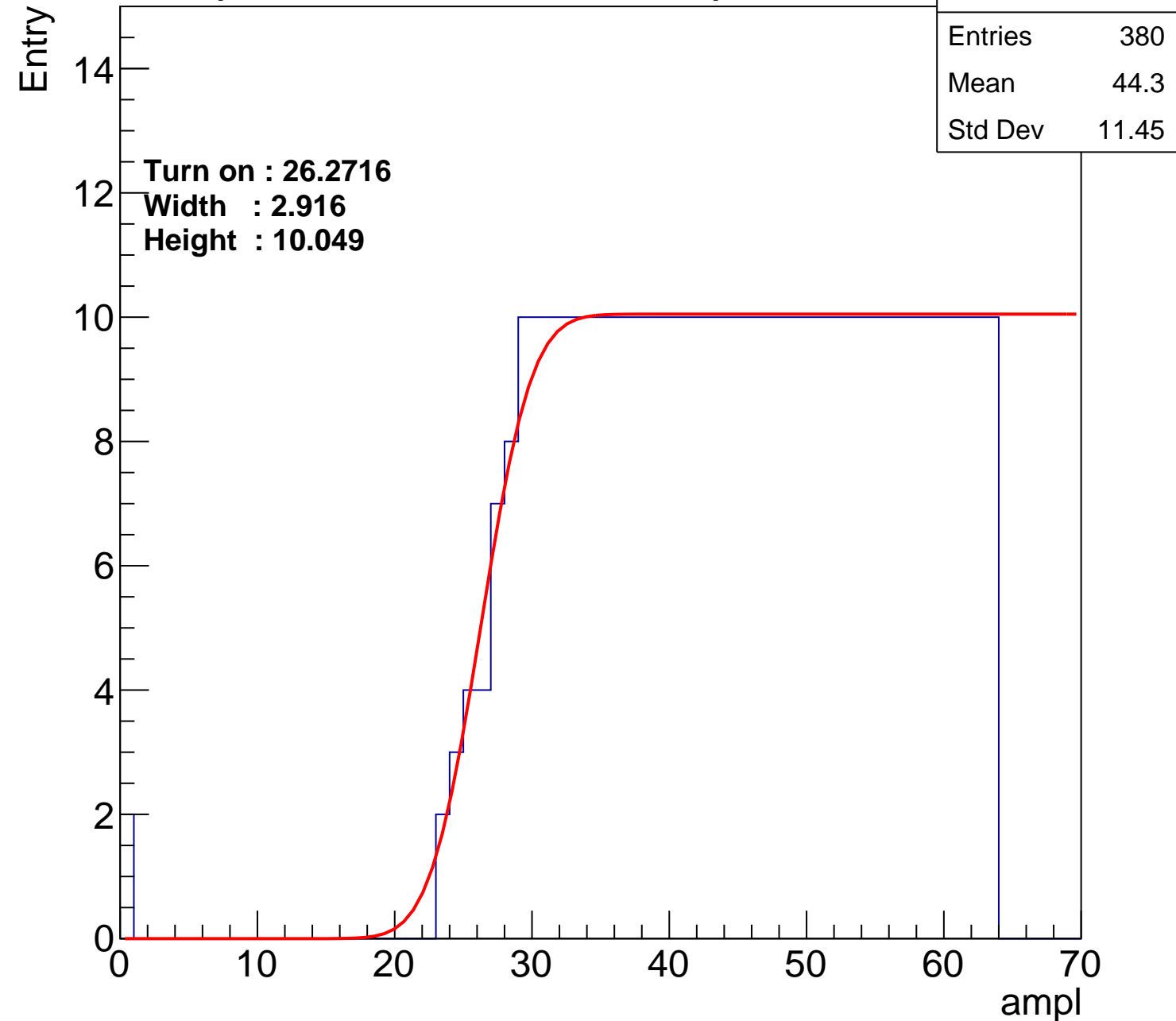
Width : 2.916

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch35

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.6
Std Dev	11.29

Turn on : 27.0936

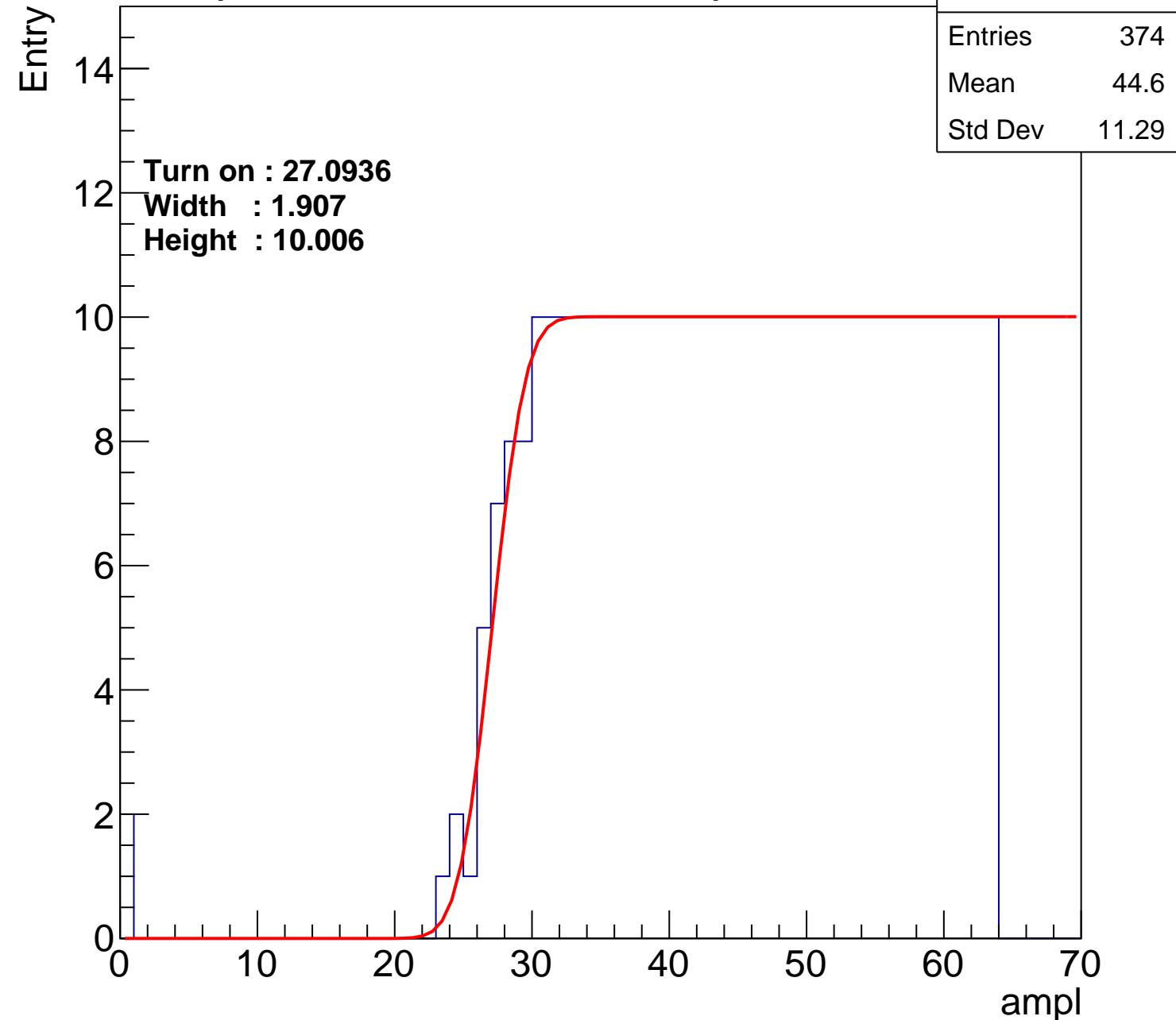
Width : 1.907

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch36

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.6
Std Dev	11.71

Turn on : 24.4889

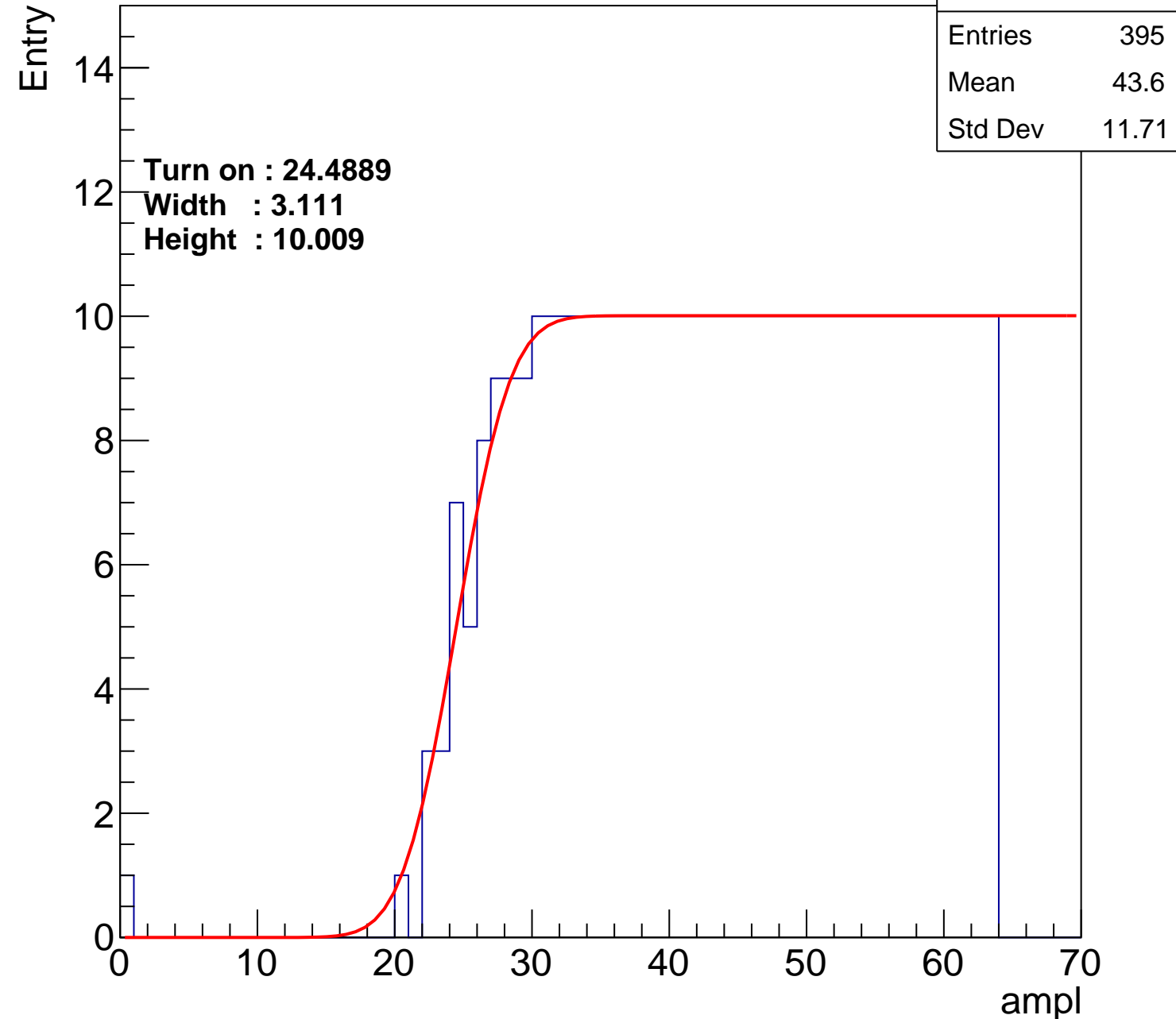
Width : 3.111

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch37

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.76
Std Dev	11.92

Turn on : 25.6333

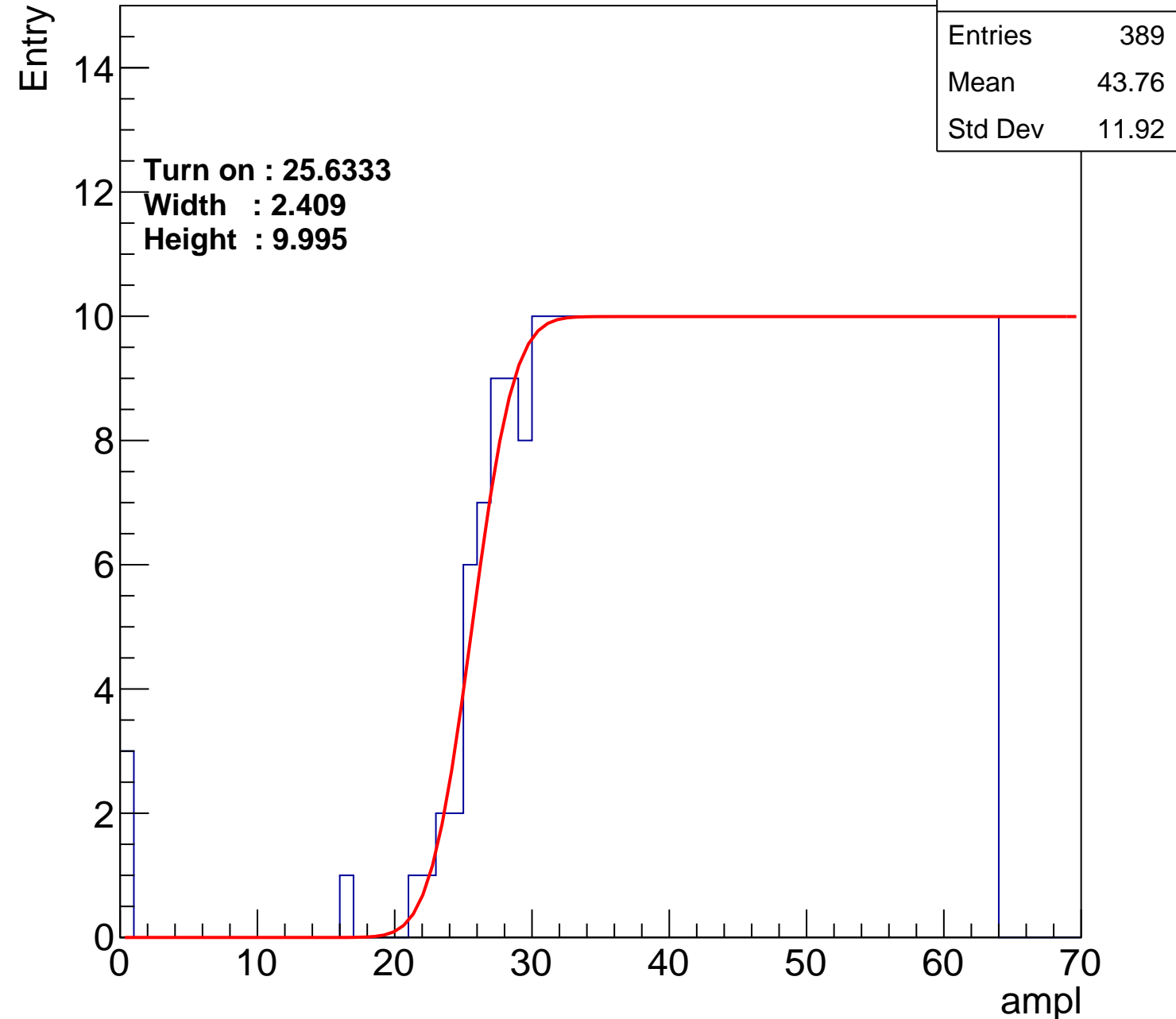
Width : 2.409

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch38

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.77
Std Dev	11.24

Turn on : 27.5417

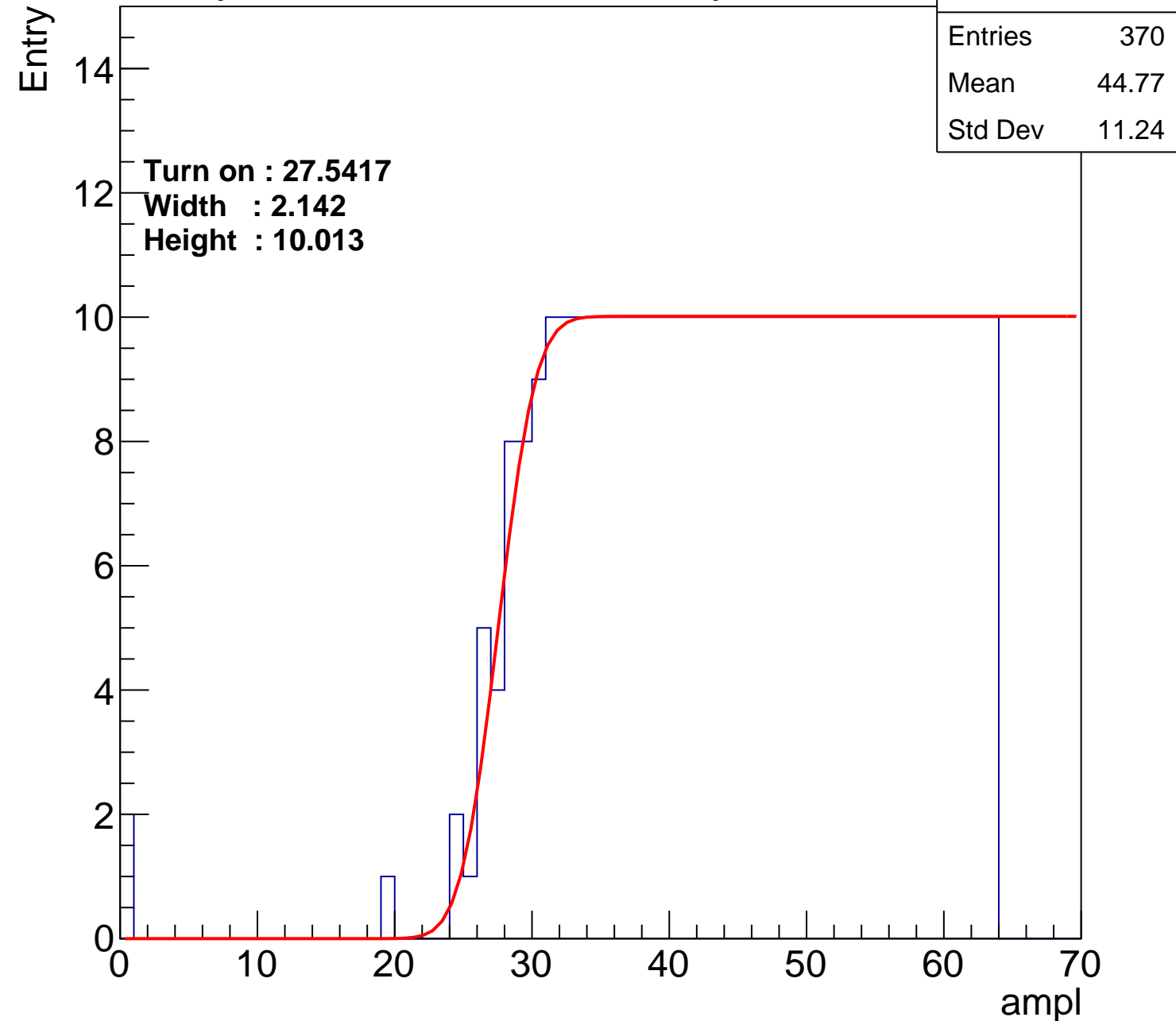
Width : 2.142

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch39

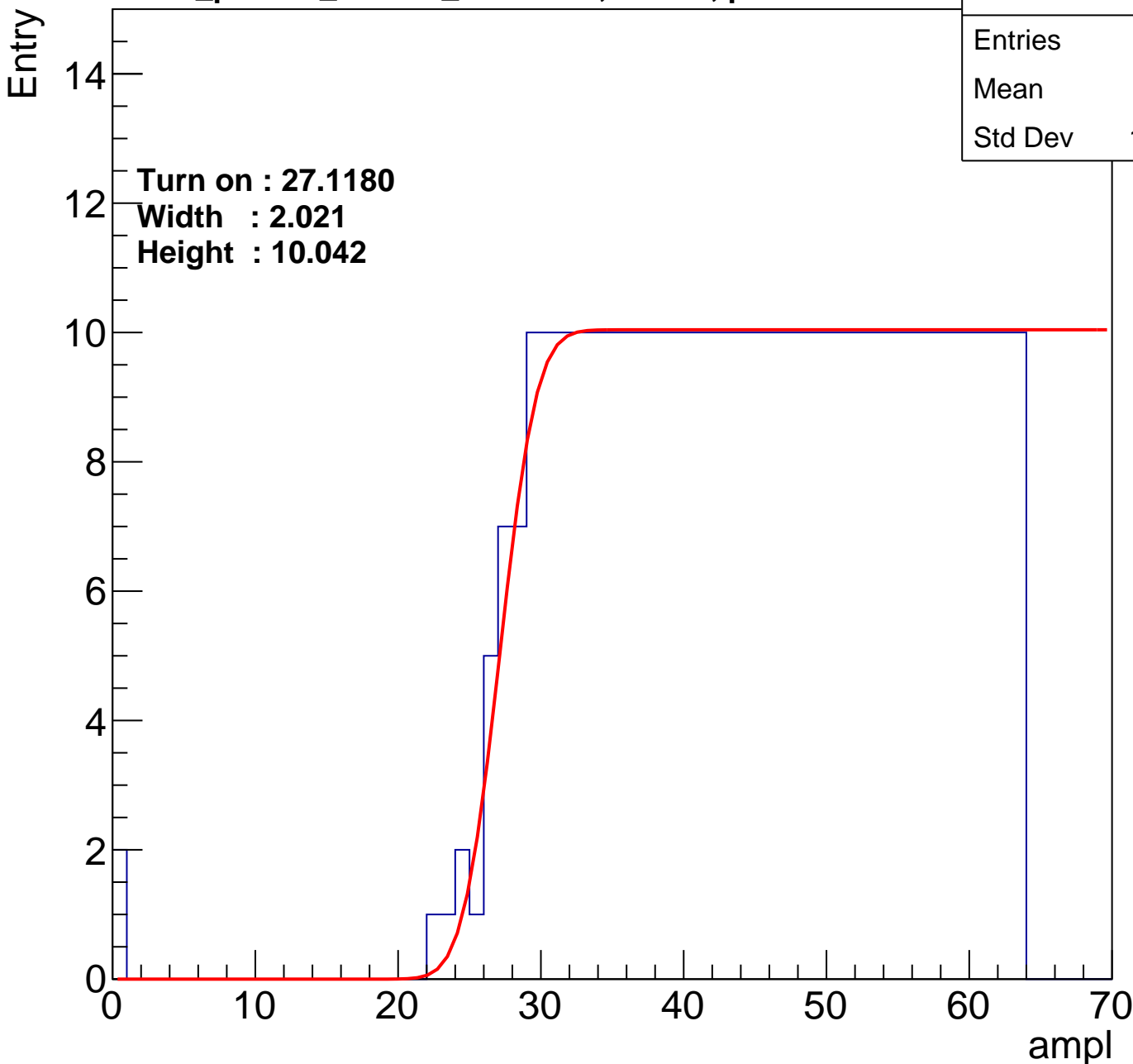
calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.5
Std Dev	11.35

Turn on : 27.1180

Width : 2.021

Height : 10.042



B0L102S, U20-ch40

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.27
Std Dev	12.28

Turn on : 24.8369

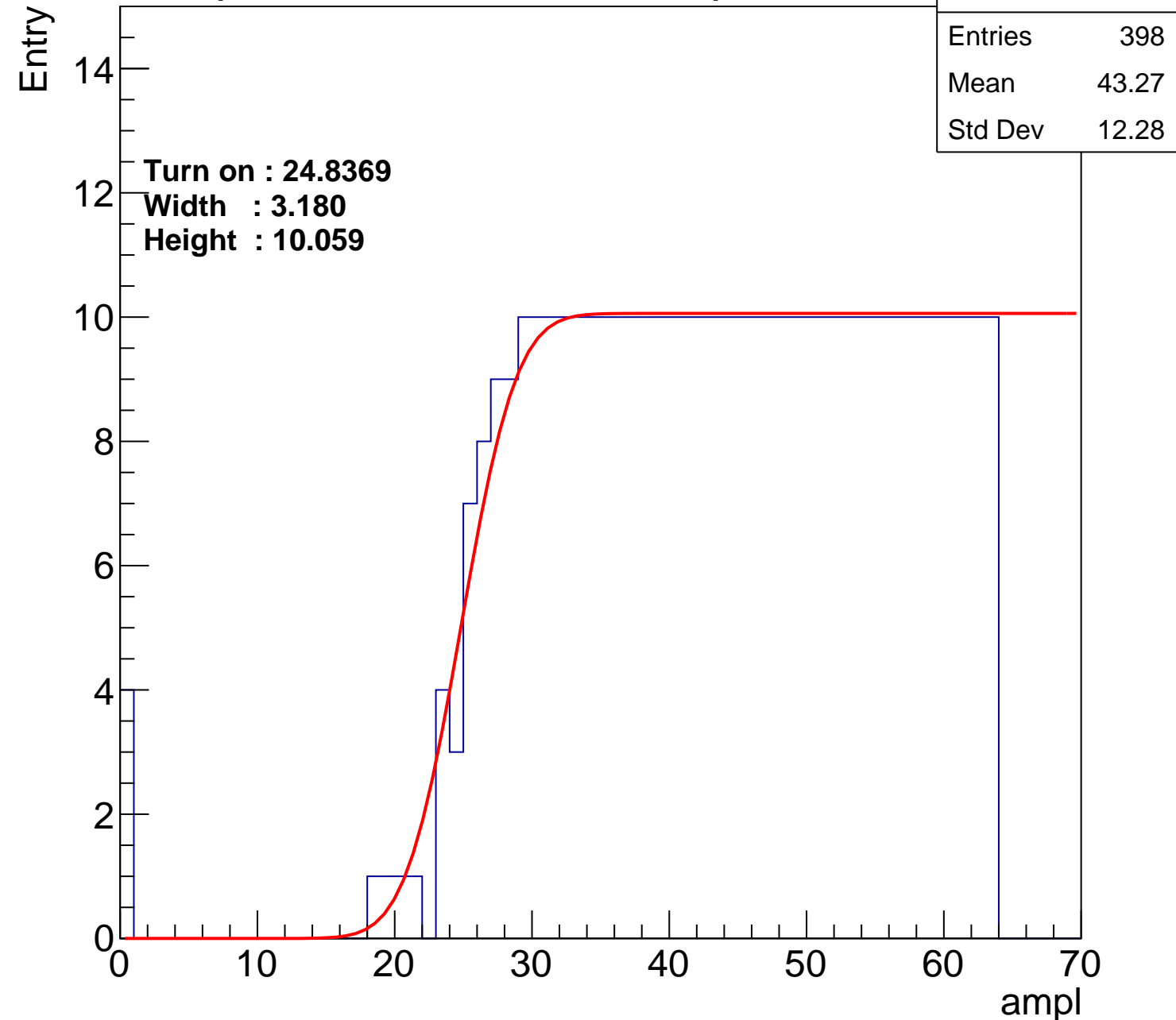
Width : 3.180

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch41

calib_packv5_042523_0143.root, FC#12, port B1

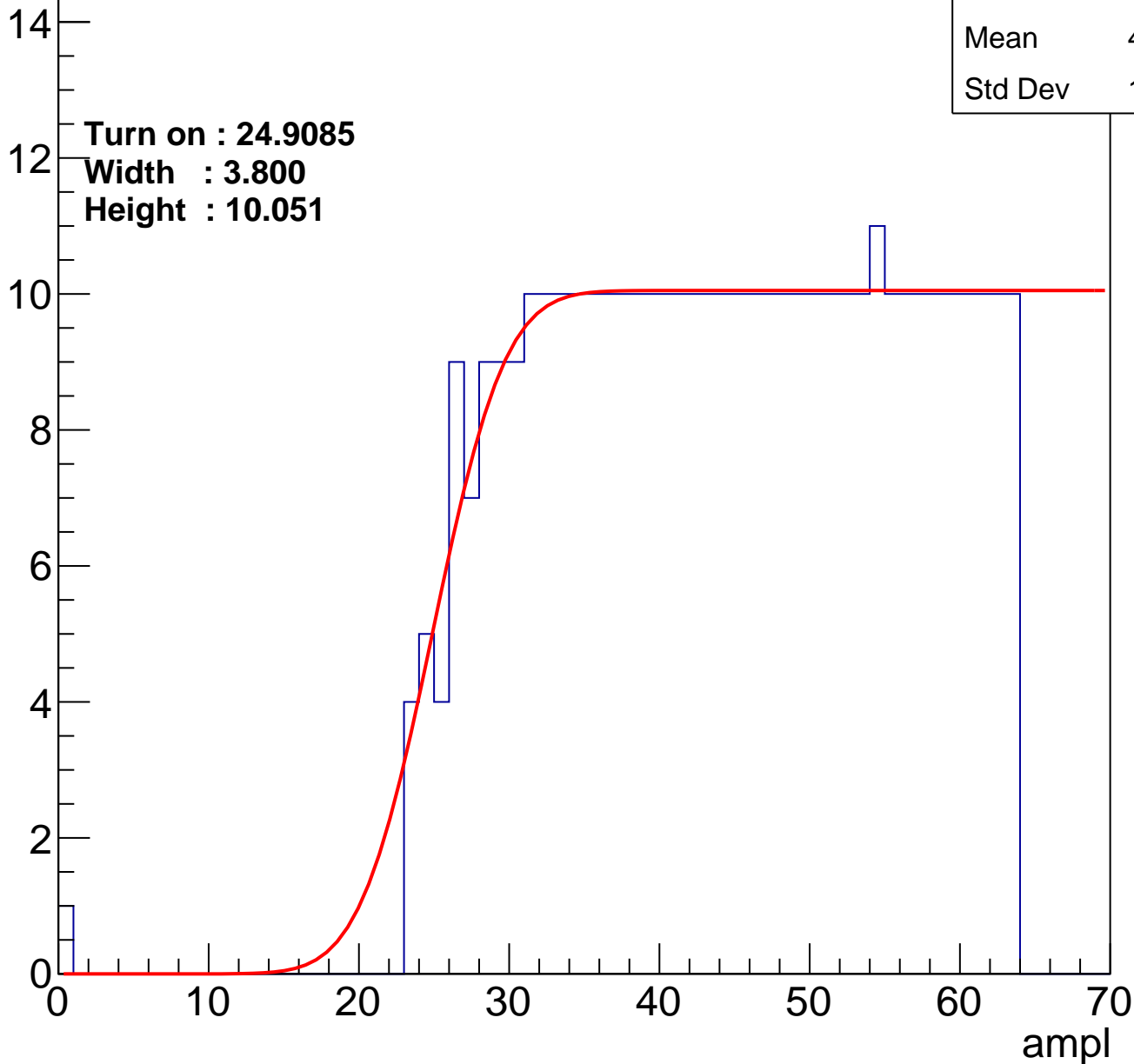
Entry

Entries	388
Mean	44.03
Std Dev	11.48

Turn on : 24.9085

Width : 3.800

Height : 10.051



B0L102S, U20-ch42

calib_packv5_042523_0143.root, FC#12, port B1

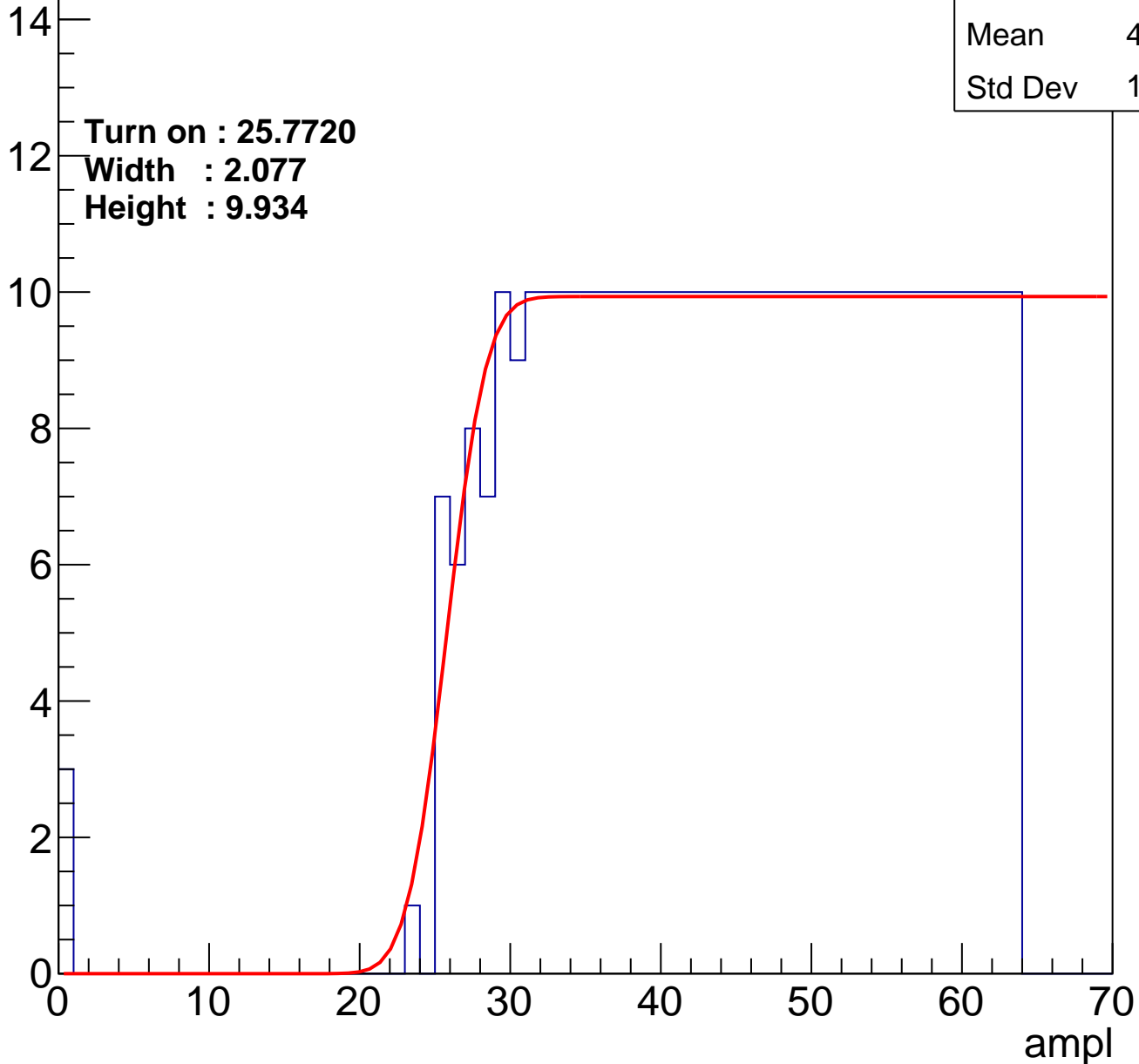
Entries	381
Mean	44.19
Std Dev	11.65

Turn on : 25.7720

Width : 2.077

Height : 9.934

Entry



B0L102S, U20-ch43

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.66
Std Dev	12.08

Turn on : 25.6661

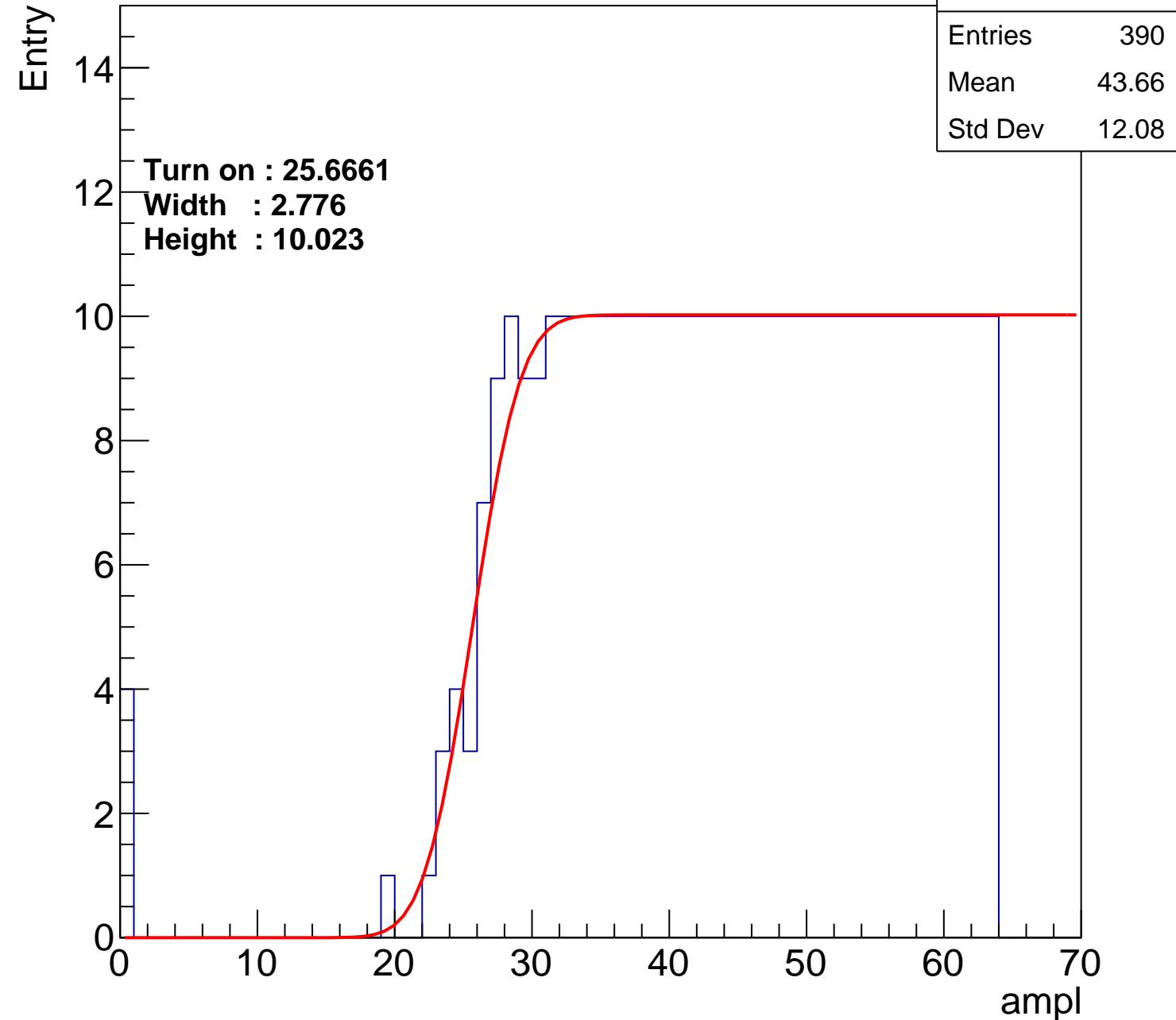
Width : 2.776

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch44

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.27
Std Dev	12.27

Turn on : 24.5000

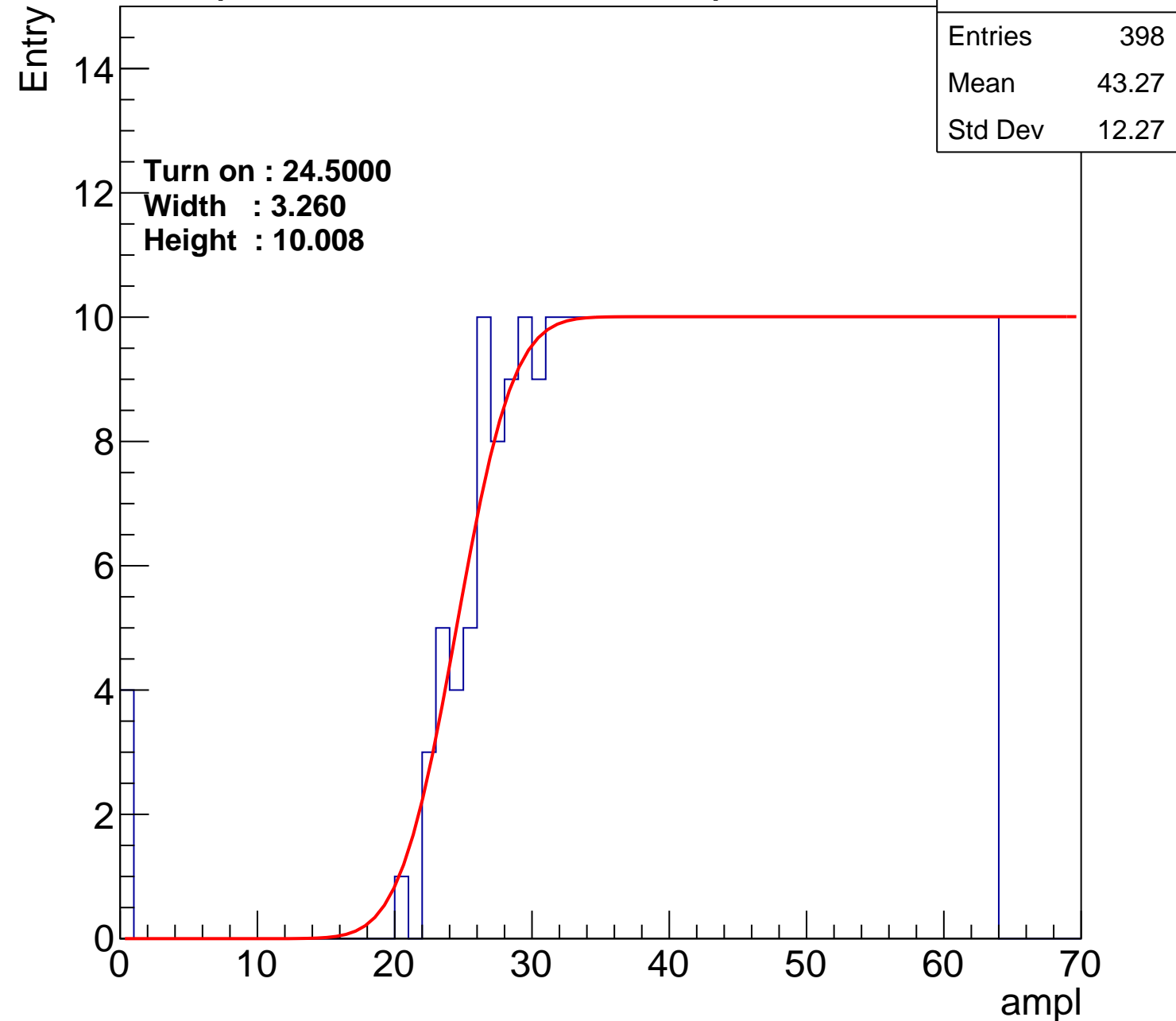
Width : 3.260

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch45

calib_packv5_042523_0143.root, FC#12, port B1

Entries	376
Mean	44.53
Std Dev	11.21

Turn on : 26.5920

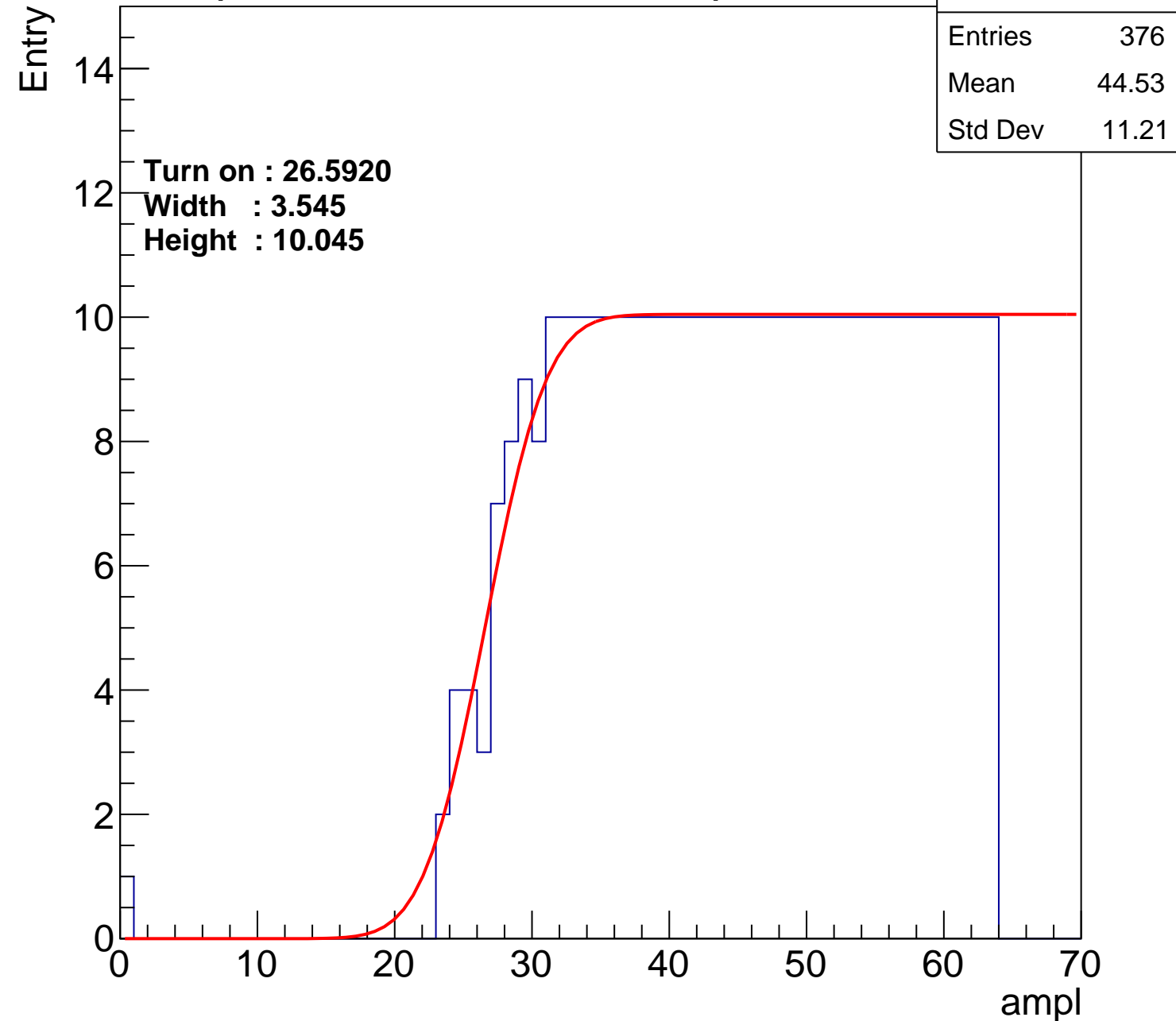
Width : 3.545

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch46

calib_packv5_042523_0143.root, FC#12, port B1

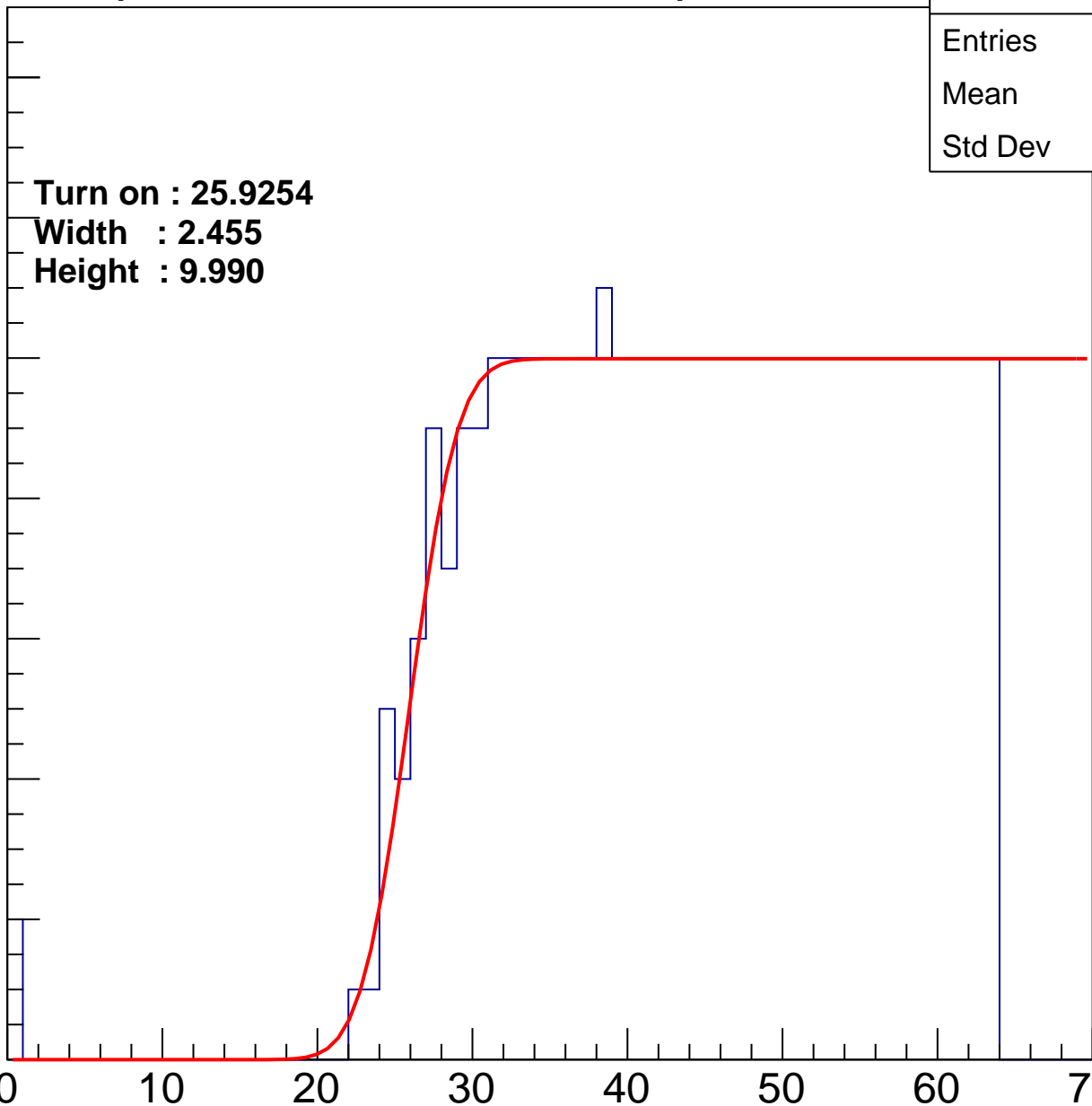
Entry

14
12
10
8
6
4
2
0

Turn on : 25.9254
Width : 2.455
Height : 9.990

Entries	384
Mean	44.11
Std Dev	11.56

ampl



B0L102S, U20-ch47

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.46
Std Dev	11.59

Turn on : 27.2411

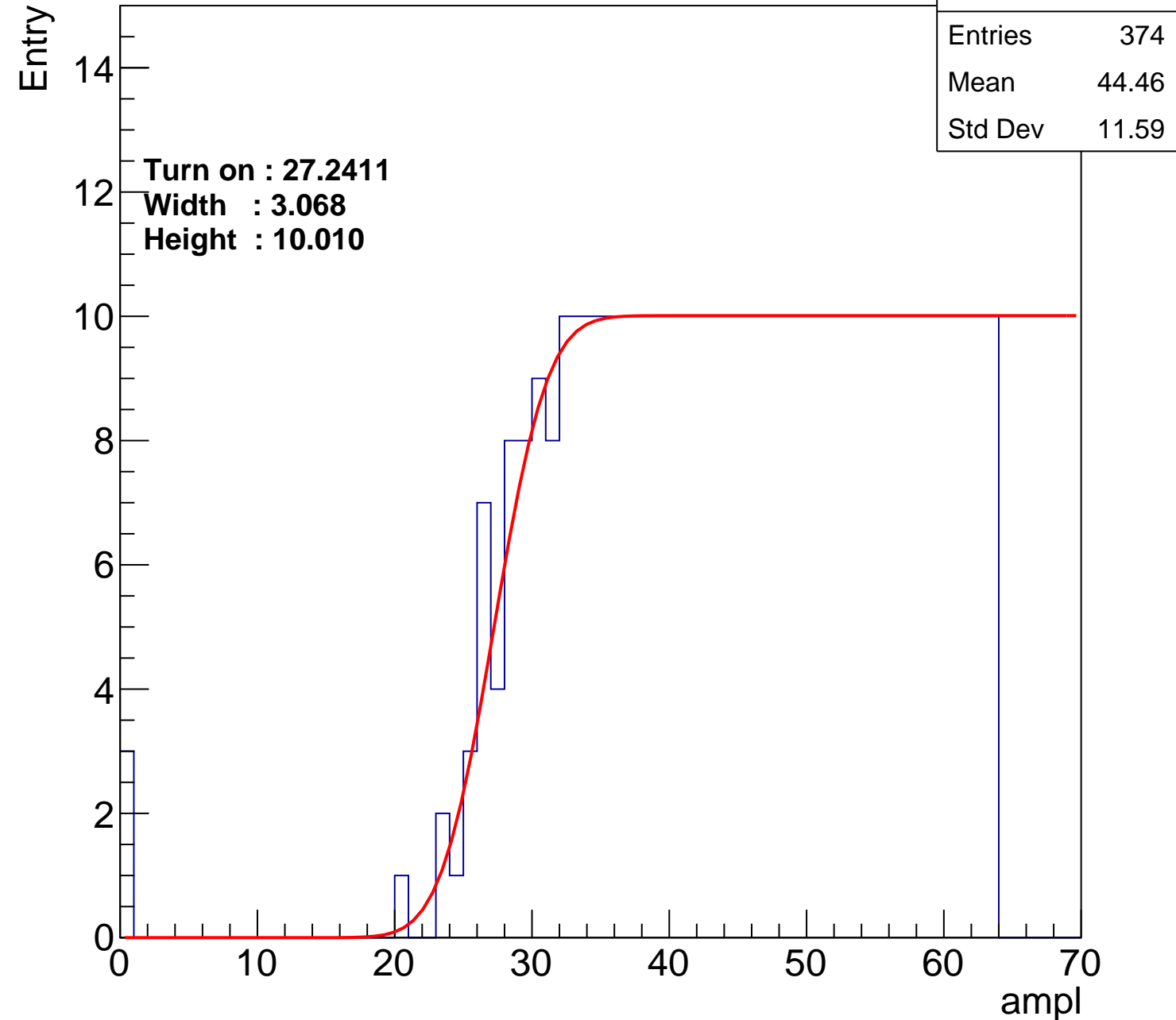
Width : 3.068

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch48

calib_packv5_042523_0143.root, FC#12, port B1

Entries	378
Mean	44.38
Std Dev	11.44

Turn on : 27.1267

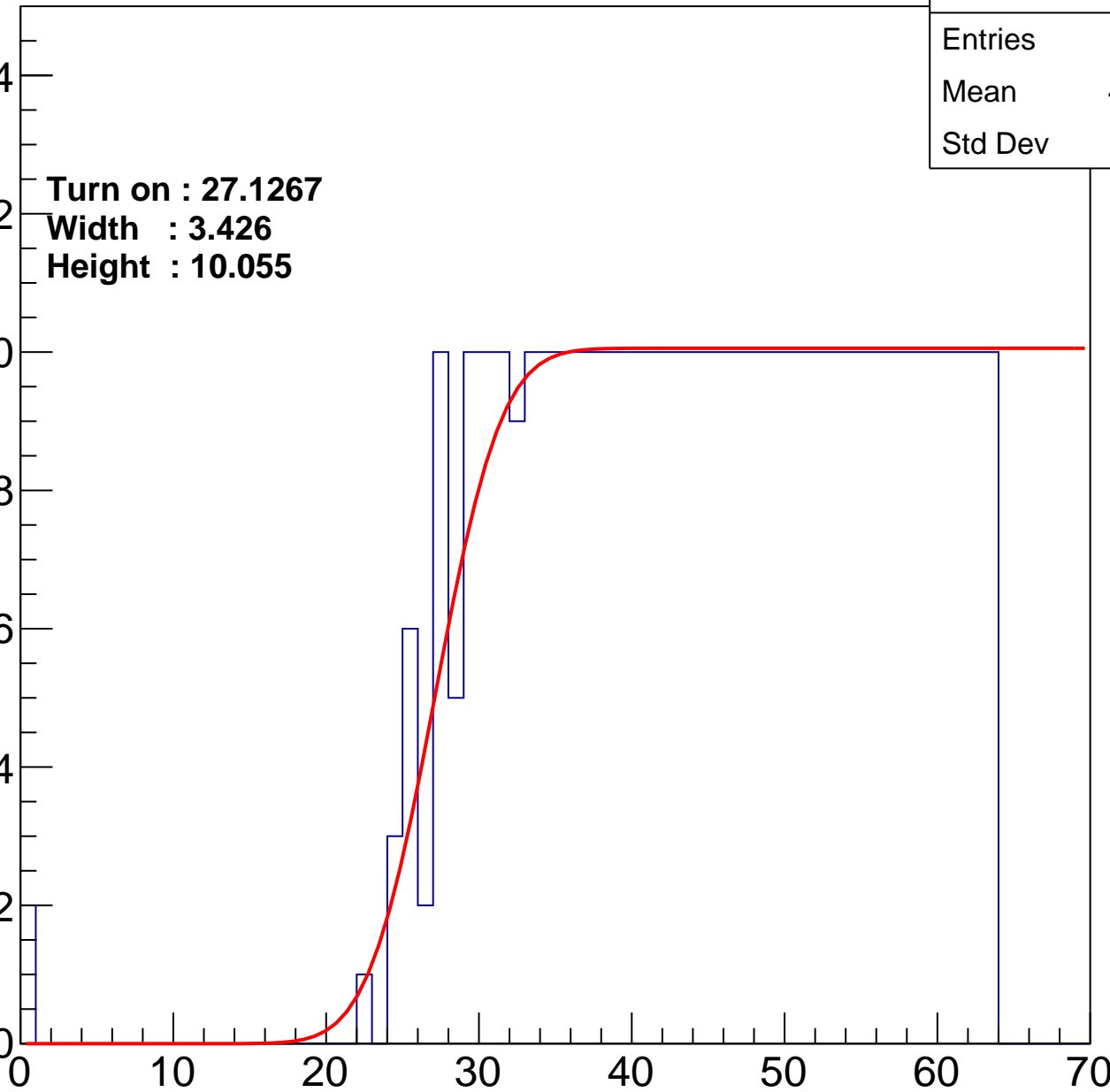
Width : 3.426

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch49

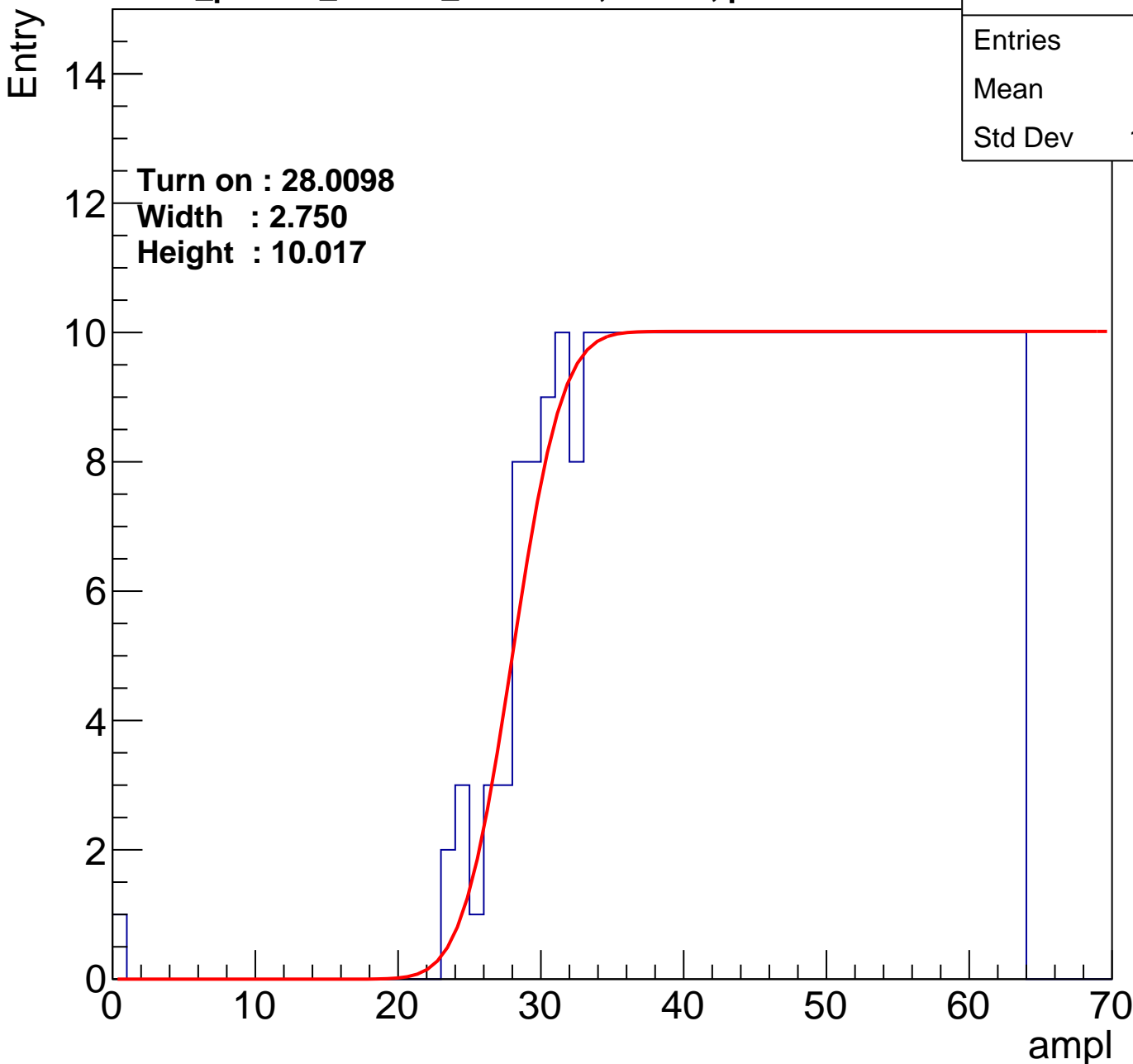
calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	45.01
Std Dev	10.97

Turn on : 28.0098

Width : 2.750

Height : 10.017



B0L102S, U20-ch50

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 27.0717

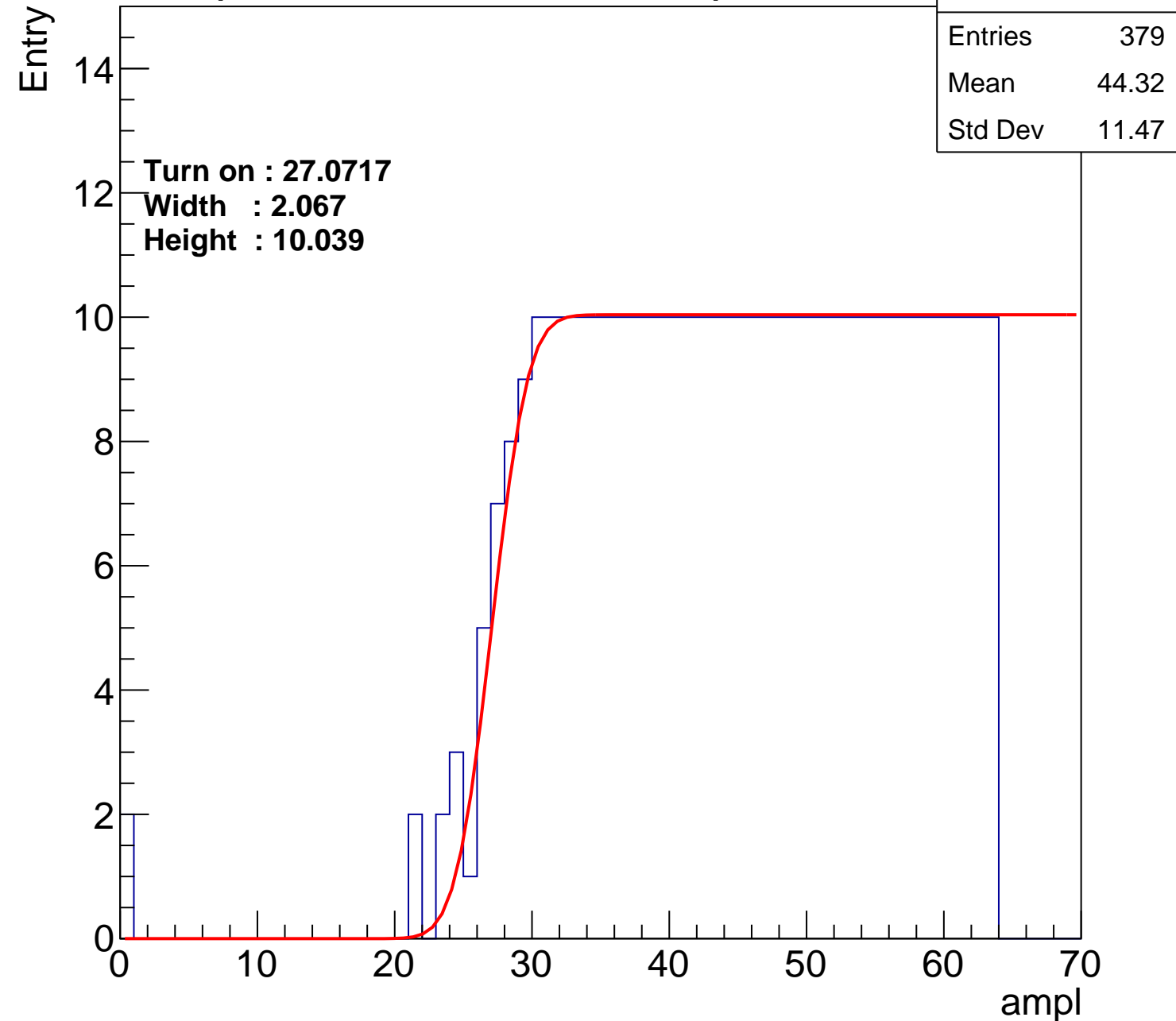
Width : 2.067

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch51

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.27
Std Dev	12

Turn on : 26.7671

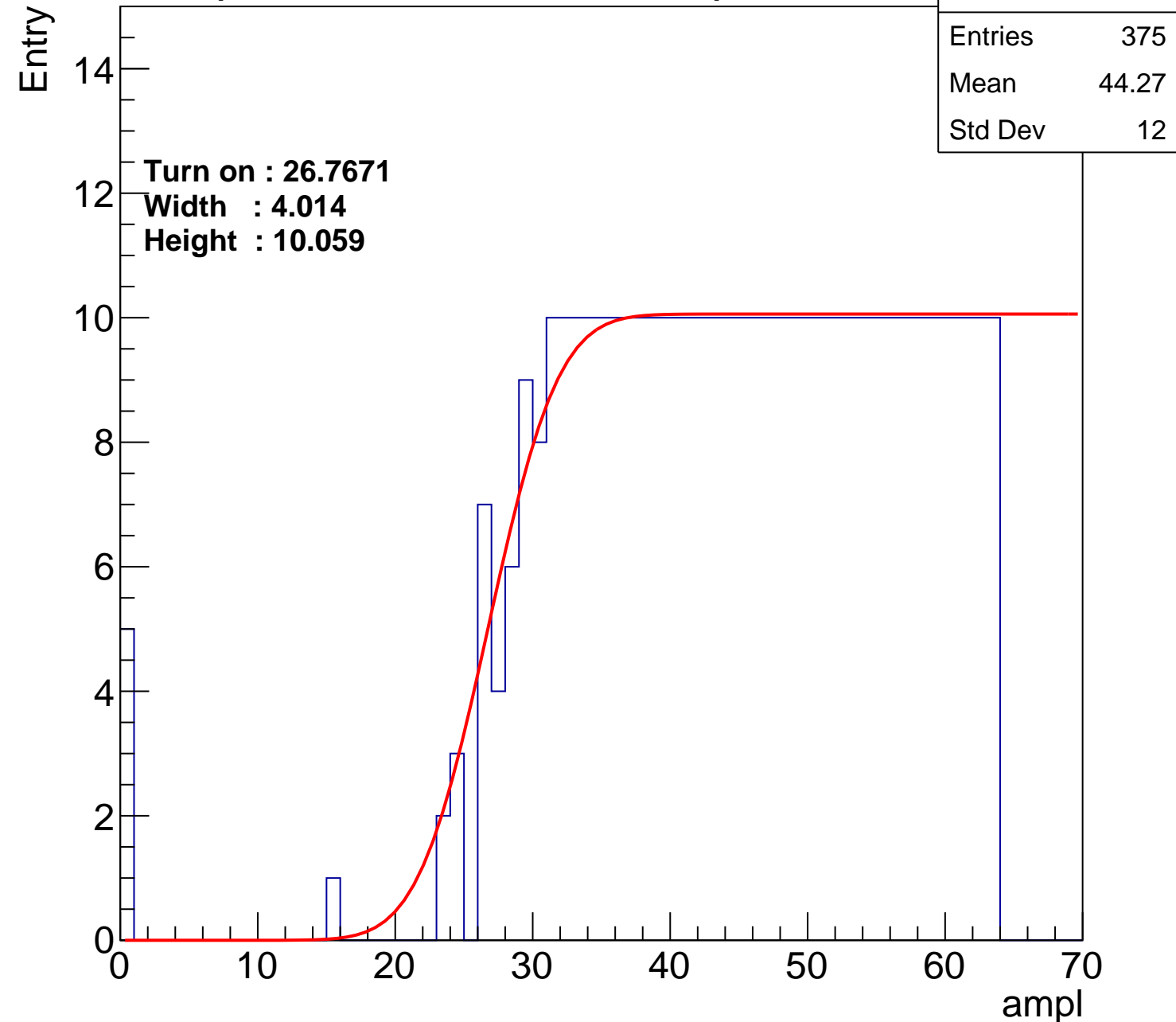
Width : 4.014

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch52

calib_packv5_042523_0143.root, FC#12, port B1

Entries	398
Mean	43.34
Std Dev	12.1

Turn on : 24.2758

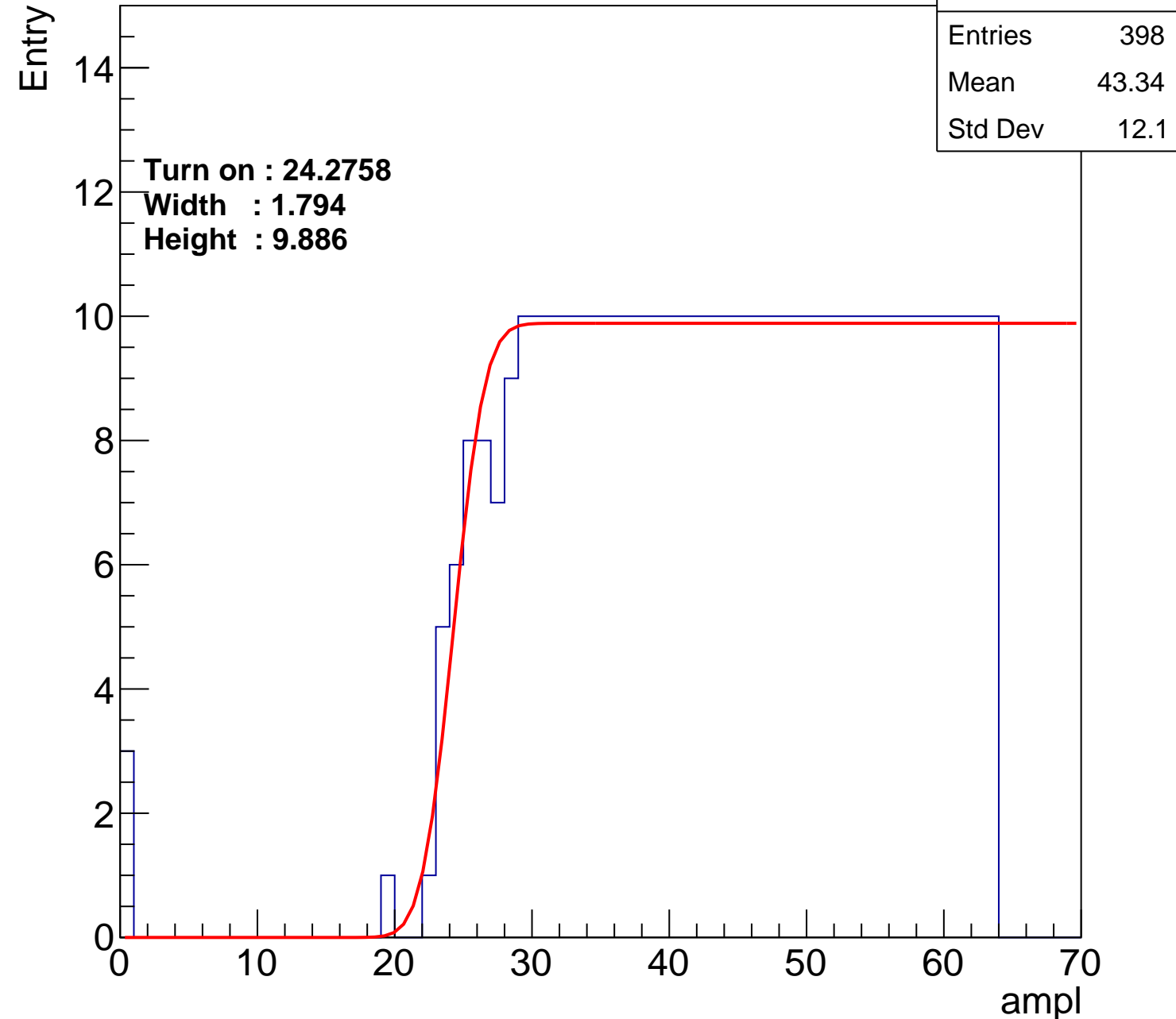
Width : 1.794

Height : 9.886

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch53

calib_packv5_042523_0143.root, FC#12, port B1

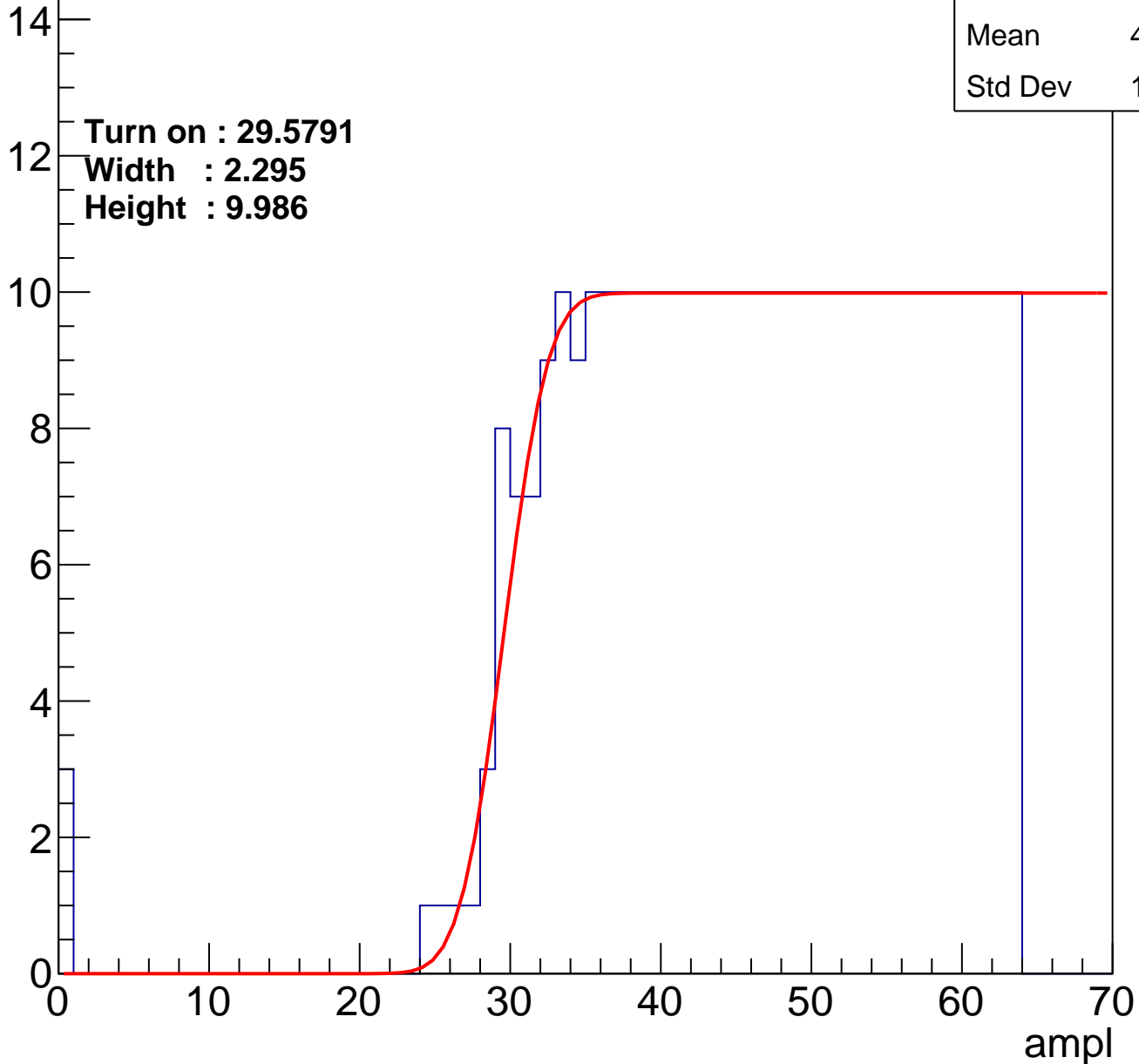
Entries	350
Mean	45.65
Std Dev	10.99

Turn on : 29.5791

Width : 2.295

Height : 9.986

Entry



B0L102S, U20-ch54

calib_packv5_042523_0143.root, FC#12, port B1

Entries	403
Mean	42.95
Std Dev	12.62

Turn on : 24.5683

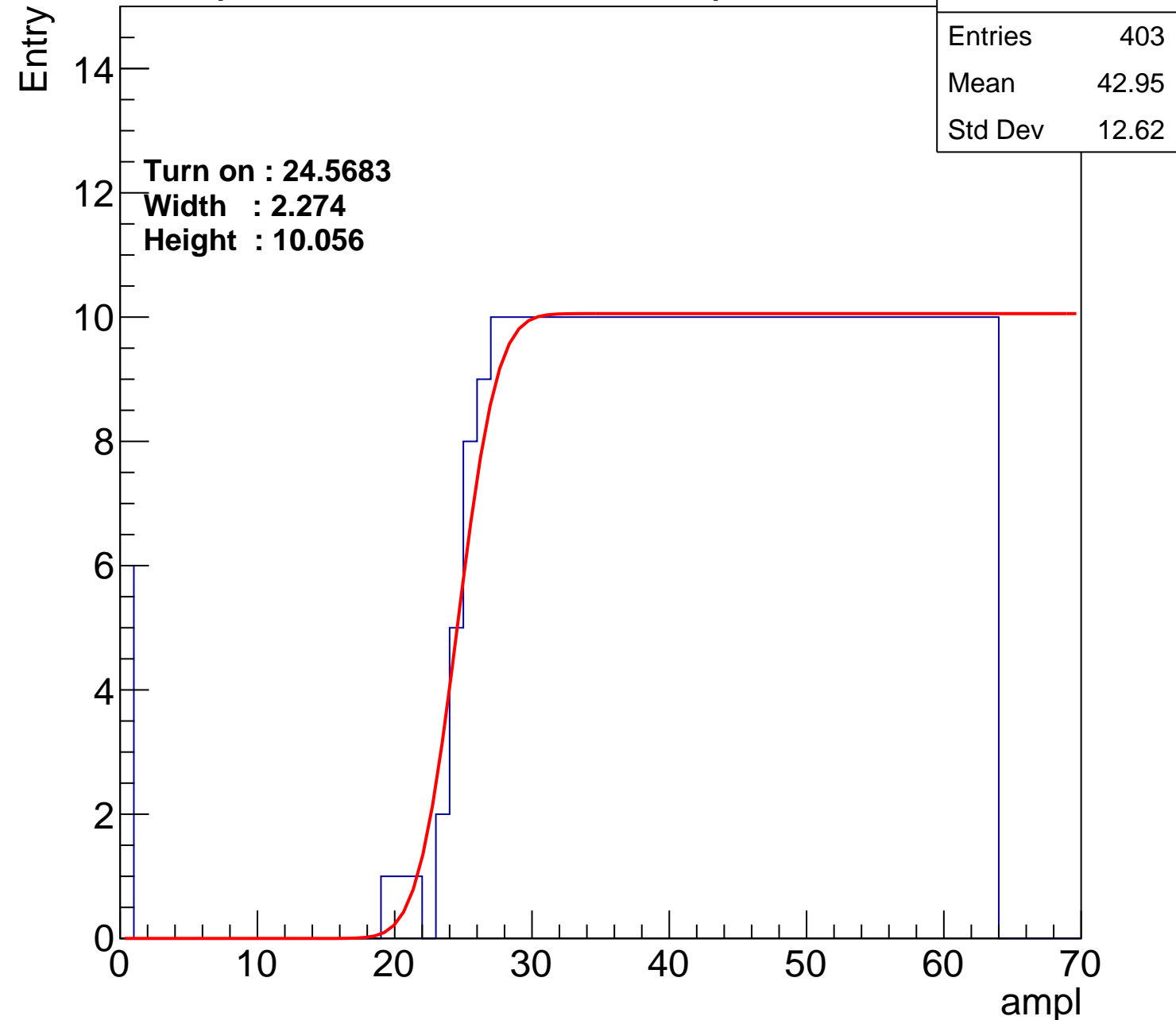
Width : 2.274

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch55

calib_packv5_042523_0143.root, FC#12, port B1

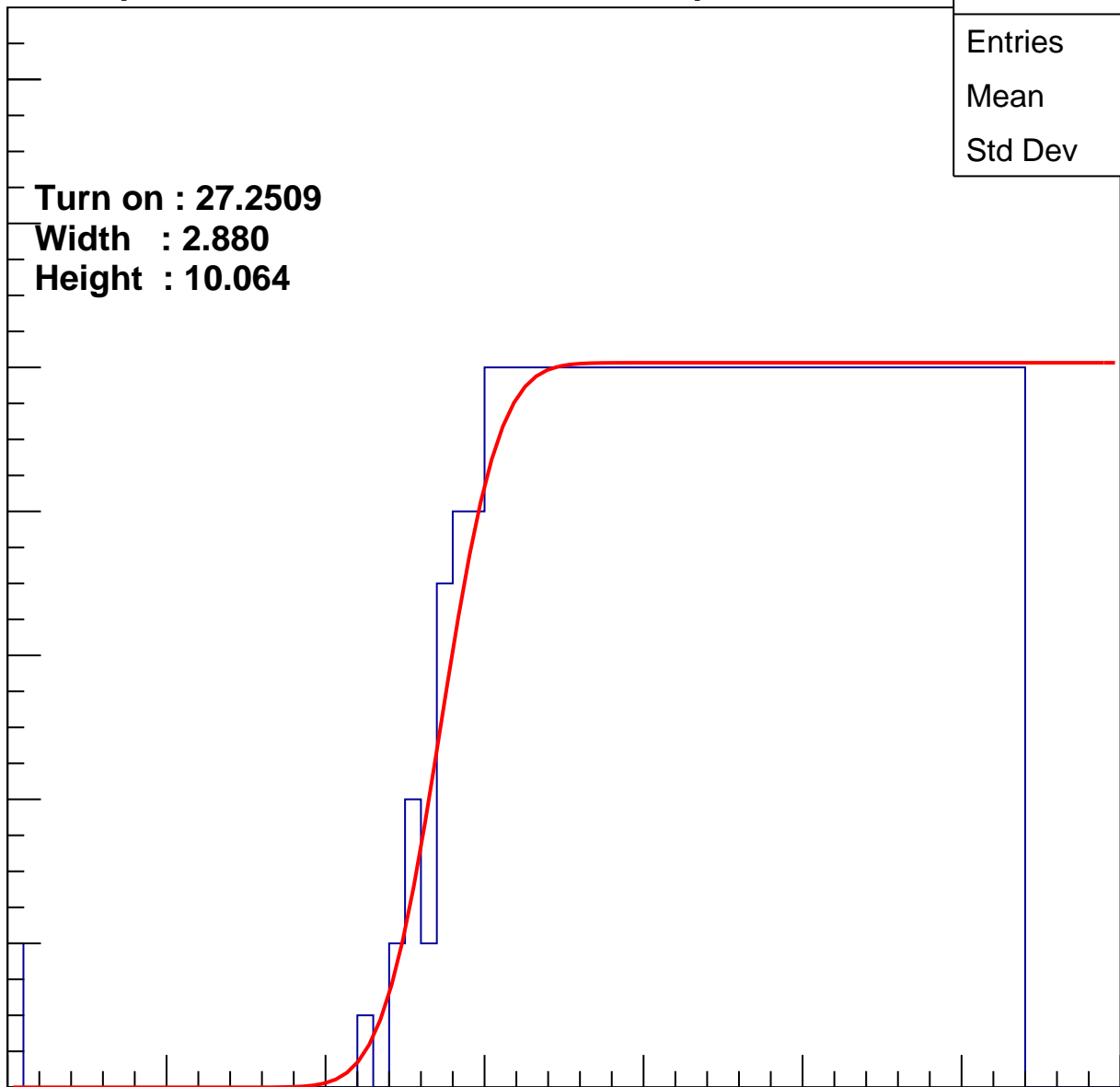
Entry

14
12
10
8
6
4
2
0

Turn on : 27.2509
Width : 2.880
Height : 10.064

Entries	374
Mean	44.59
Std Dev	11.31

ampl



B0L102S, U20-ch56

calib_packv5_042523_0143.root, FC#12, port B1

Entries	385
Mean	44.02
Std Dev	11.63

Turn on : 26.2604

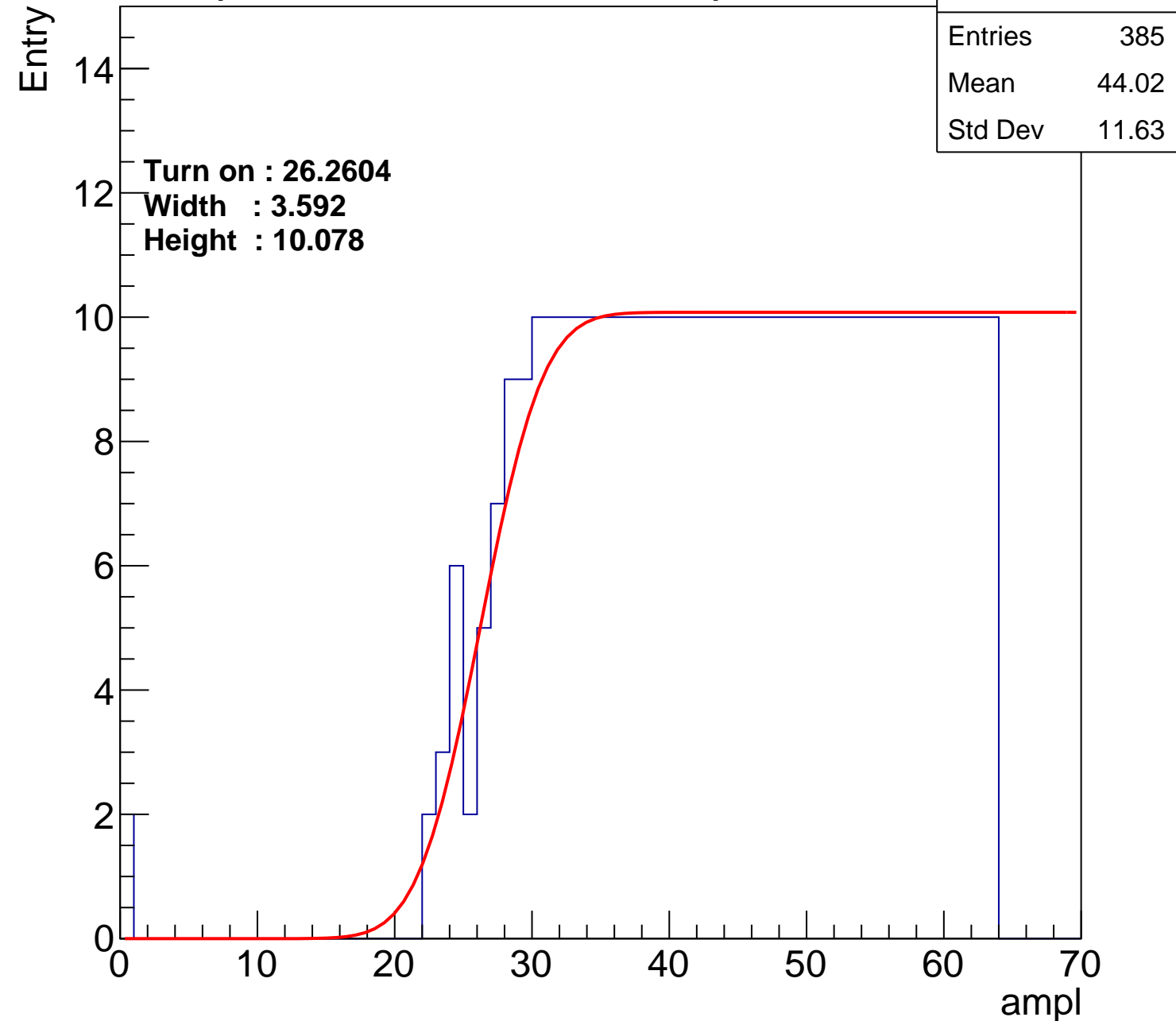
Width : 3.592

Height : 10.078

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch57

calib_packv5_042523_0143.root, FC#12, port B1

Entries	374
Mean	44.66
Std Dev	11.11

Turn on : 26.4714

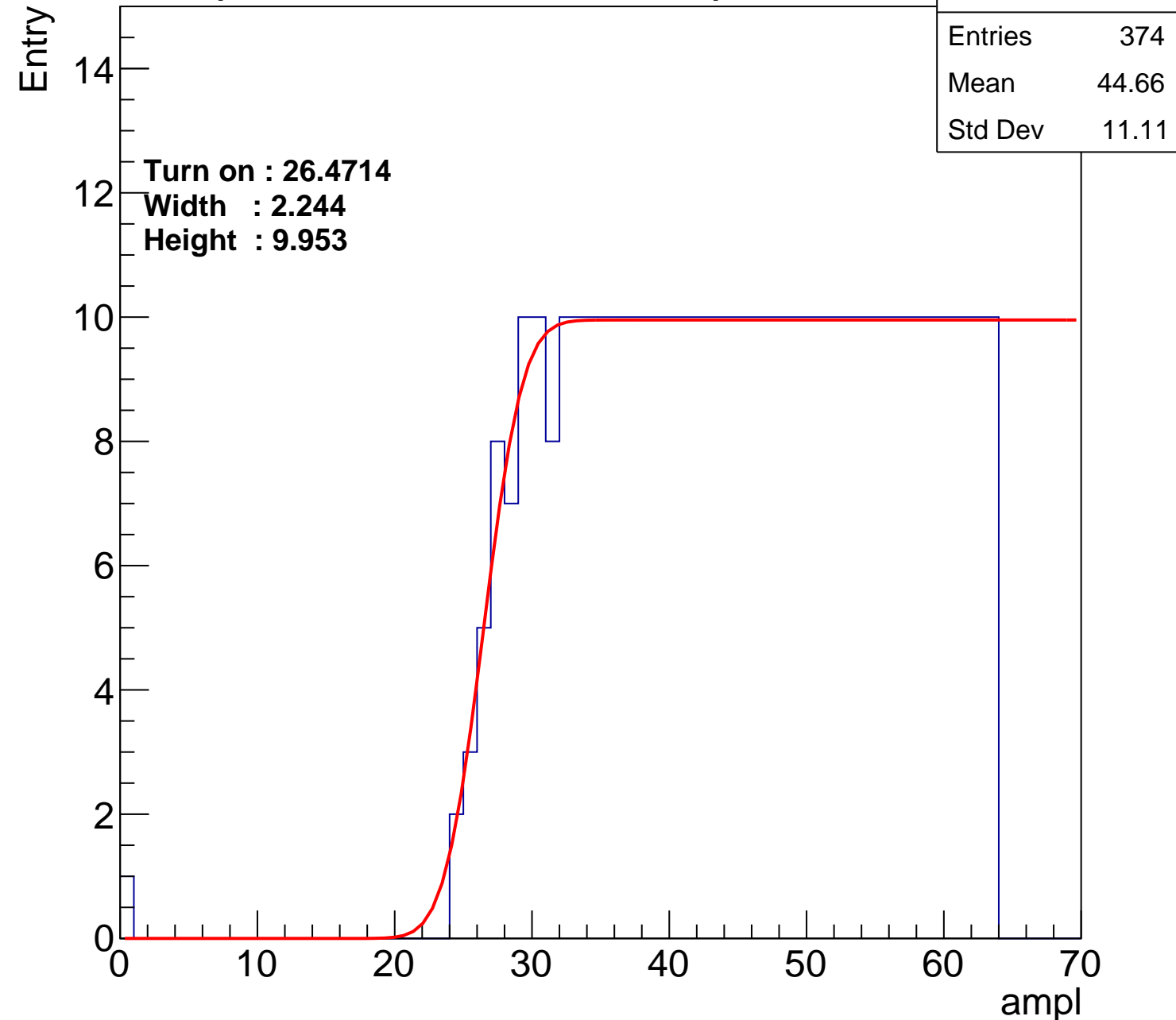
Width : 2.244

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch58

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 27.0023

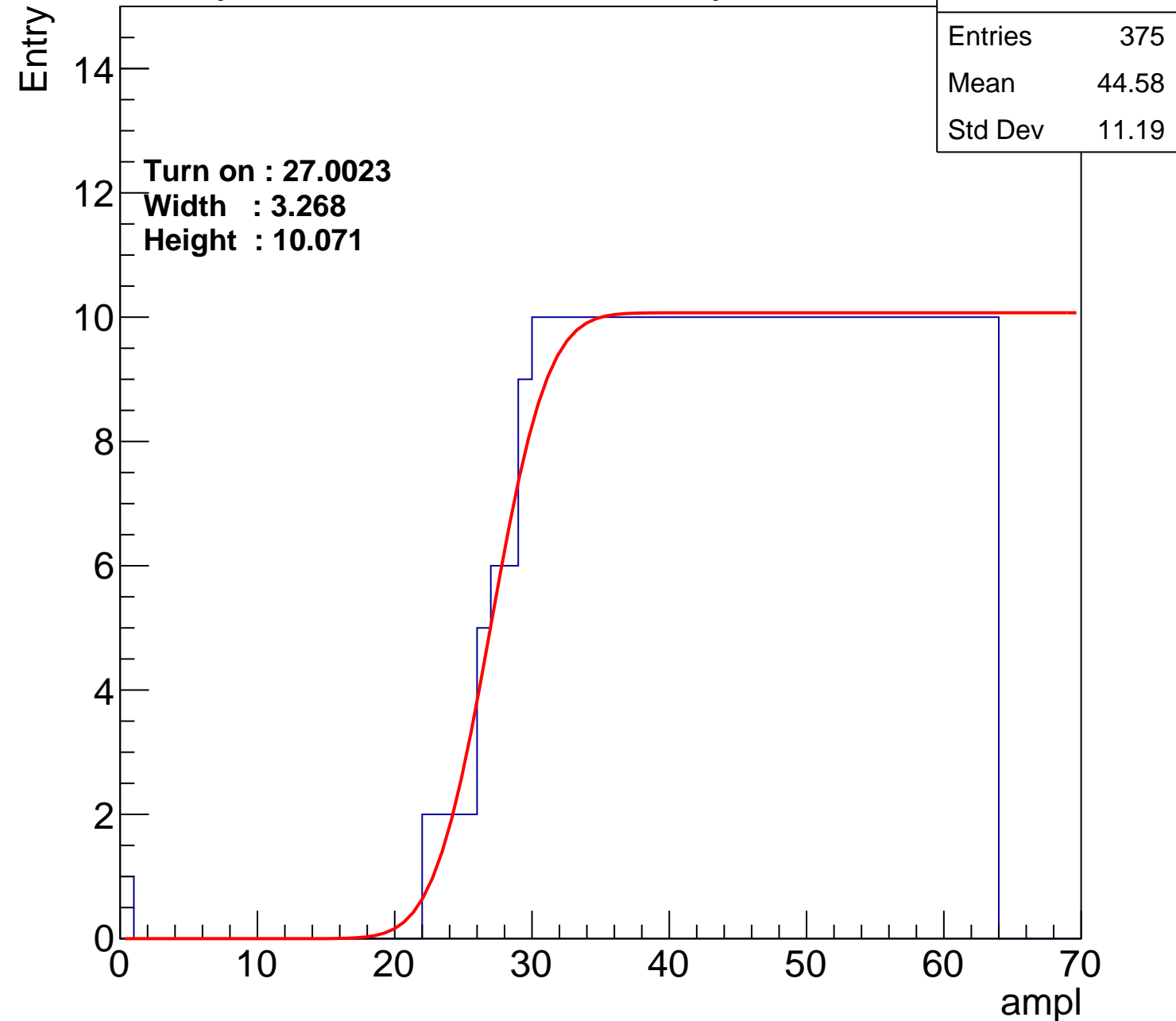
Width : 3.268

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch59

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.84
Std Dev	11.03

Turn on : 27.0123

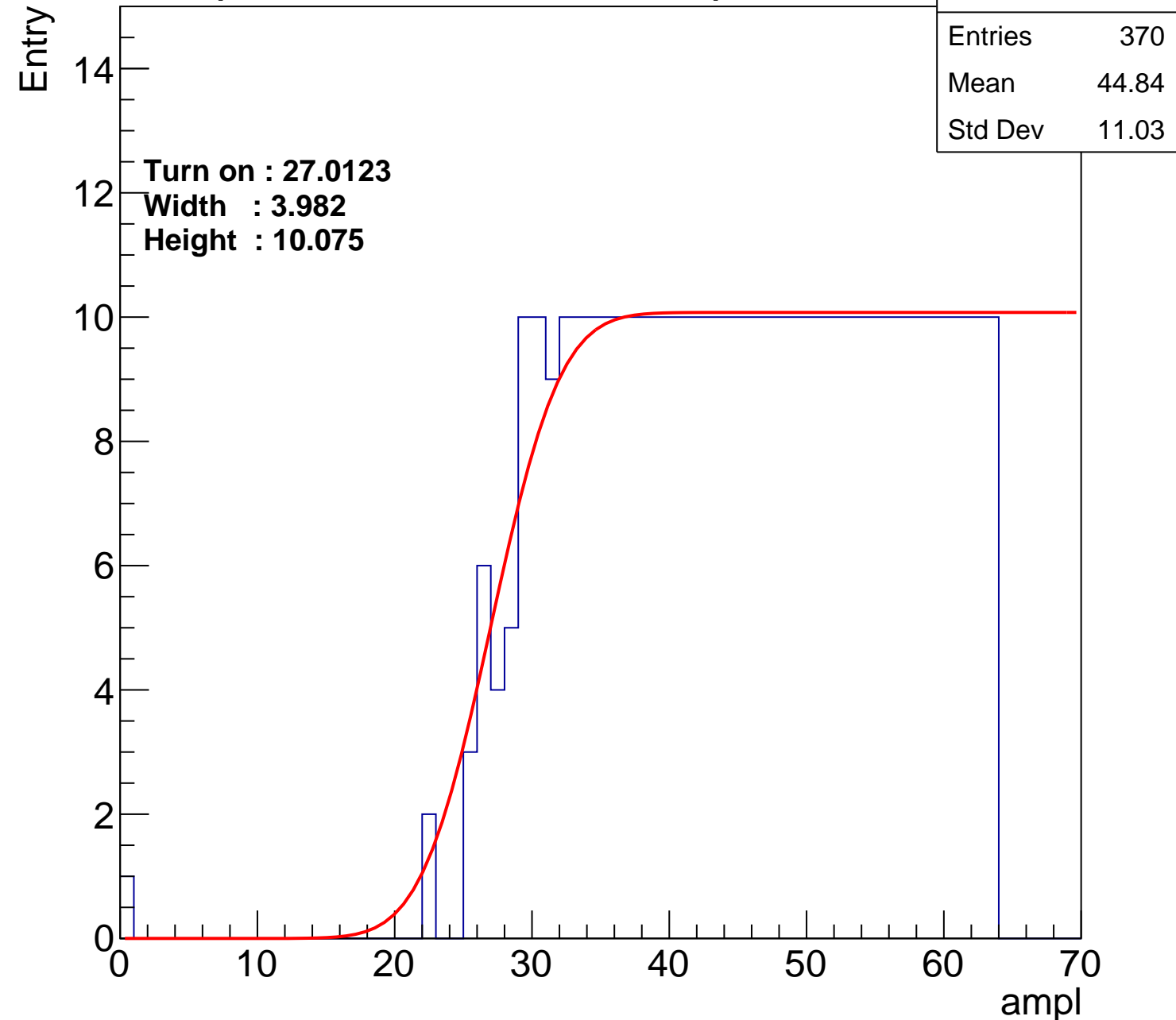
Width : 3.982

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch60

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.52
Std Dev	12.04

Turn on : 25.2397

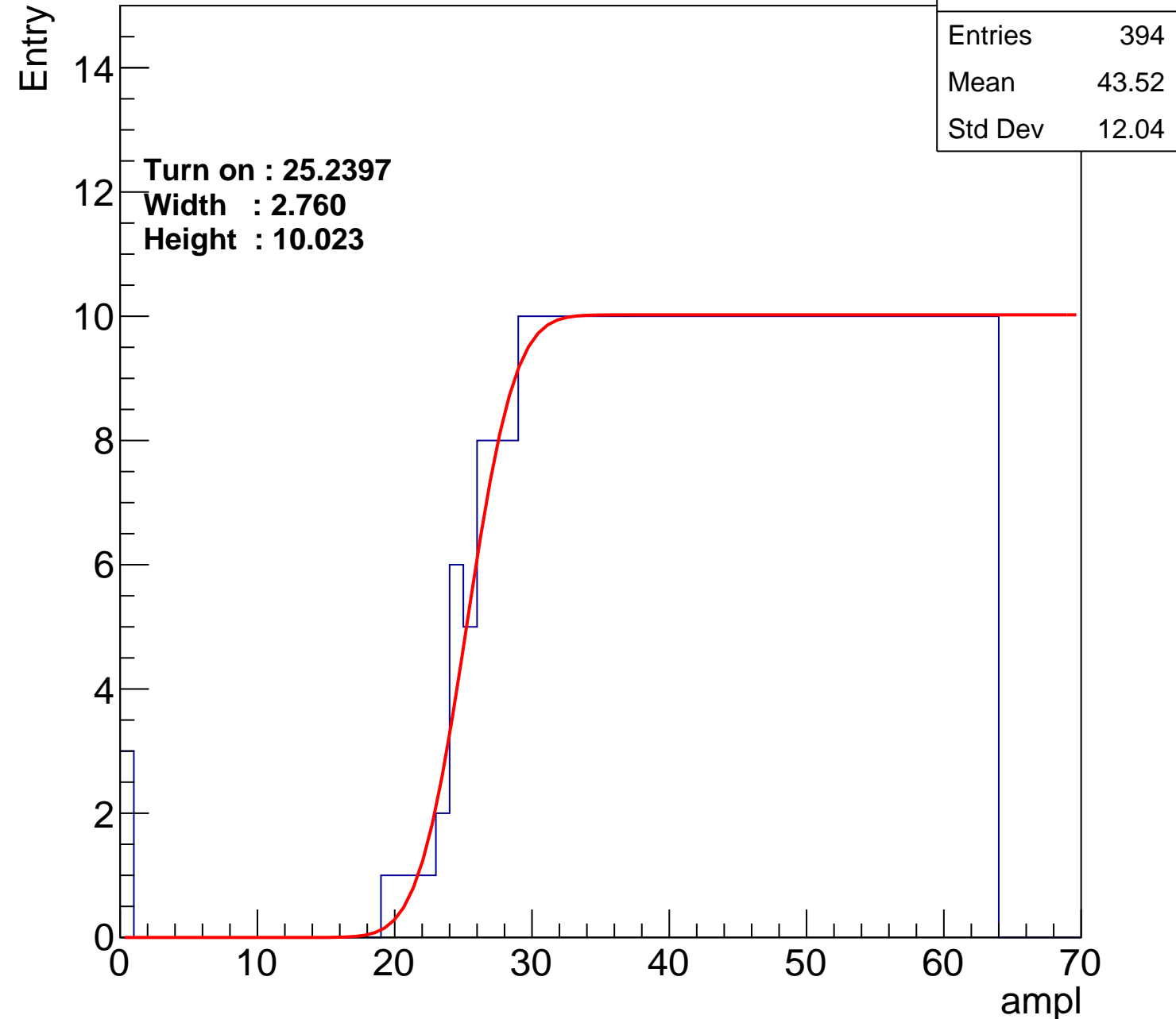
Width : 2.760

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch61

calib_packv5_042523_0143.root, FC#12, port B1

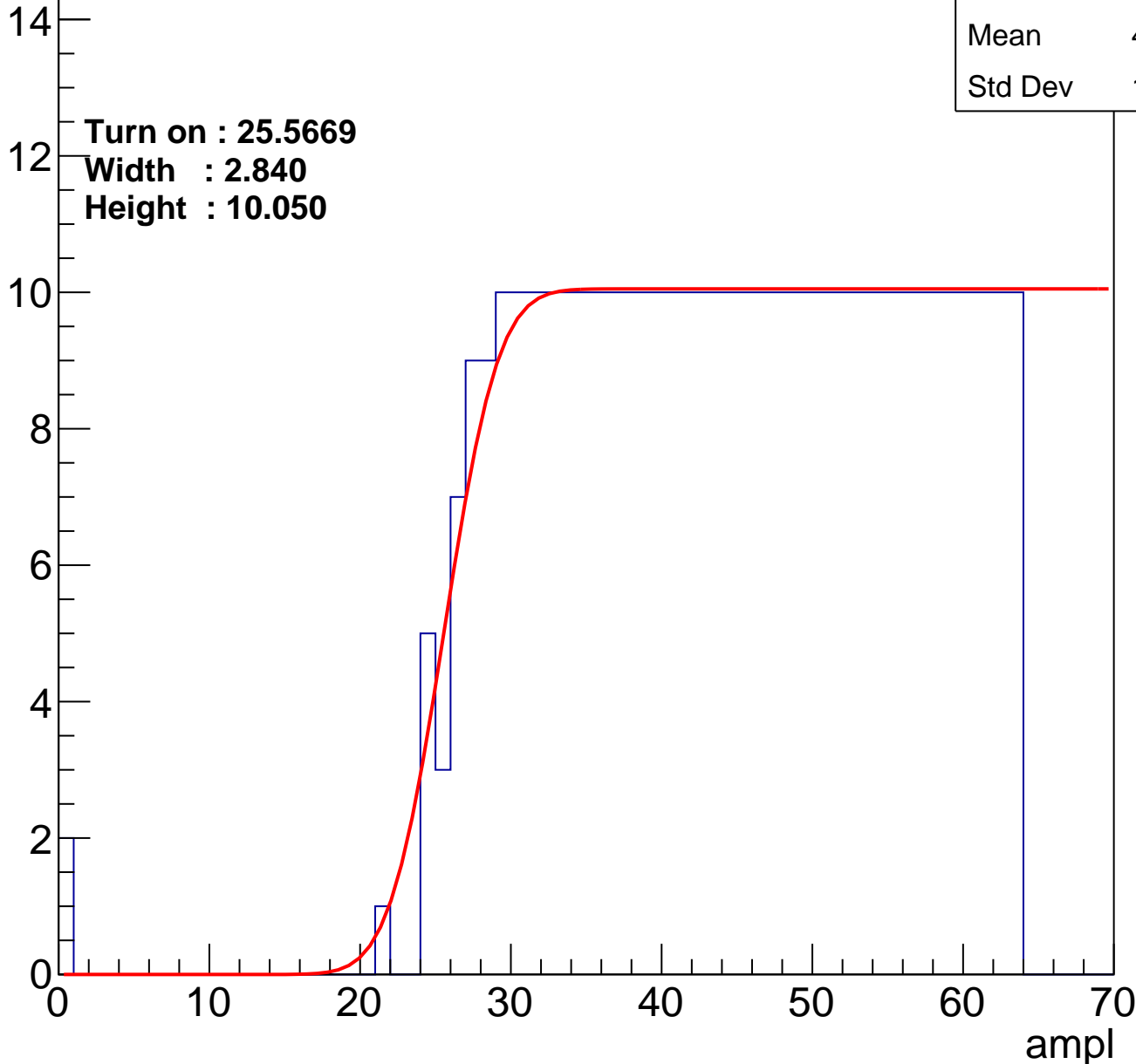
Entries	386
Mean	44.02
Std Dev	11.58

Turn on : 25.5669

Width : 2.840

Height : 10.050

Entry



B0L102S, U20-ch62

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.78
Std Dev	12

Turn on : 26.1415

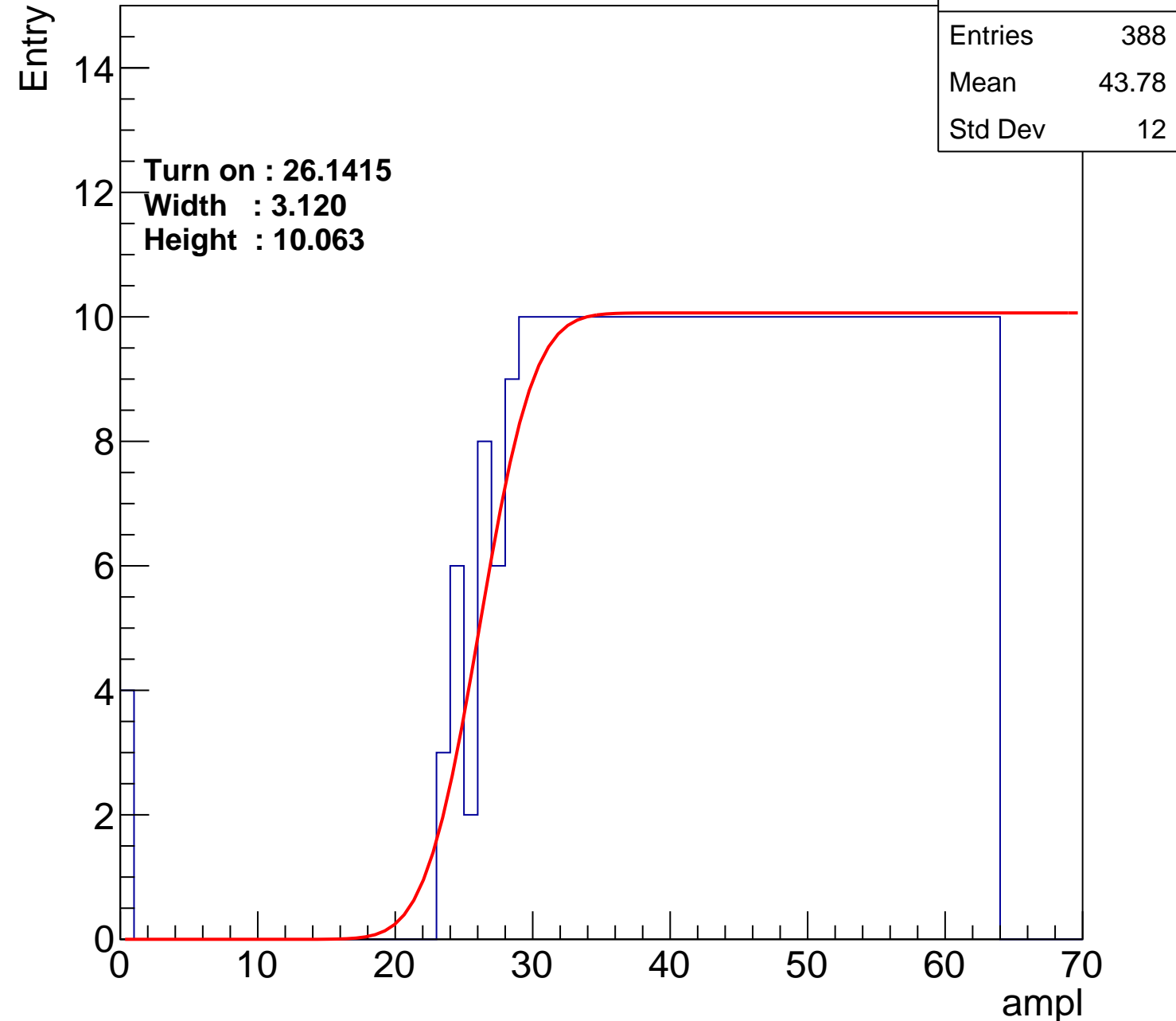
Width : 3.120

Height : 10.063

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch63

calib_packv5_042523_0143.root, FC#12, port B1

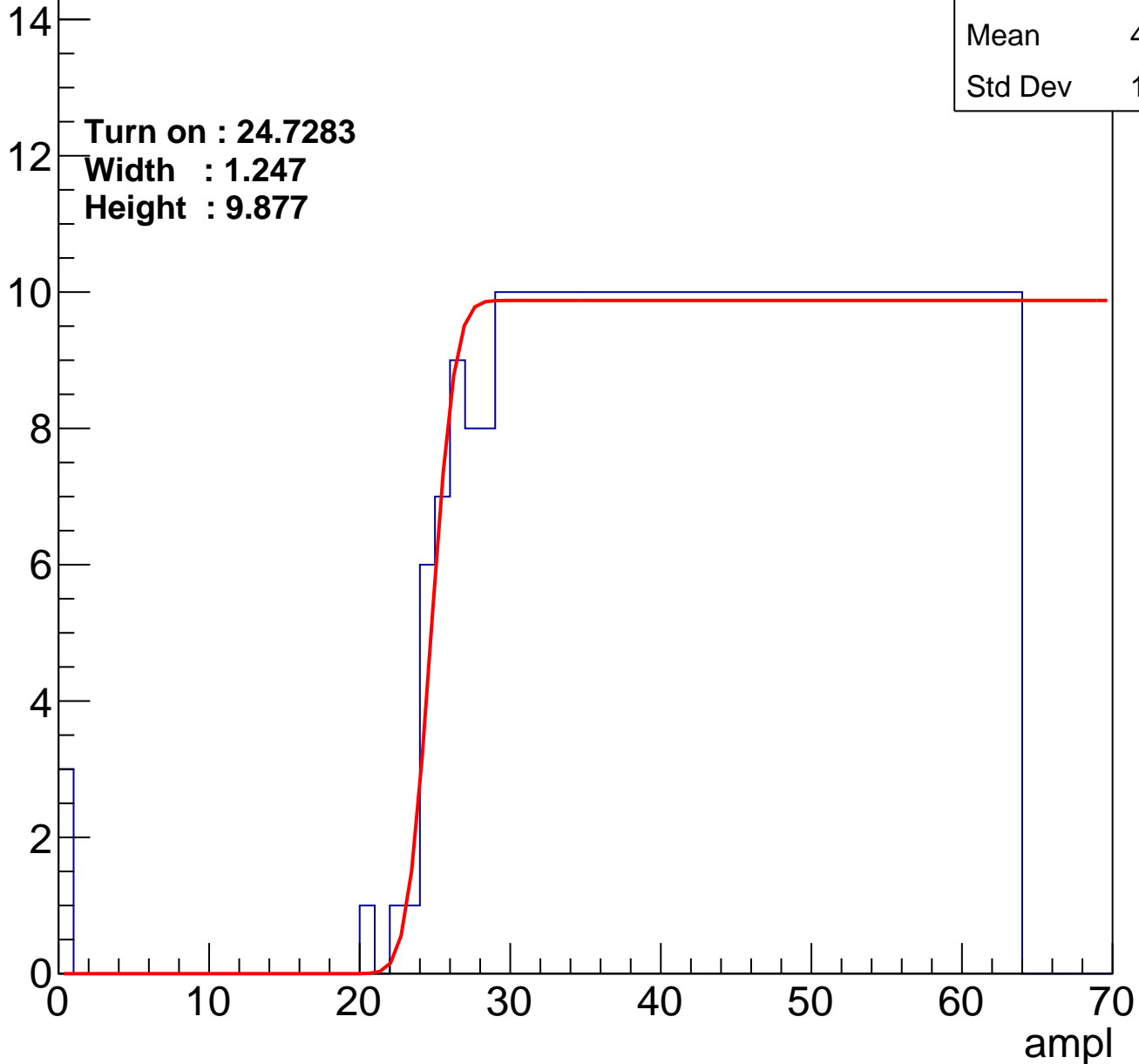
Entries	394
Mean	43.55
Std Dev	11.98

Turn on : 24.7283

Width : 1.247

Height : 9.877

Entry



B0L102S, U20-ch64

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.96
Std Dev	11.56

Turn on : 25.4160

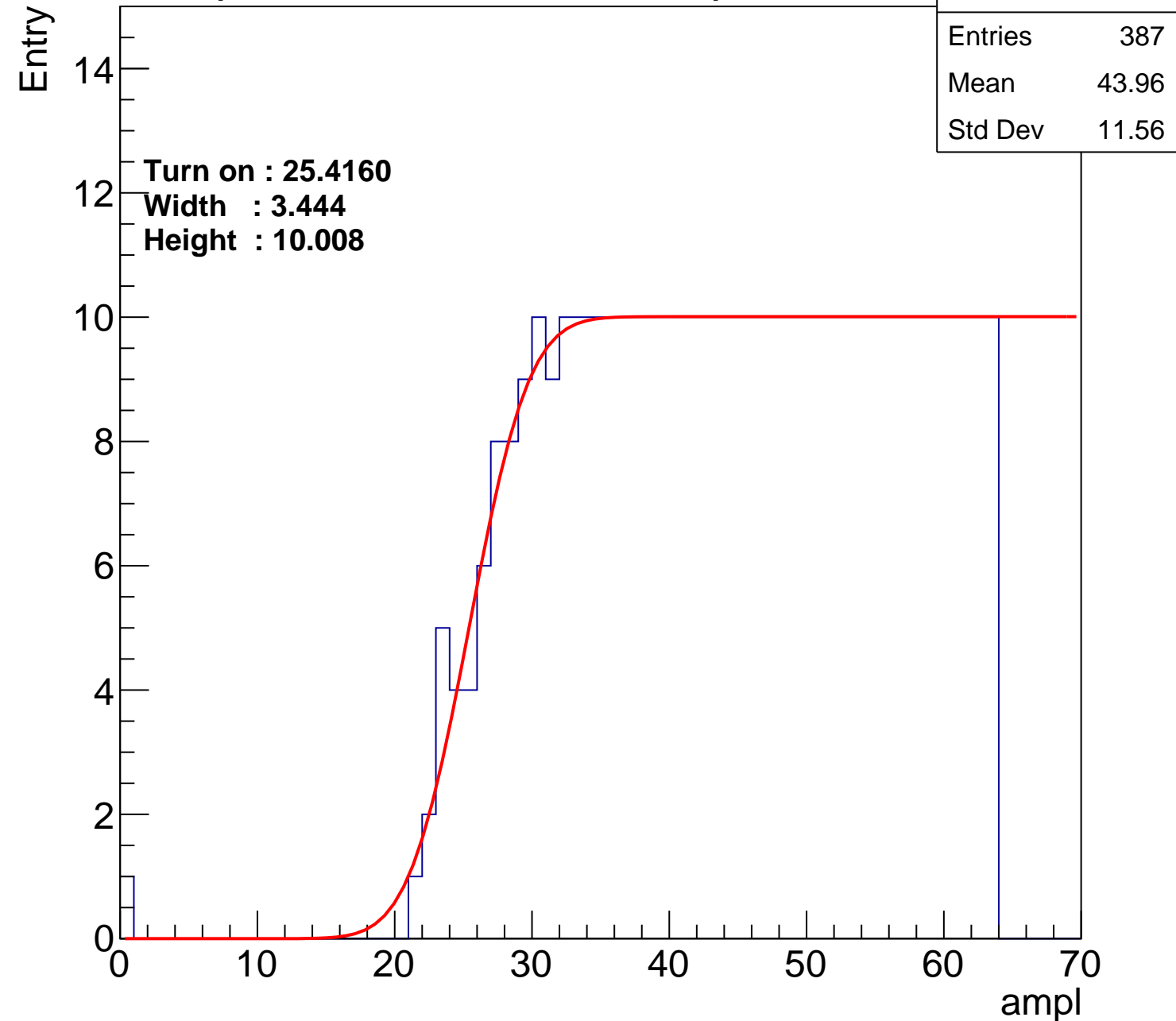
Width : 3.444

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch65

calib_packv5_042523_0143.root, FC#12, port B1

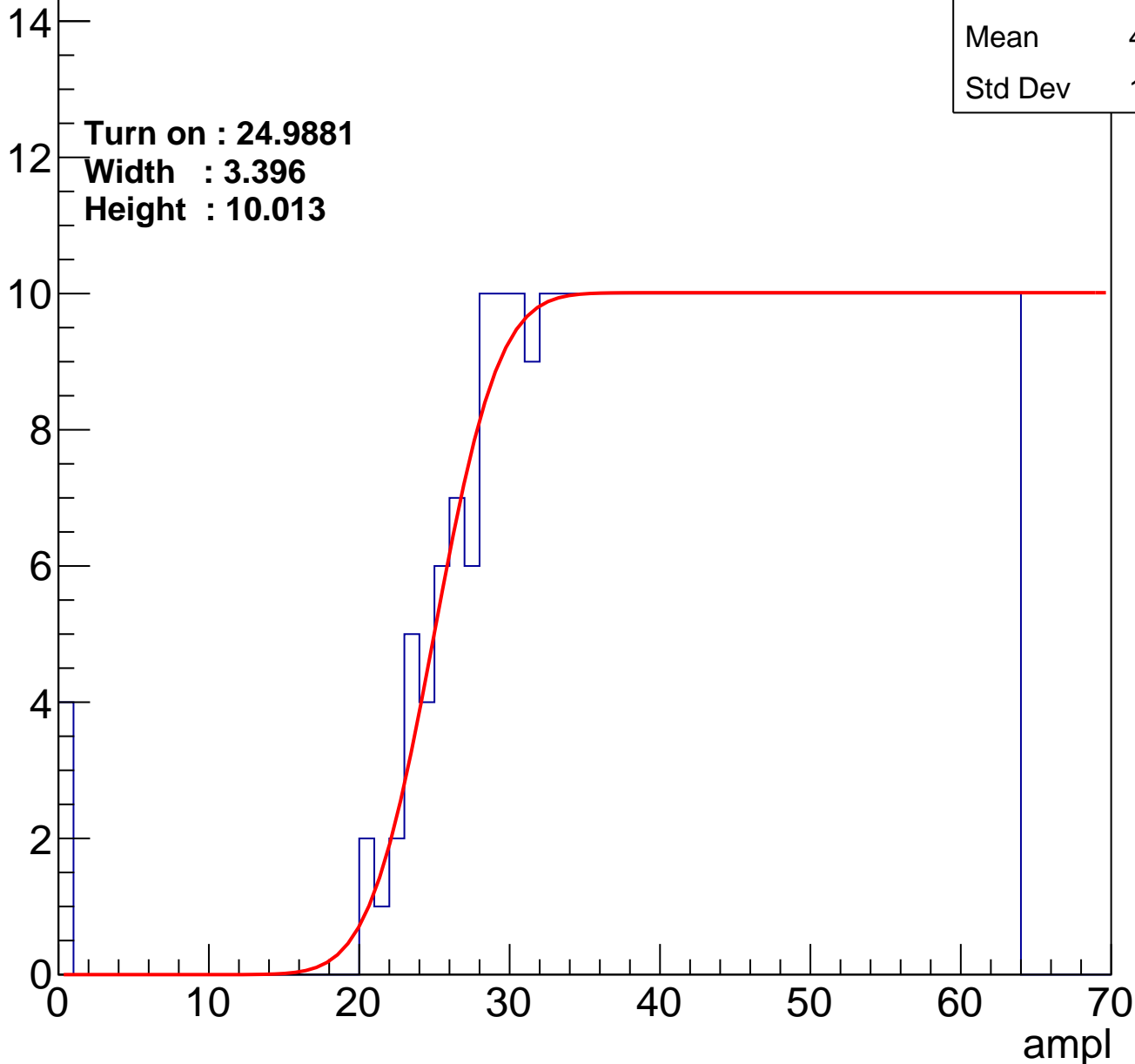
Entries	396
Mean	43.33
Std Dev	12.28

Turn on : 24.9881

Width : 3.396

Height : 10.013

Entry



B0L102S, U20-ch66

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.77
Std Dev	11.1

Turn on : 27.2315

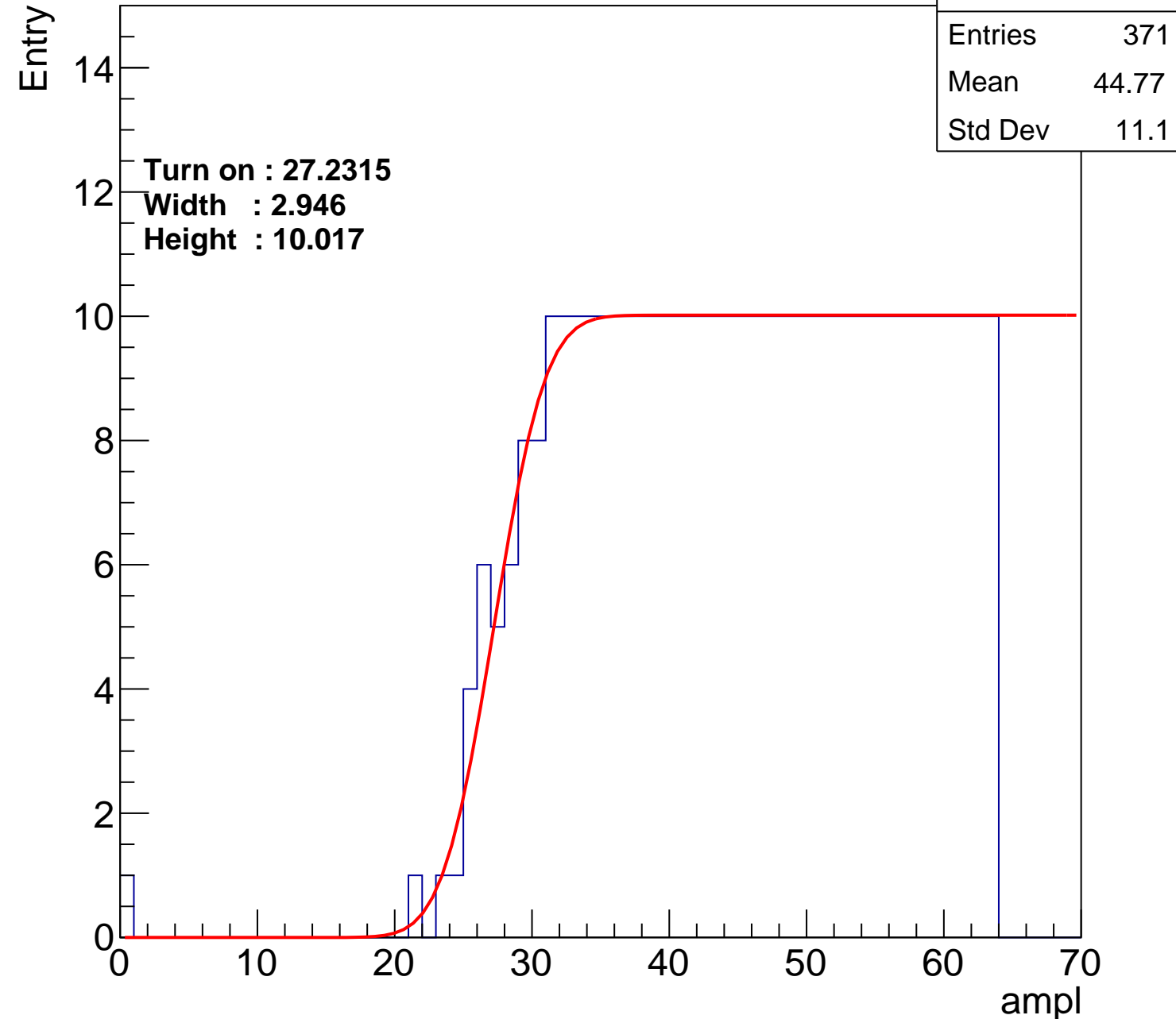
Width : 2.946

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch67

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 26.3024

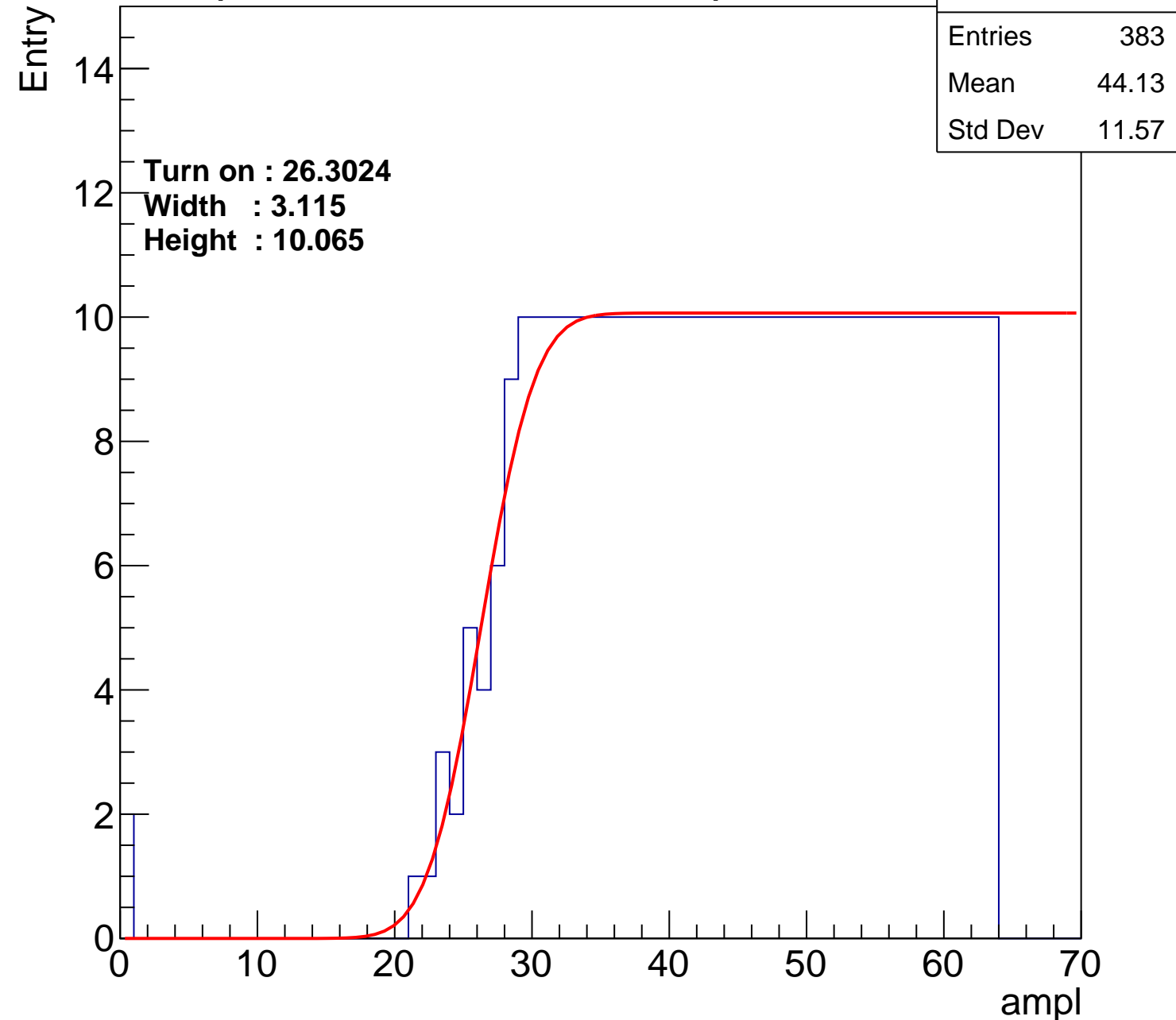
Width : 3.115

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch68

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	44.24
Std Dev	11.38

Turn on : 26.4183

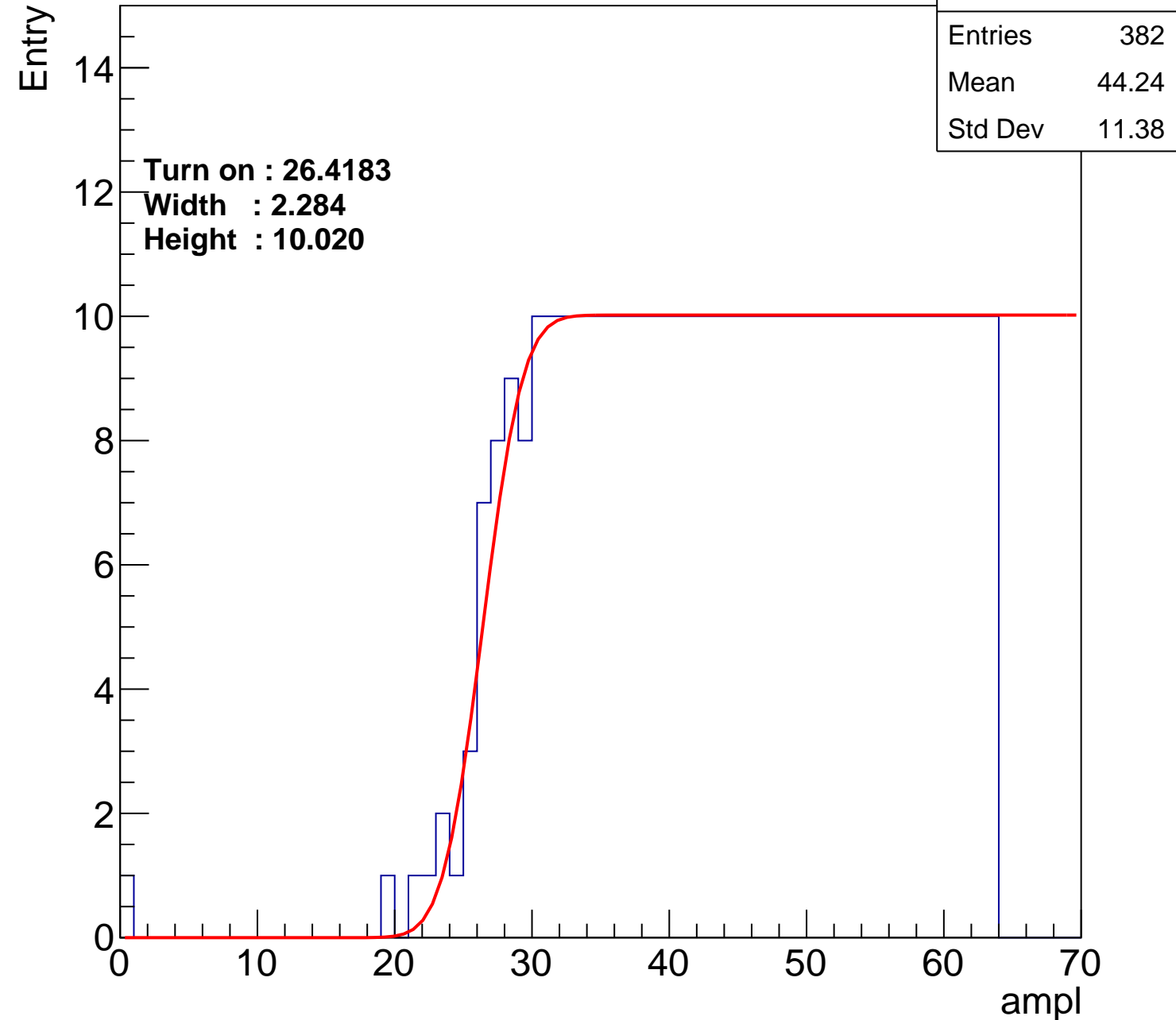
Width : 2.284

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch69

calib_packv5_042523_0143.root, FC#12, port B1

Entries	407
Mean	42.95
Std Dev	12.18

Turn on : 23.3518

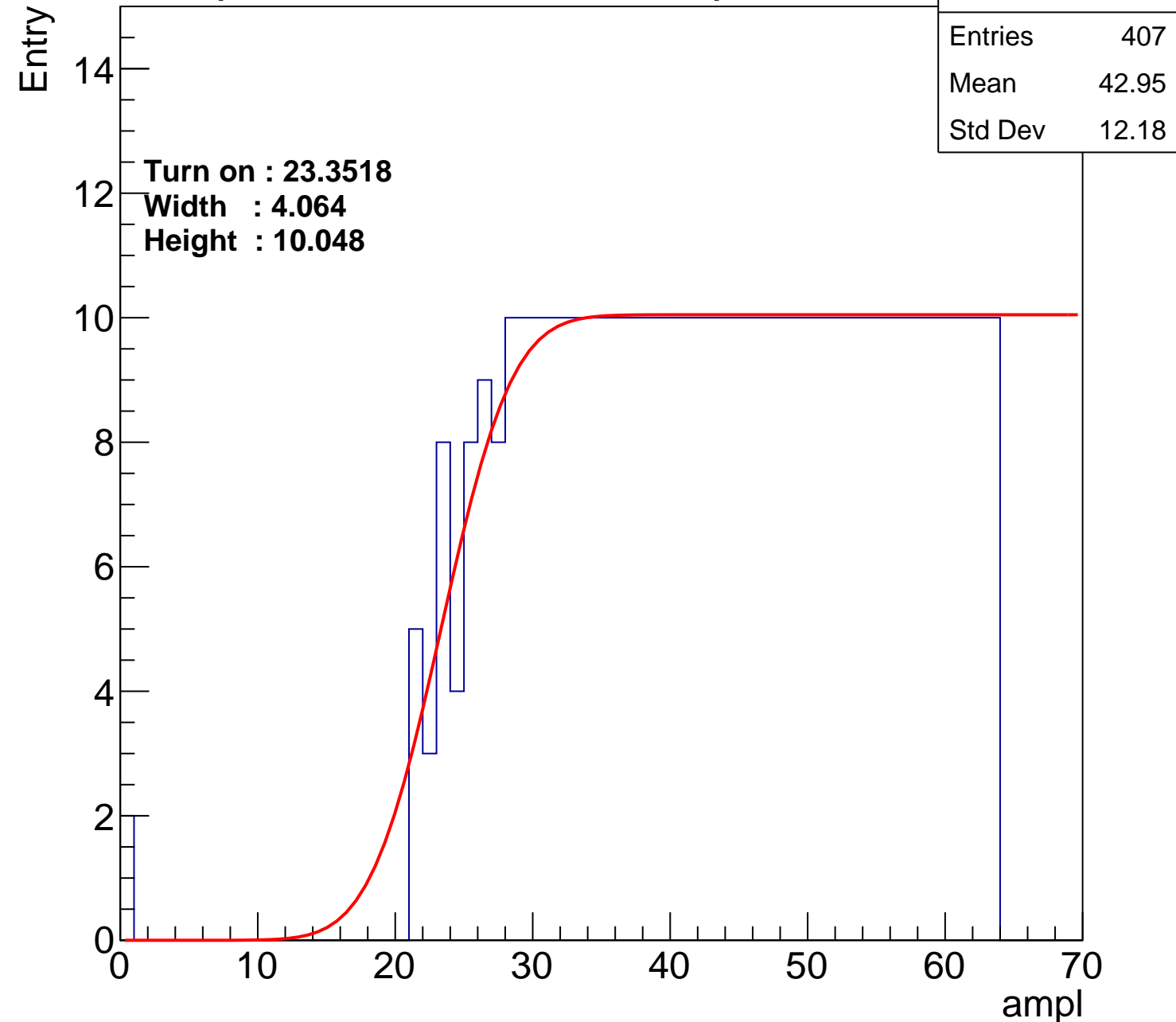
Width : 4.064

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch70

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.71
Std Dev	11.64

Turn on : 25.4288

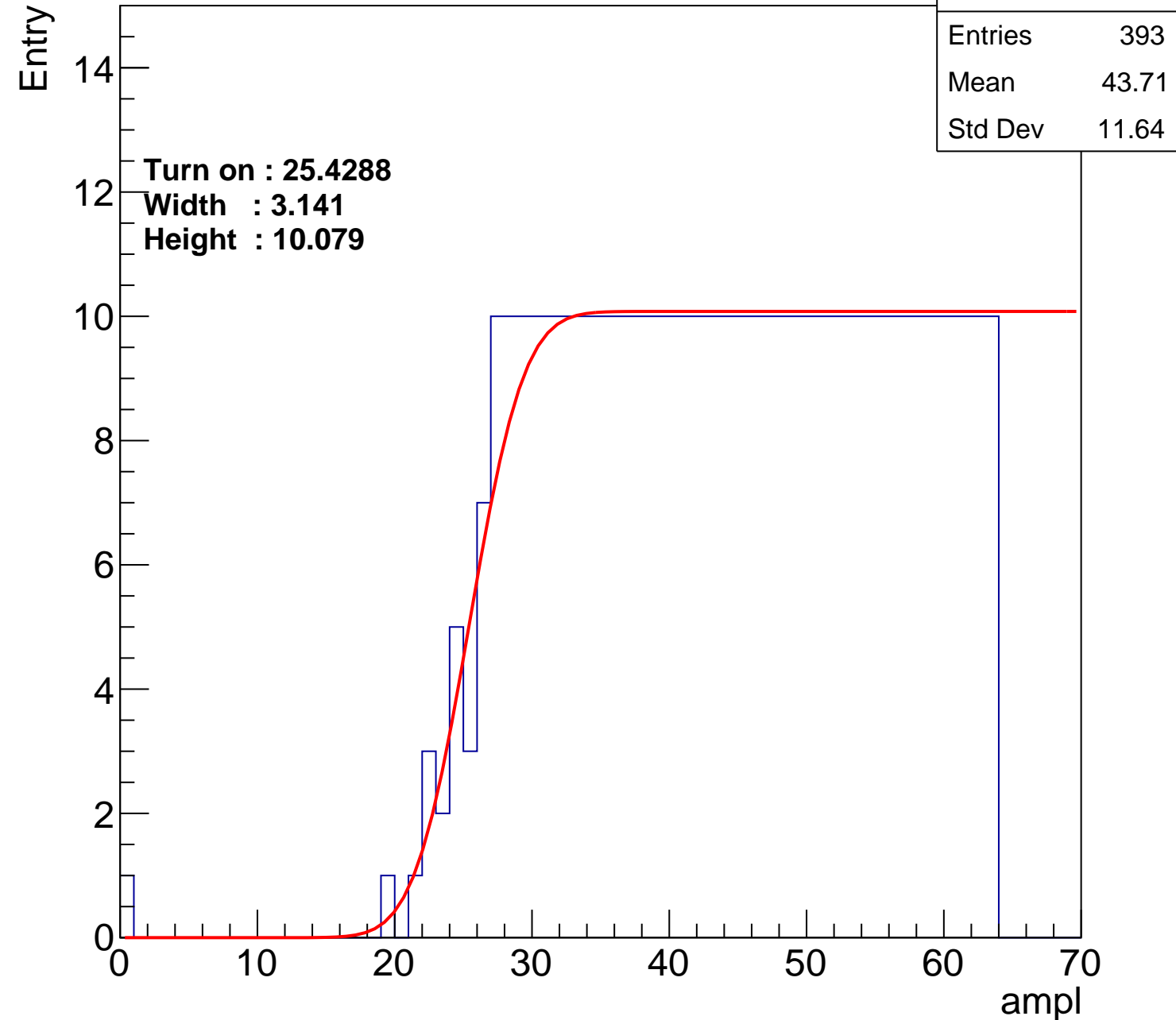
Width : 3.141

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch71

calib_packv5_042523_0143.root, FC#12, port B1

Entries	390
Mean	43.74
Std Dev	11.86

Turn on : 25.2173

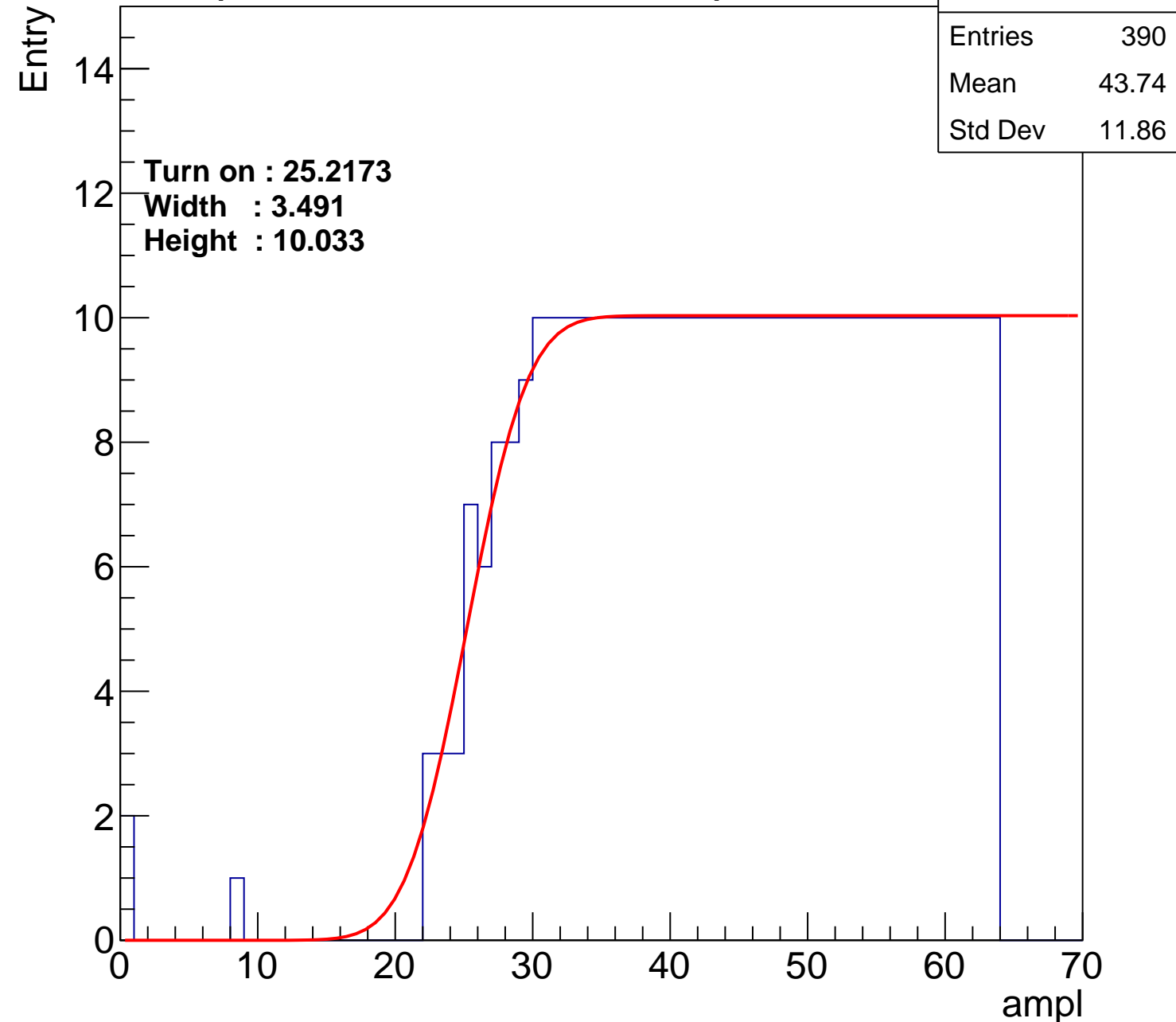
Width : 3.491

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch72

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.6570

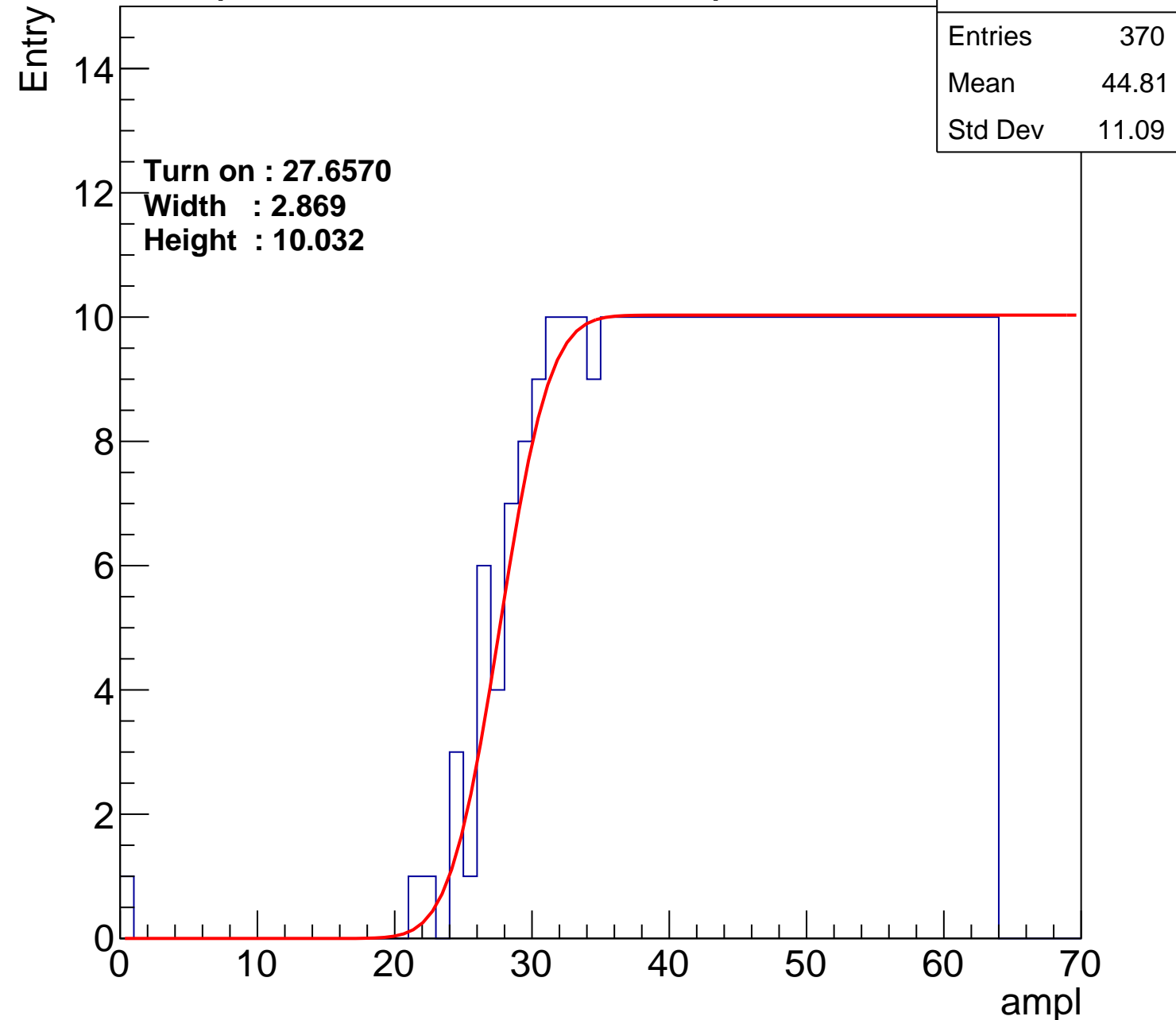
Width : 2.869

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch73

calib_packv5_042523_0143.root, FC#12, port B1

Entries	364
Mean	45.02
Std Dev	11.15

Turn on : 28.0370

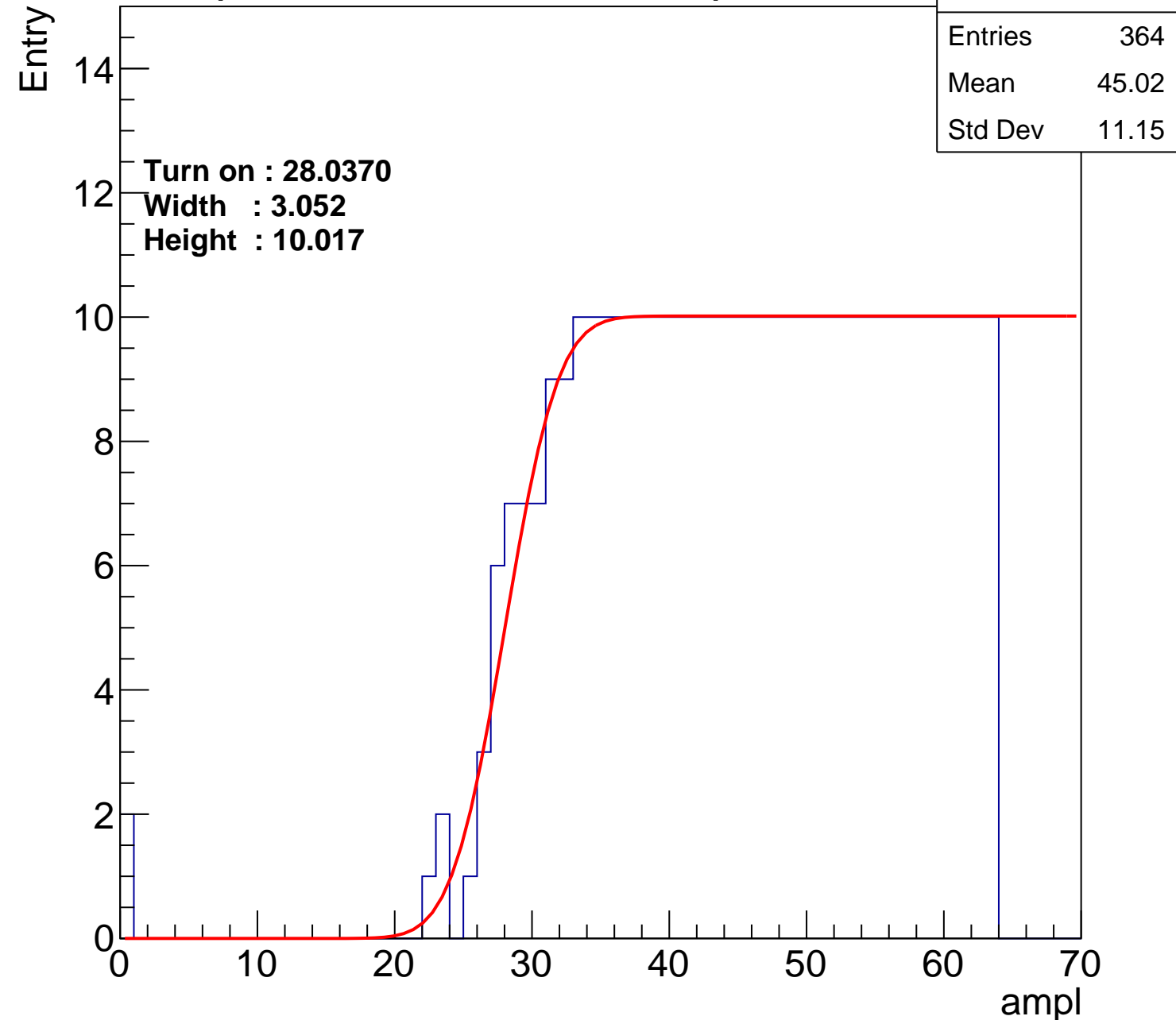
Width : 3.052

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch74

calib_packv5_042523_0143.root, FC#12, port B1

Entries	400
Mean	43.27
Std Dev	12.05

Turn on : 23.8278

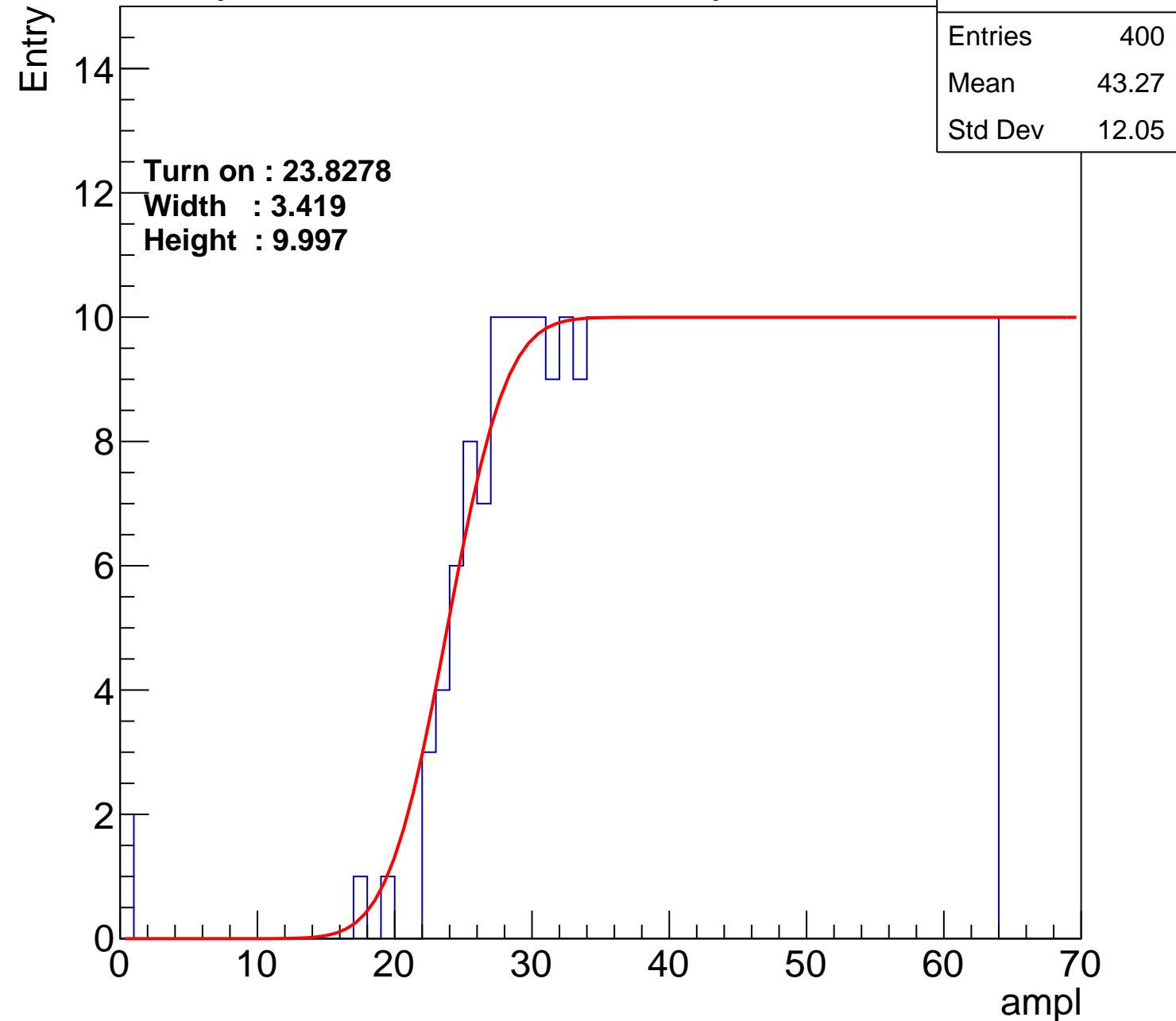
Width : 3.419

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch75

calib_packv5_042523_0143.root, FC#12, port B1

Entries	367
Mean	44.89
Std Dev	11.2

Turn on : 27.8033

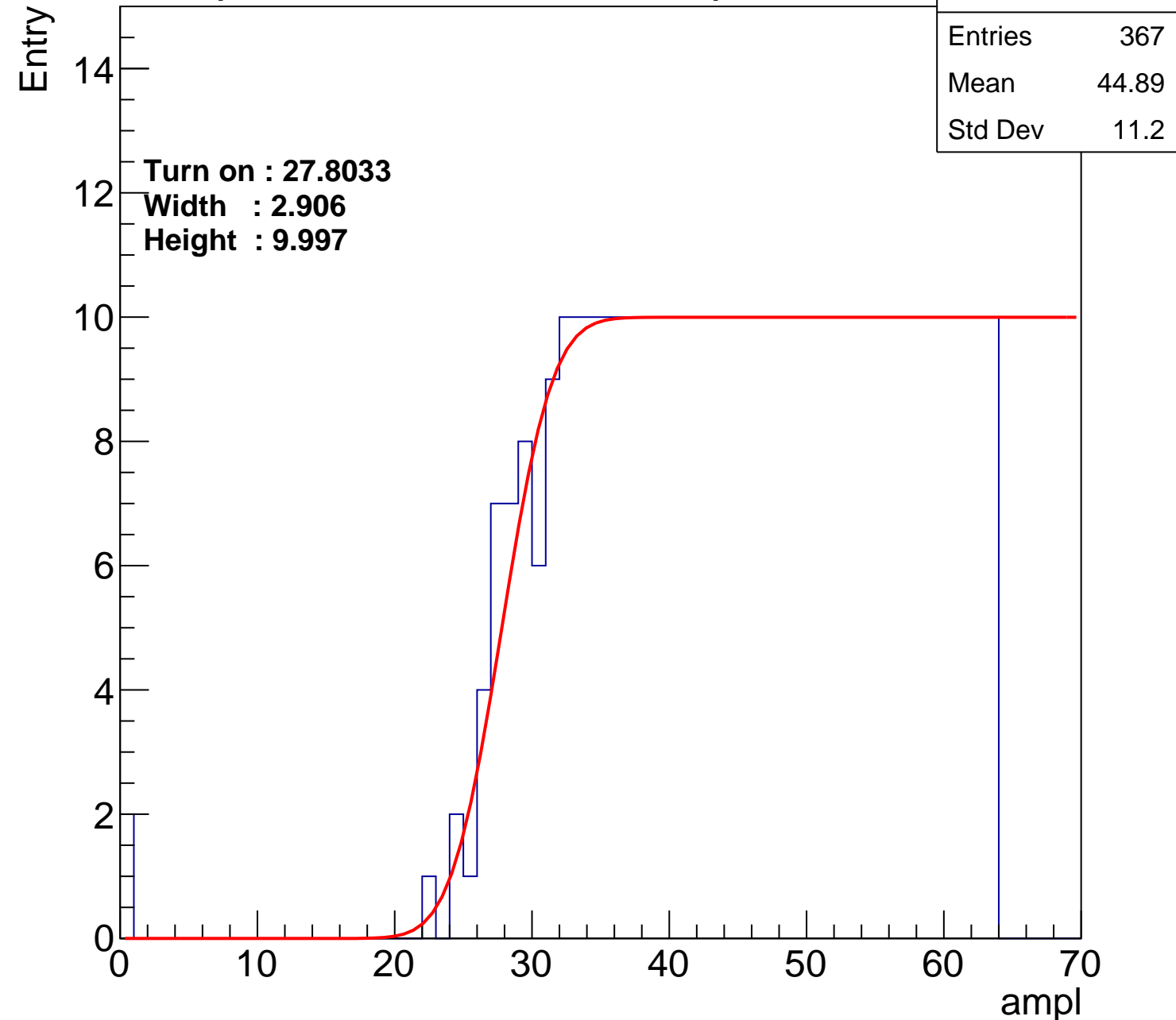
Width : 2.906

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch76

calib_packv5_042523_0143.root, FC#12, port B1

Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.1847

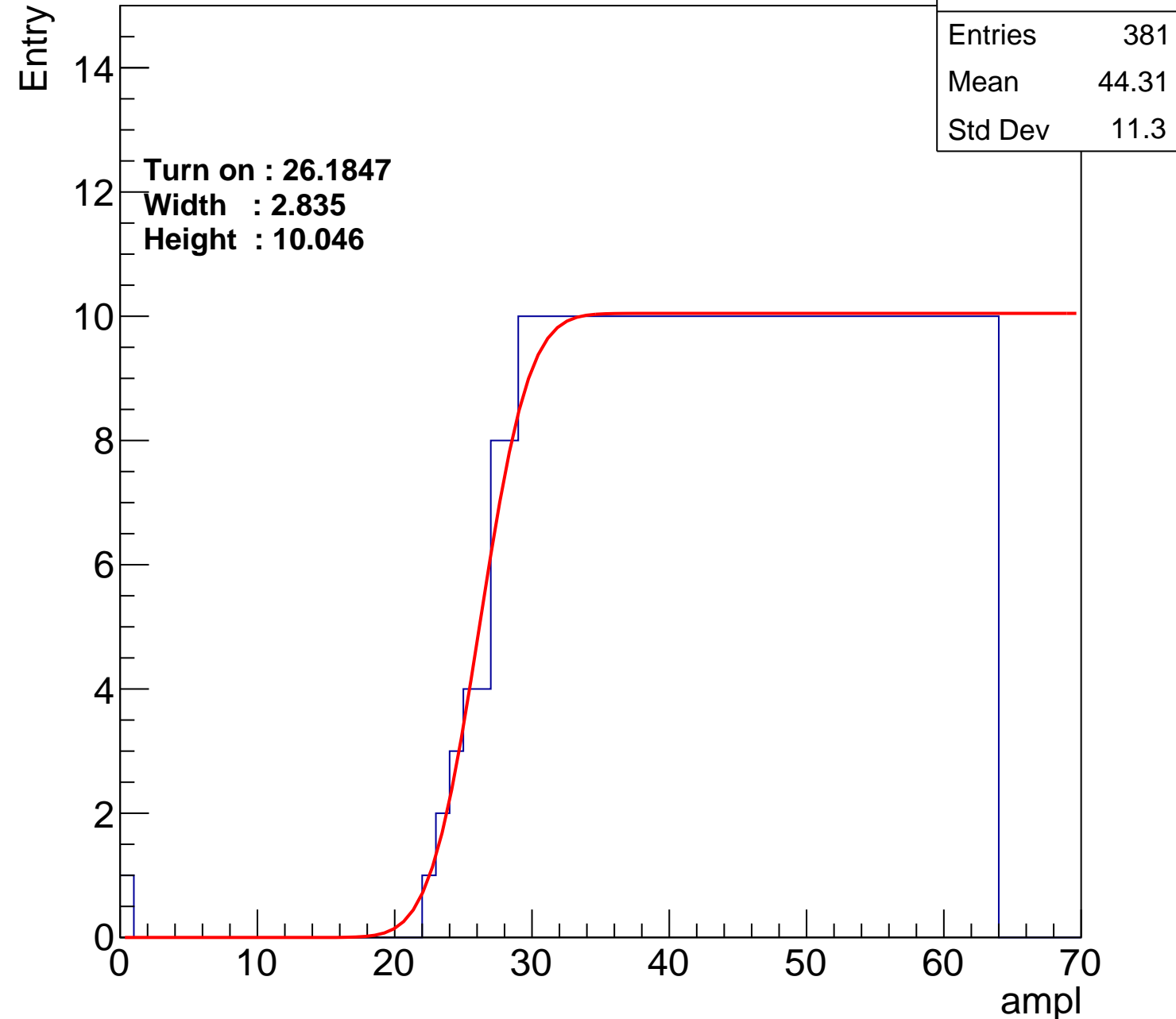
Width : 2.835

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch77

calib_packv5_042523_0143.root, FC#12, port B1

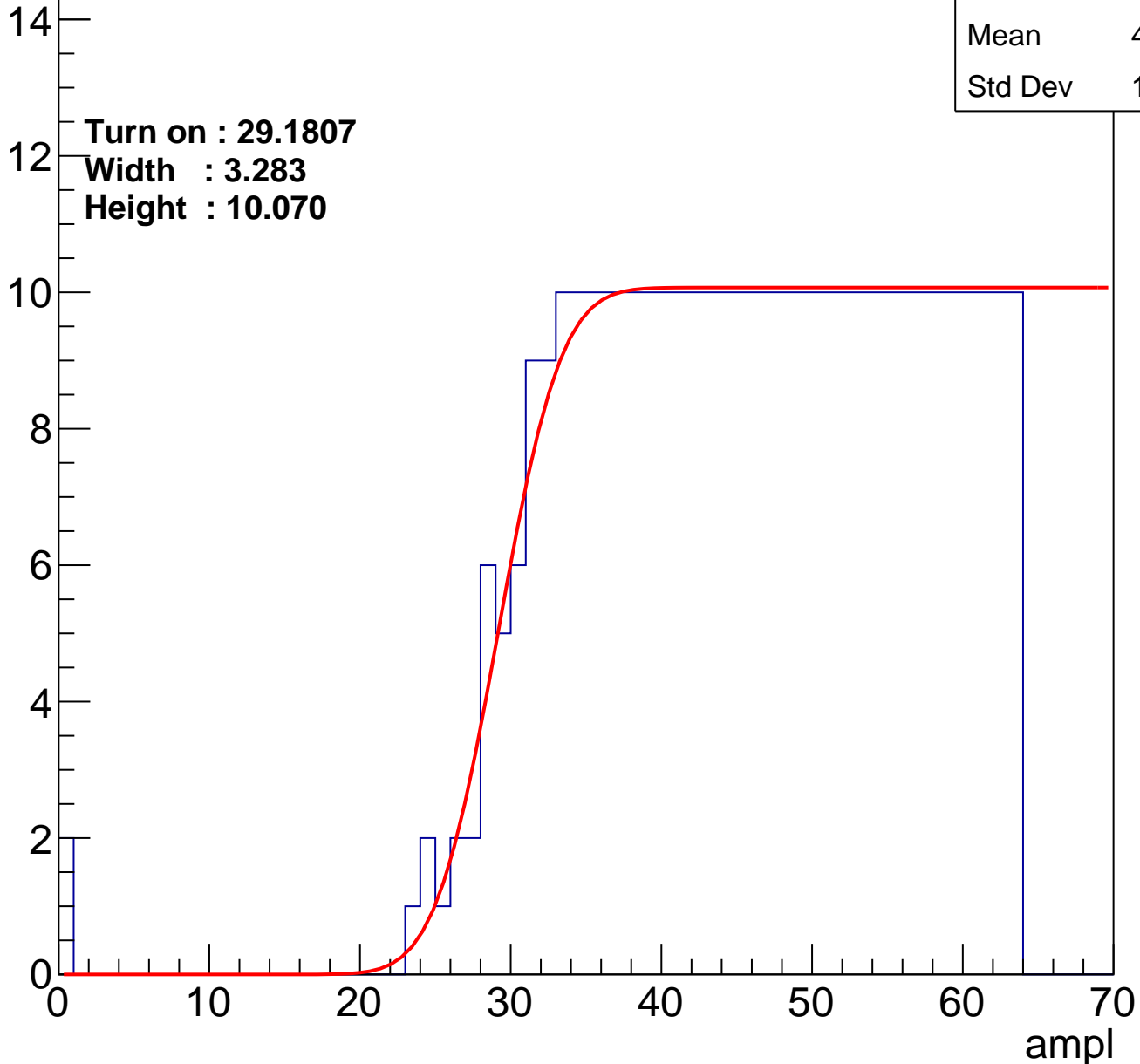
Entries	355
Mean	45.47
Std Dev	10.92

Turn on : 29.1807

Width : 3.283

Height : 10.070

Entry



B0L102S, U20-ch78

calib_packv5_042523_0143.root, FC#12, port B1

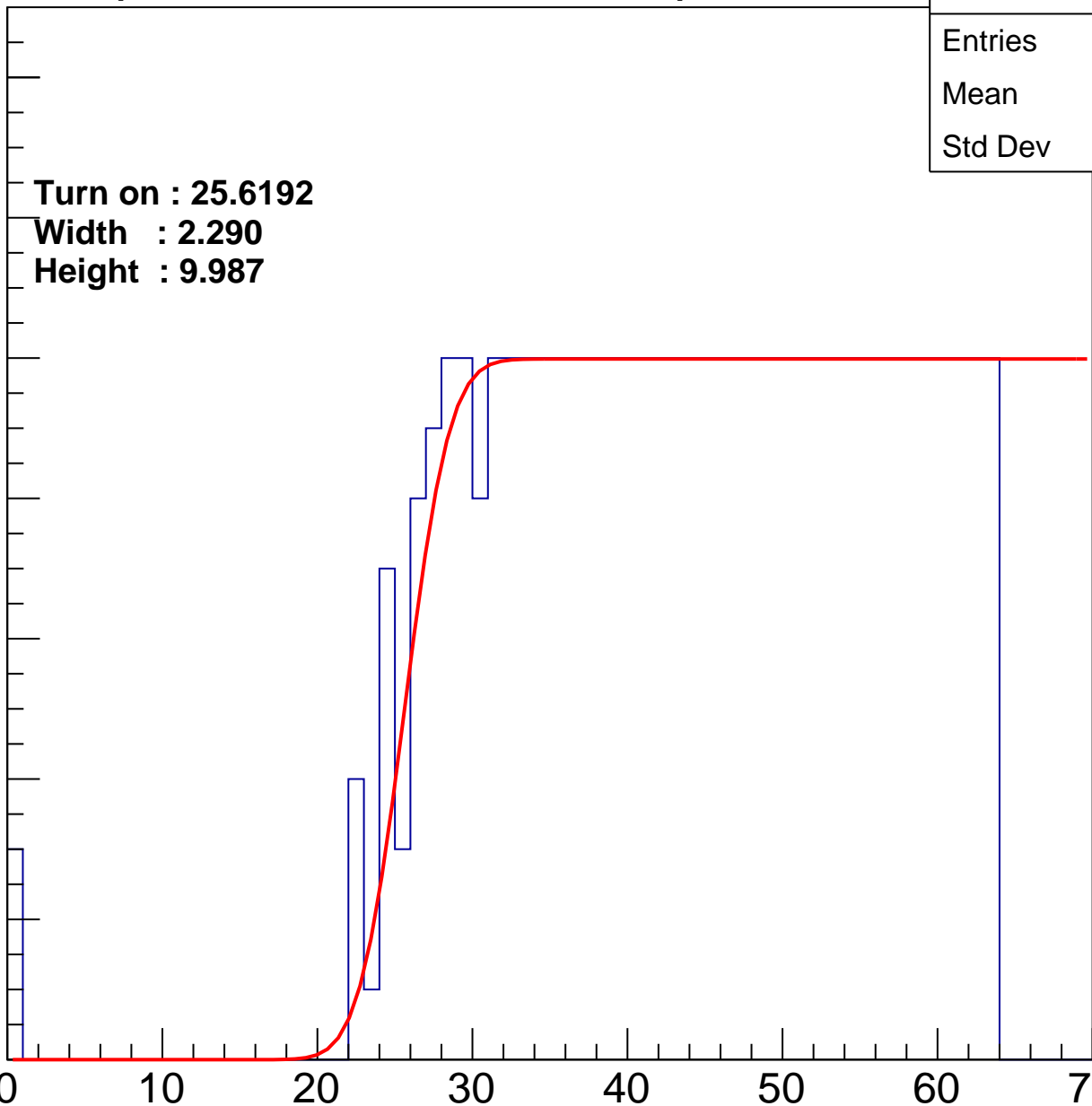
Entry

14
12
10
8
6
4
2
0

Turn on : 25.6192
Width : 2.290
Height : 9.987

Entries	393
Mean	43.58
Std Dev	11.99

ampl



B0L102S, U20-ch79

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.61
Std Dev	11.71

Turn on : 25.0404

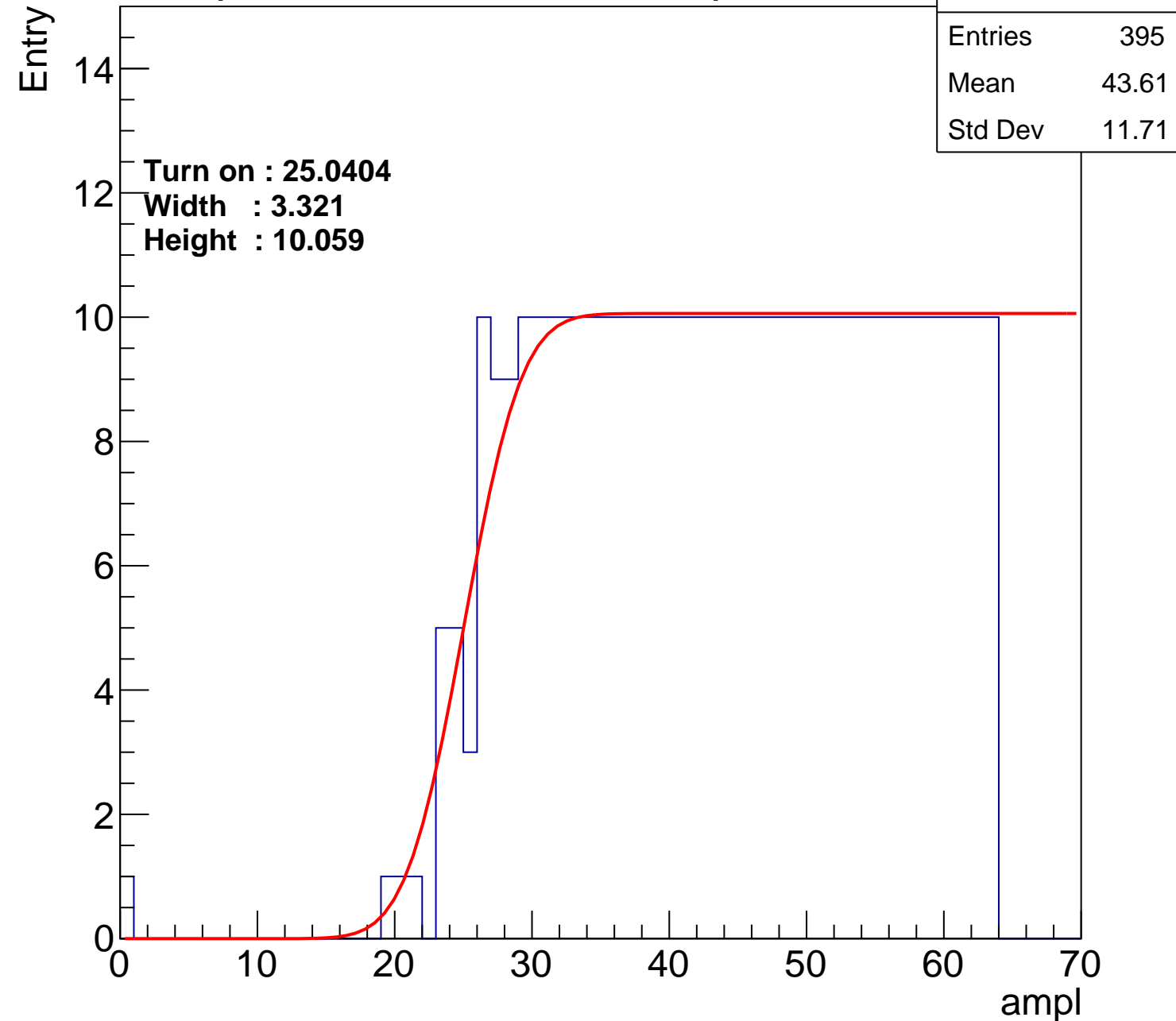
Width : 3.321

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch80

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.65
Std Dev	11.46

Turn on : 27.9647

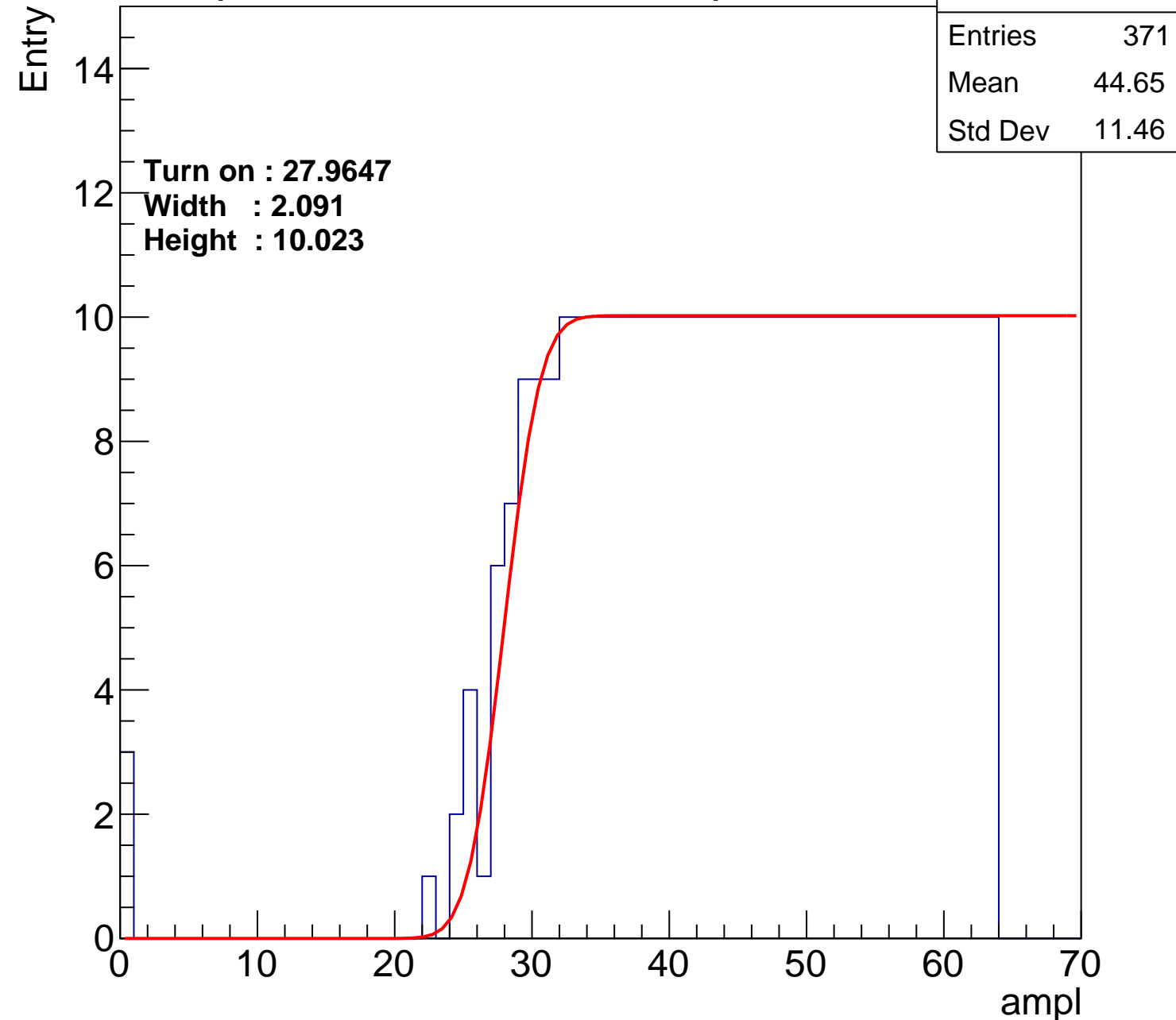
Width : 2.091

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch81

calib_packv5_042523_0143.root, FC#12, port B1

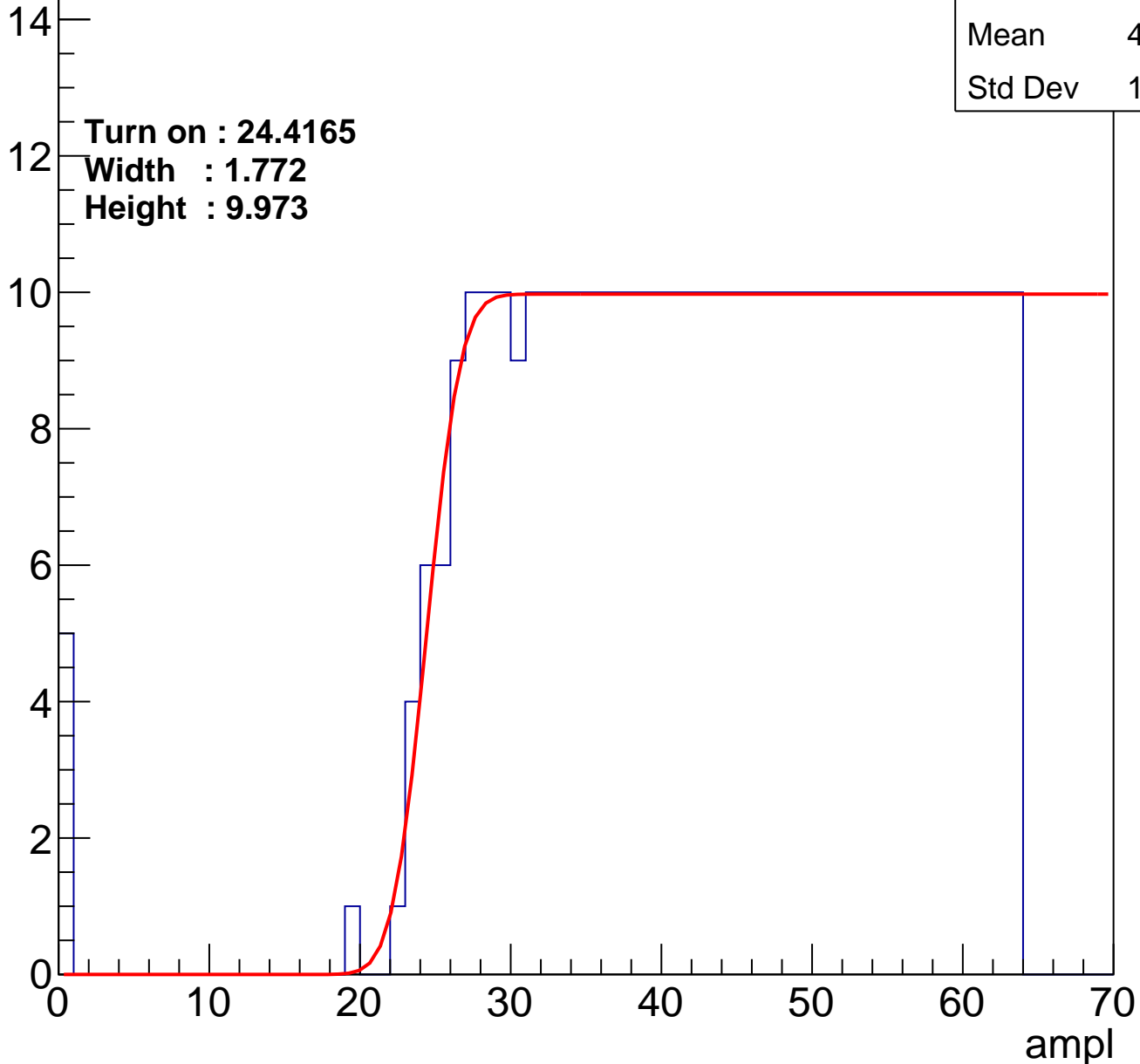
Entries	401
Mean	43.09
Std Dev	12.44

Turn on : 24.4165

Width : 1.772

Height : 9.973

Entry



B0L102S, U20-ch82

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.98
Std Dev	11.18

Turn on : 28.1052

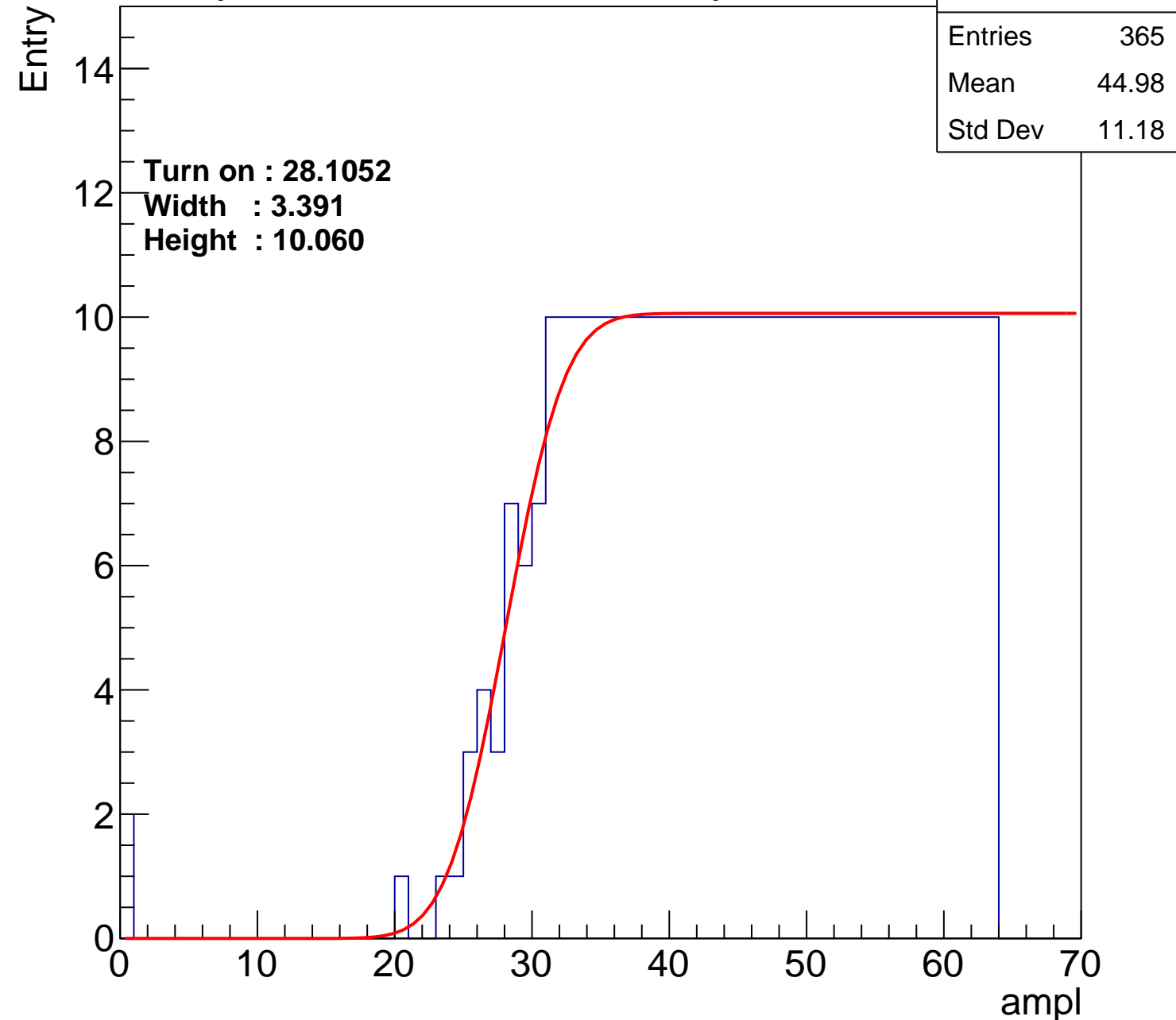
Width : 3.391

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch83

calib_packv5_042523_0143.root, FC#12, port B1

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 27.3769

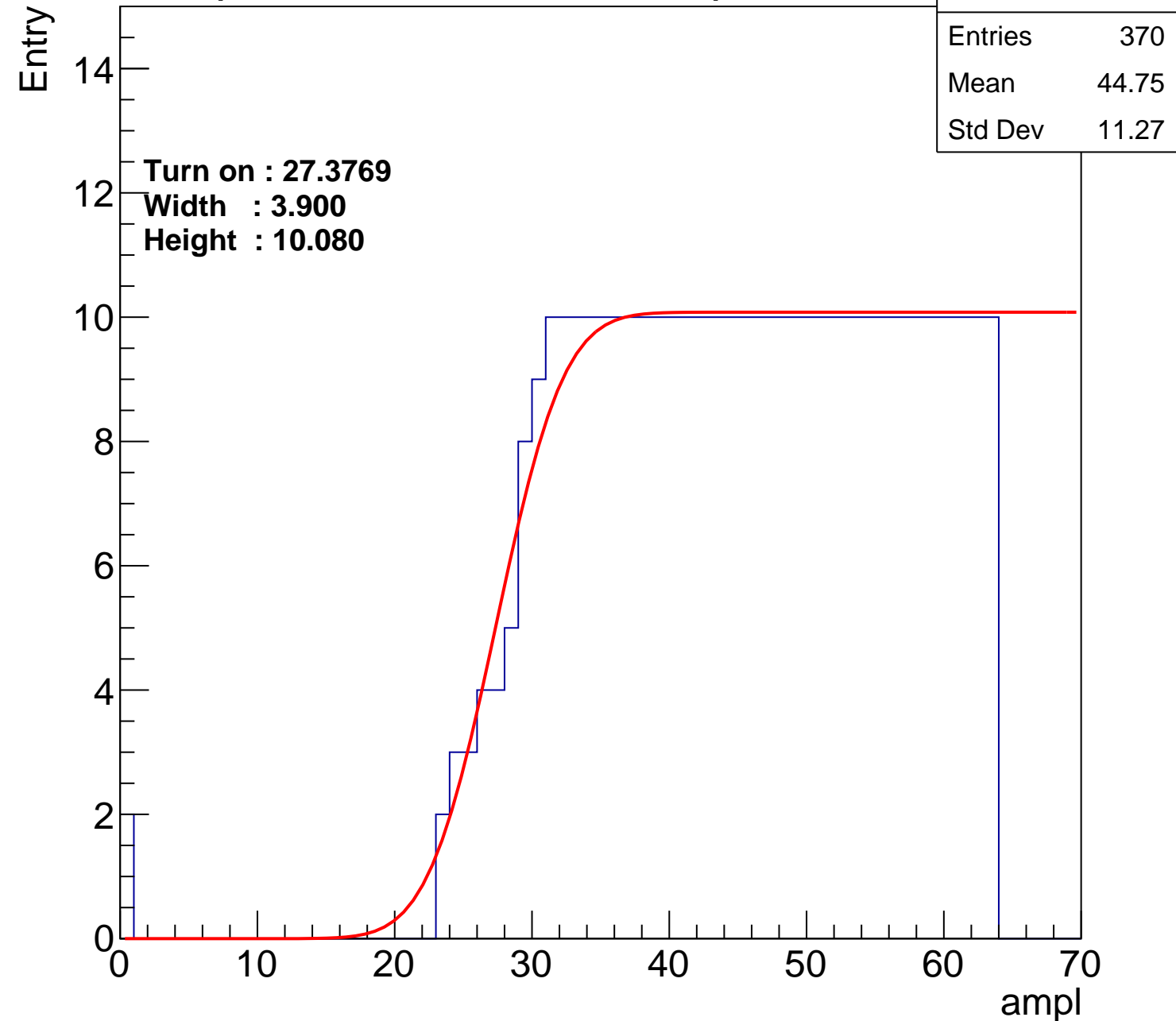
Width : 3.900

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch84

calib_packv5_042523_0143.root, FC#12, port B1

Entries	394
Mean	43.51
Std Dev	12.04

Turn on : 25.3644

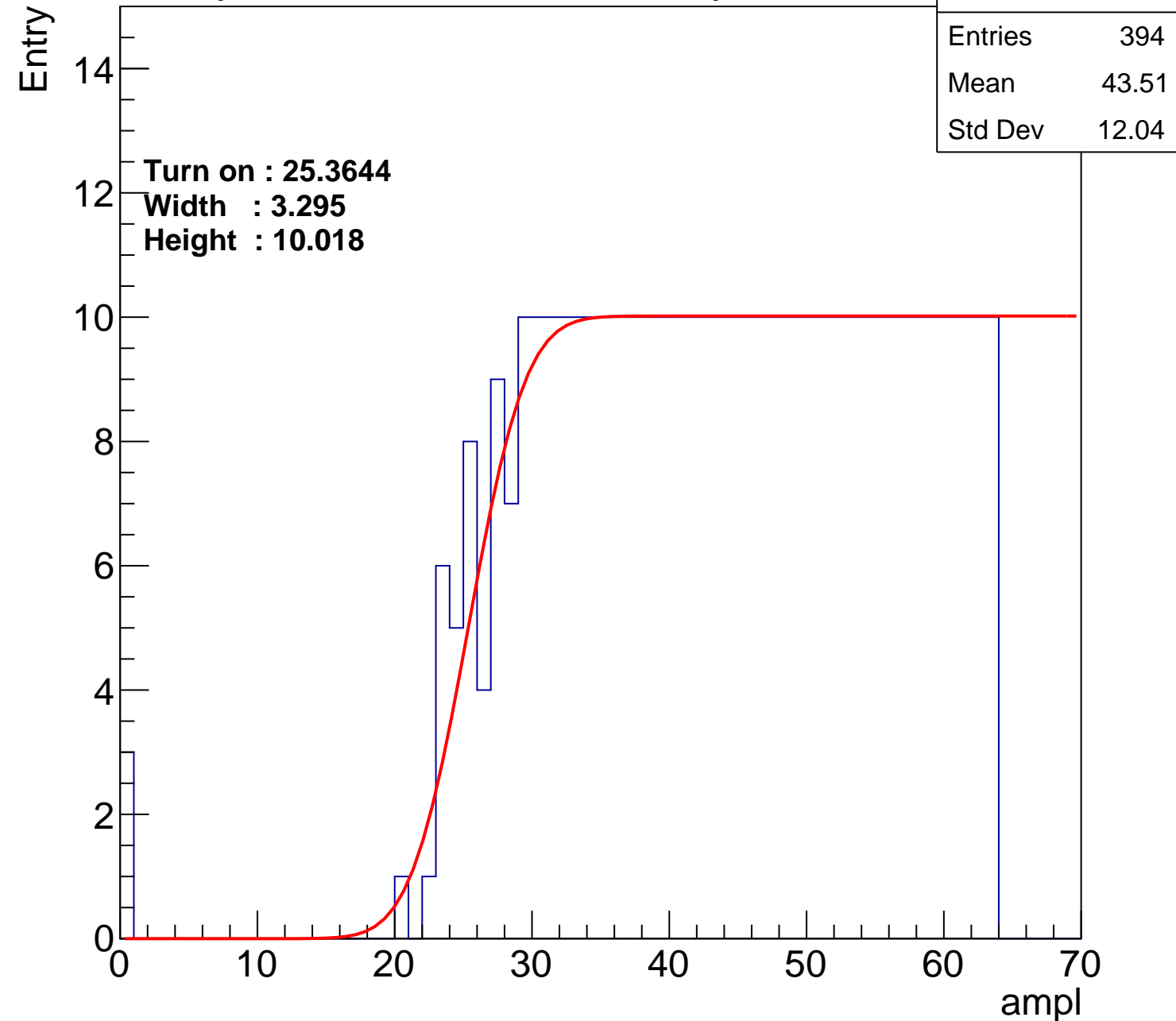
Width : 3.295

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch85

calib_packv5_042523_0143.root, FC#12, port B1

Entries	359
Mean	45.32
Std Dev	10.83

Turn on : 28.1525

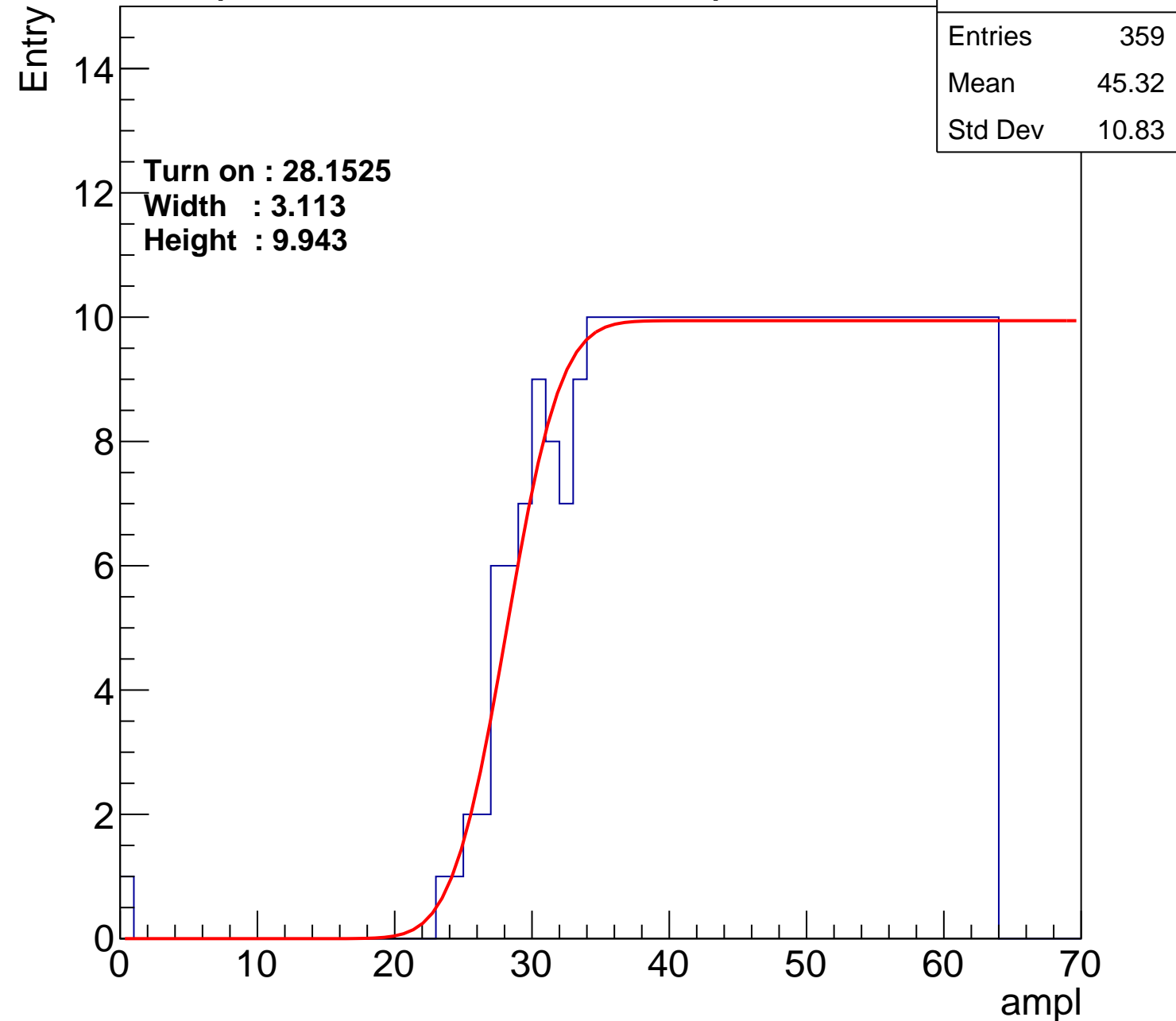
Width : 3.113

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch86

calib_packv5_042523_0143.root, FC#12, port B1

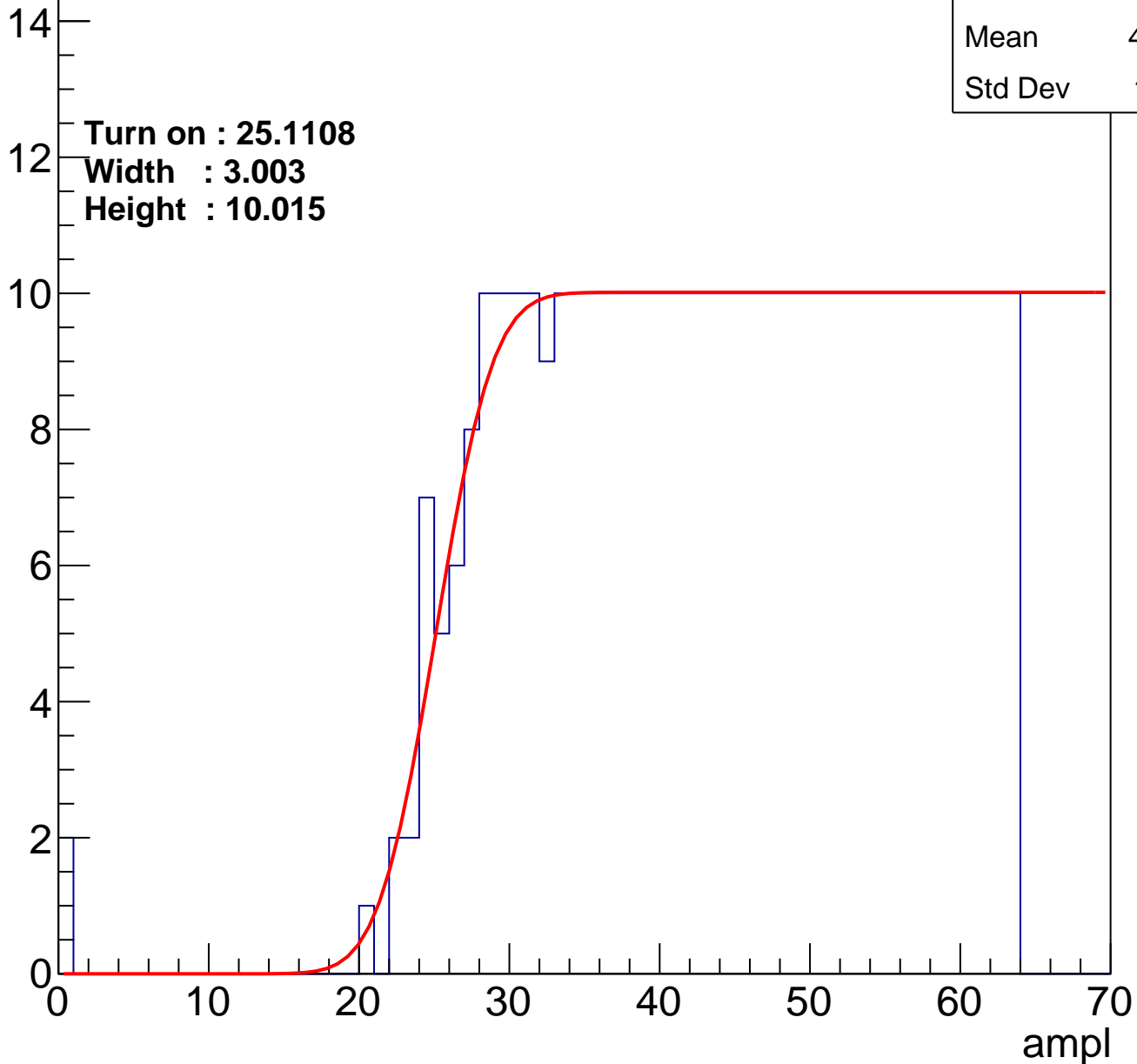
Entries	392
Mean	43.68
Std Dev	11.81

Turn on : 25.1108

Width : 3.003

Height : 10.015

Entry



B0L102S, U20-ch87

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.67
Std Dev	11.85

Turn on : 25.4911

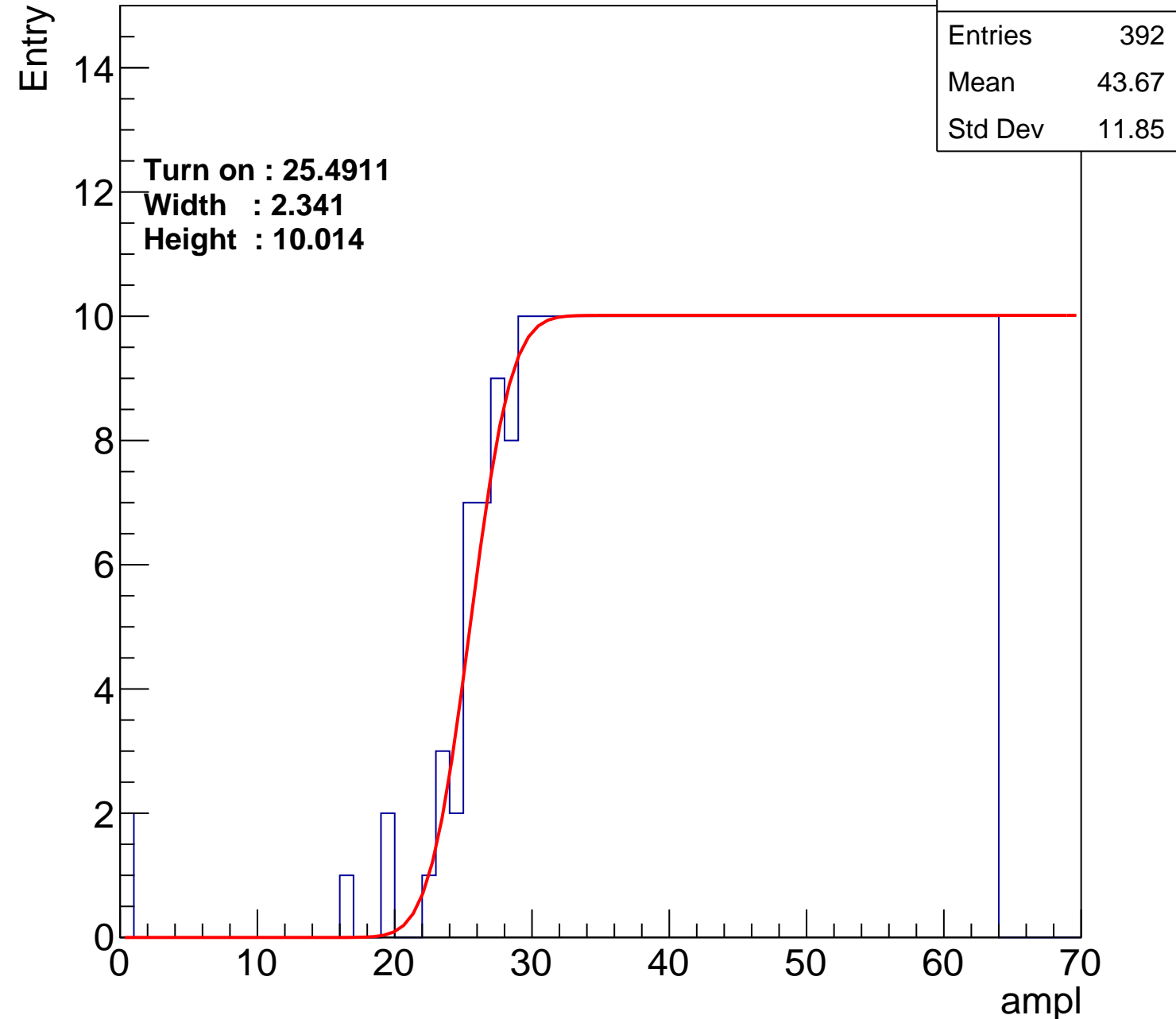
Width : 2.341

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch88

calib_packv5_042523_0143.root, FC#12, port B1

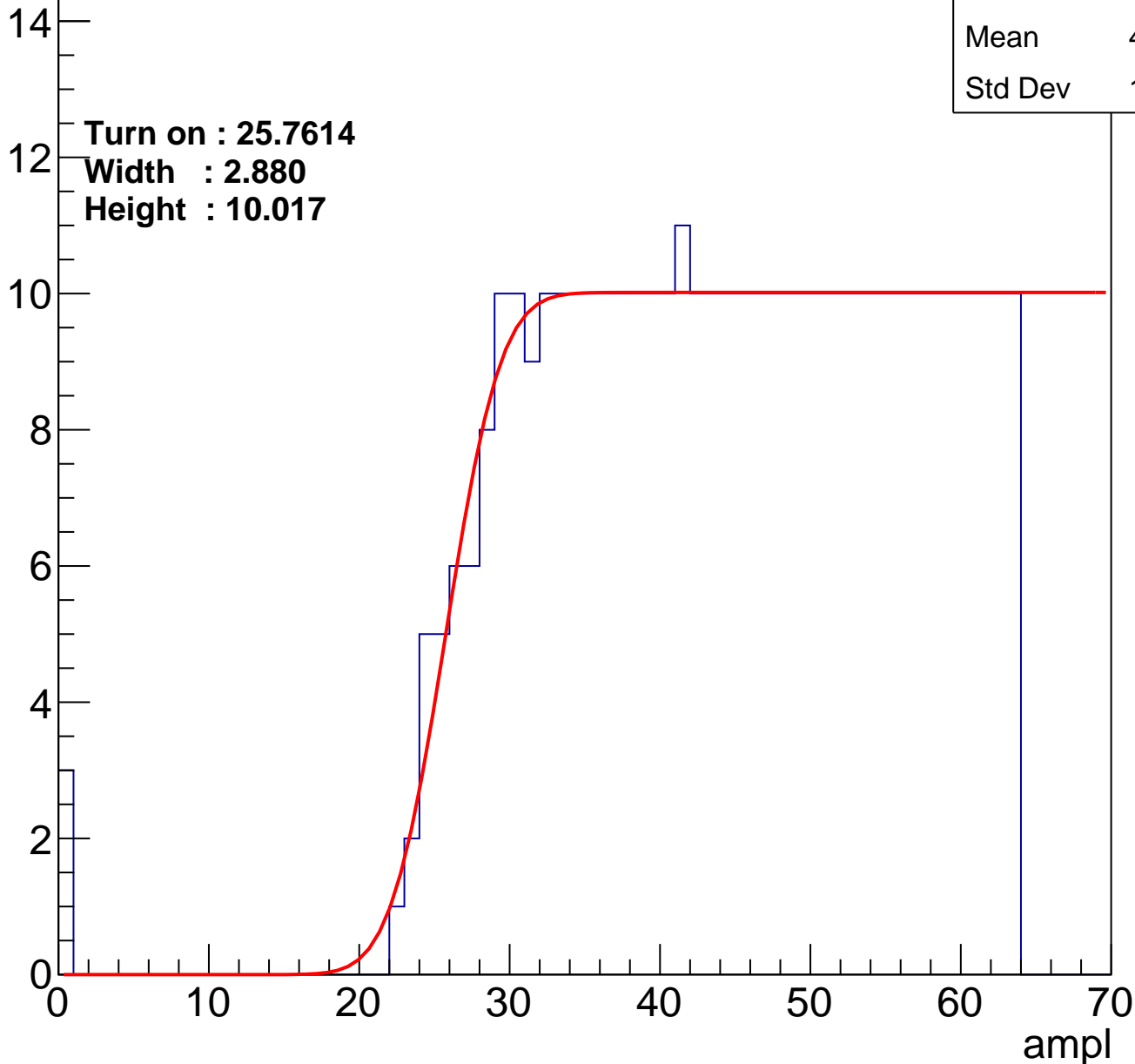
Entry

Entries	386
Mean	43.95
Std Dev	11.79

Turn on : 25.7614

Width : 2.880

Height : 10.017



B0L102S, U20-ch89

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.73
Std Dev	11.6

Turn on : 24.7053

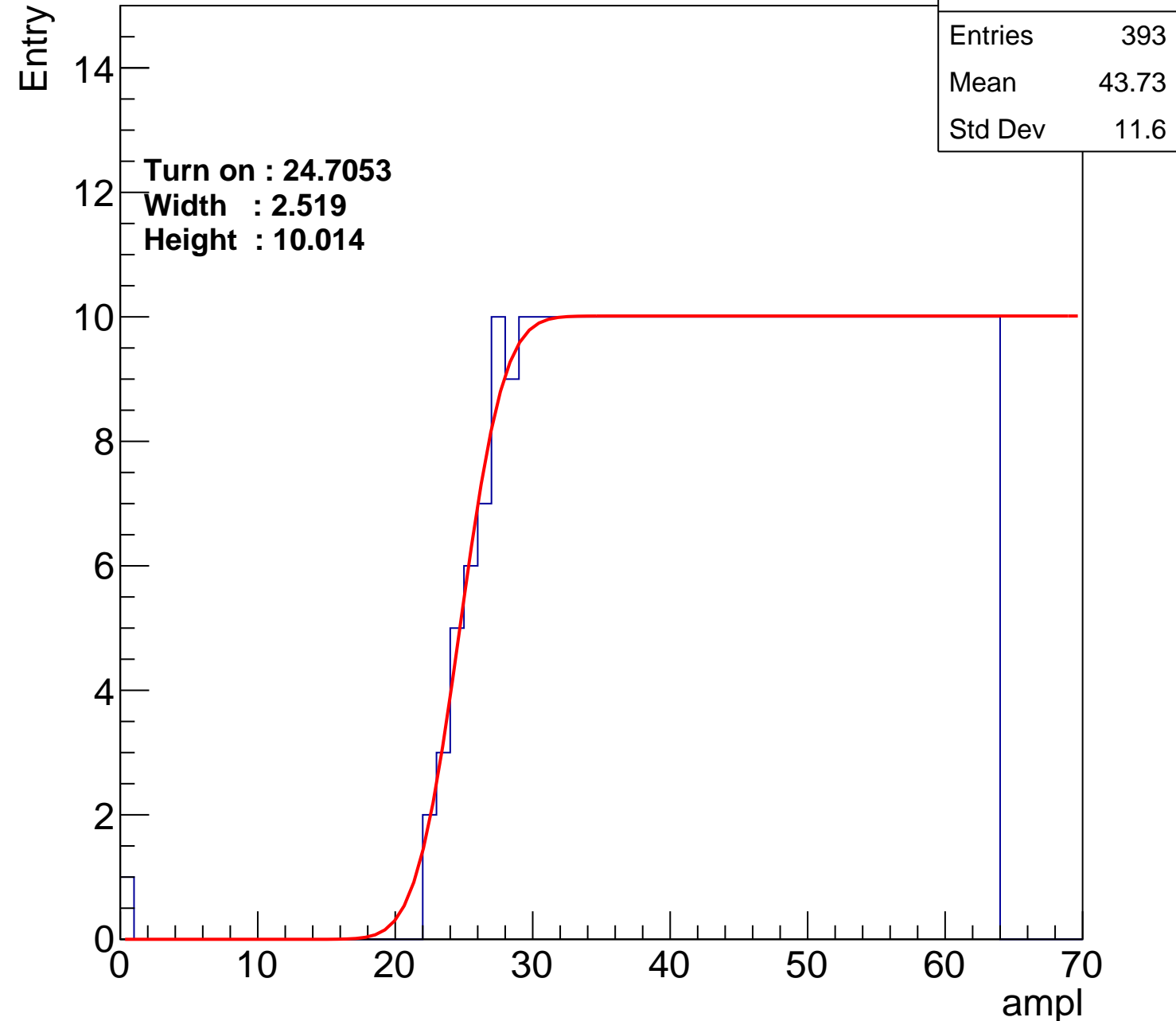
Width : 2.519

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch90

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.05
Std Dev	11.76

Turn on : 26.1249

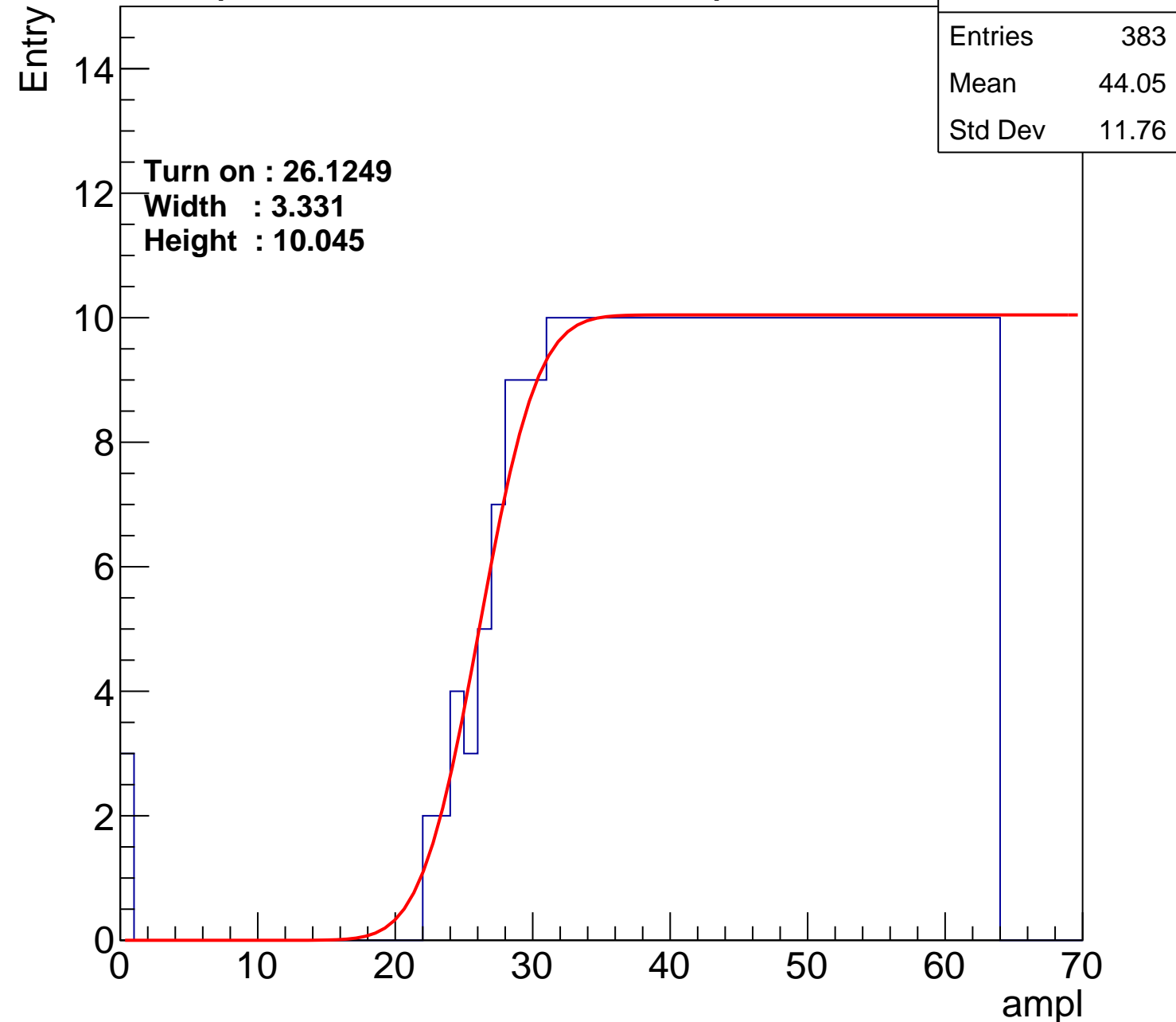
Width : 3.331

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch91

calib_packv5_042523_0143.root, FC#12, port B1

Entries	393
Mean	43.51
Std Dev	12.16

Turn on : 25.2027

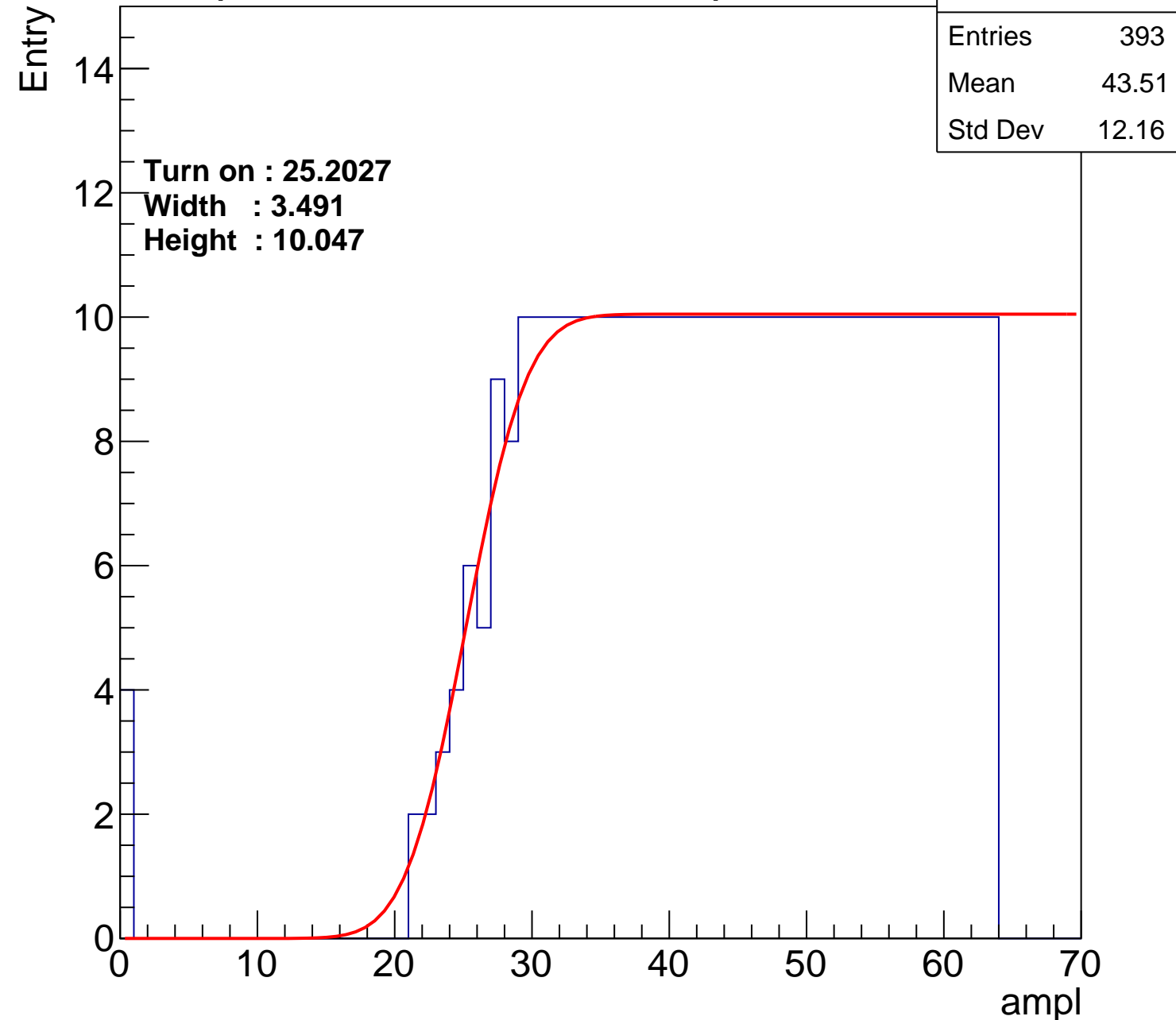
Width : 3.491

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch92

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.7
Std Dev	11.23

Turn on : 27.0475

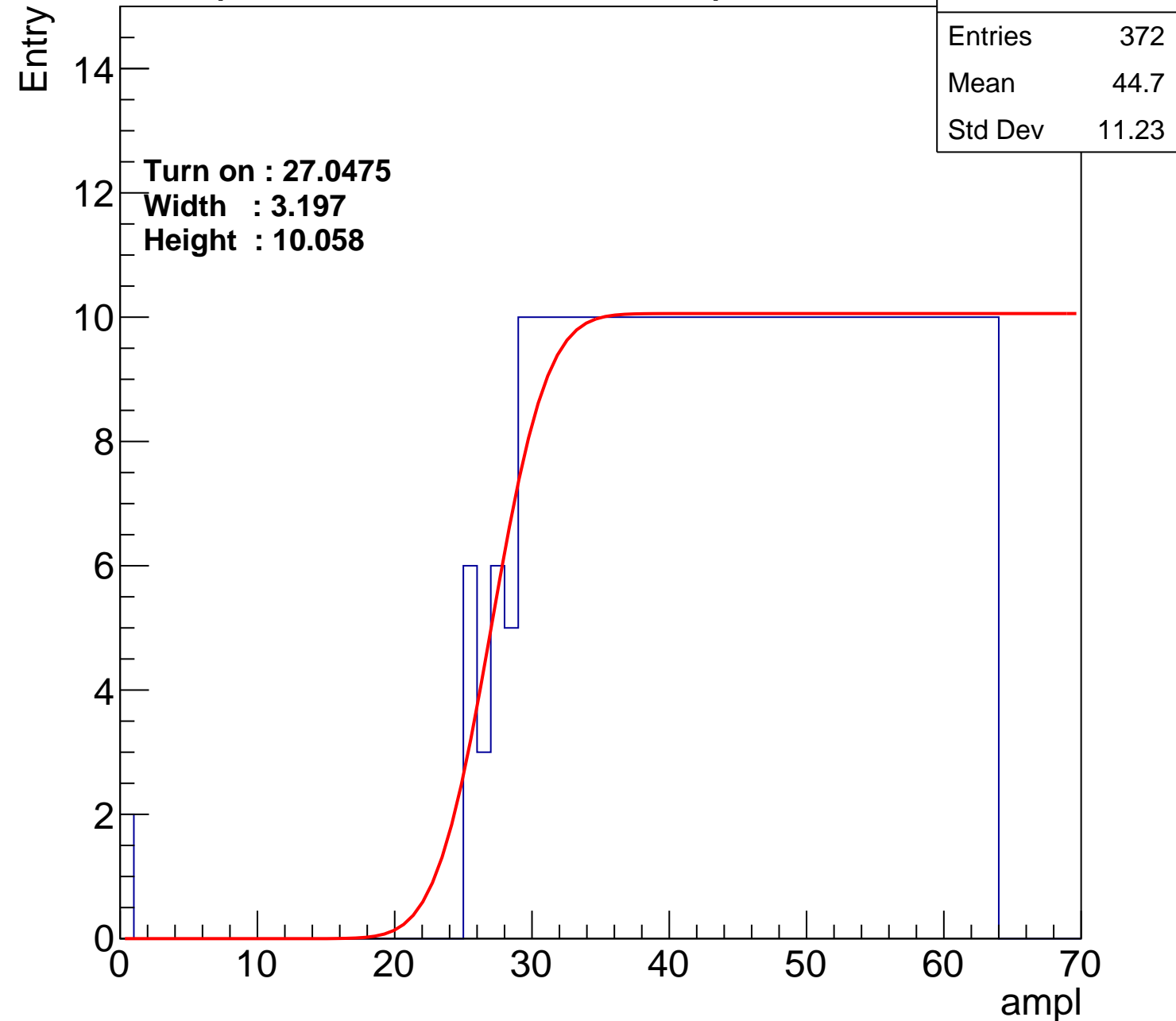
Width : 3.197

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch93

calib_packv5_042523_0143.root, FC#12, port B1

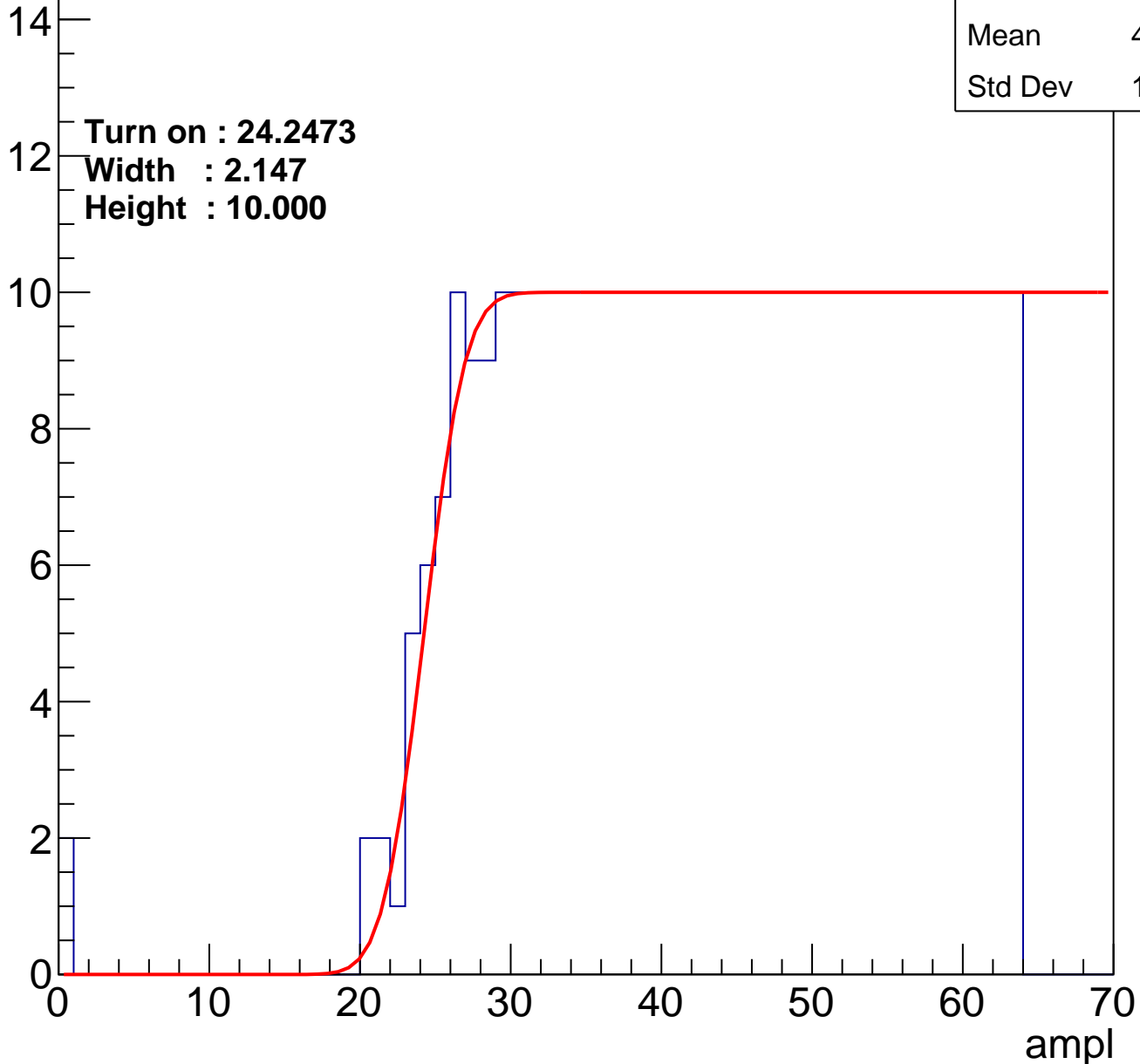
Entries	403
Mean	43.16
Std Dev	12.06

Turn on : 24.2473

Width : 2.147

Height : 10.000

Entry



B0L102S, U20-ch94

calib_packv5_042523_0143.root, FC#12, port B1

Entries	387
Mean	43.84
Std Dev	11.89

Turn on : 25.6838

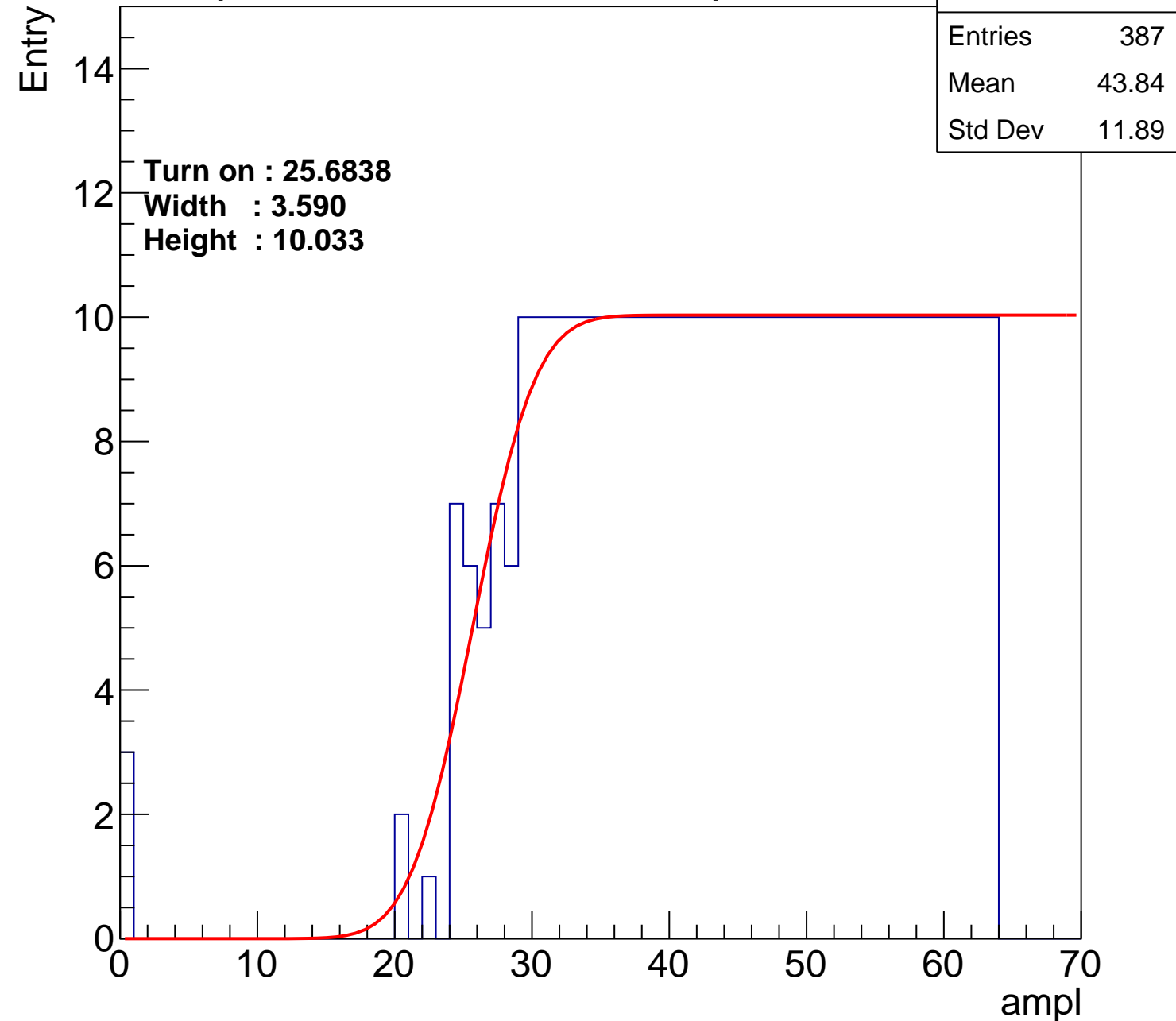
Width : 3.590

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch95

calib_packv5_042523_0143.root, FC#12, port B1

Entries	361
Mean	45.02
Std Dev	11.49

Turn on : 28.4243

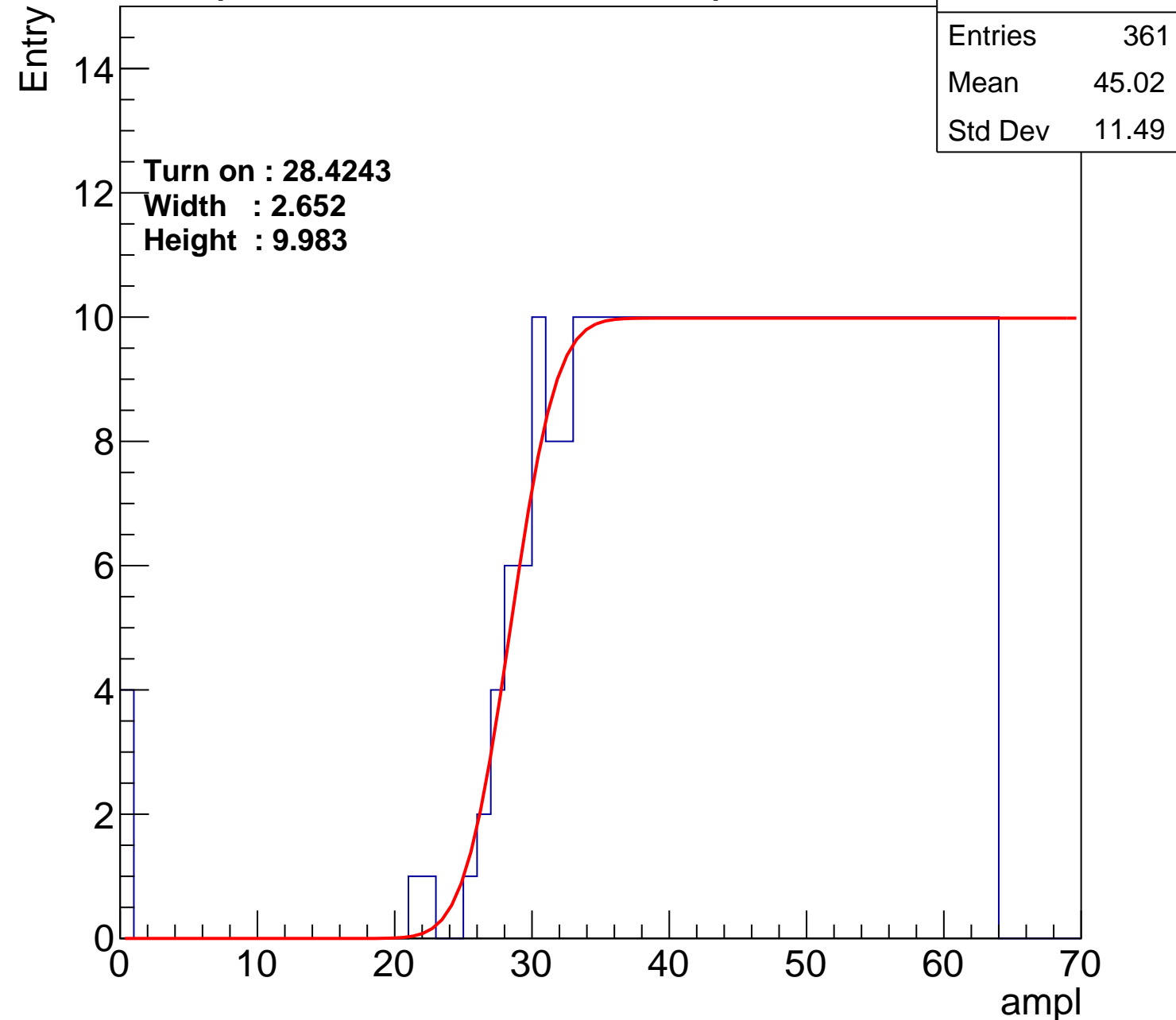
Width : 2.652

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch96

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.38
Std Dev	12.12

Turn on : 24.5216

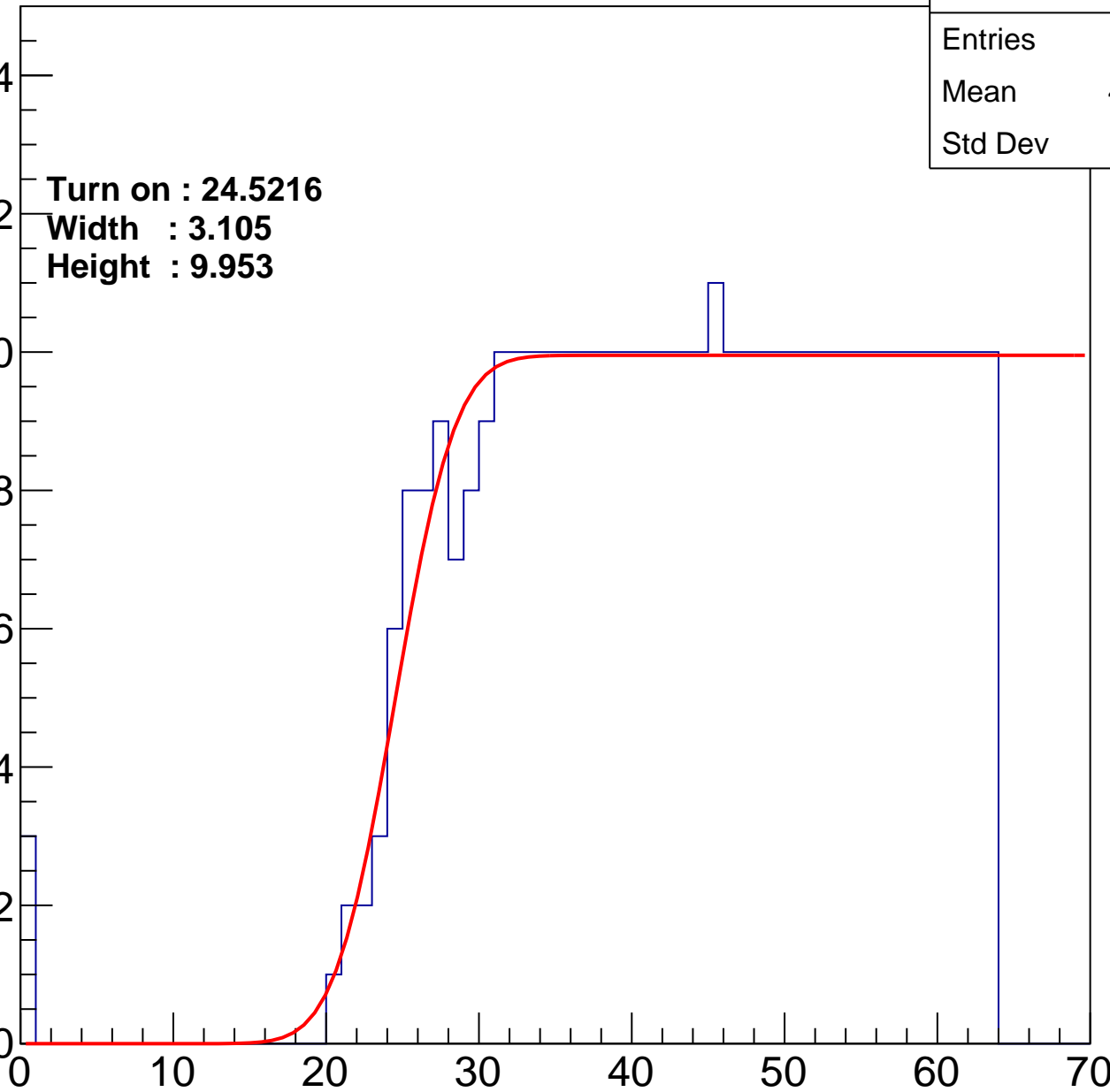
Width : 3.105

Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch97

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 25.9842

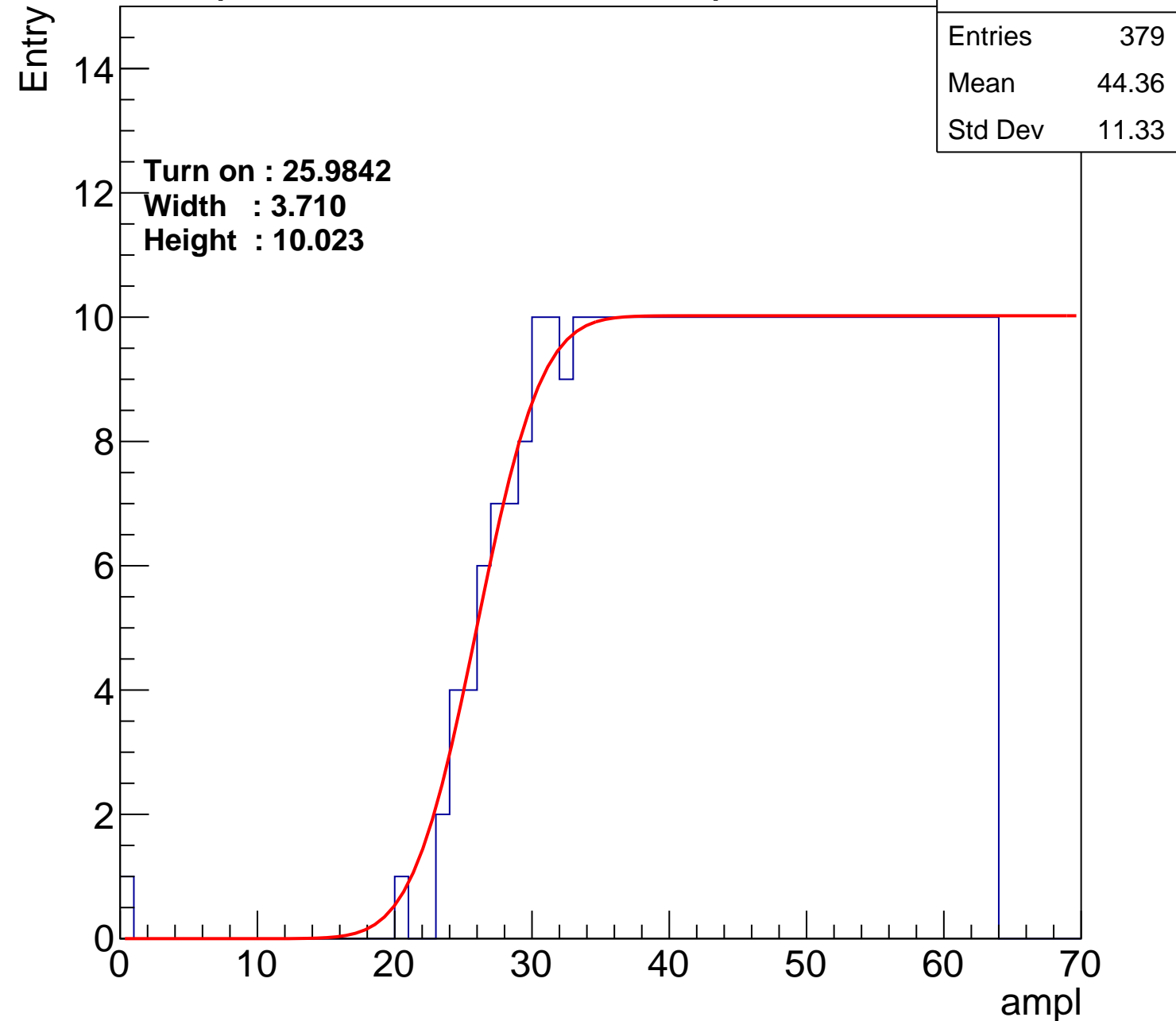
Width : 3.710

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch98

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.81
Std Dev	11.28

Turn on : 27.8493

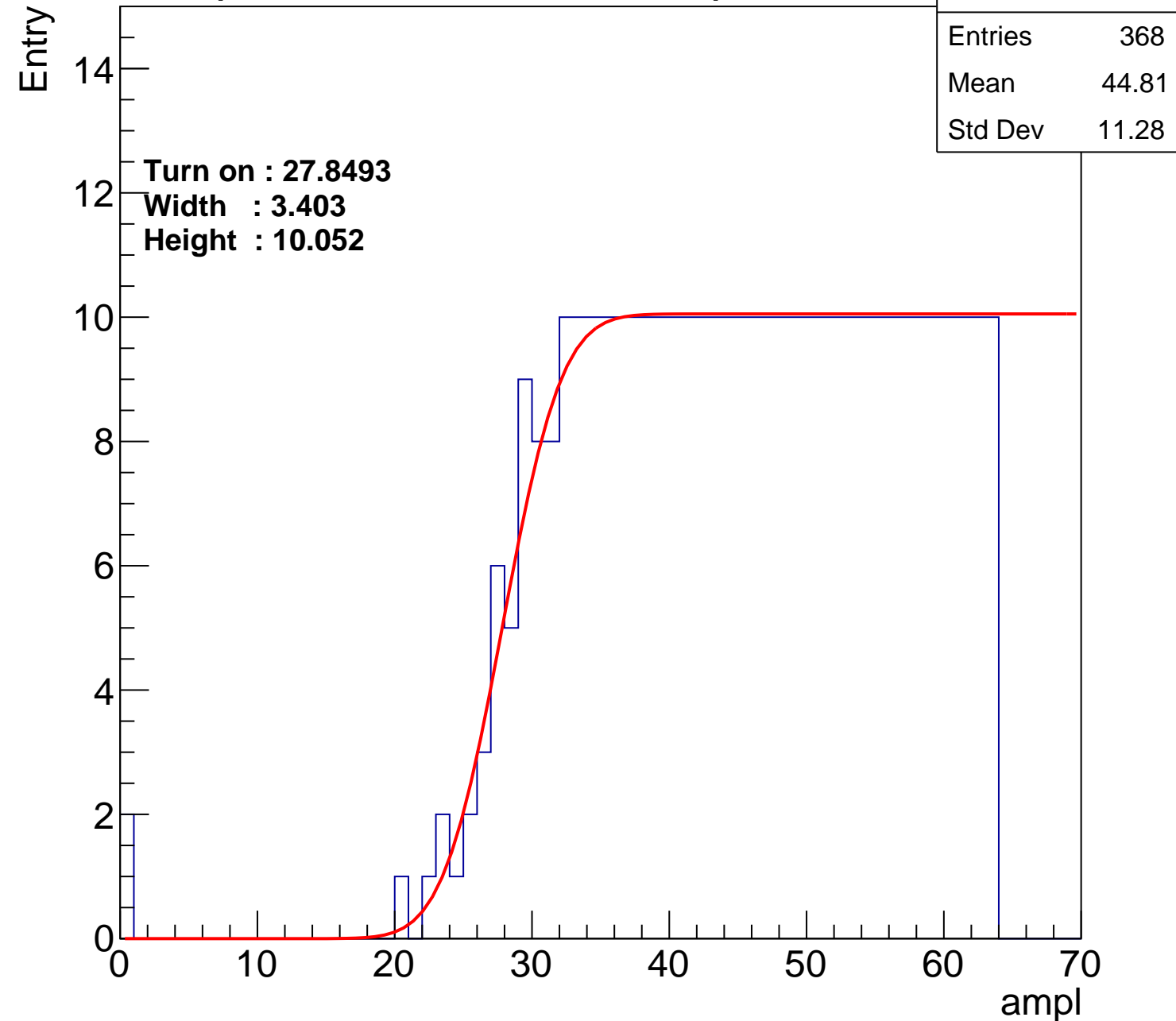
Width : 3.403

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch99

calib_packv5_042523_0143.root, FC#12, port B1

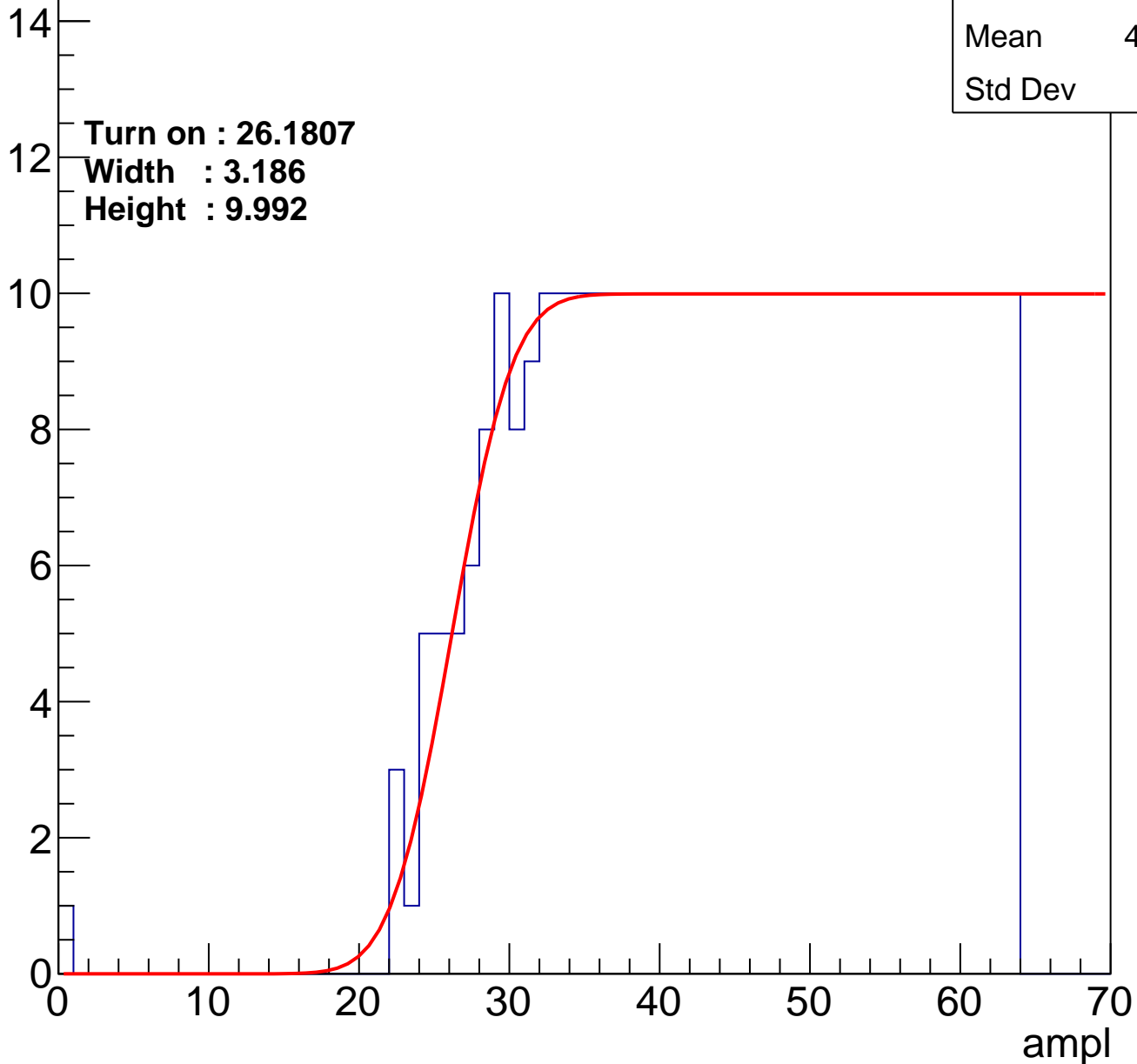
Entries	381
Mean	44.25
Std Dev	11.4

Turn on : 26.1807

Width : 3.186

Height : 9.992

Entry



B0L102S, U20-ch100

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.51
Std Dev	11.38

Turn on : 26.6719

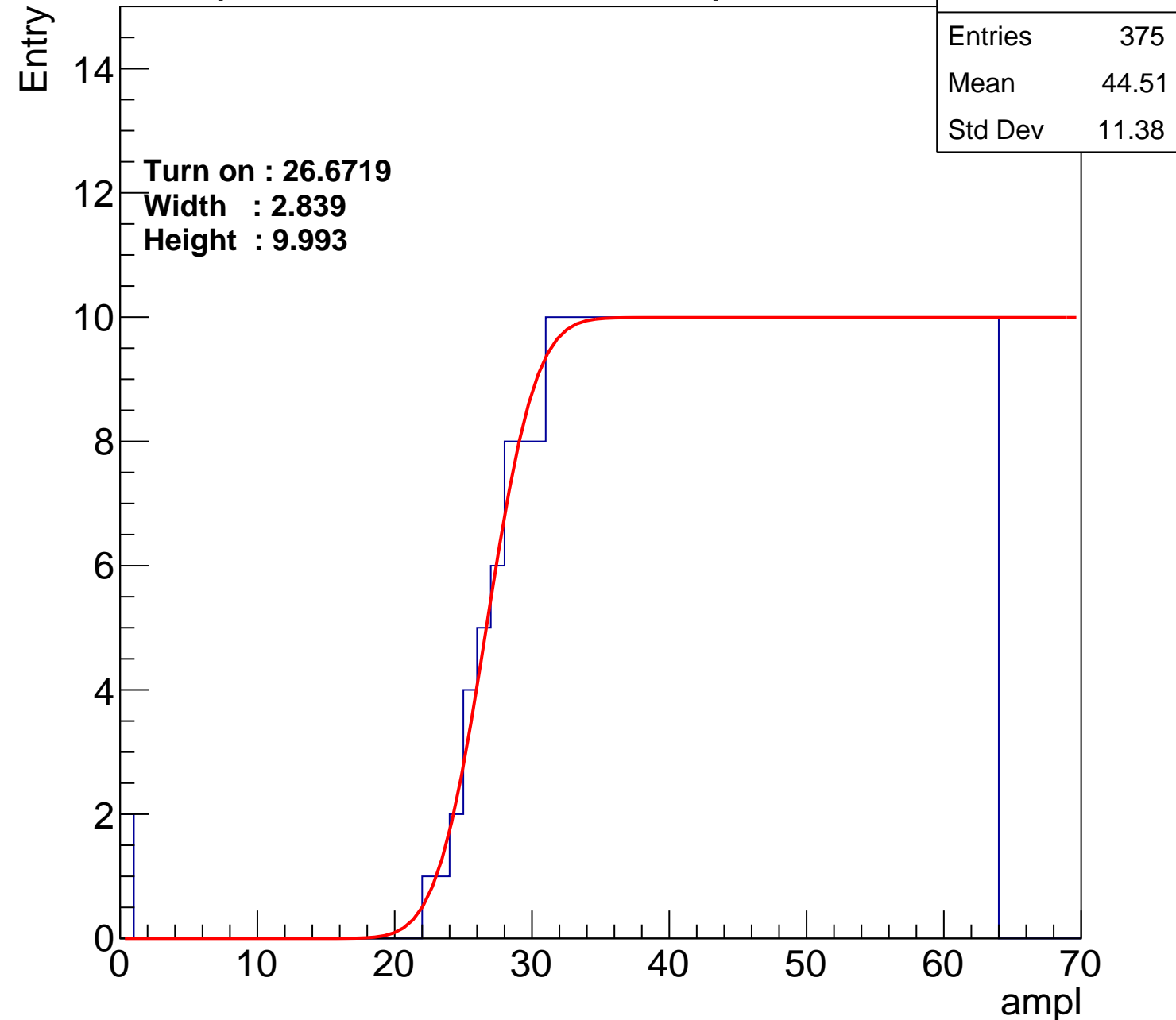
Width : 2.839

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch101

calib_packv5_042523_0143.root, FC#12, port B1

Entries	392
Mean	43.72
Std Dev	11.77

Turn on : 25.1194

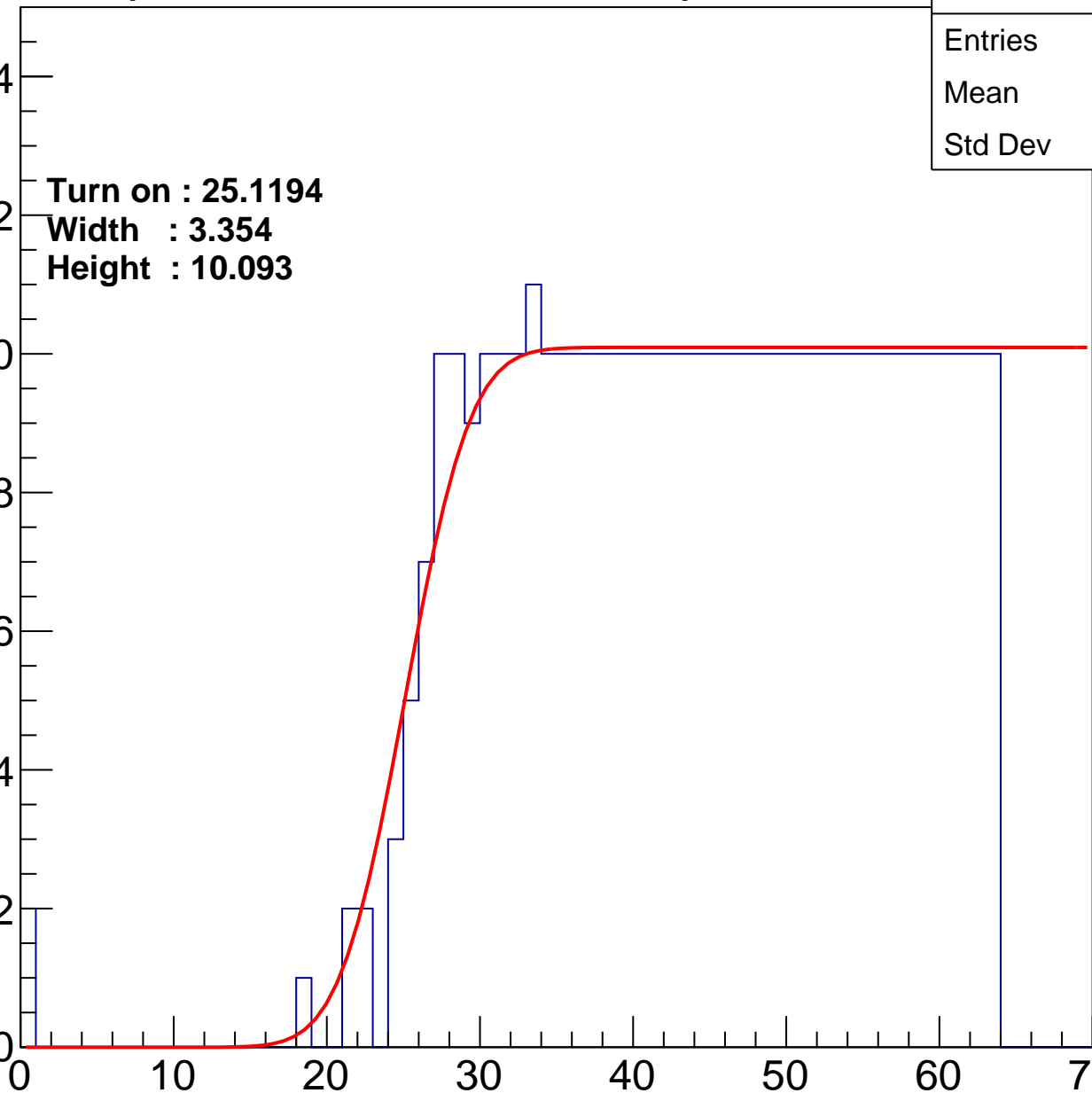
Width : 3.354

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch102

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.39
Std Dev	12.02

Turn on : 24.4092

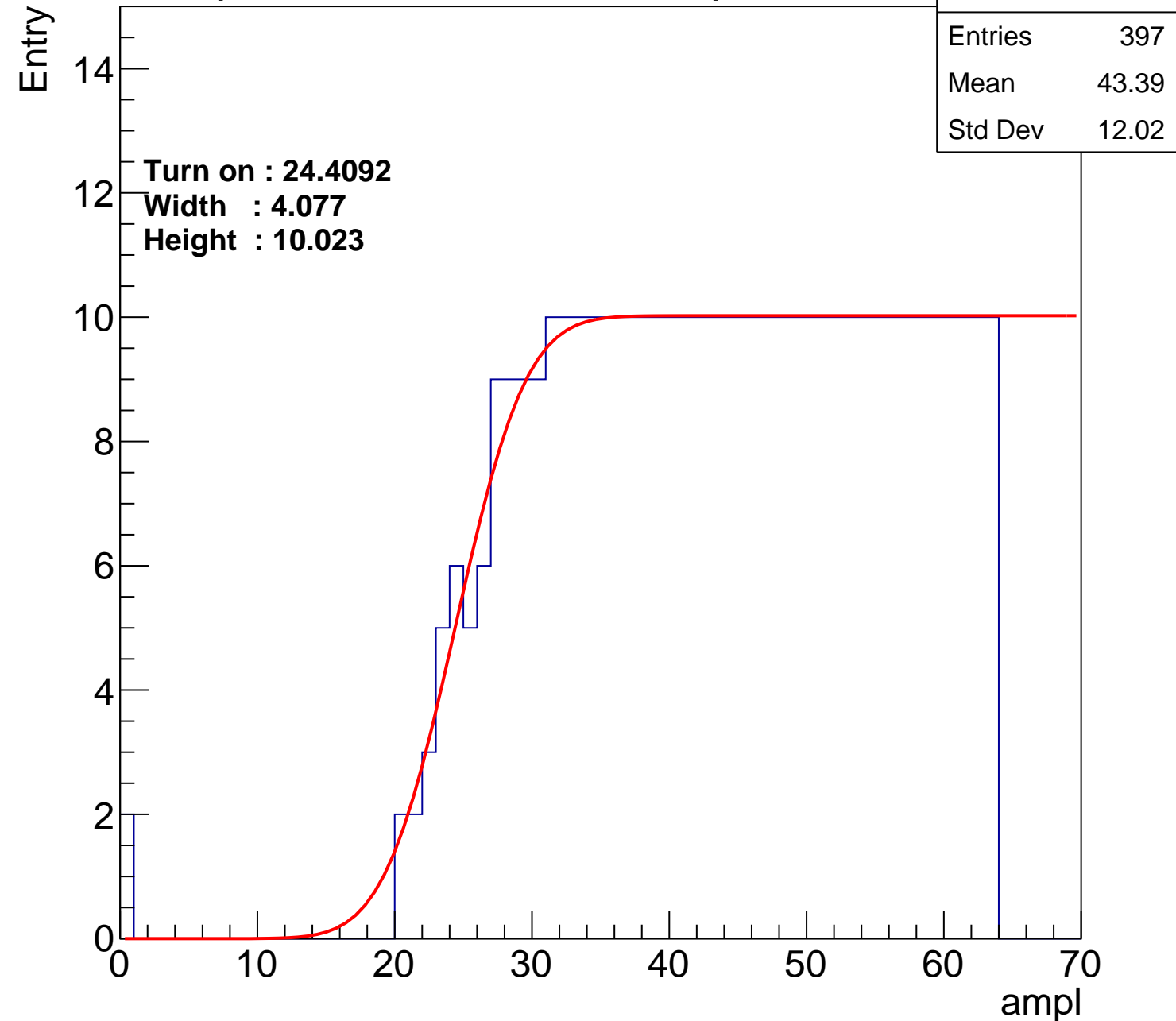
Width : 4.077

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch103

calib_packv5_042523_0143.root, FC#12, port B1

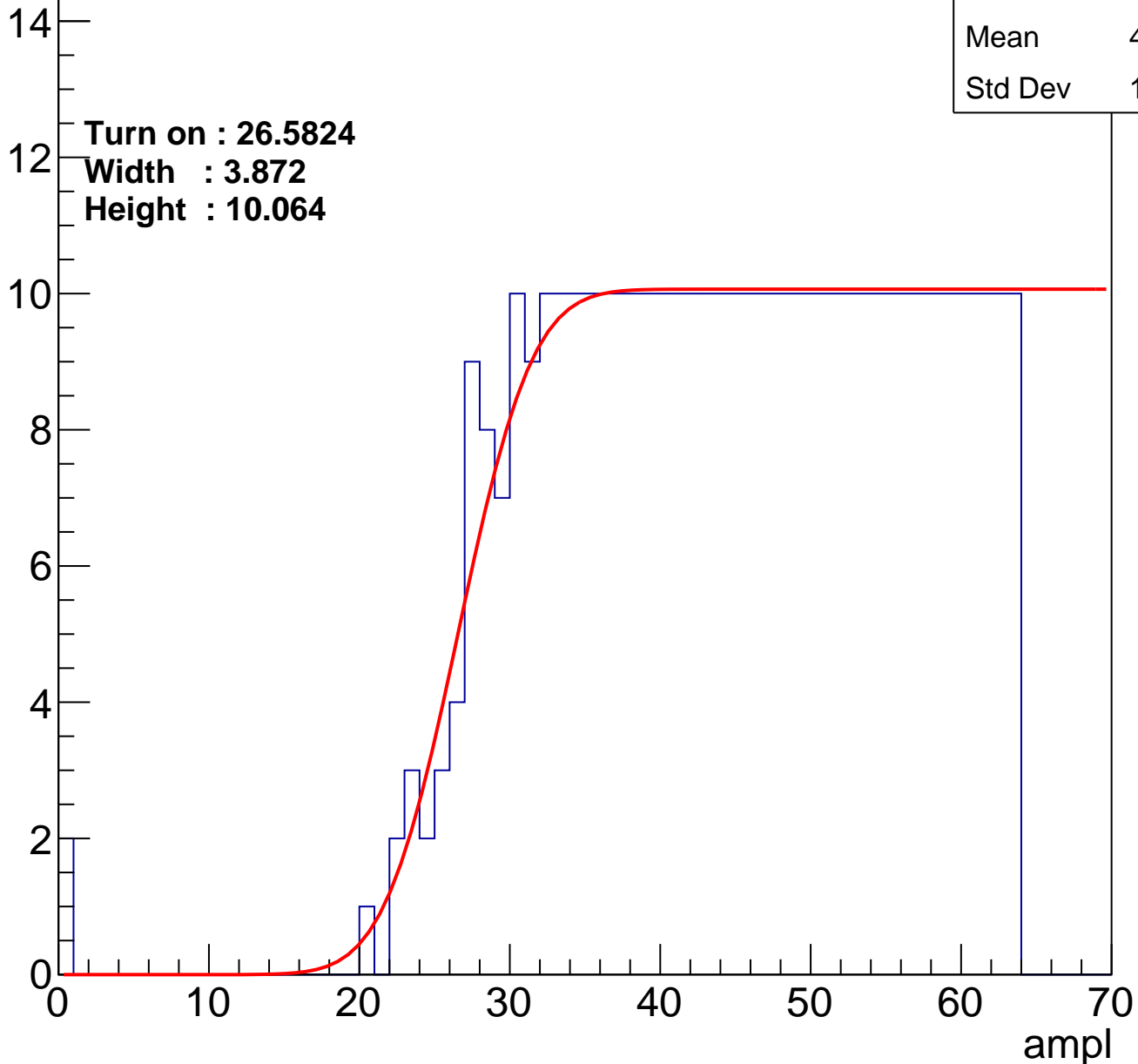
Entries	380
Mean	44.23
Std Dev	11.56

Turn on : 26.5824

Width : 3.872

Height : 10.064

Entry



B0L102S, U20-ch104

calib_packv5_042523_0143.root, FC#12, port B1

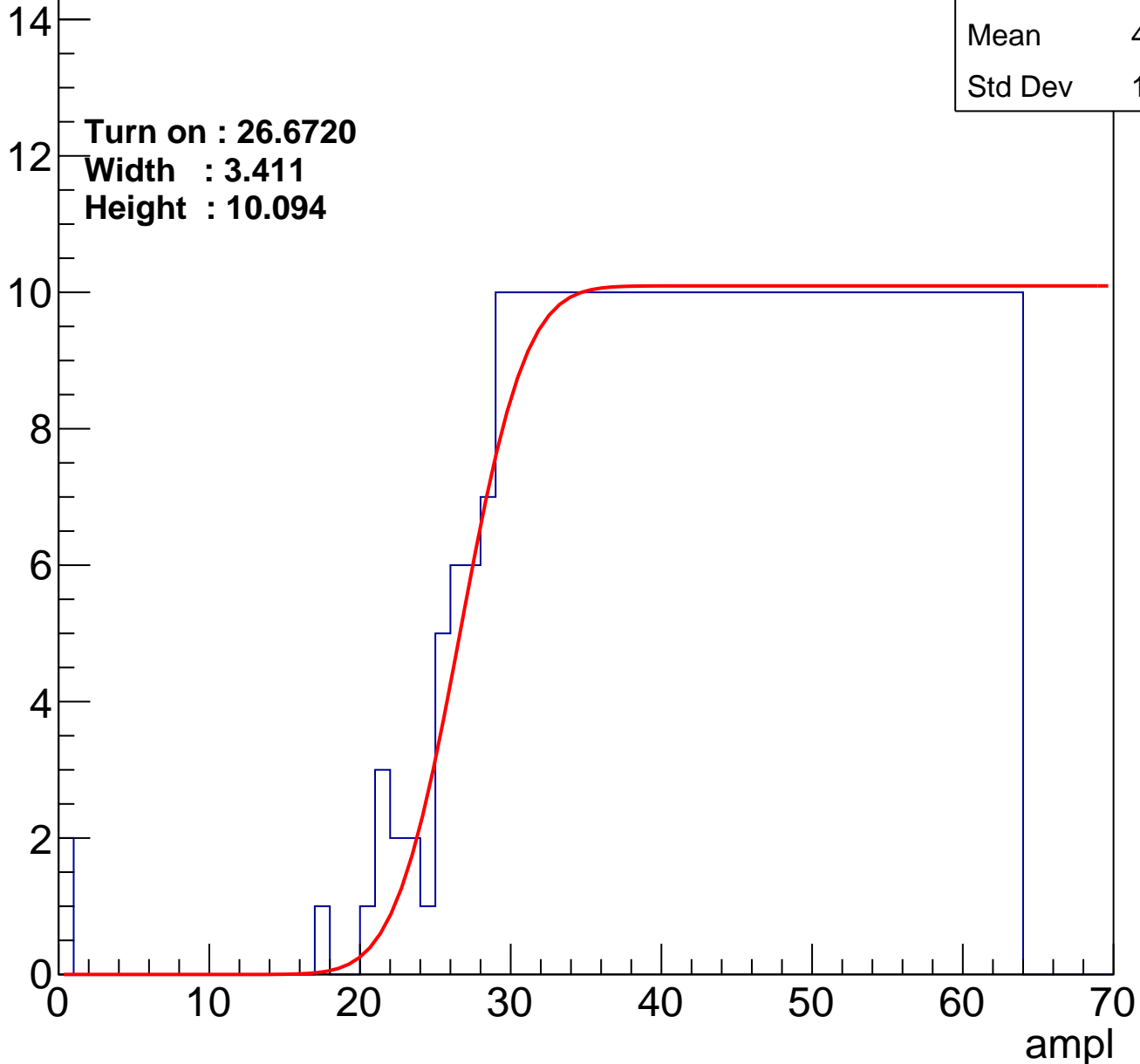
Entries	386
Mean	43.92
Std Dev	11.76

Turn on : 26.6720

Width : 3.411

Height : 10.094

Entry



B0L102S, U20-ch105

calib_packv5_042523_0143.root, FC#12, port B1

Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 27.7495

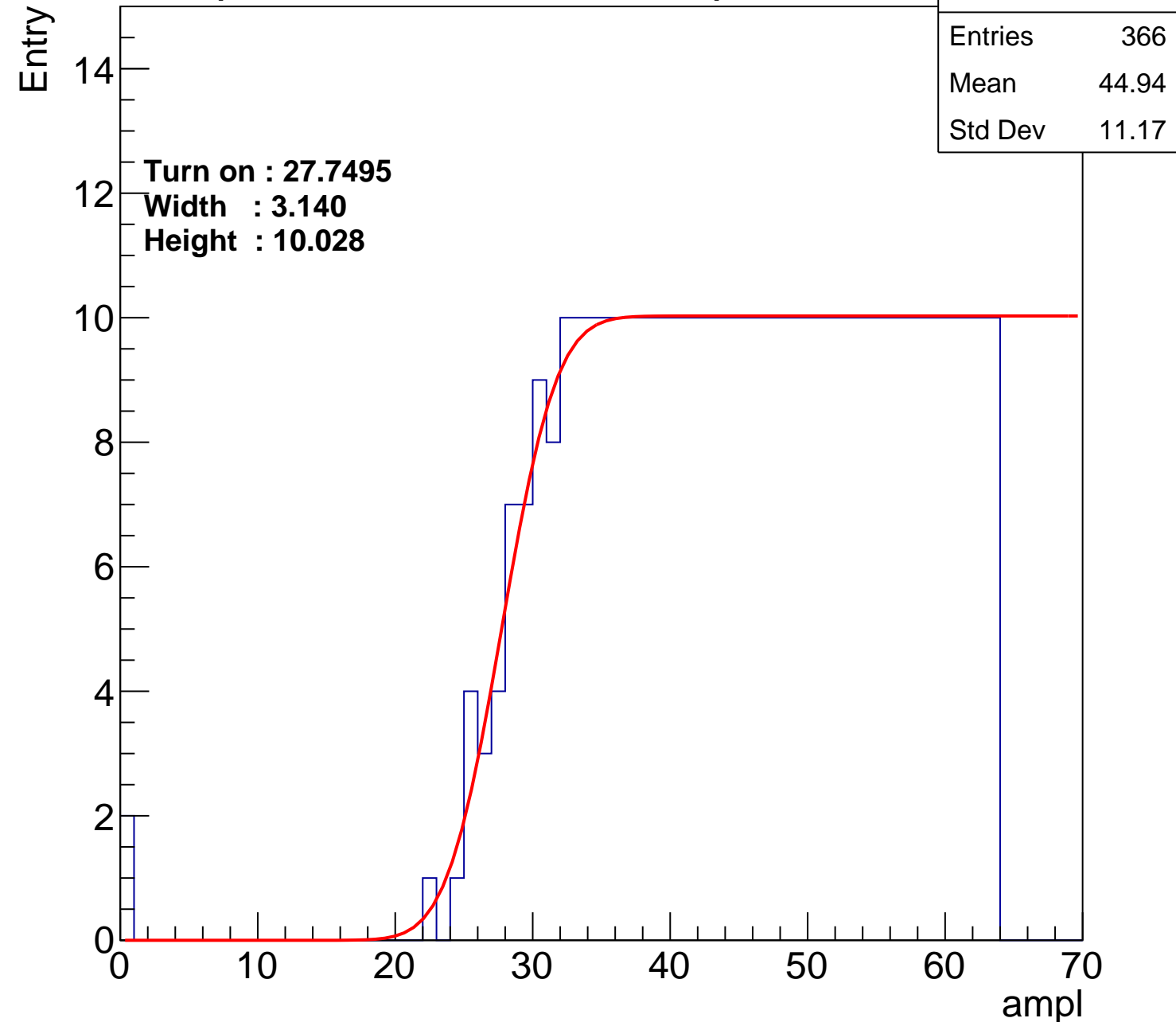
Width : 3.140

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch106

calib_packv5_042523_0143.root, FC#12, port B1

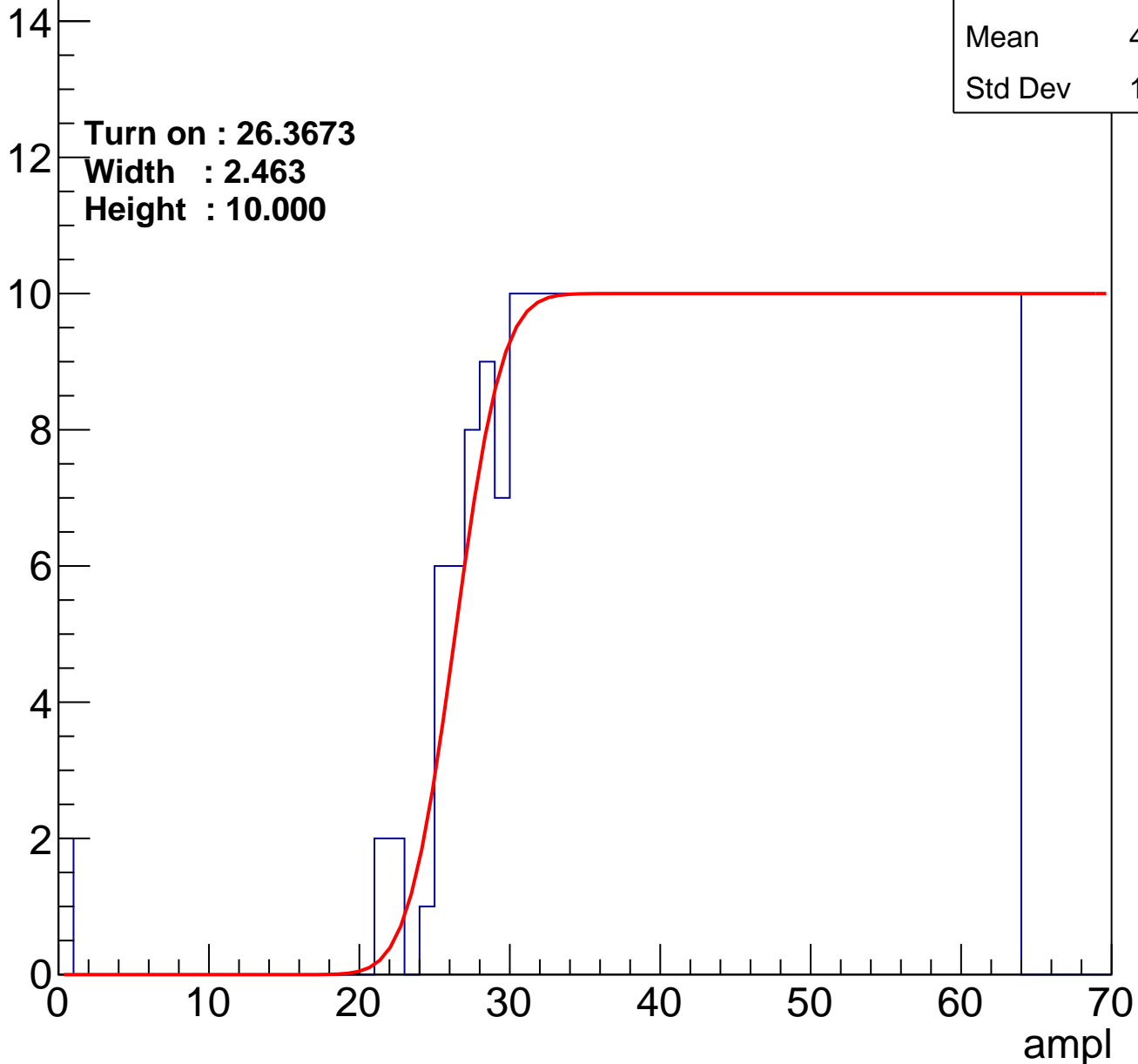
Entries	383
Mean	44.12
Std Dev	11.59

Turn on : 26.3673

Width : 2.463

Height : 10.000

Entry



B0L102S, U20-ch107

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.63
Std Dev	12.24

Turn on : 25.7282

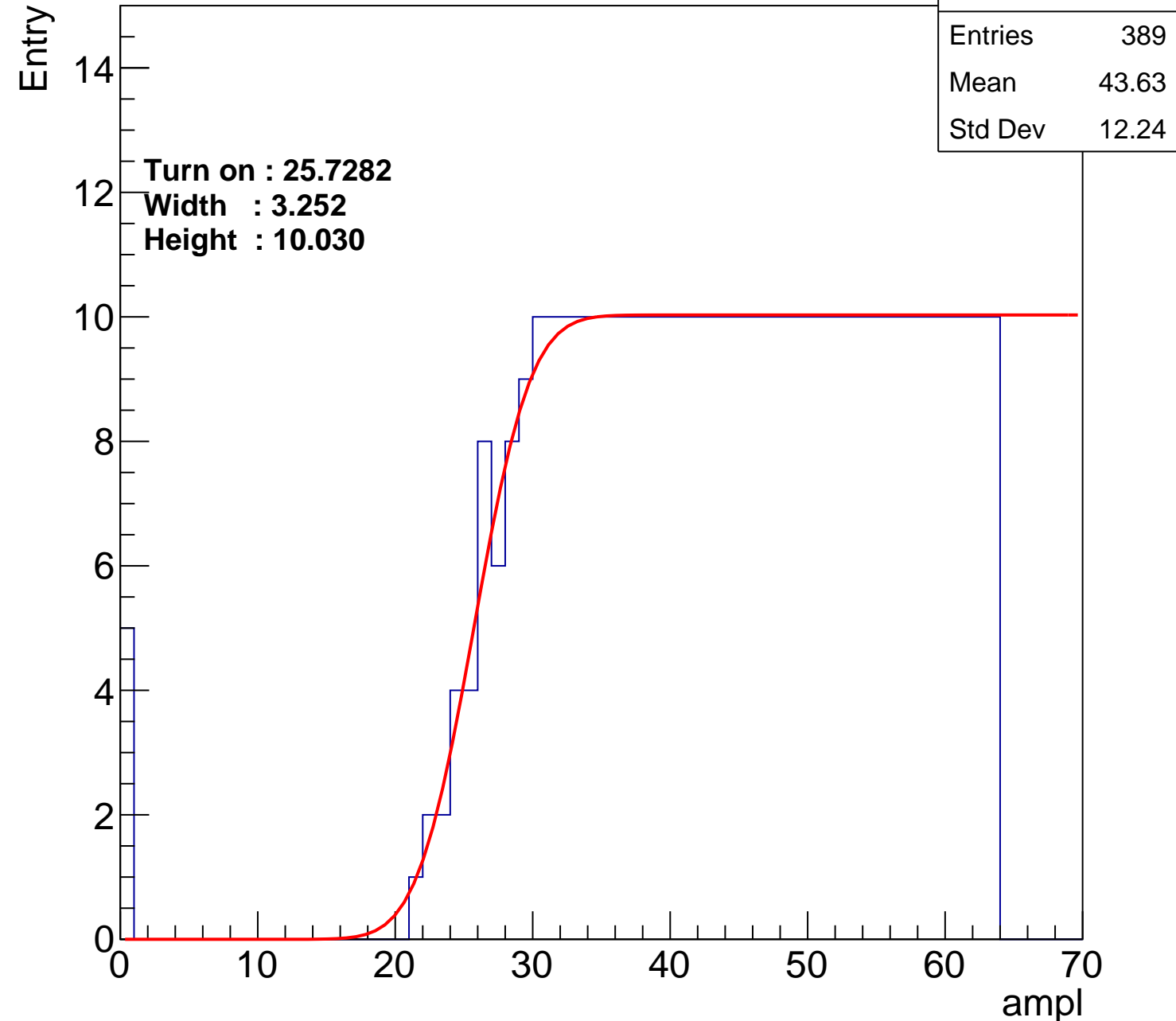
Width : 3.252

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch108

calib_packv5_042523_0143.root, FC#12, port B1

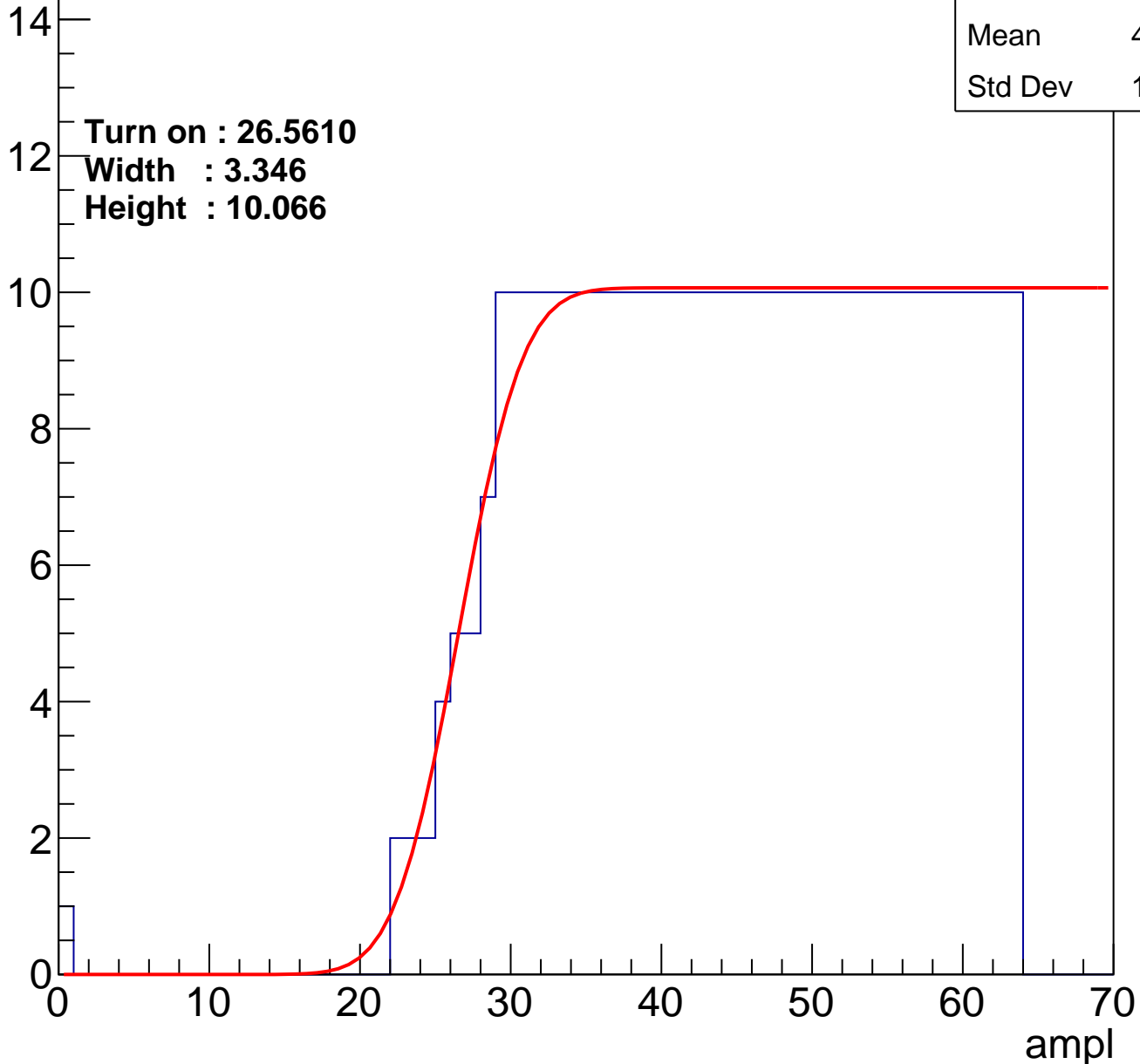
Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.5610

Width : 3.346

Height : 10.066

Entry



B0L102S, U20-ch109

calib_packv5_042523_0143.root, FC#12, port B1

Entries	380
Mean	44.31
Std Dev	11.45

Turn on : 26.0075

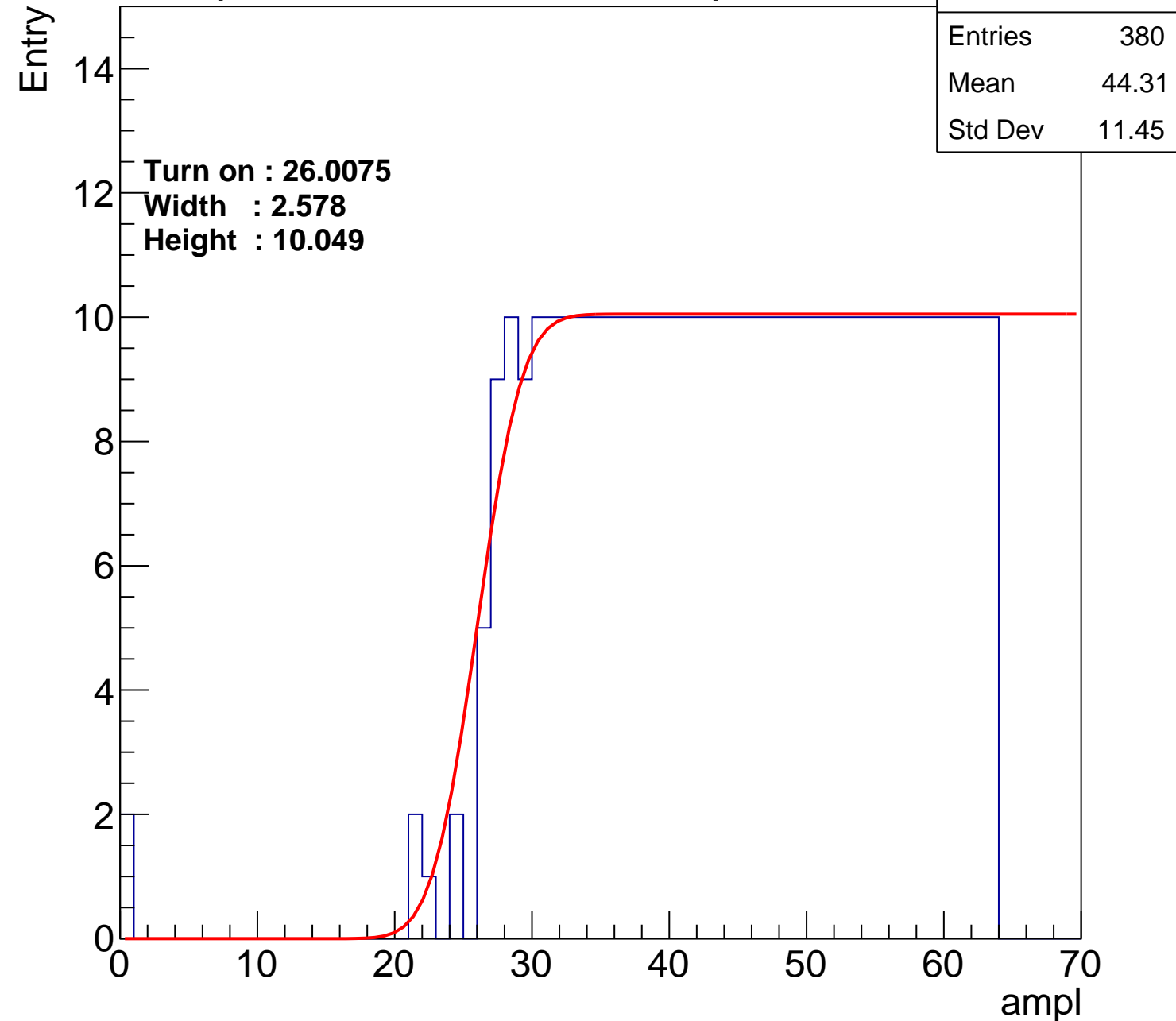
Width : 2.578

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch110

calib_packv5_042523_0143.root, FC#12, port B1

Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 27.6401

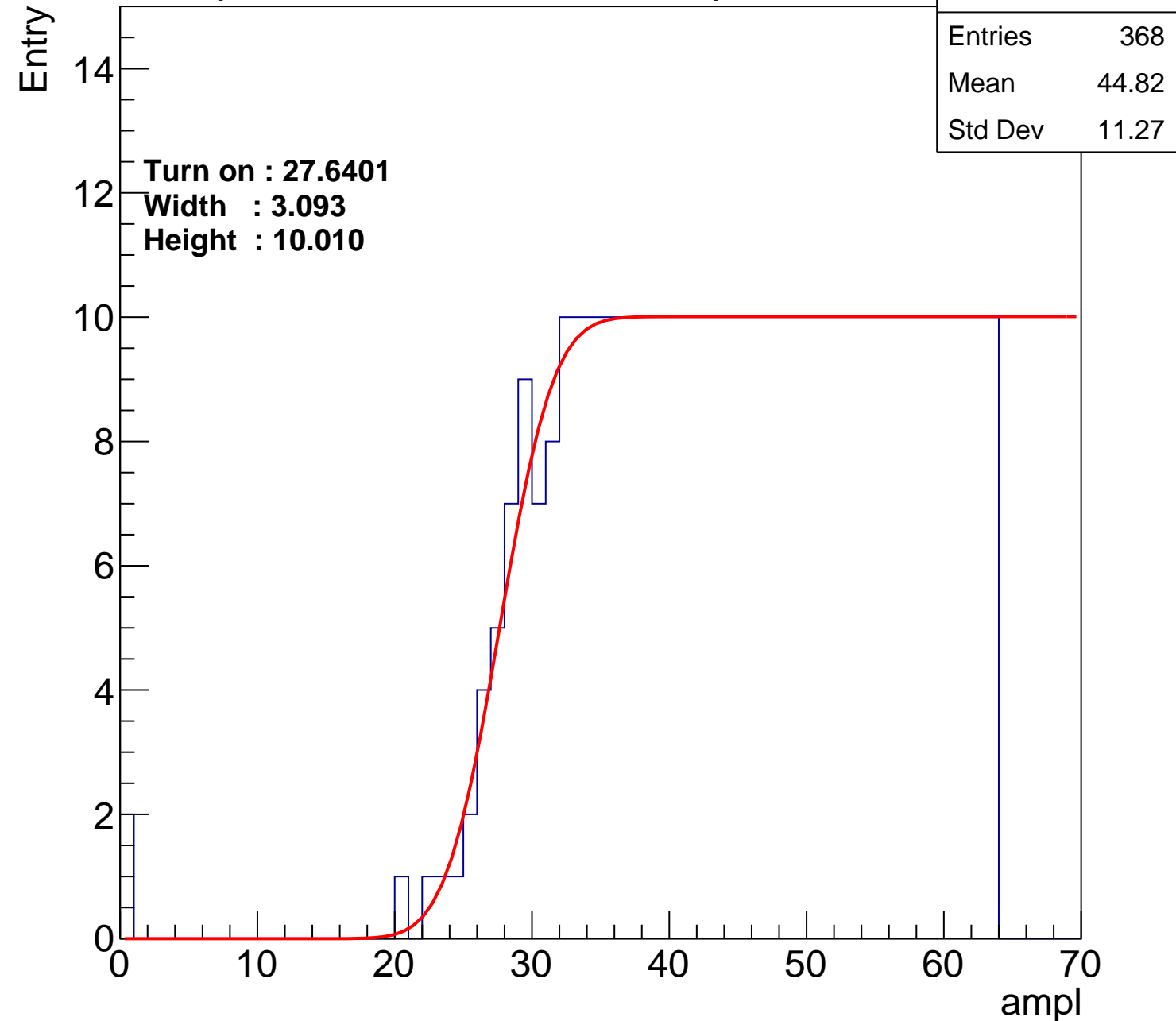
Width : 3.093

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch111

calib_packv5_042523_0143.root, FC#12, port B1

Entries	372
Mean	44.44
Std Dev	11.89

Turn on : 28.0187

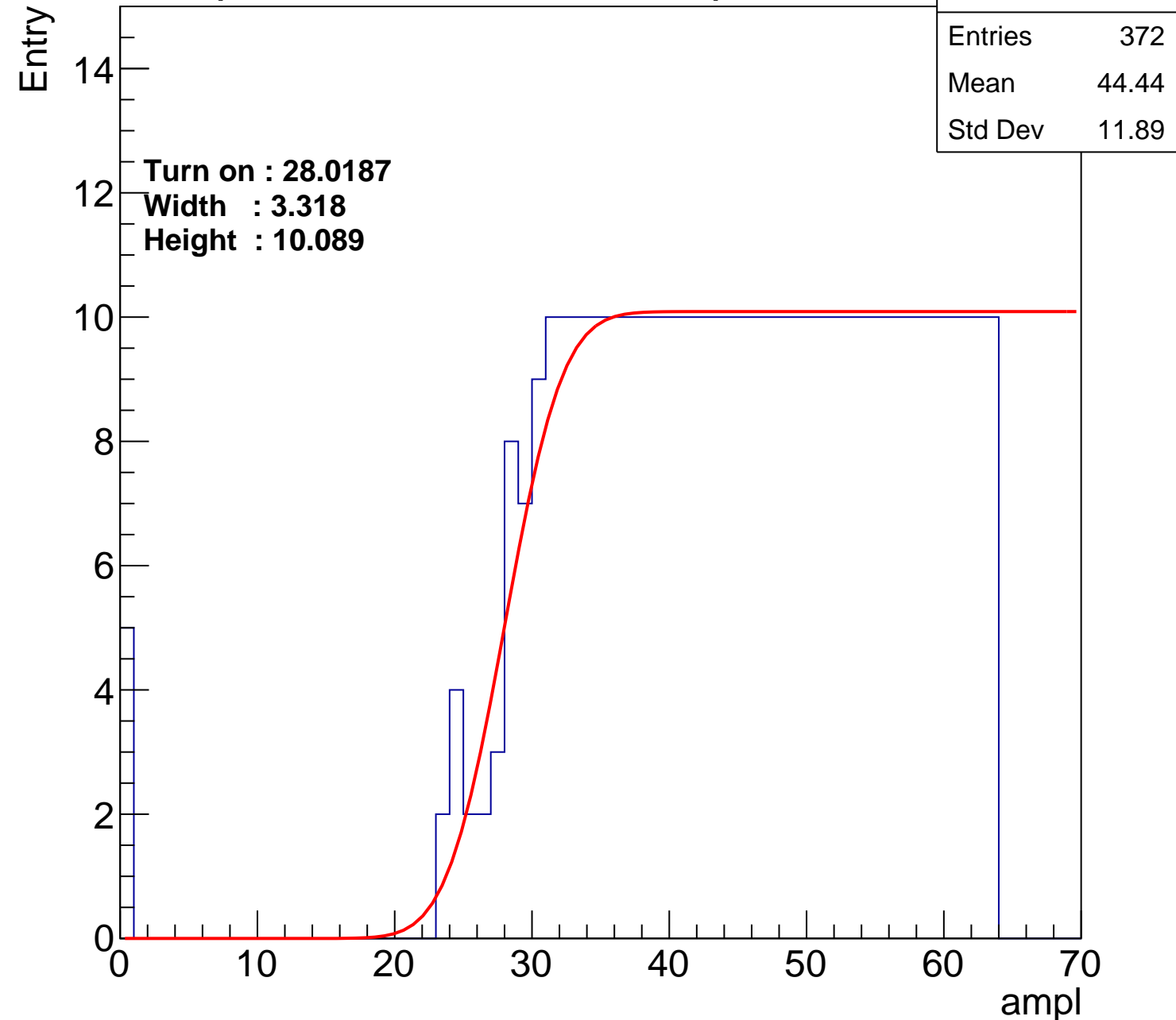
Width : 3.318

Height : 10.089

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch112

calib_packv5_042523_0143.root, FC#12, port B1

Entries	375
Mean	44.36
Std Dev	11.77

Turn on : 27.2307

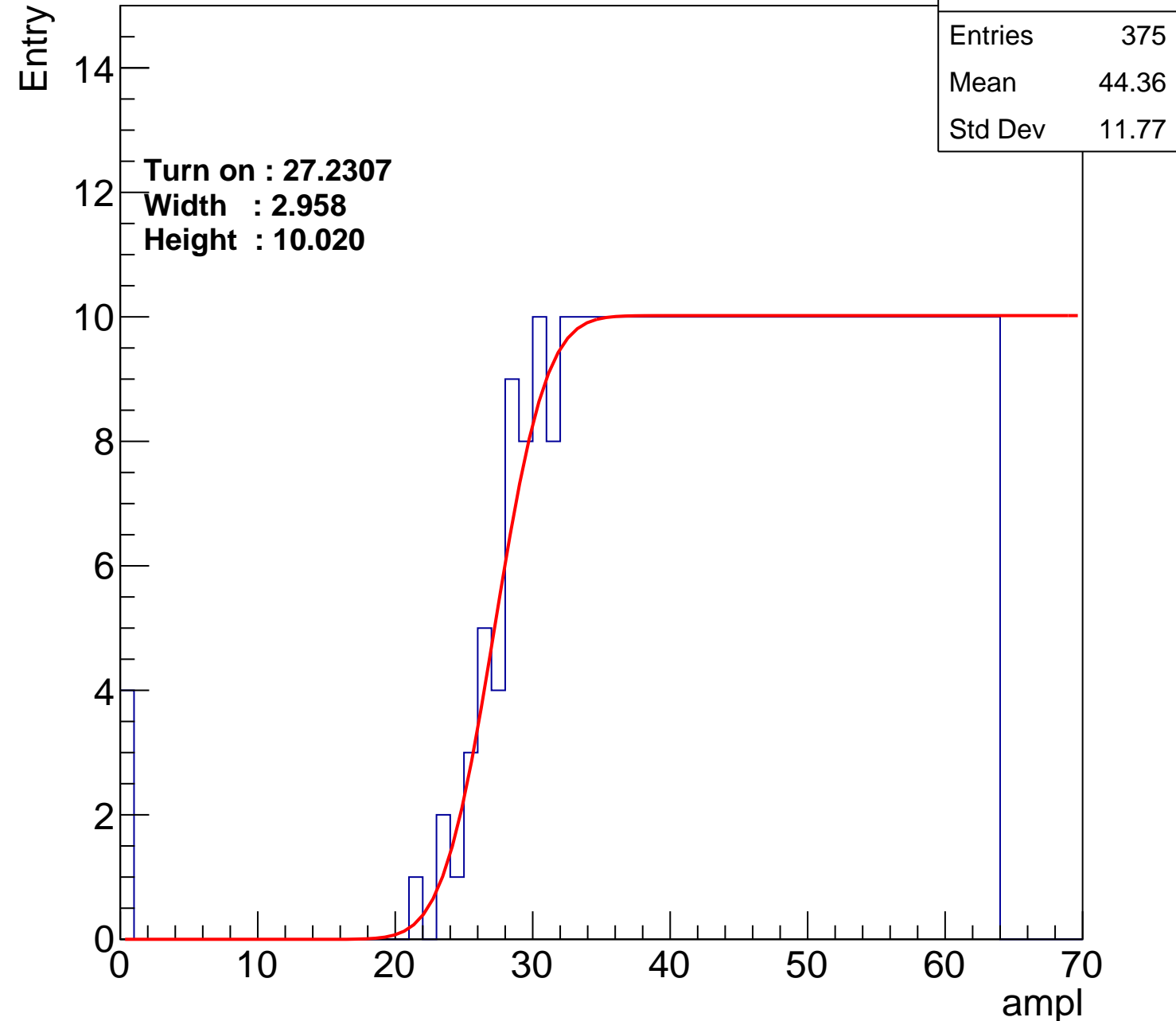
Width : 2.958

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch113

calib_packv5_042523_0143.root, FC#12, port B1

Entries	371
Mean	44.57
Std Dev	11.59

Turn on : 27.7995

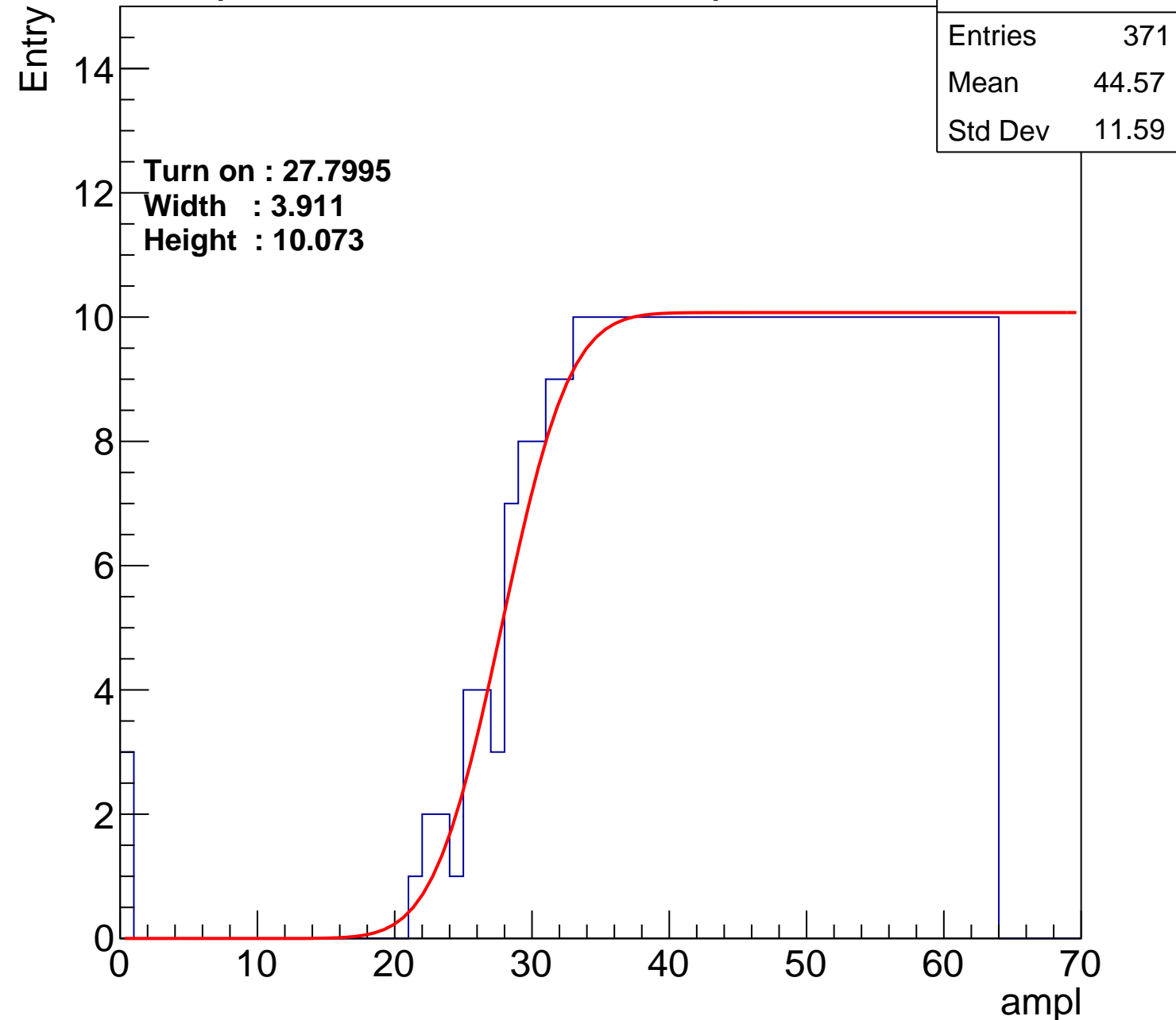
Width : 3.911

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch114

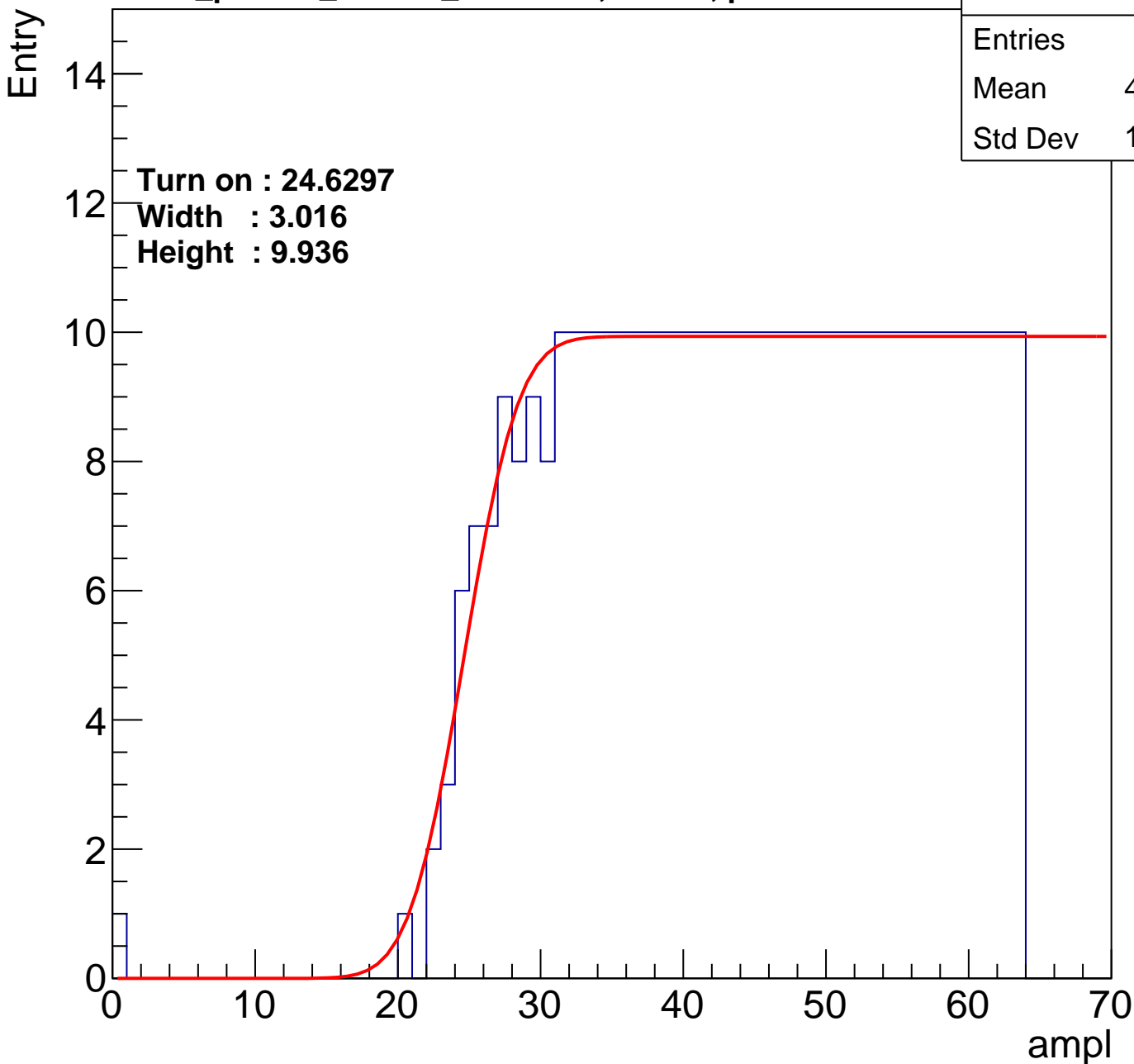
calib_packv5_042523_0143.root, FC#12, port B1

Entries	391
Mean	43.76
Std Dev	11.65

Turn on : 24.6297

Width : 3.016

Height : 9.936



B0L102S, U20-ch115

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.96
Std Dev	11.68

Turn on : 25.8029

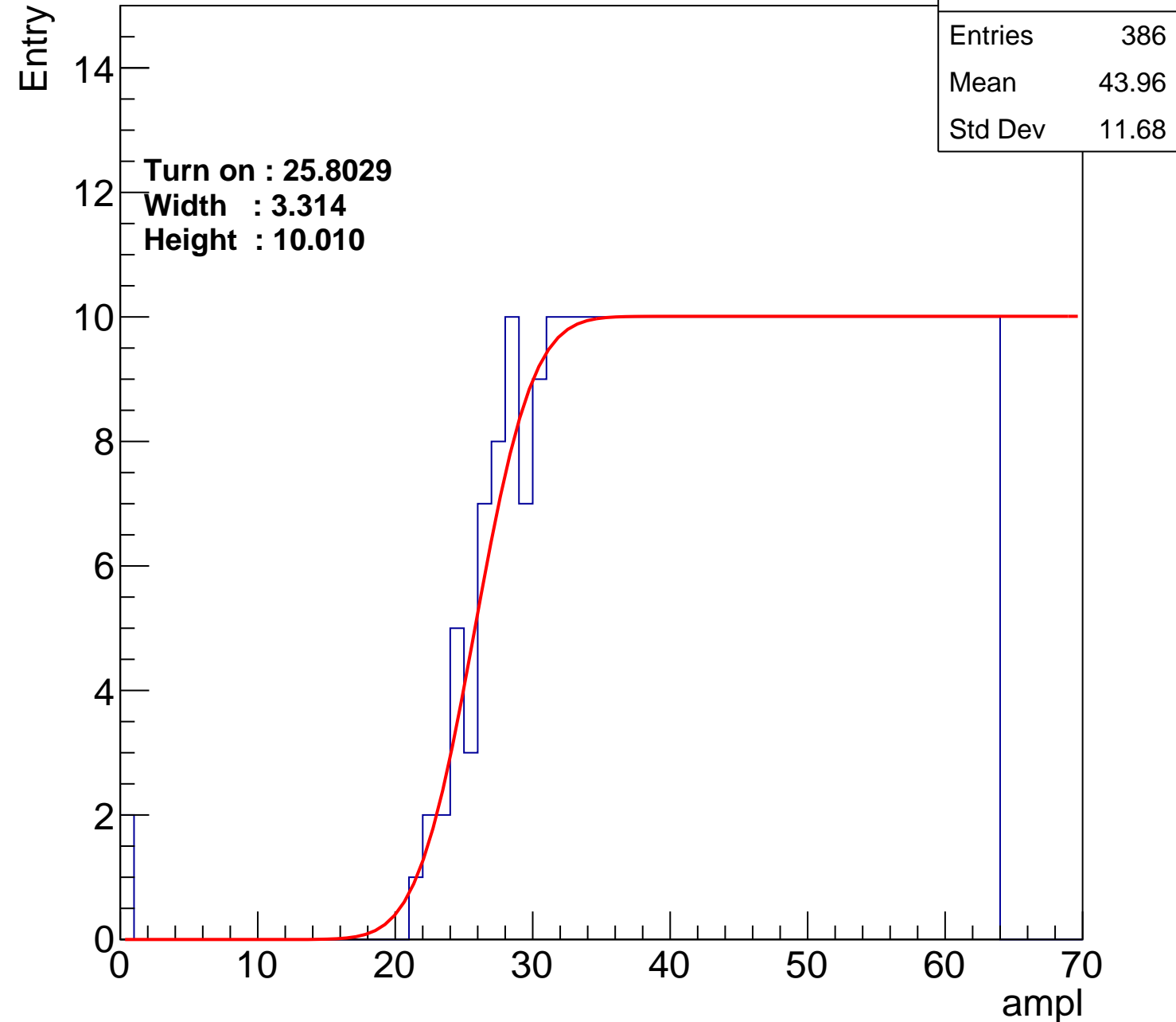
Width : 3.314

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch116

calib_packv5_042523_0143.root, FC#12, port B1

Entries	382
Mean	43.93
Std Dev	12.15

Turn on : 26.4623

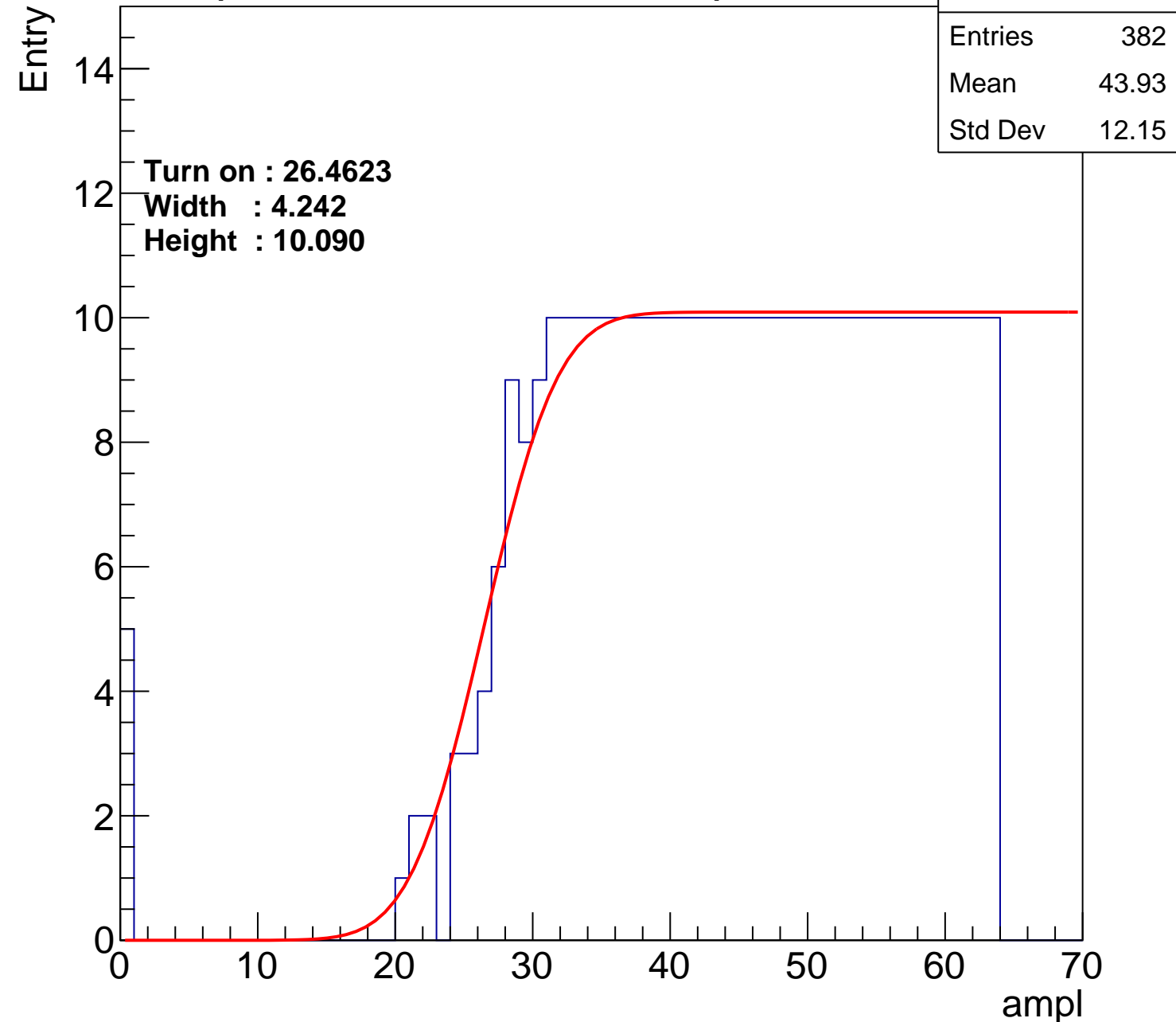
Width : 4.242

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch117

calib_packv5_042523_0143.root, FC#12, port B1

Entries	397
Mean	43.35
Std Dev	12.2

Turn on : 24.6651

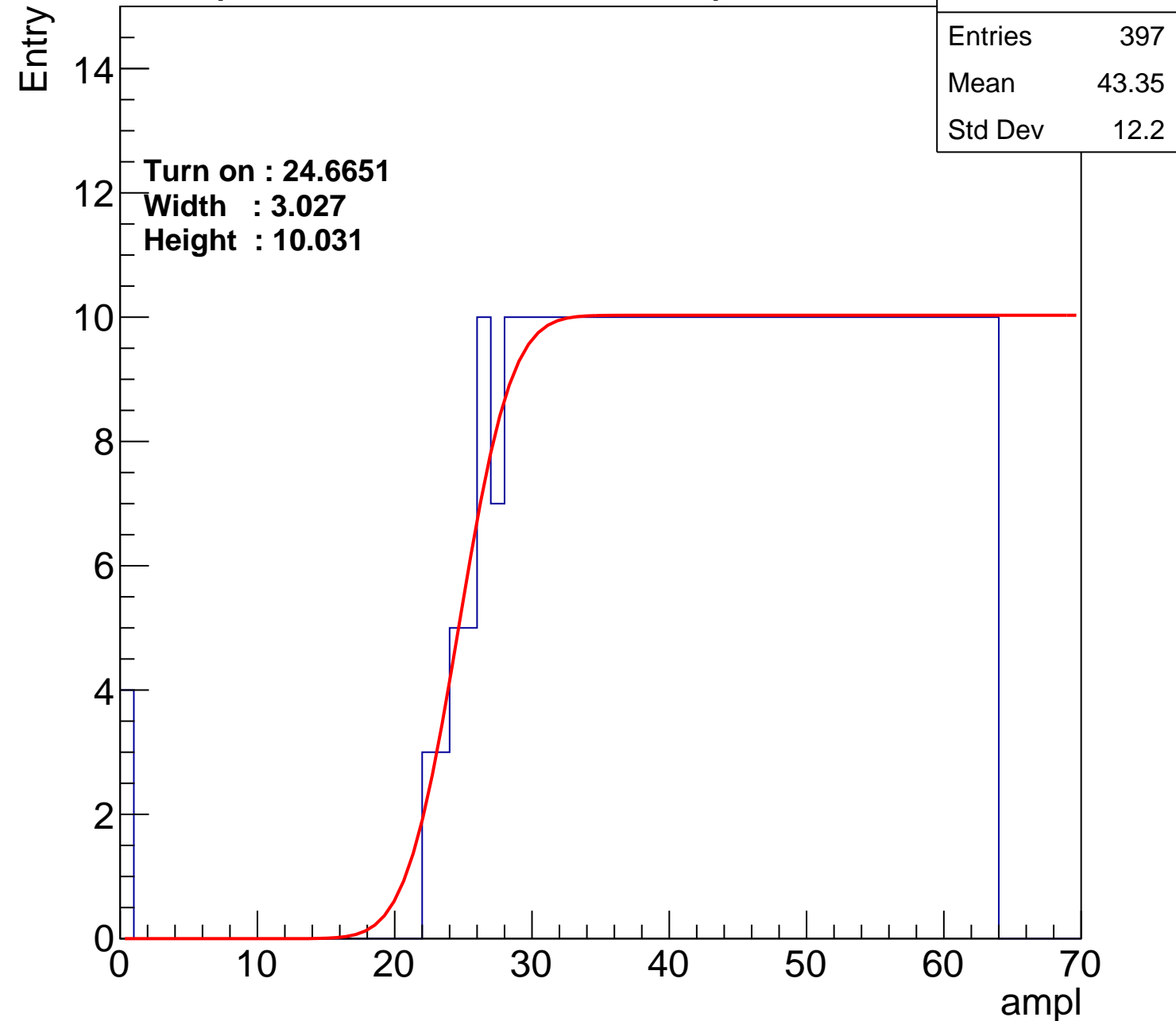
Width : 3.027

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch118

calib_packv5_042523_0143.root, FC#12, port B1

Entries	386
Mean	43.89
Std Dev	11.86

Turn on : 25.7446

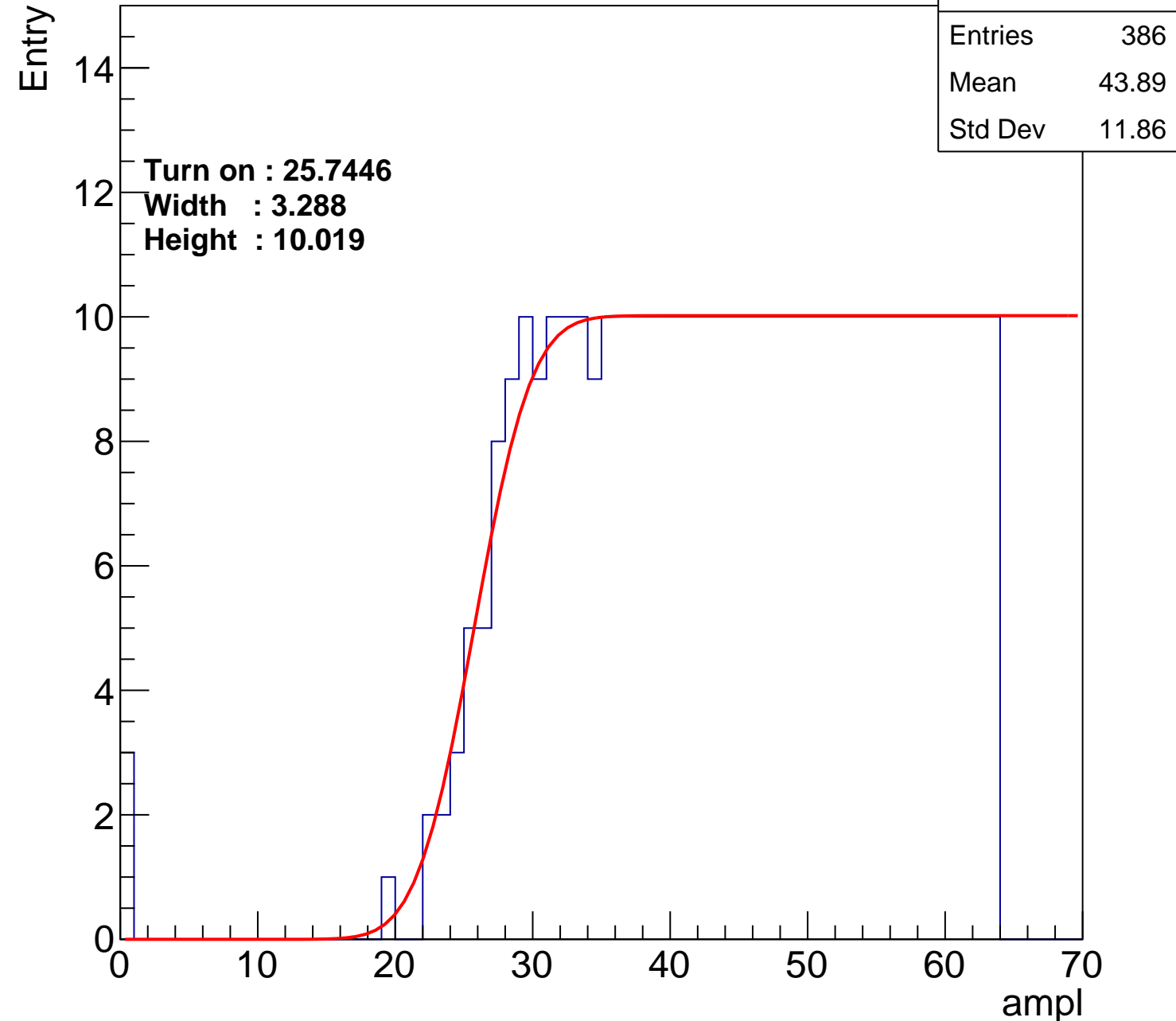
Width : 3.288

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch119

calib_packv5_042523_0143.root, FC#12, port B1

Entries	389
Mean	43.71
Std Dev	12.08

Turn on : 25.0933

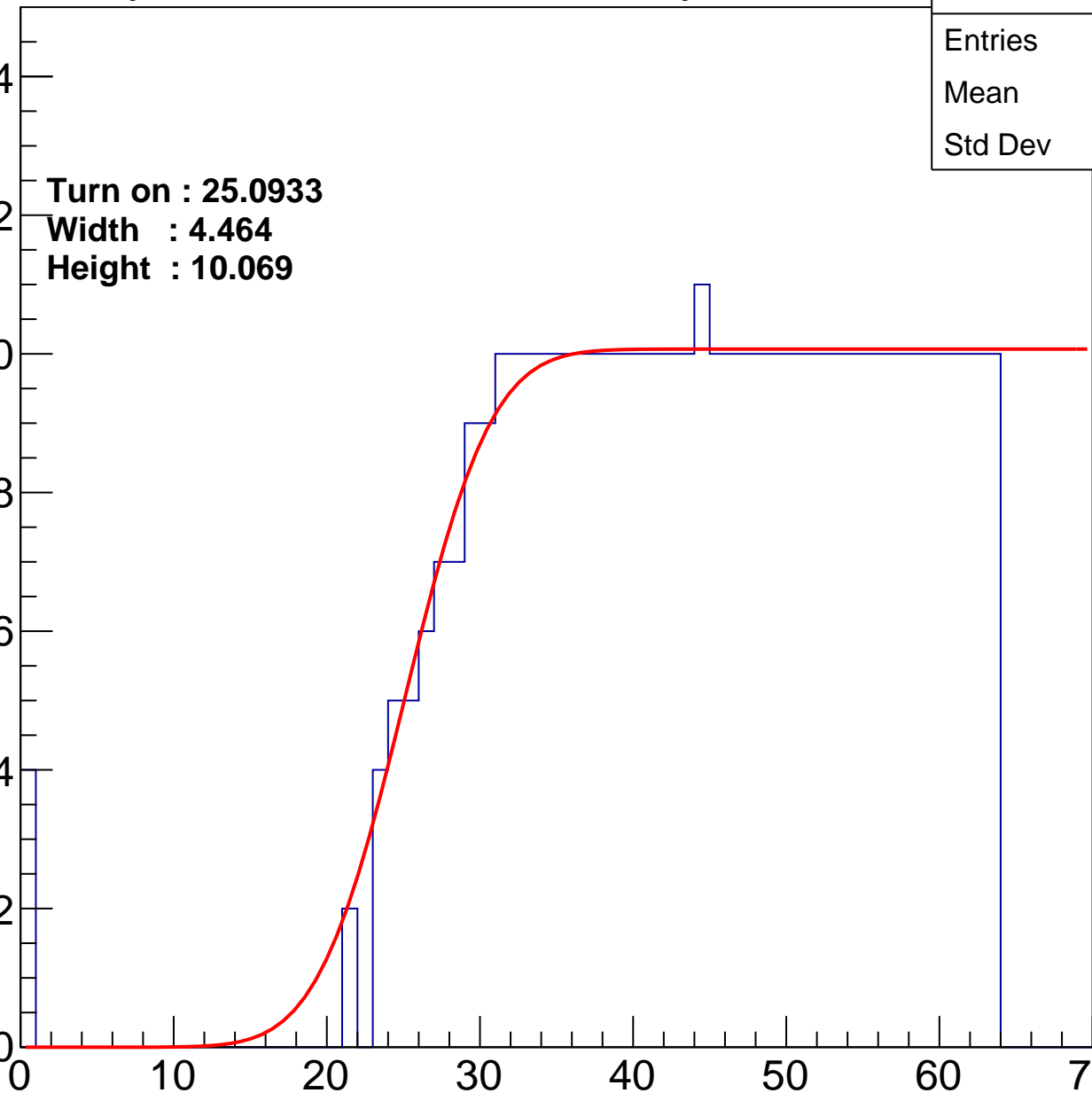
Width : 4.464

Height : 10.069

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch120

calib_packv5_042523_0143.root, FC#12, port B1

Entries	384
Mean	43.94
Std Dev	11.87

Turn on : 25.7315

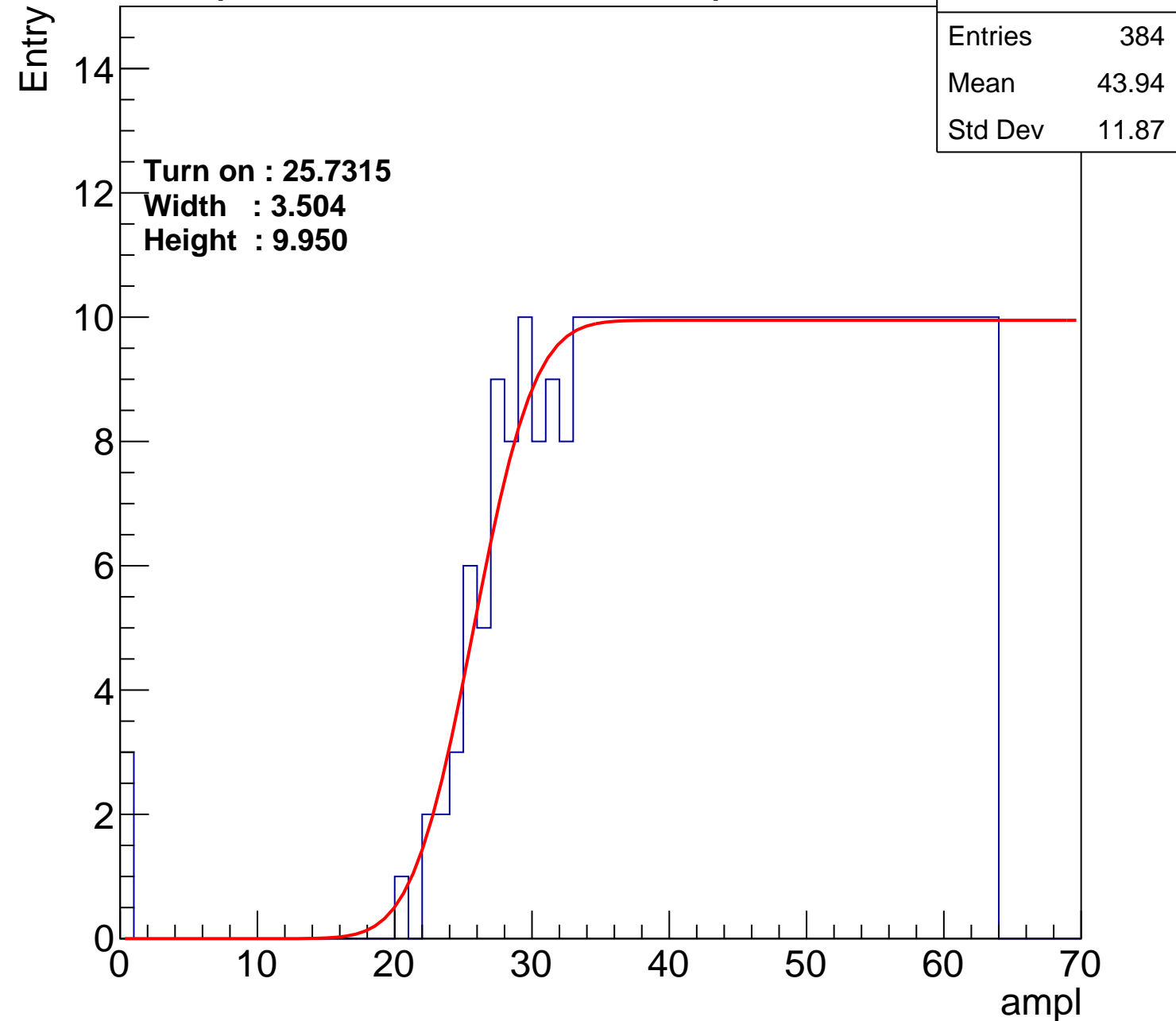
Width : 3.504

Height : 9.950

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch121

calib_packv5_042523_0143.root, FC#12, port B1

Entries	379
Mean	44.36
Std Dev	11.33

Turn on : 26.5523

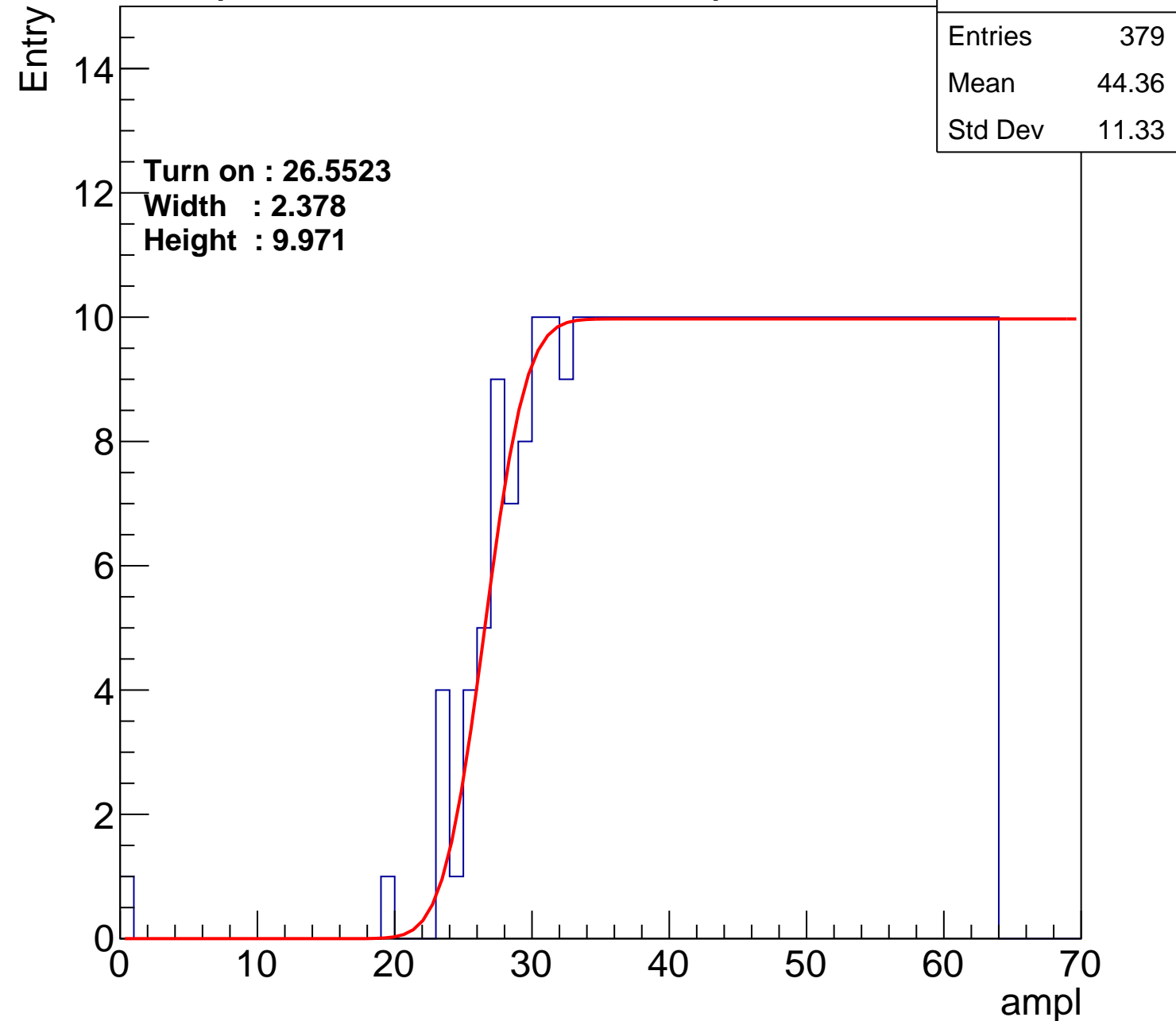
Width : 2.378

Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch122

calib_packv5_042523_0143.root, FC#12, port B1

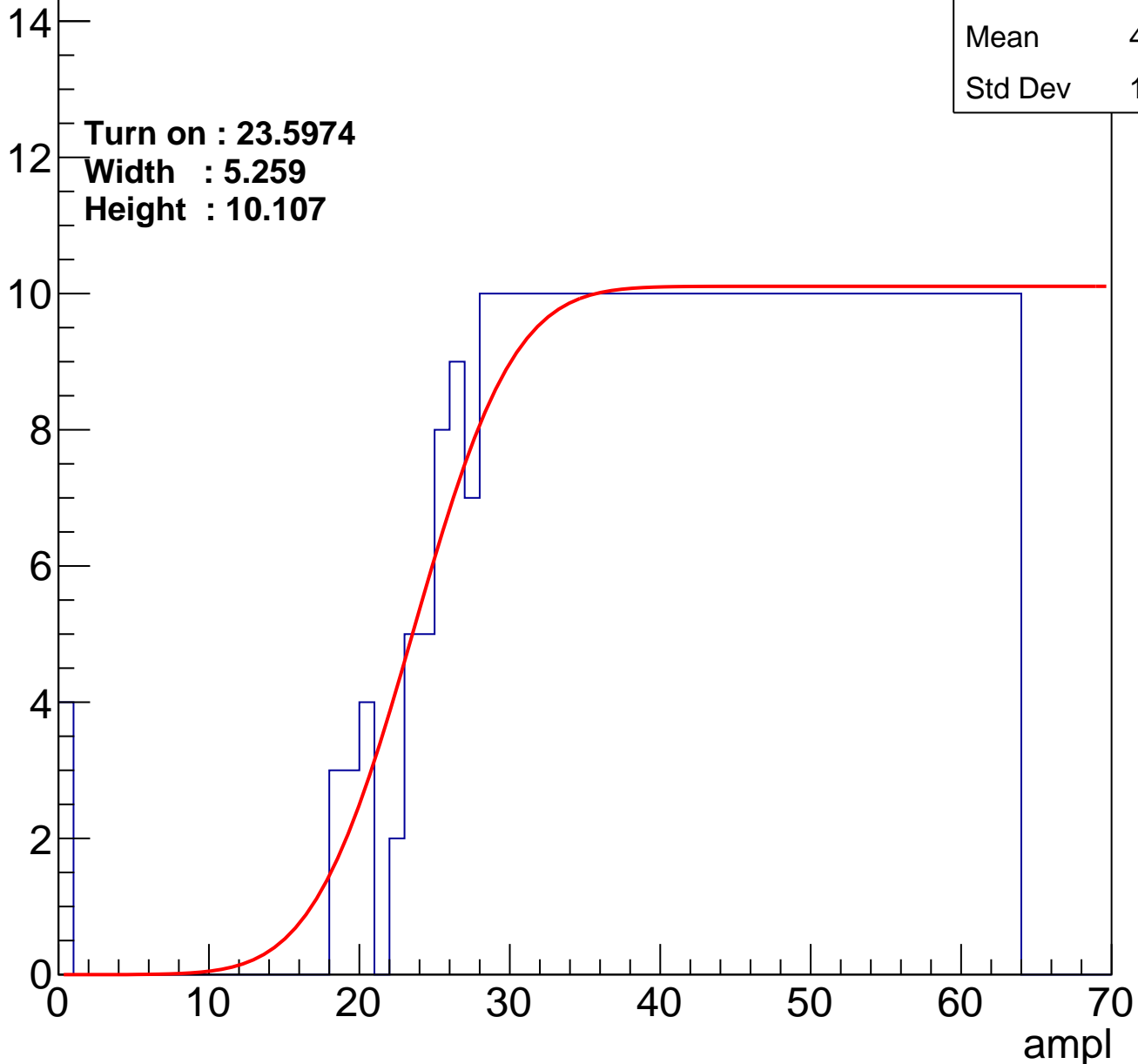
Entries	410
Mean	42.62
Std Dev	12.67

Turn on : 23.5974

Width : 5.259

Height : 10.107

Entry



B0L102S, U20-ch123

calib_packv5_042523_0143.root, FC#12, port B1

Entries	365
Mean	44.99
Std Dev	11.14

Turn on : 27.9094

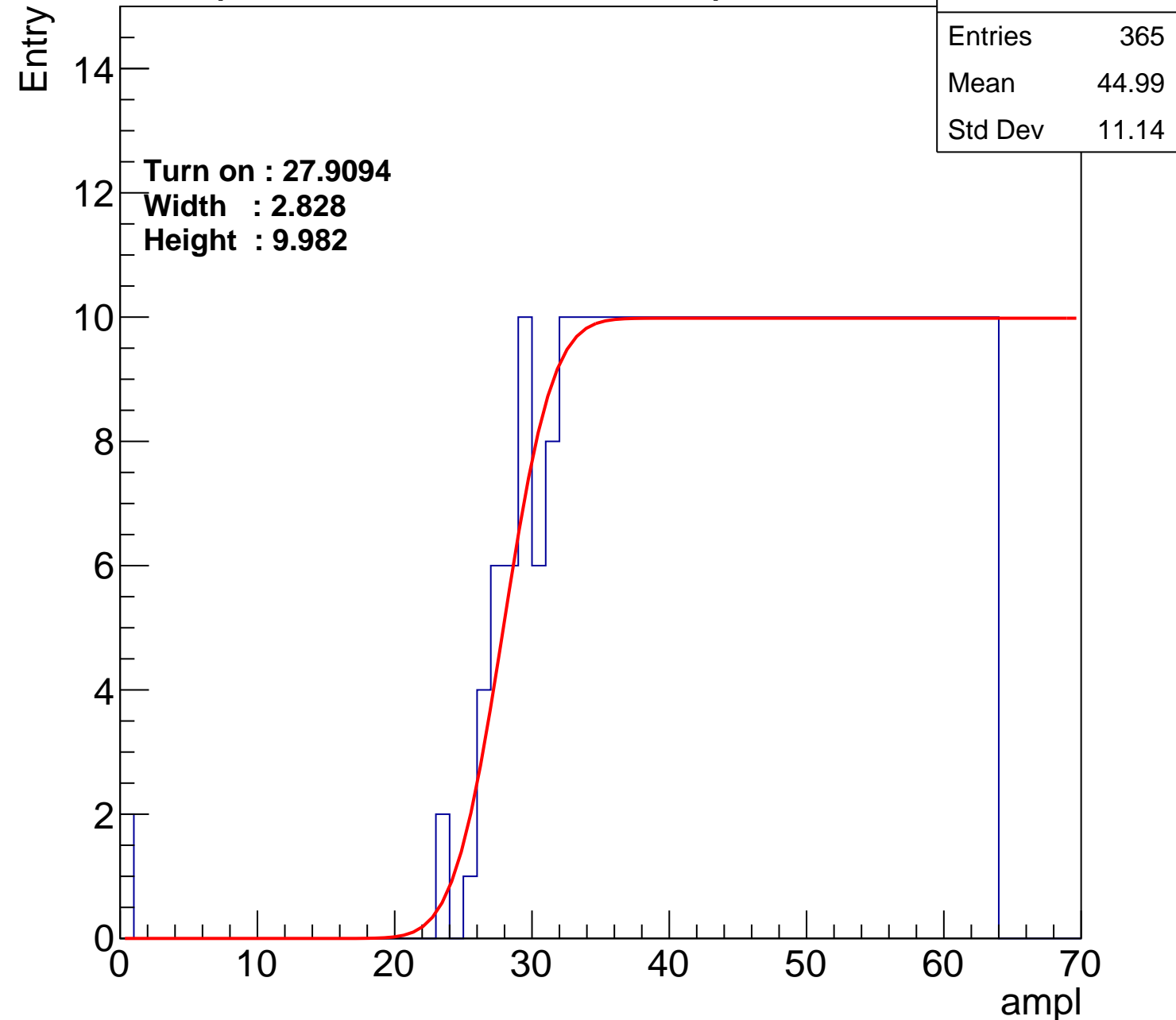
Width : 2.828

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch124

calib_packv5_042523_0143.root, FC#12, port B1

Entries	388
Mean	43.89
Std Dev	11.68

Turn on : 25.9446

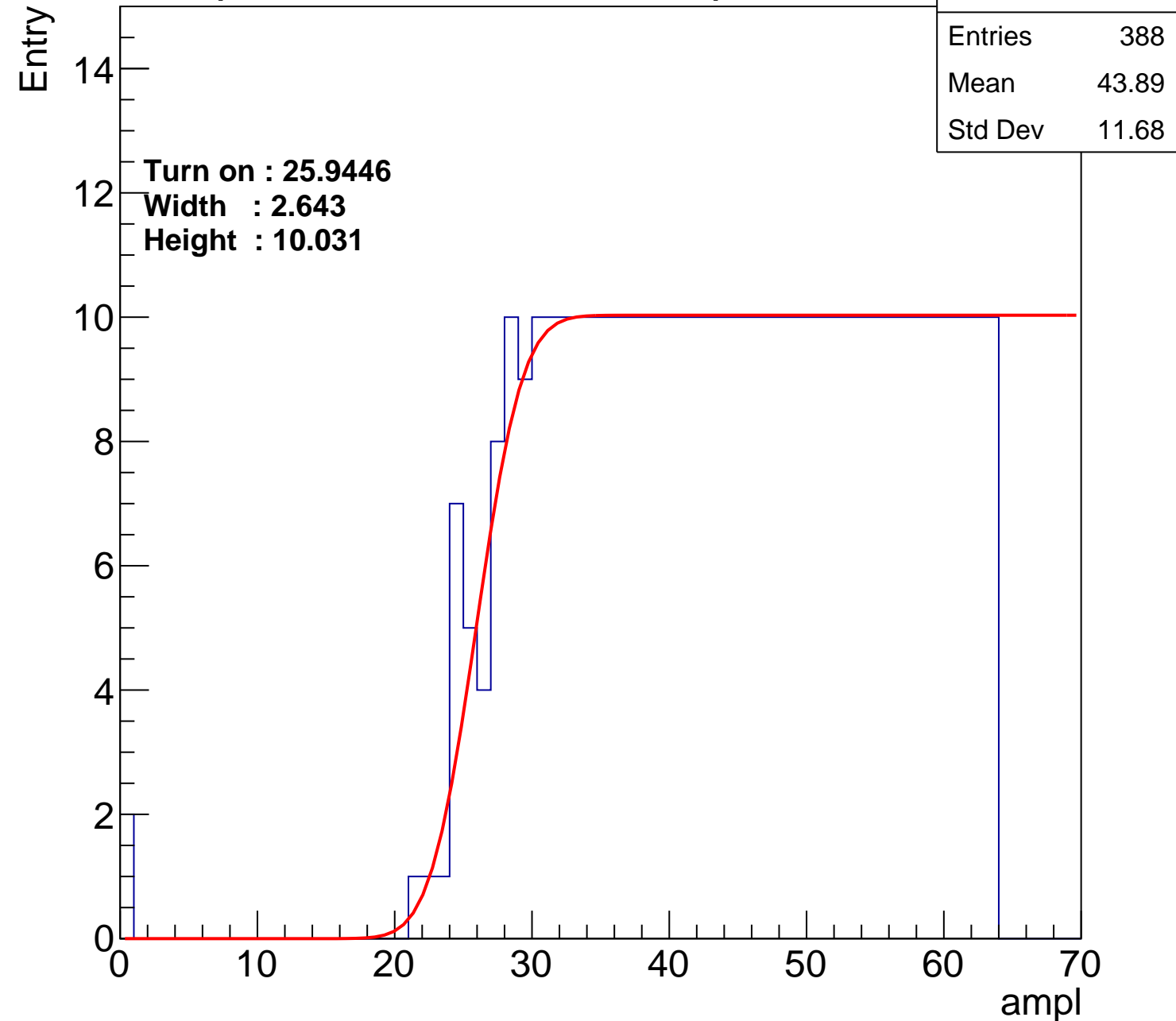
Width : 2.643

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch125

calib_packv5_042523_0143.root, FC#12, port B1

Entries	395
Mean	43.43
Std Dev	12.18

Turn on : 25.4081

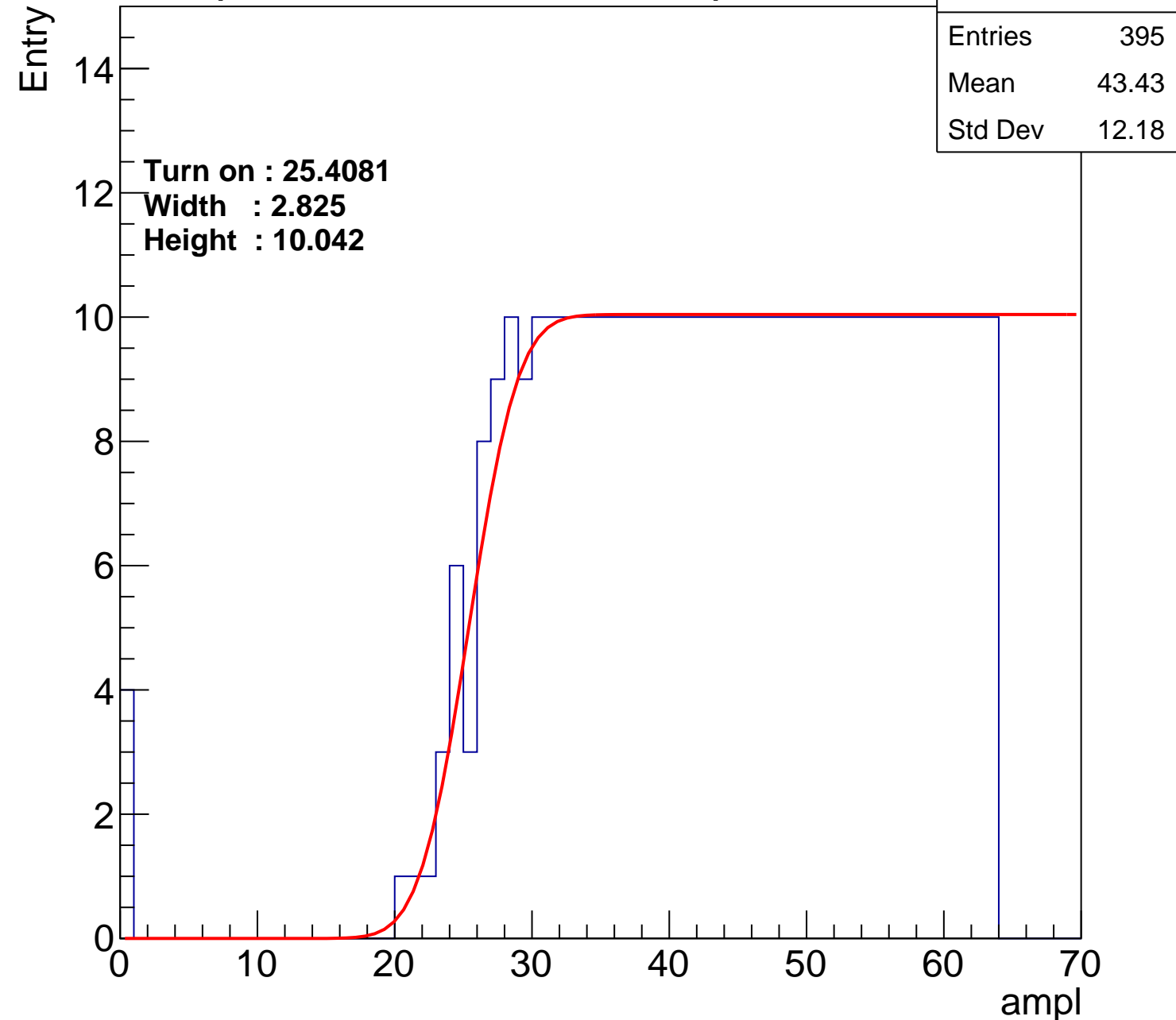
Width : 2.825

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch126

calib_packv5_042523_0143.root, FC#12, port B1

Entries	383
Mean	44.11
Std Dev	11.54

Turn on : 26.7004

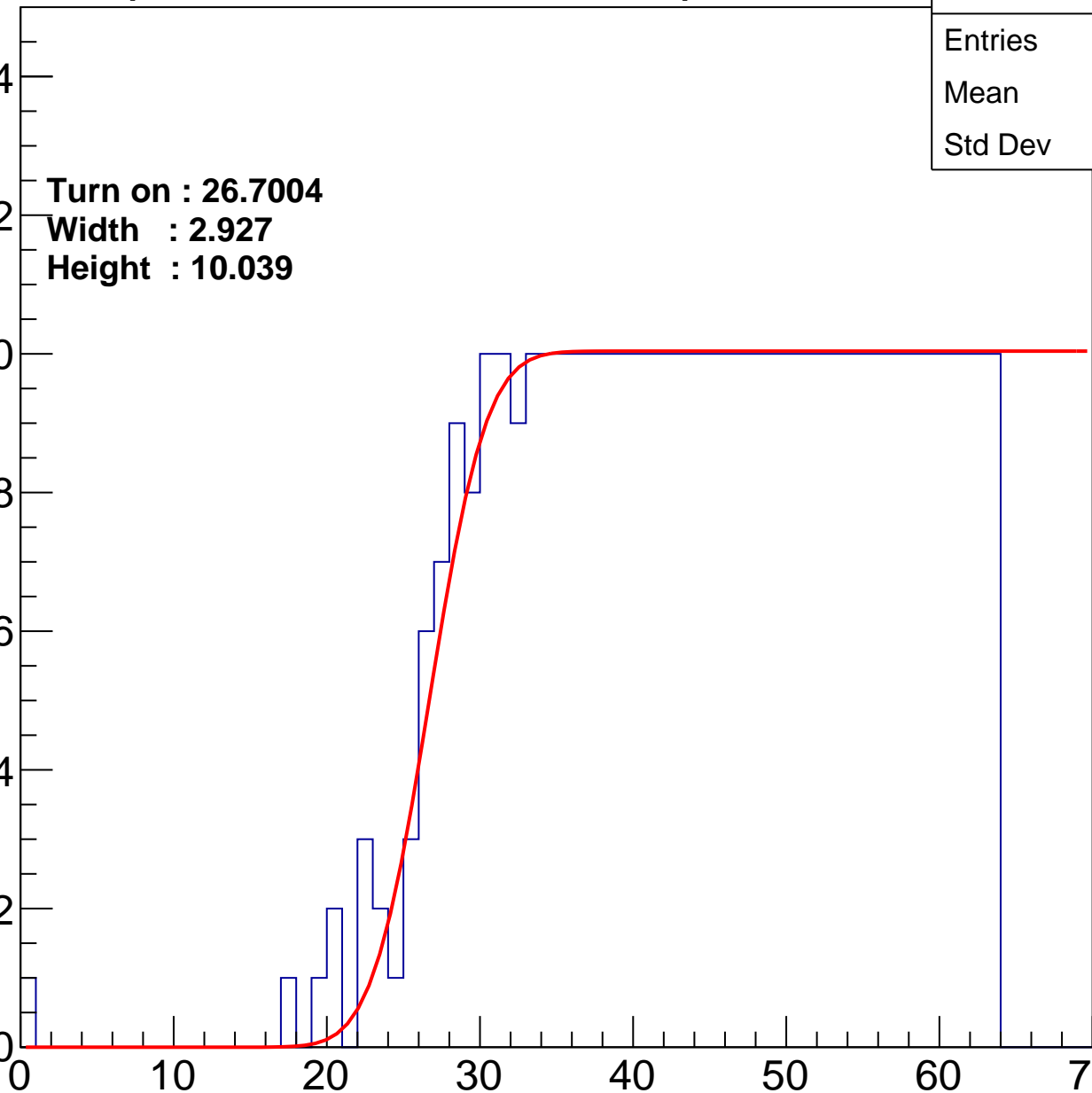
Width : 2.927

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B0L102S, U20-ch127

calib_packv5_042523_0143.root, FC#12, port B1

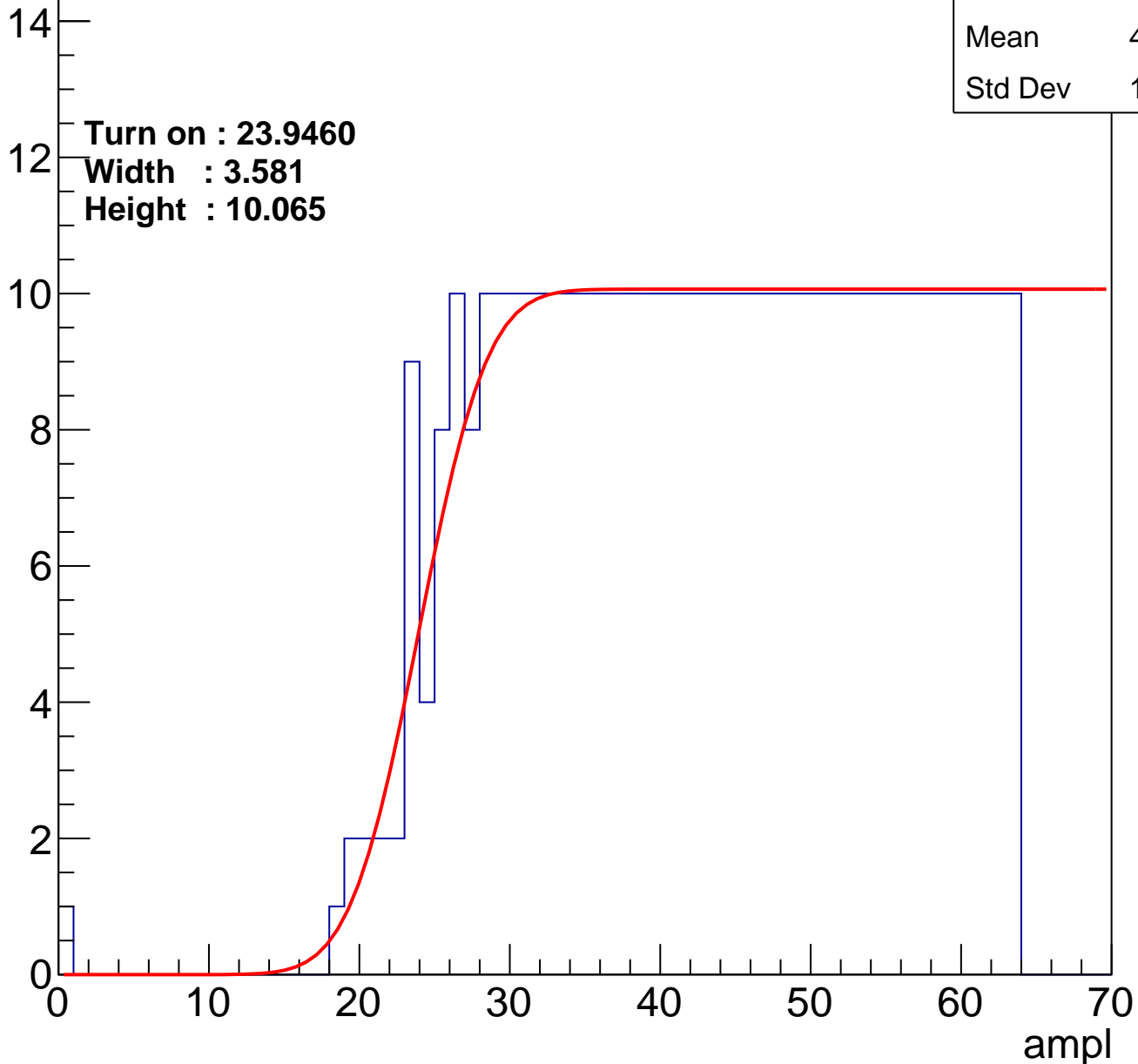
Entries	409
Mean	42.89
Std Dev	12.13

Turn on : 23.9460

Width : 3.581

Height : 10.065

Entry



B0L102S, U20-ch127

calib_packv5_042523_0143.root, FC#12, port B1

Entries	409
Mean	42.89
Std Dev	12.13

Turn on : 23.9460

Width : 3.581

Height : 10.065

Entry

