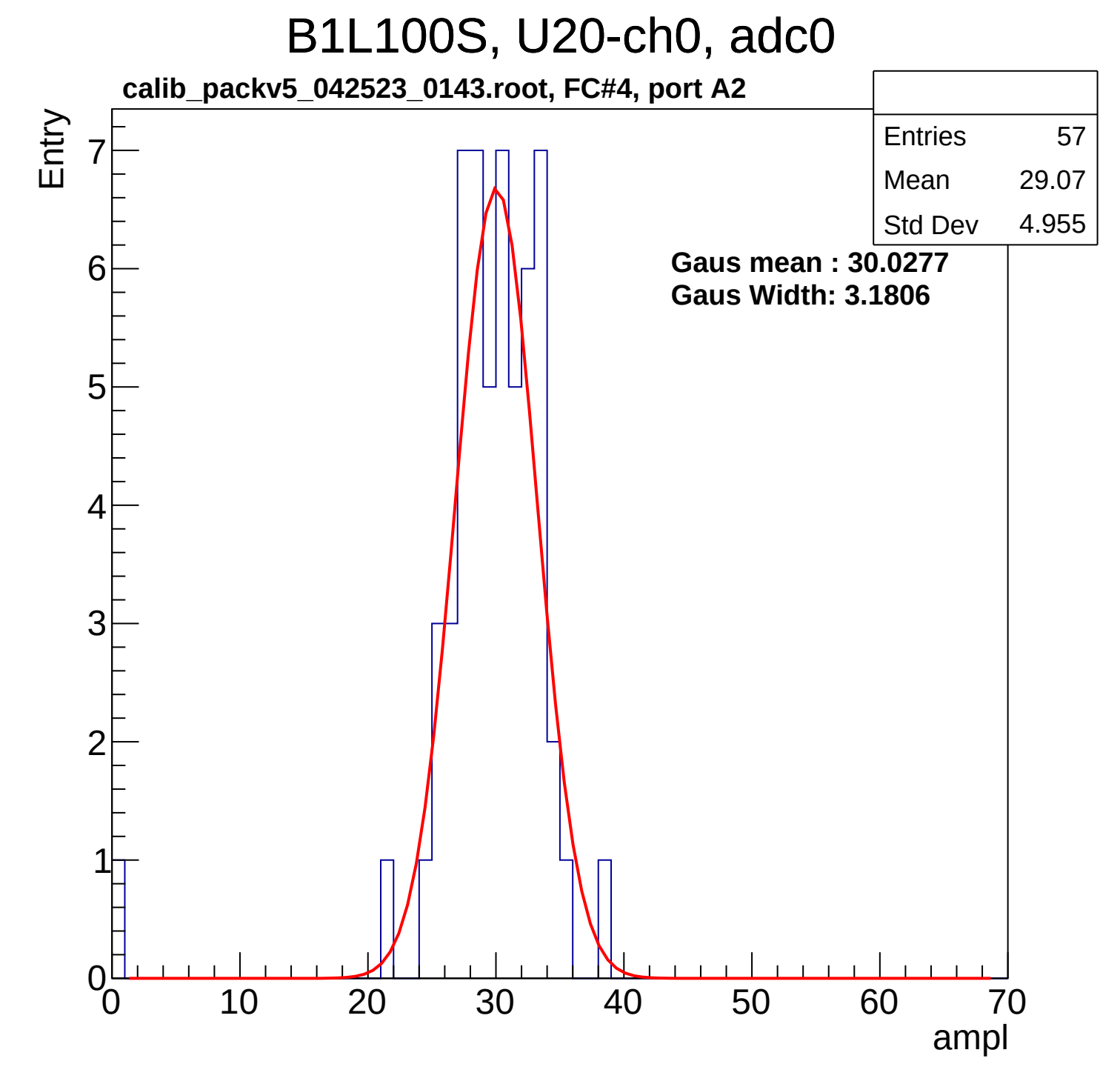
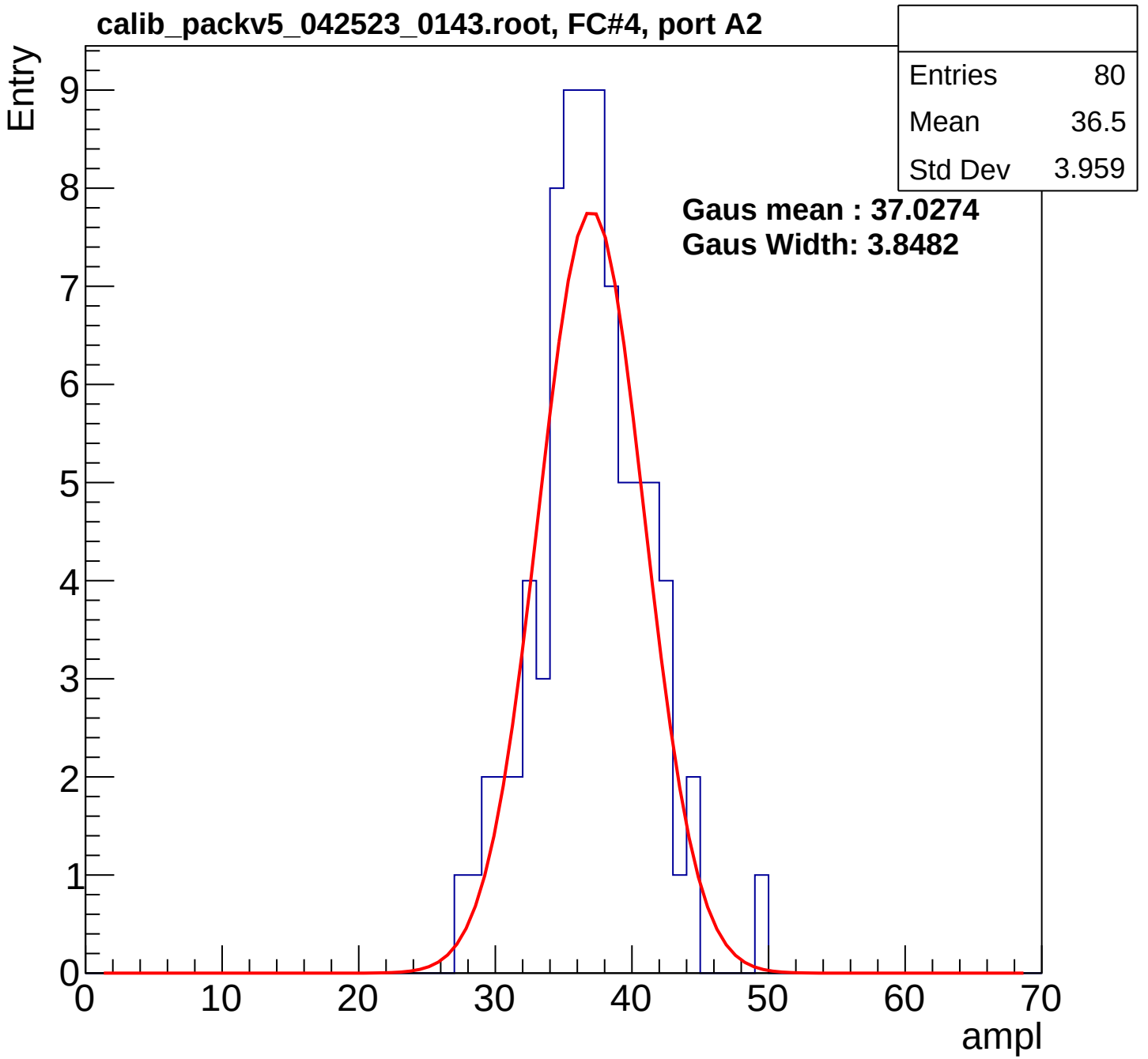

B1L100S, U20-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.07
Std Dev 4.955
Gaus mean : 30.0277
Gaus Width: 3.1806
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.5
Std Dev 3.959
Gaus mean : 37.0274
Gaus Width: 3.8482
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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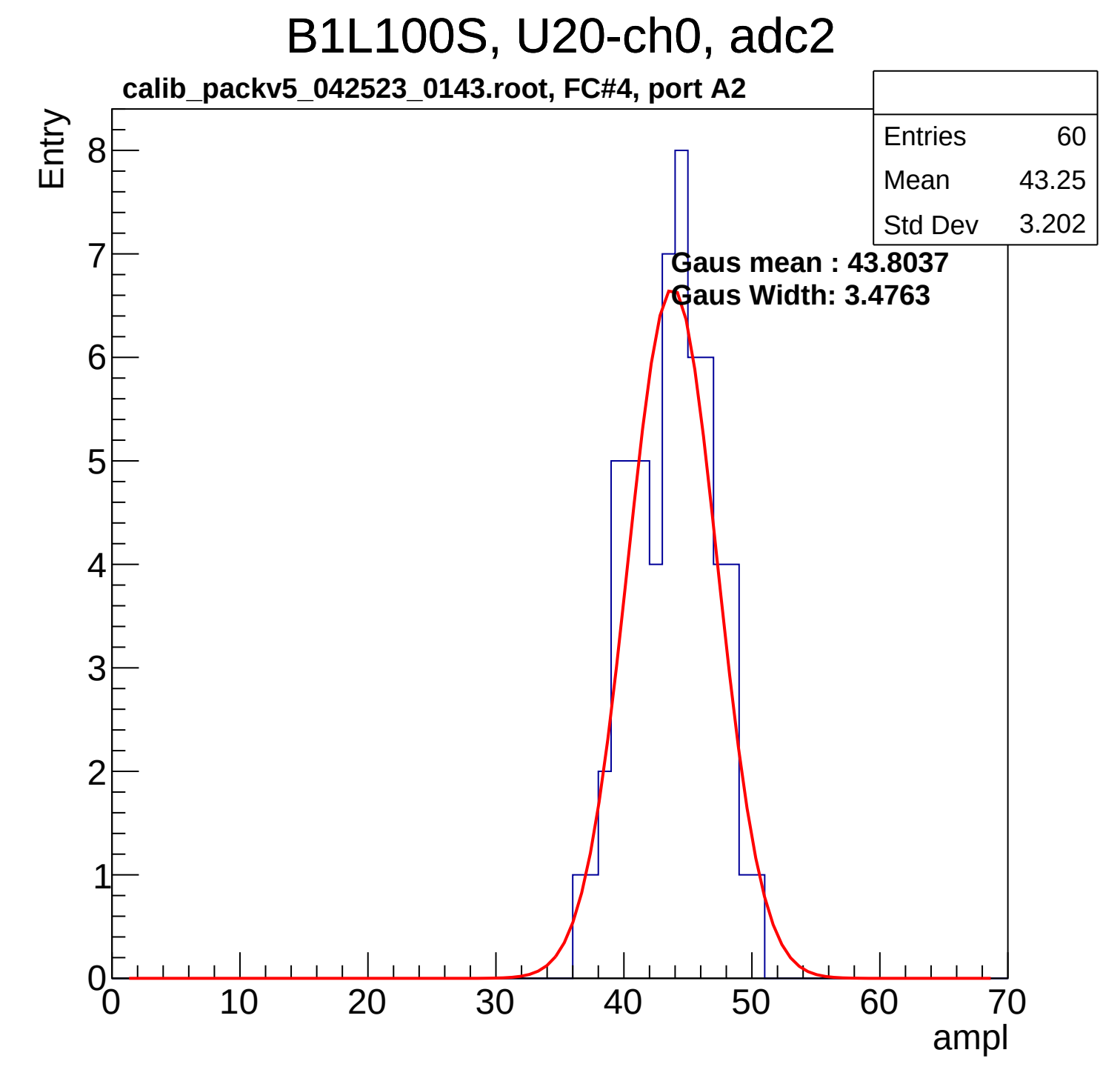
B1L100S, U20-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.25
Std Dev 3.202
Gaus mean : 43.8037
Gaus Width: 3.4763
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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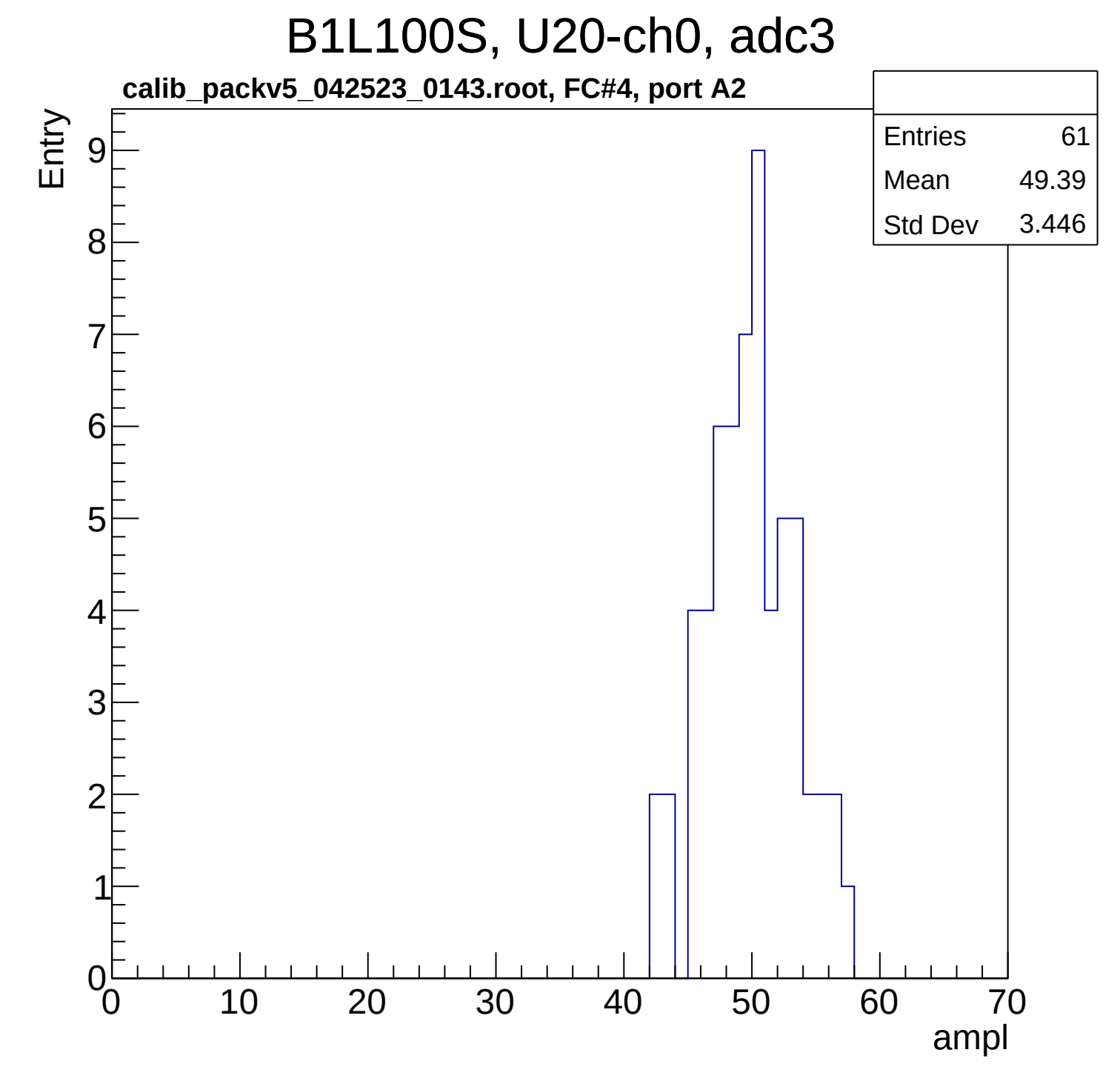
B1L100S, U20-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.39
Std Dev 3.446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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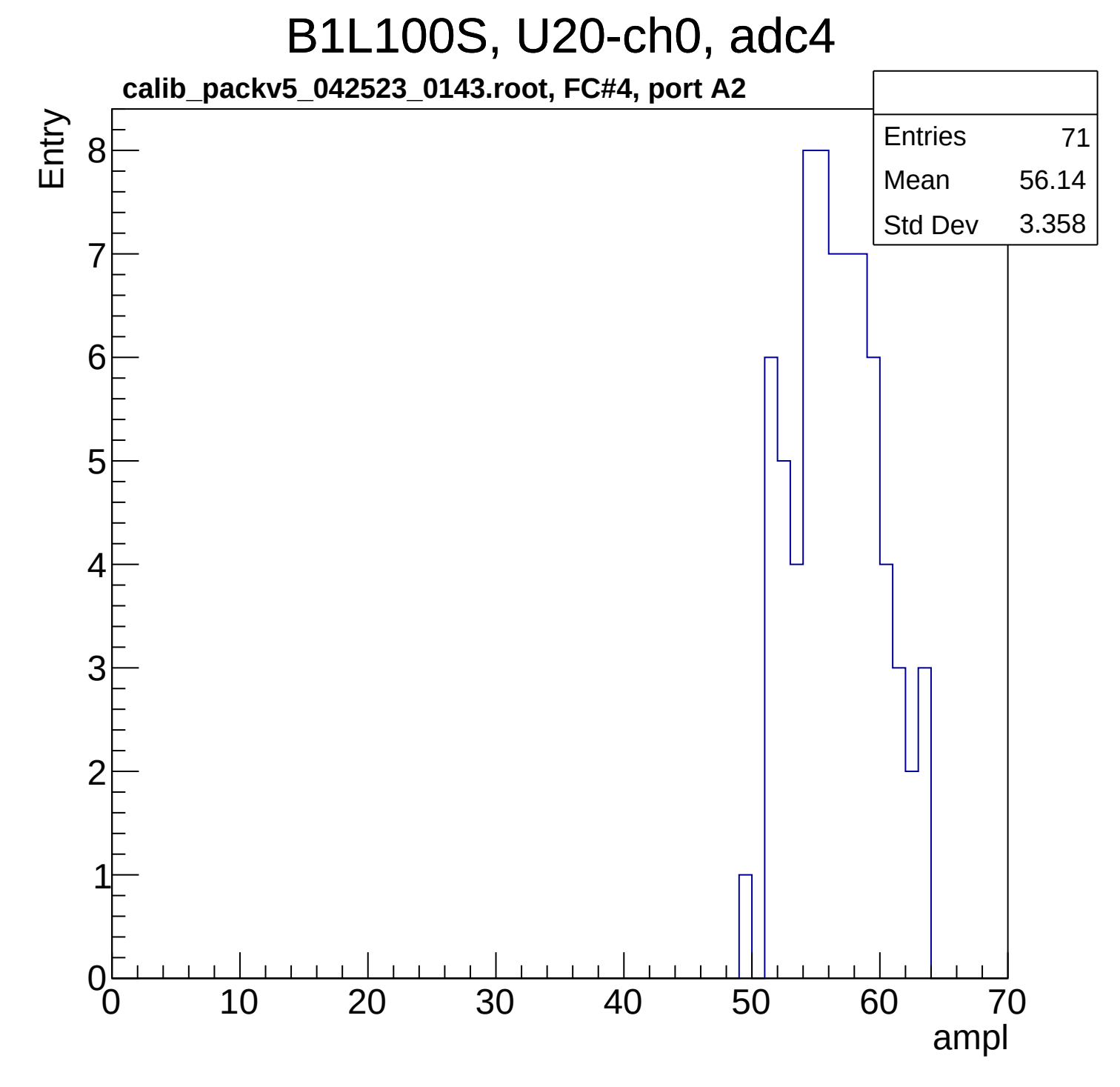
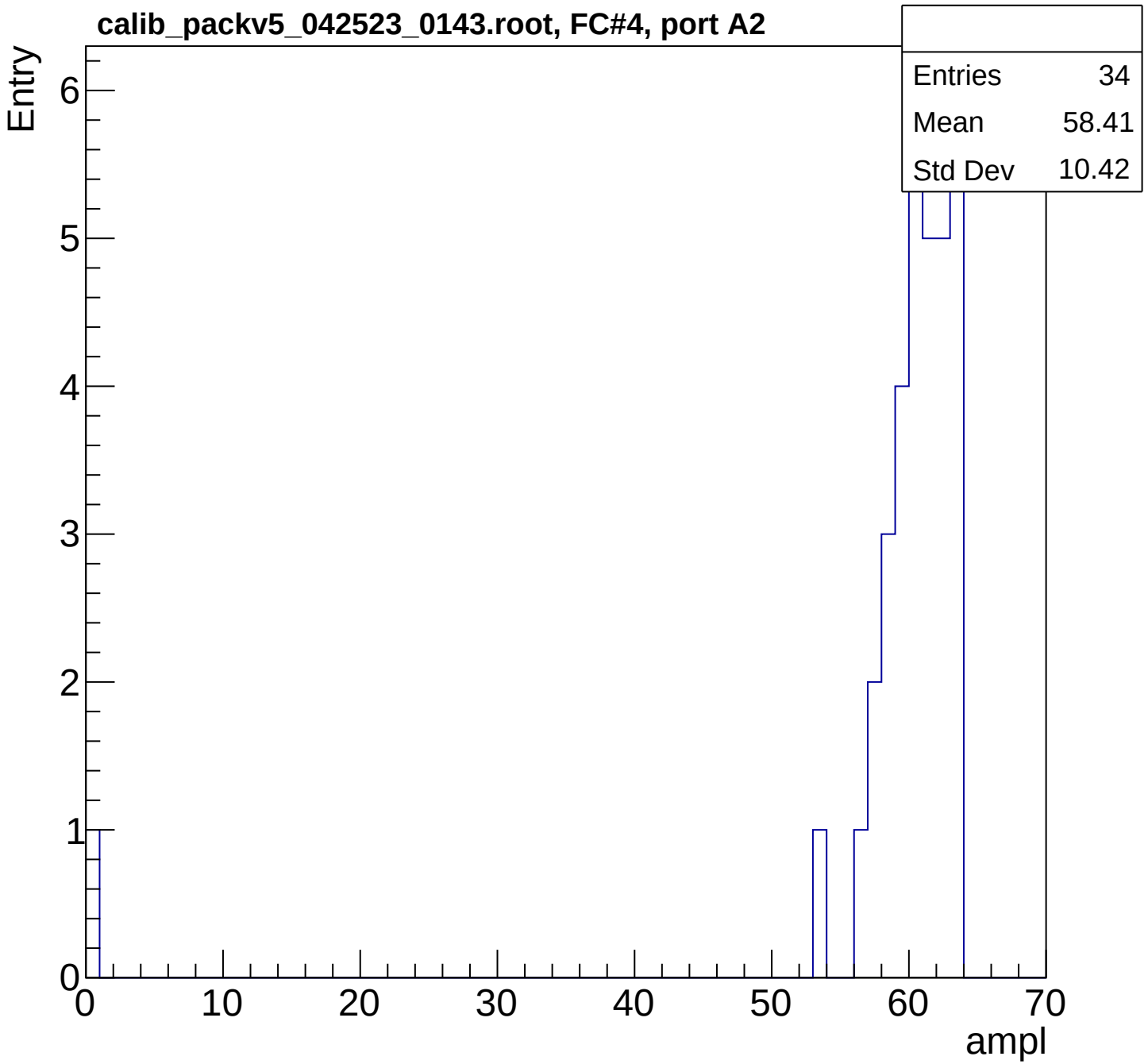

B1L100S, U20-ch0, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 56.14
Std Dev 3.358
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch0, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.41
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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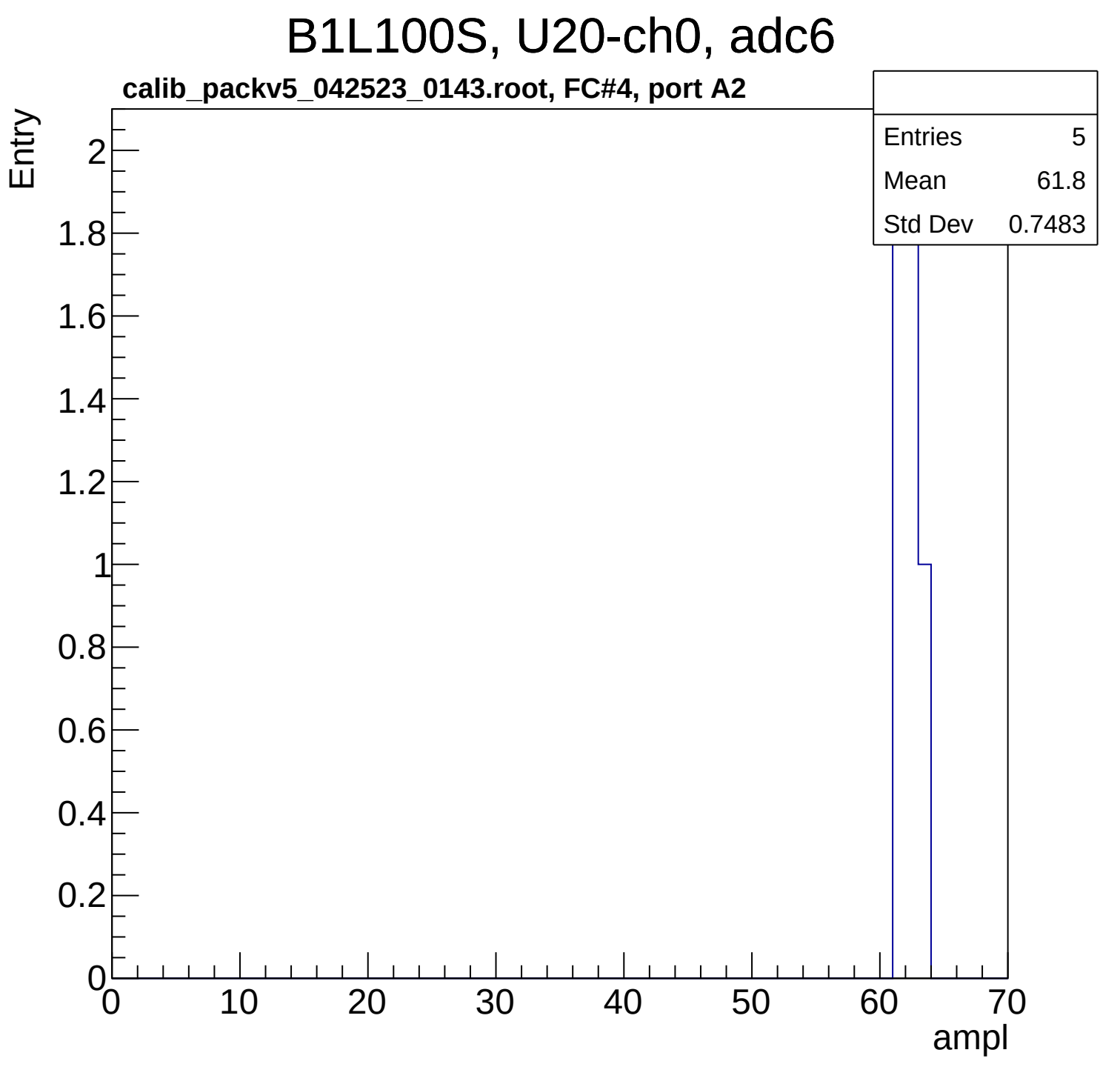
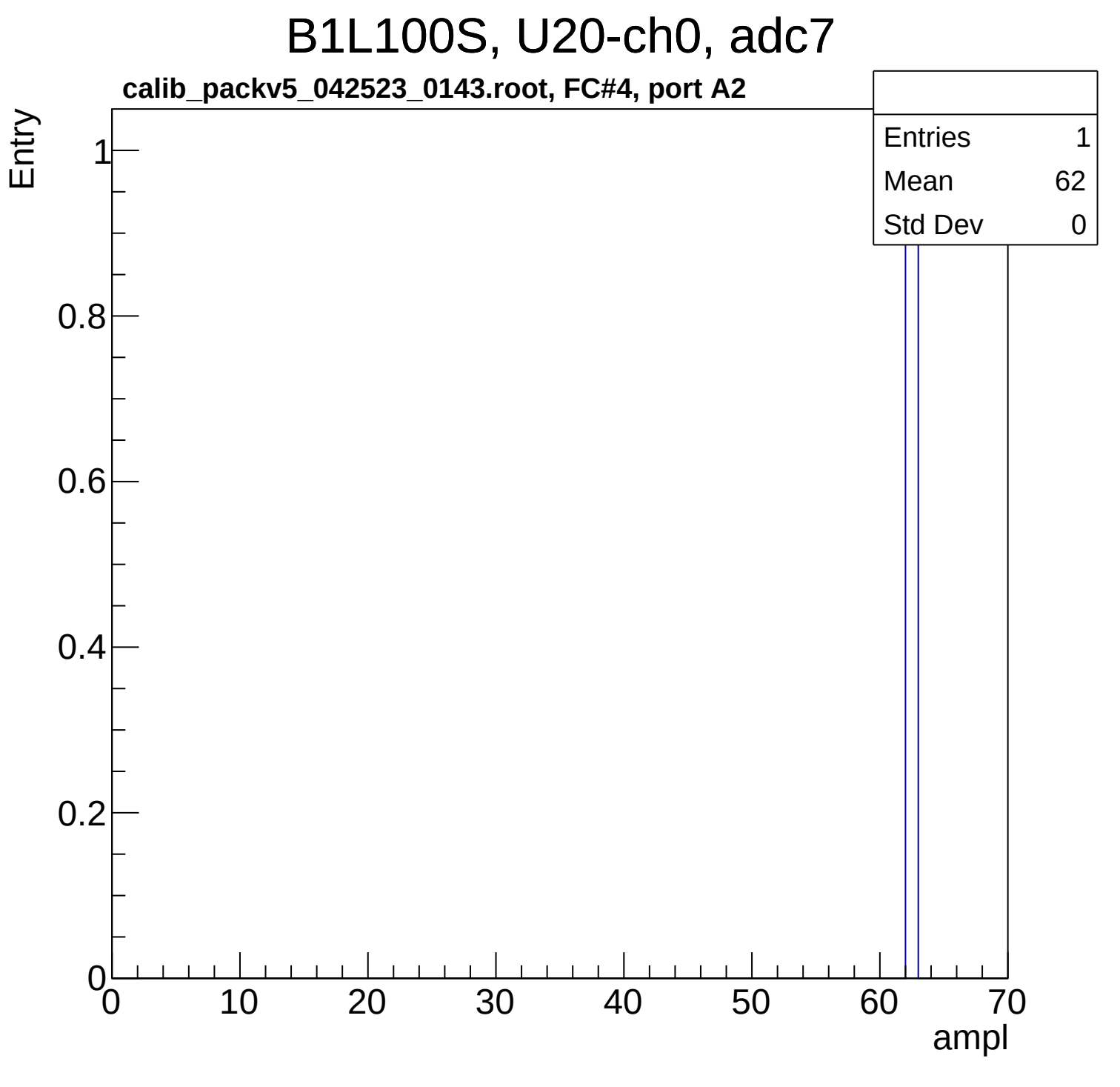

B1L100S, U20-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch0, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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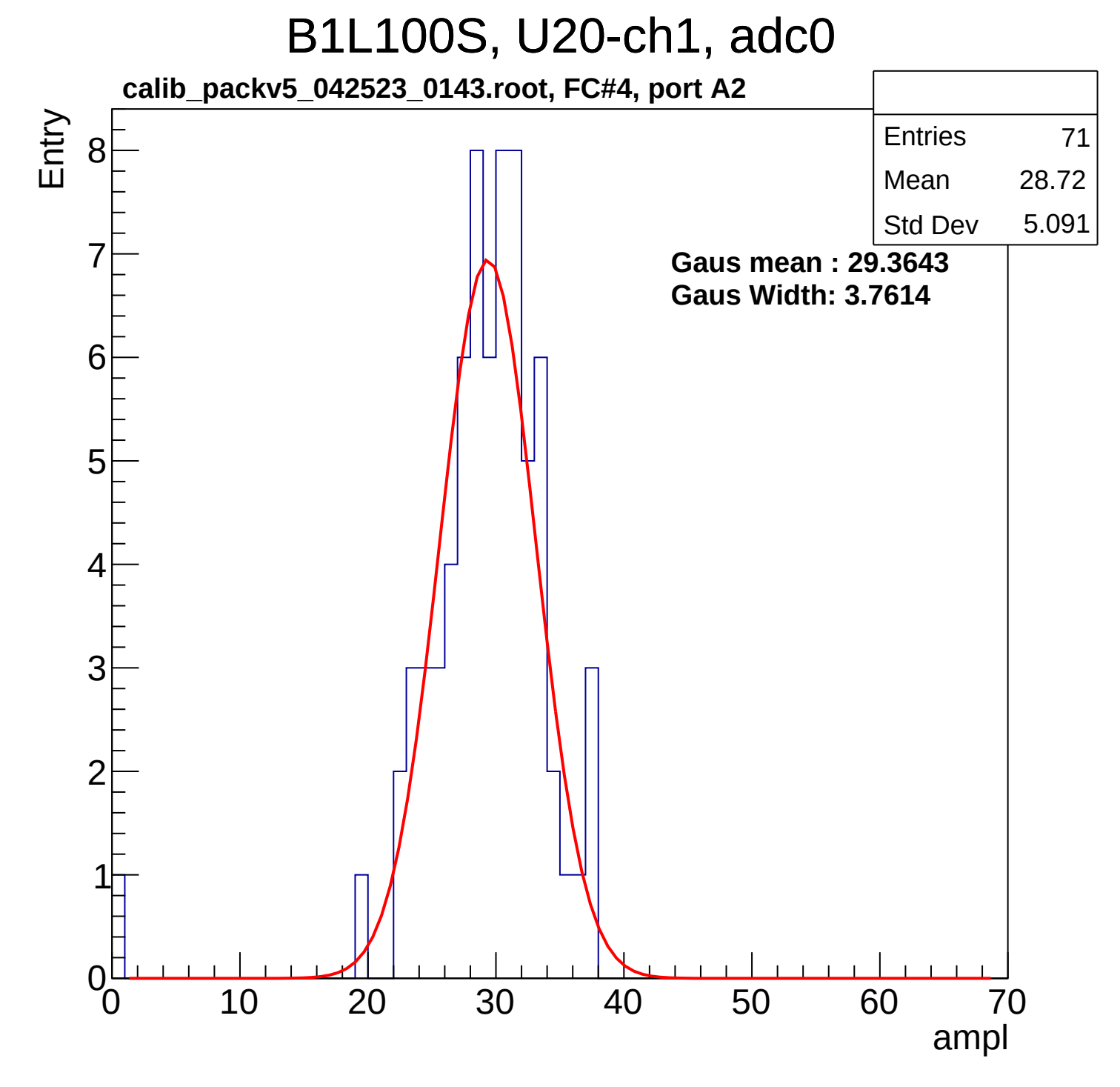
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 28.72
Std Dev 5.091
Gaus mean : 29.3643
Gaus Width: 3.7614
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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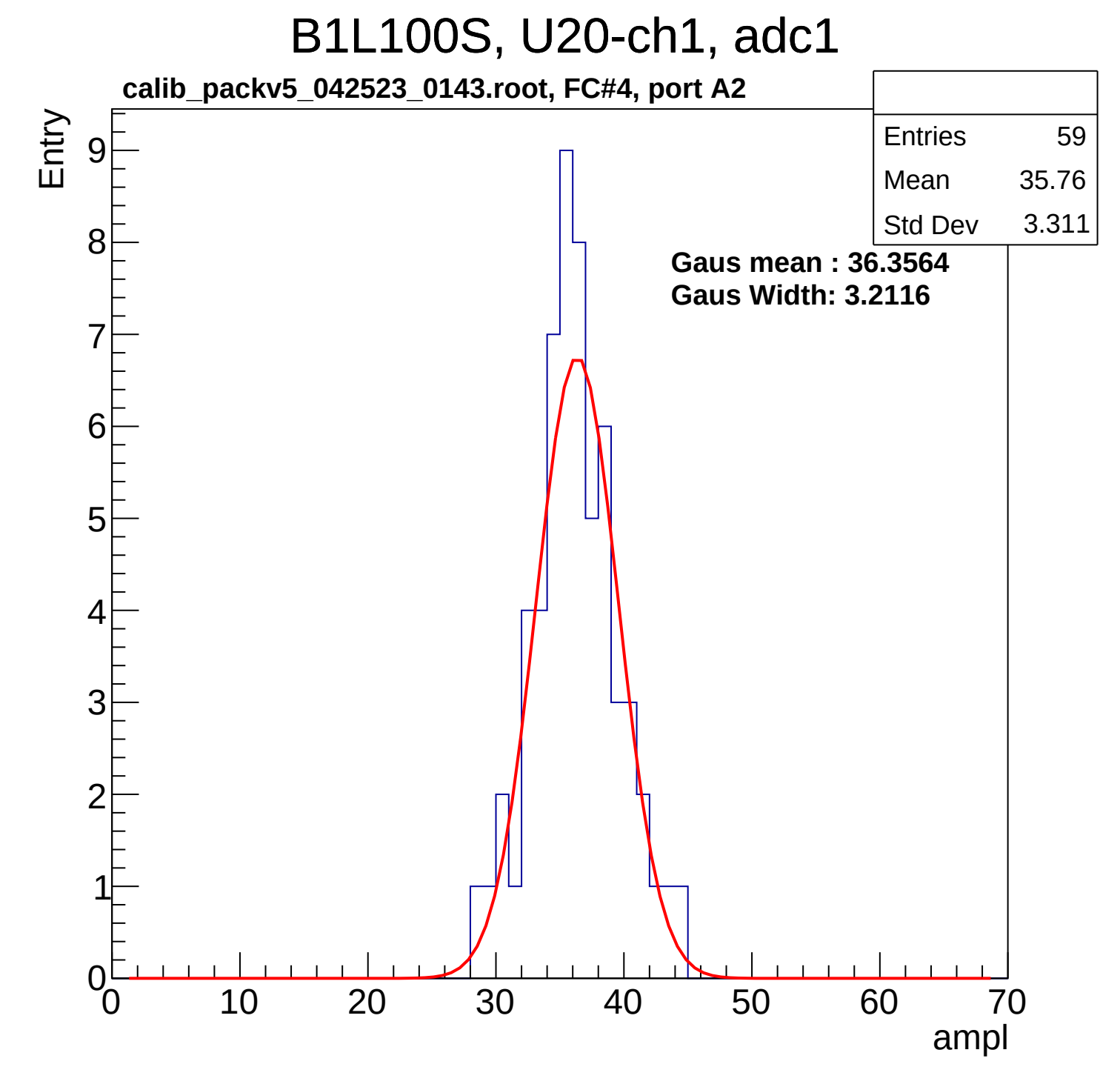
B1L100S, U20-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.76
Std Dev 3.311
Gaus mean : 36.3564
Gaus Width: 3.2116
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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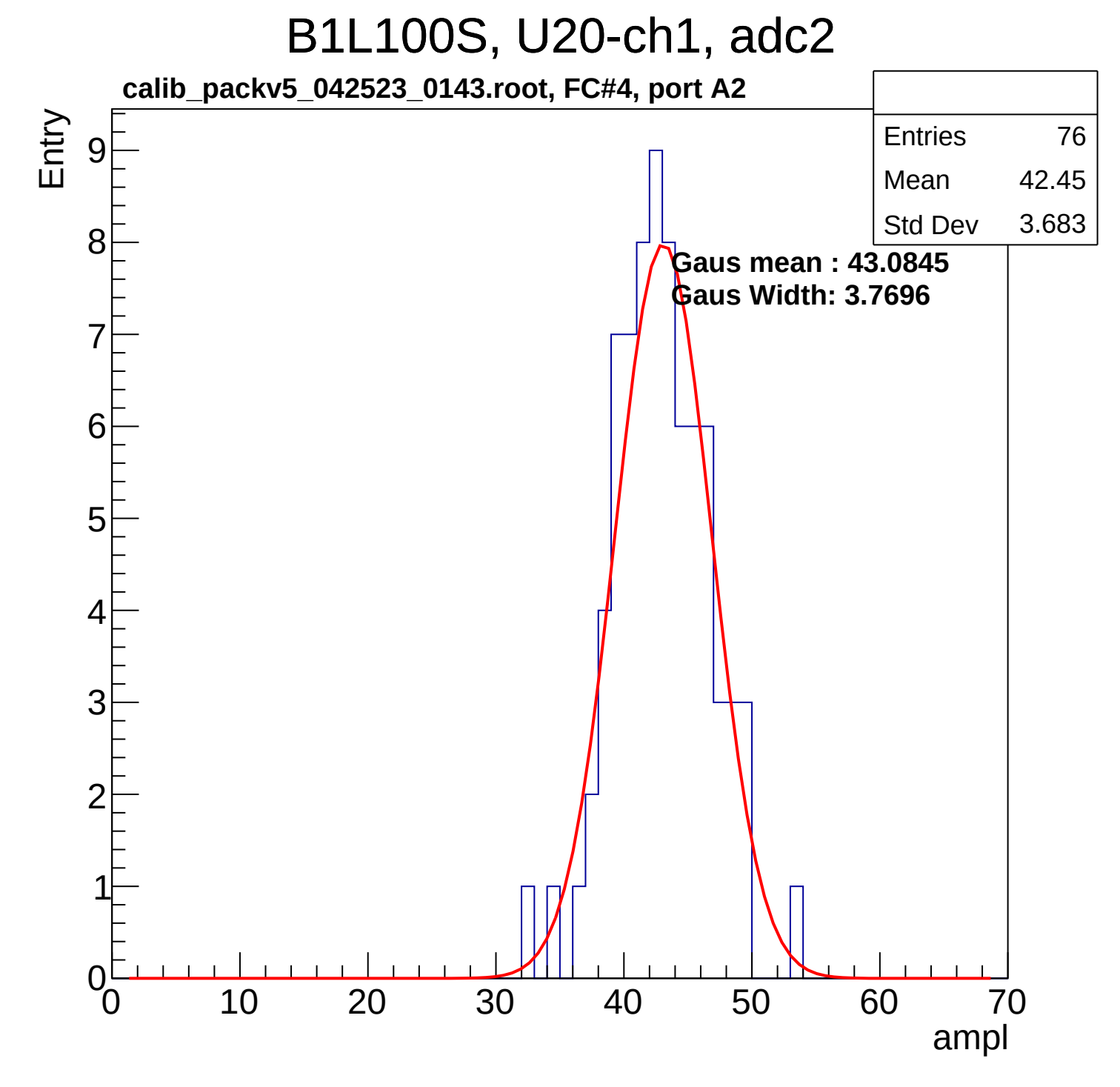
B1L100S, U20-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 42.45
Std Dev 3.683
Gaus mean : 43.0845
Gaus Width: 3.7696
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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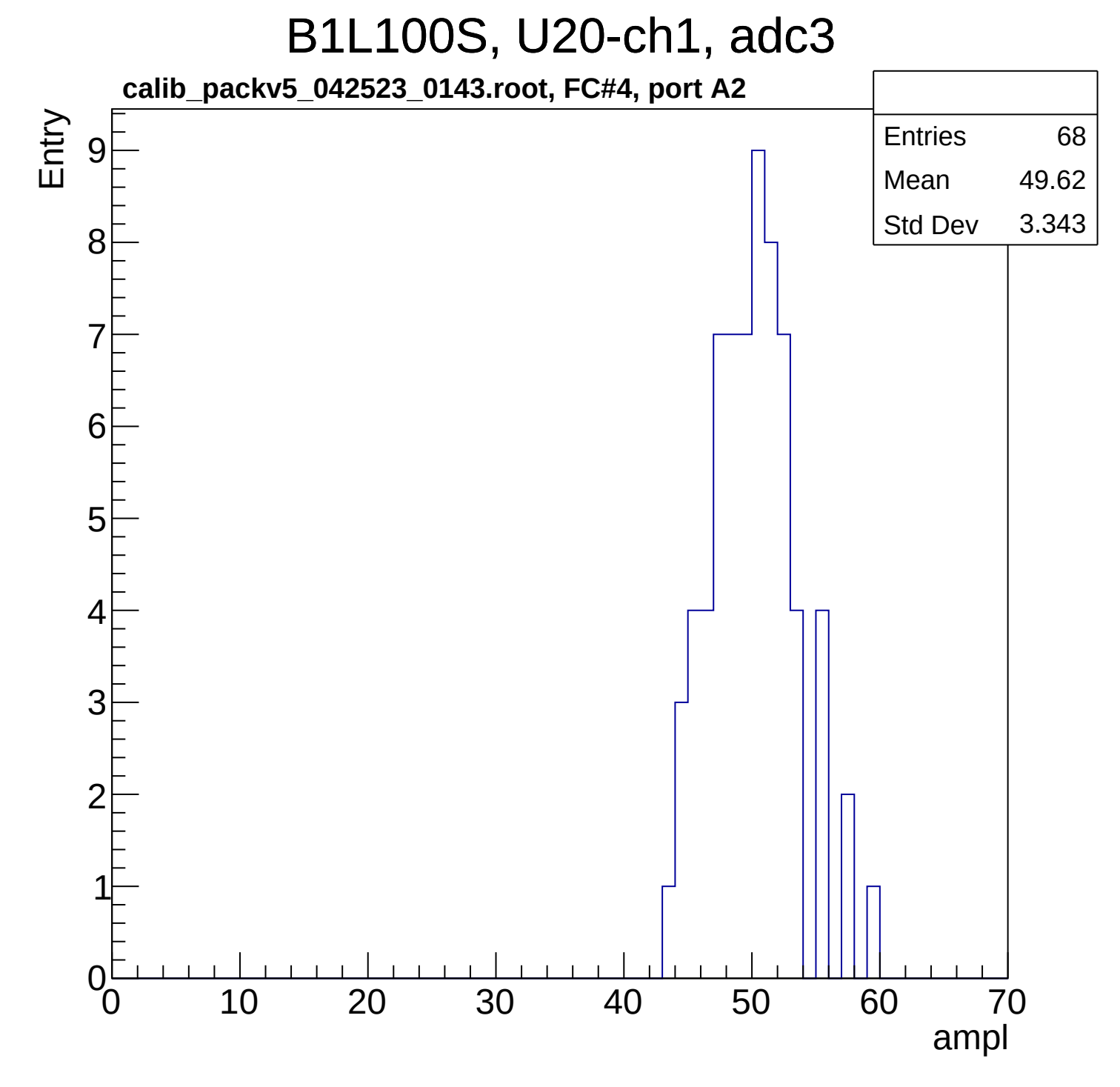
B1L100S, U20-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.62
Std Dev 3.343
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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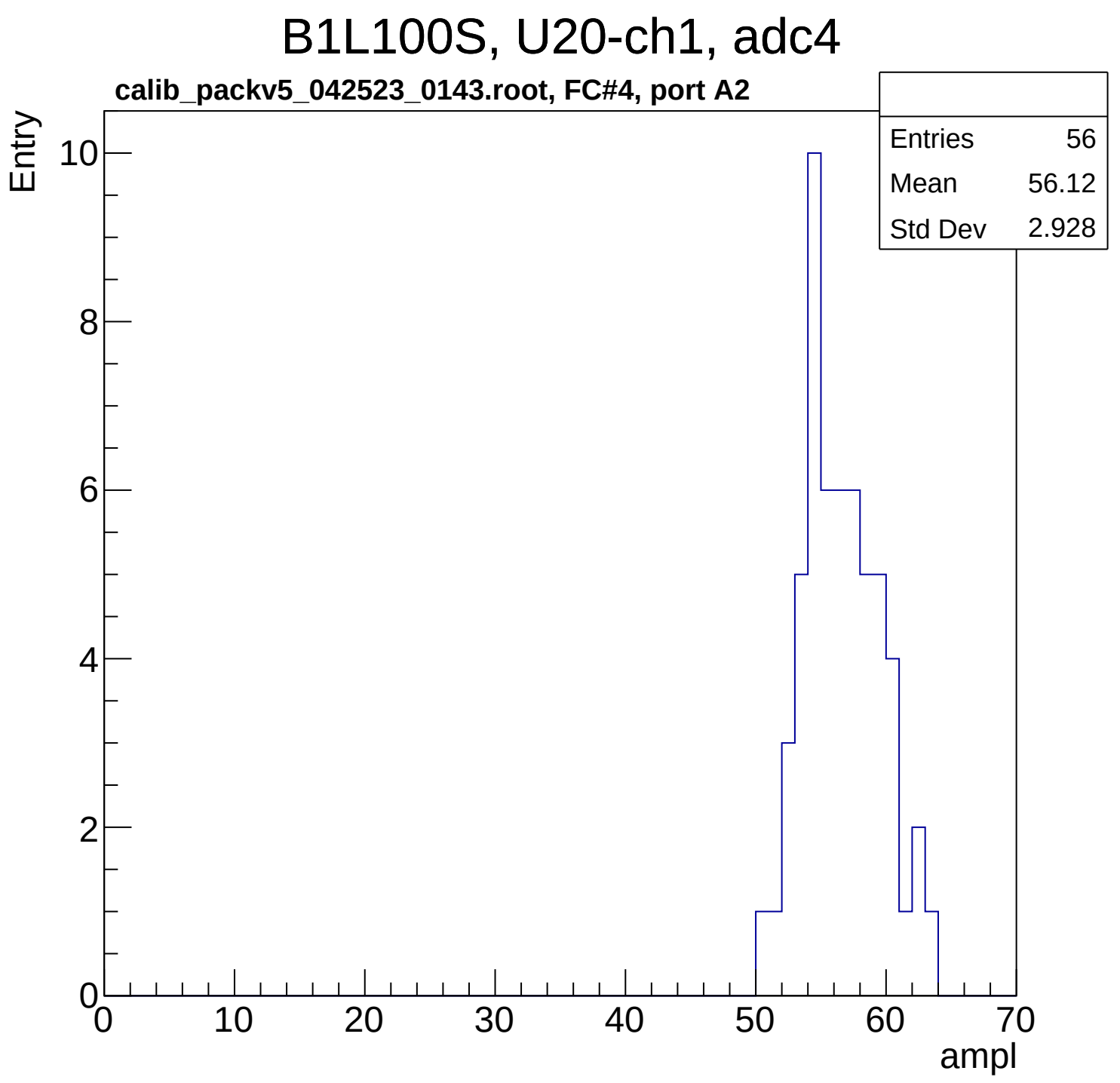
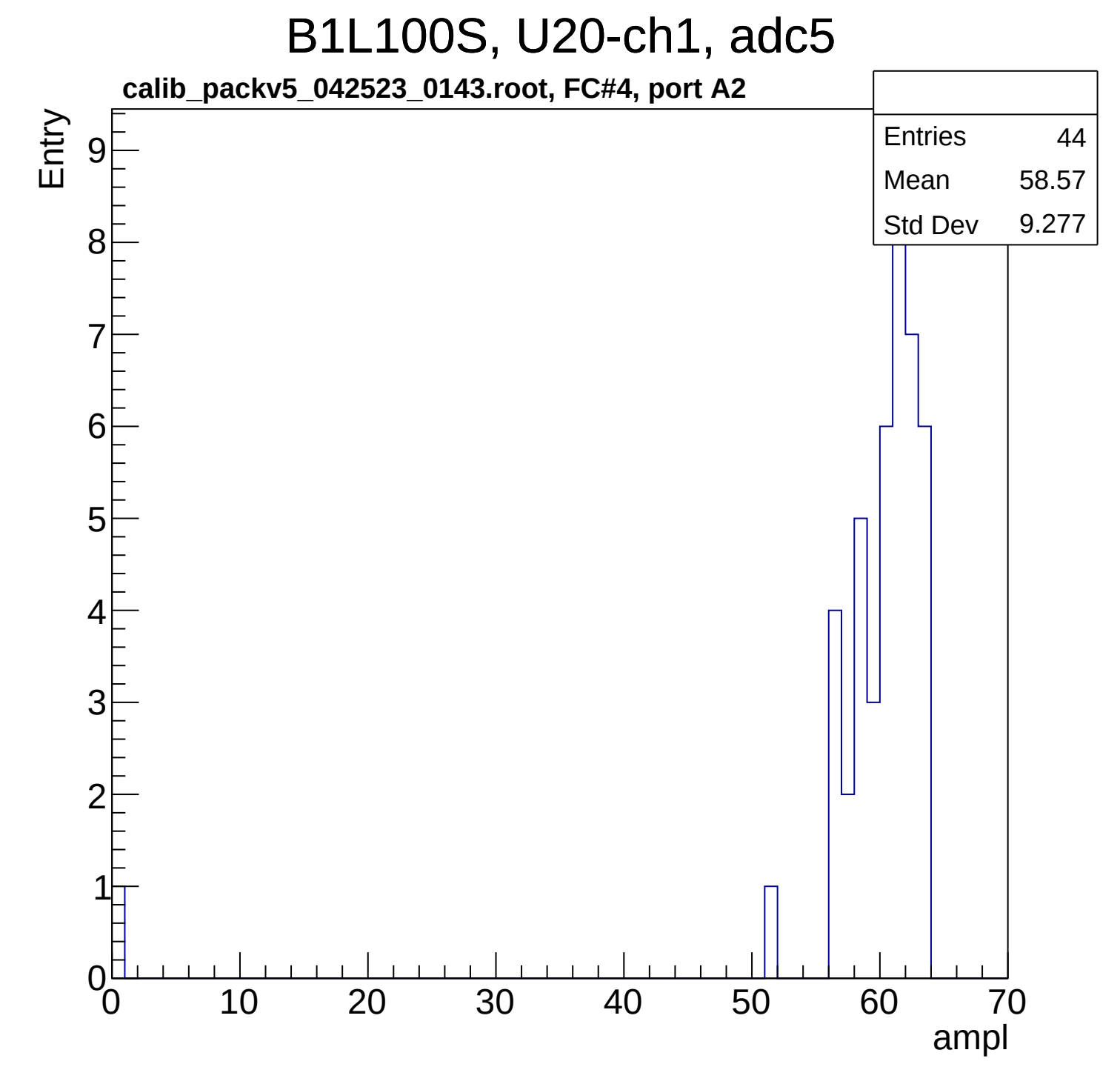

B1L100S, U20-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.12
Std Dev 2.928
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.57
Std Dev 9.277
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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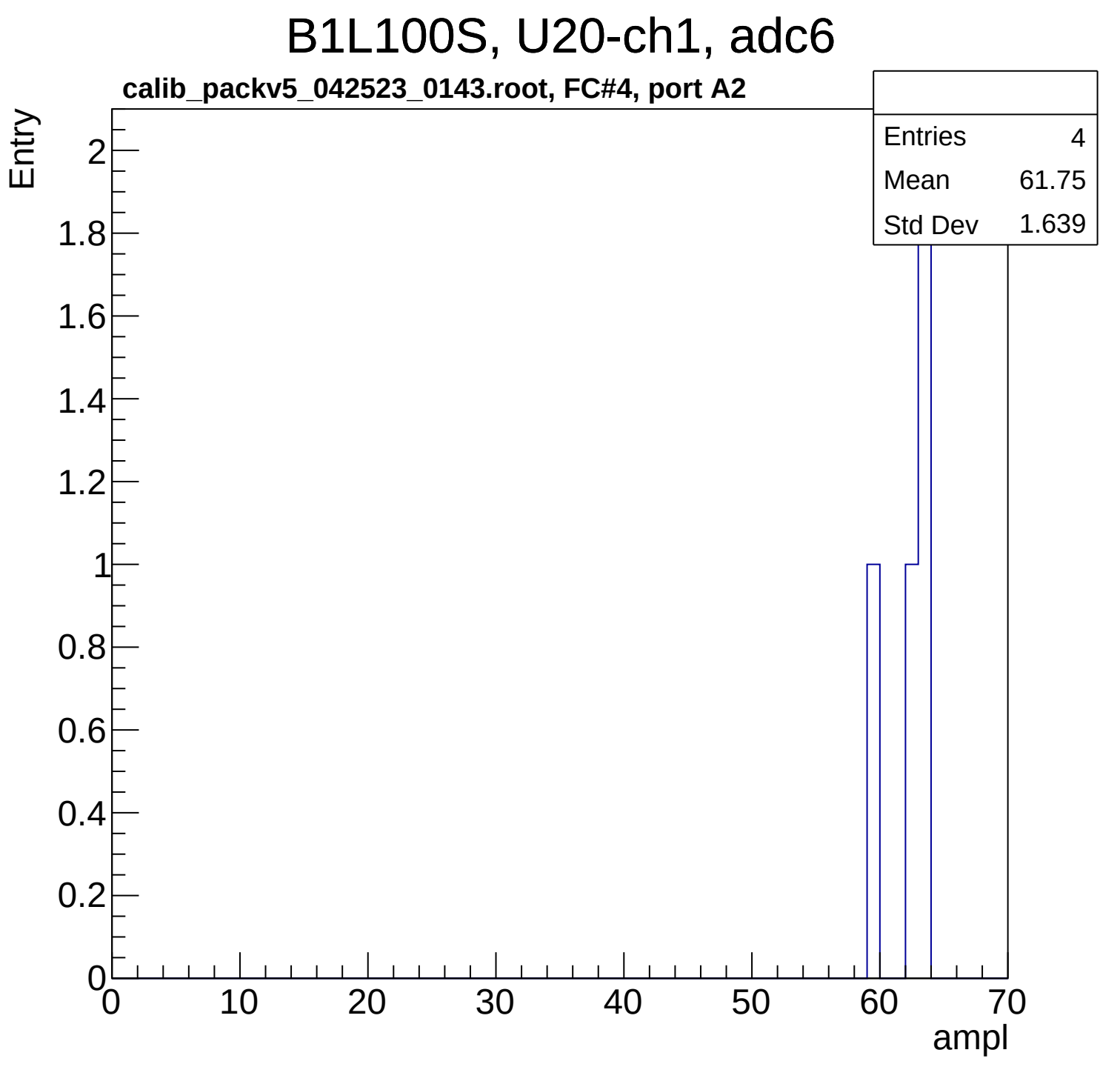
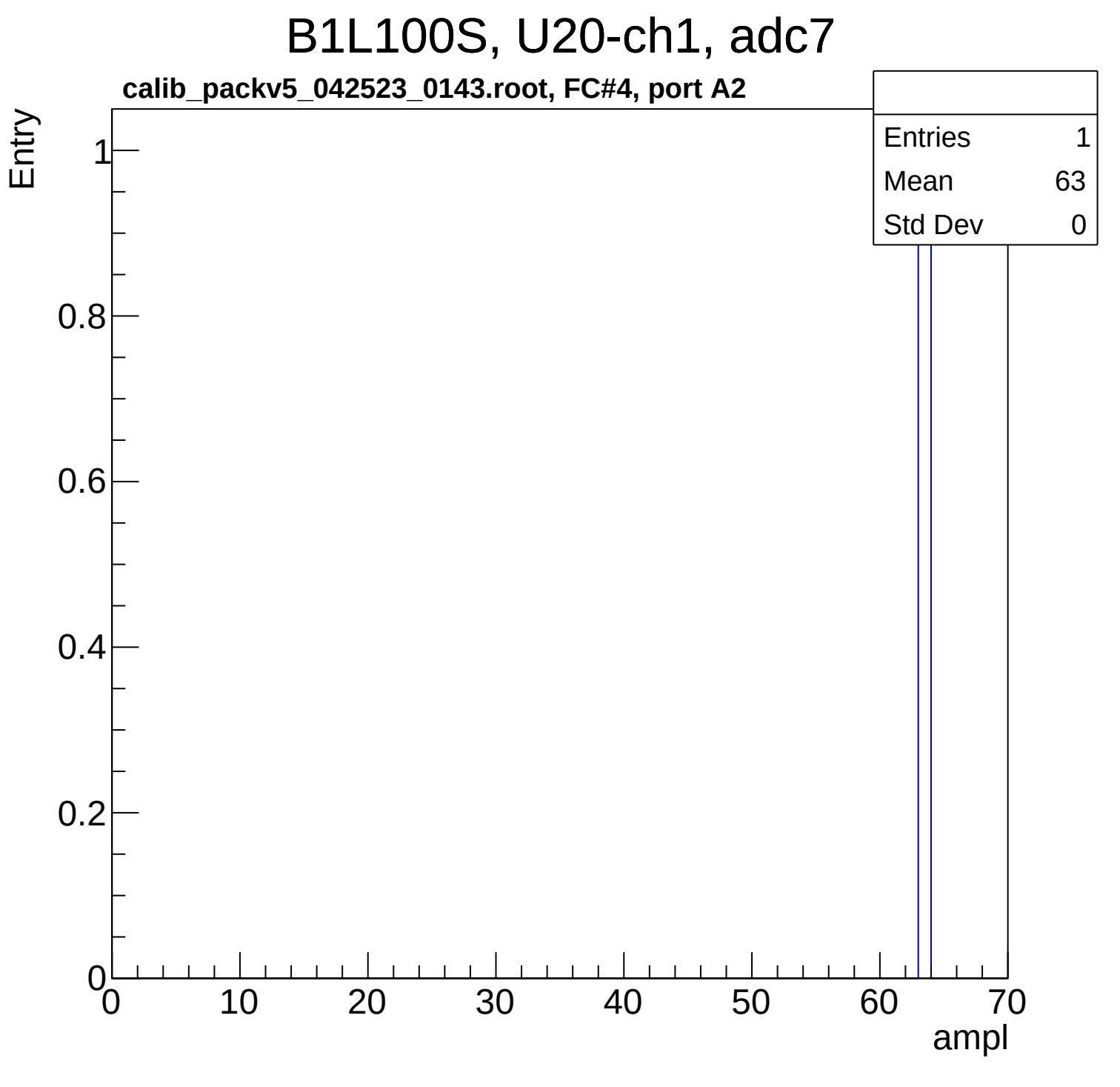
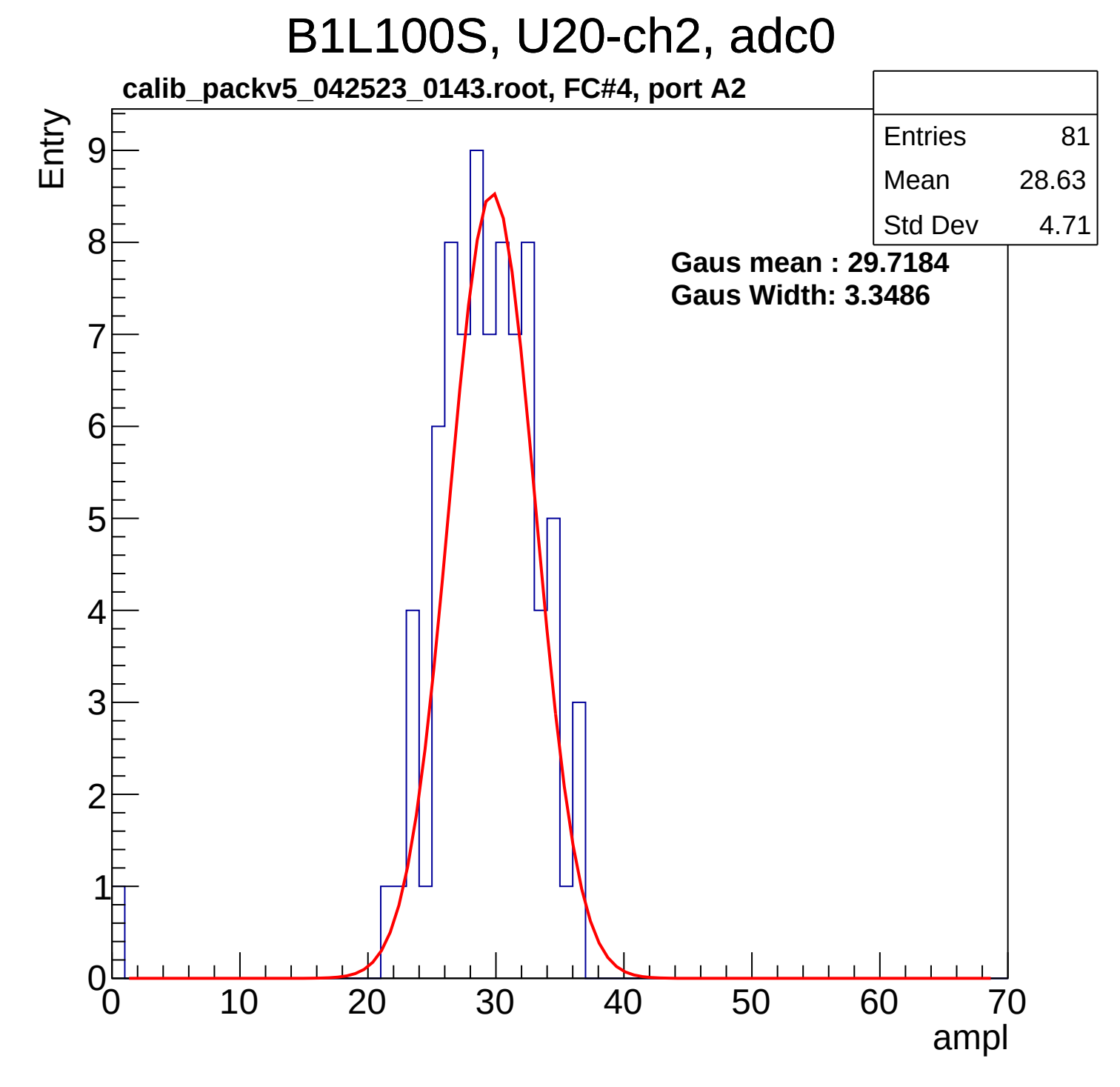

B1L100S, U20-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 1.639
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch1, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 28.63
Std Dev 4.71
Gaus mean : 29.7184
Gaus Width: 3.3486
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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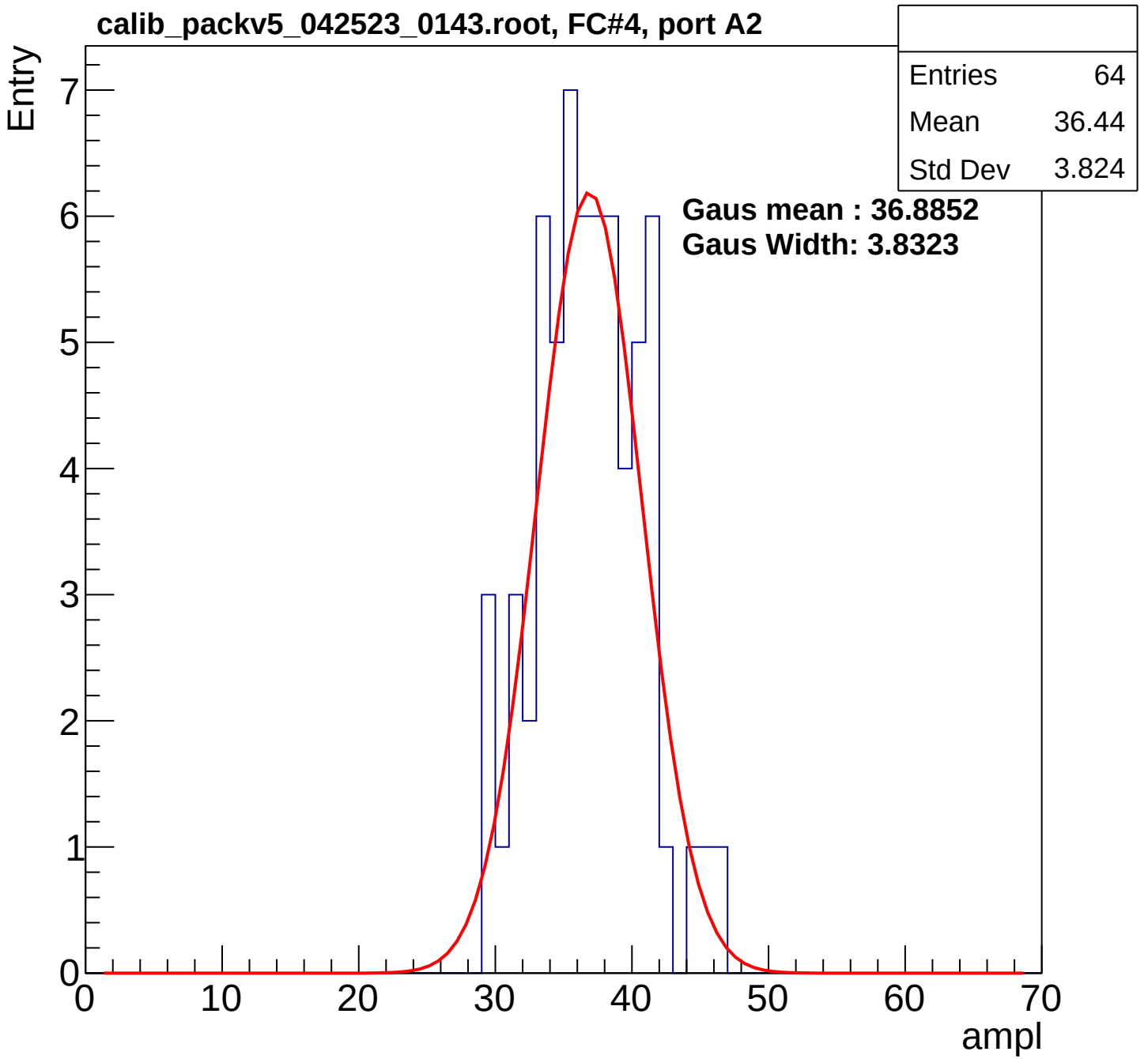
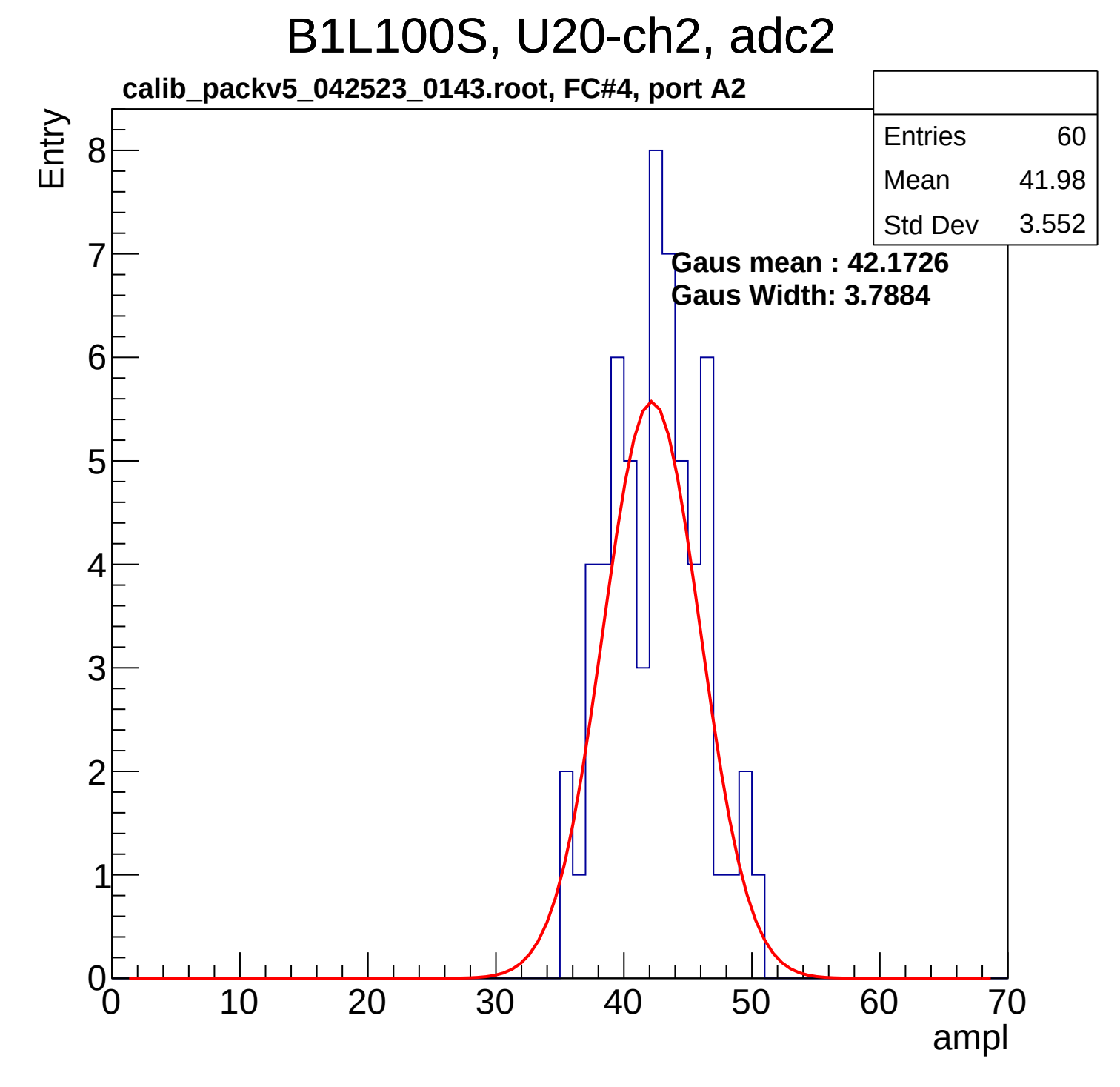

B1L100S, U20-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.44
Std Dev 3.824
Gaus mean : 36.8852
Gaus Width: 3.8323
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.98
Std Dev 3.552
Gaus mean : 42.1726
Gaus Width: 3.7884
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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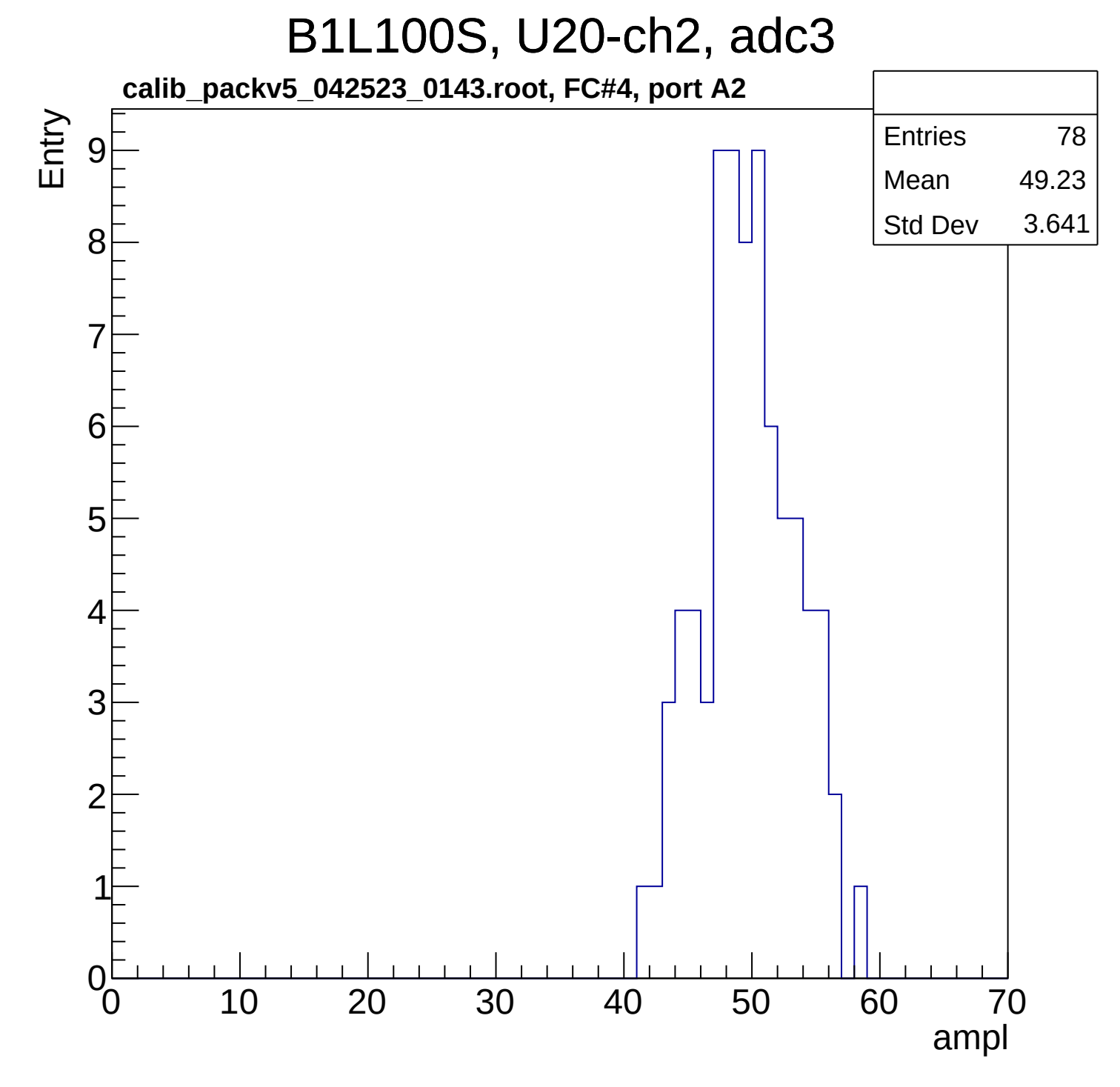
B1L100S, U20-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 49.23
Std Dev 3.641
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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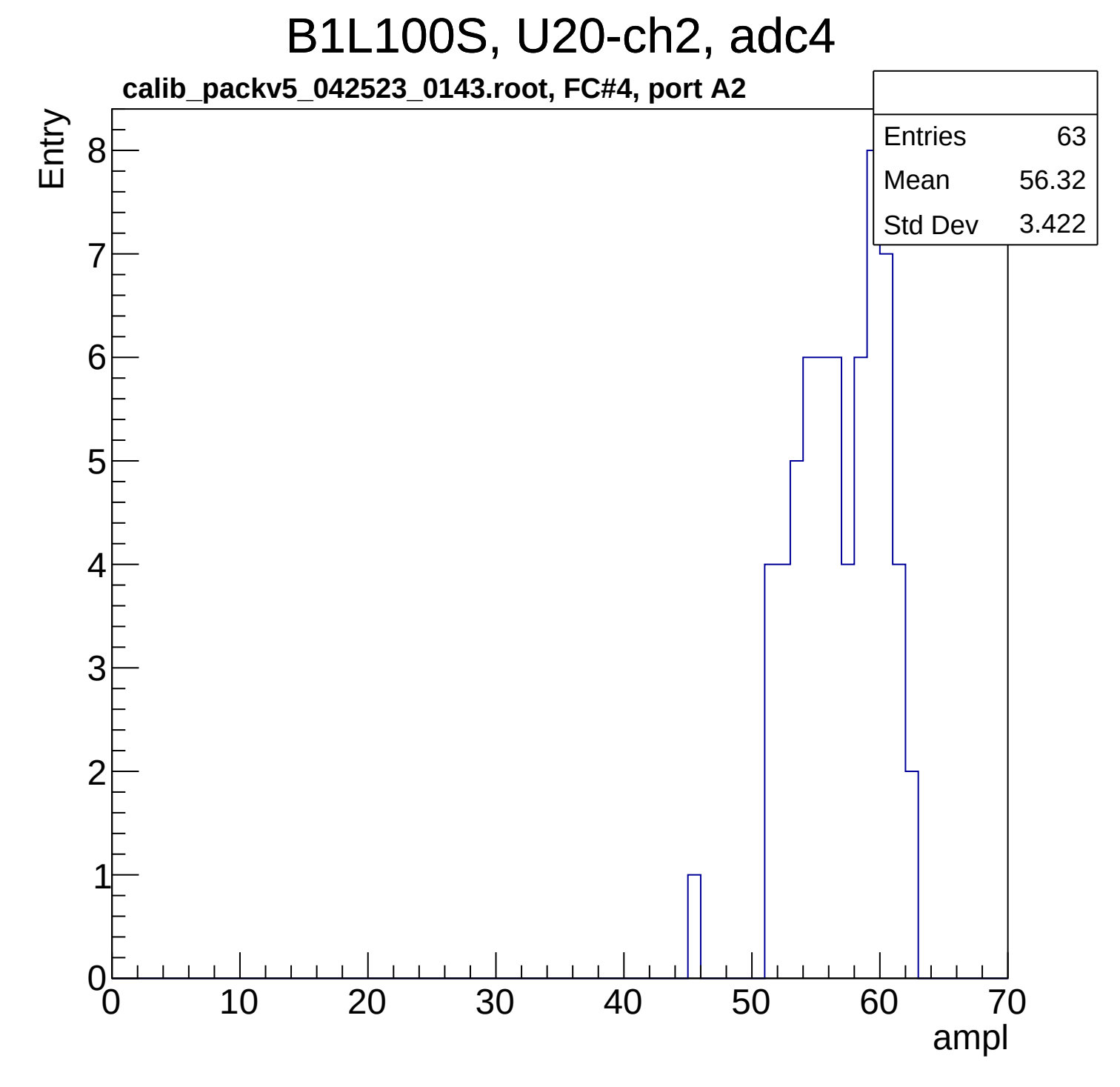
B1L100S, U20-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.32
Std Dev 3.422
Entry
ampl
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4
5
6
7
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0
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20
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60
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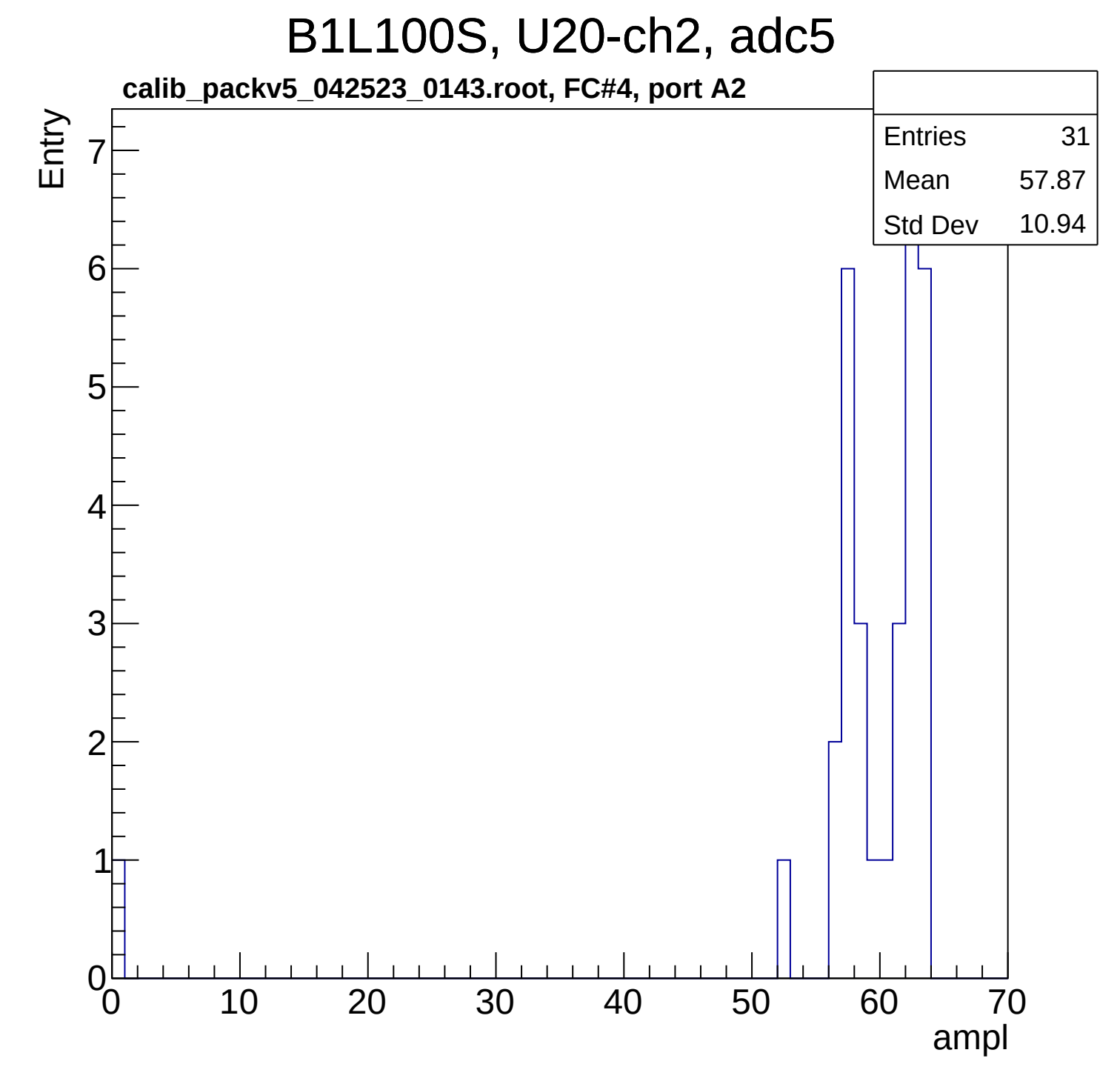
B1L100S, U20-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 57.87
Std Dev 10.94
Entry
ampl
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4
5
6
7
0
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20
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40
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60
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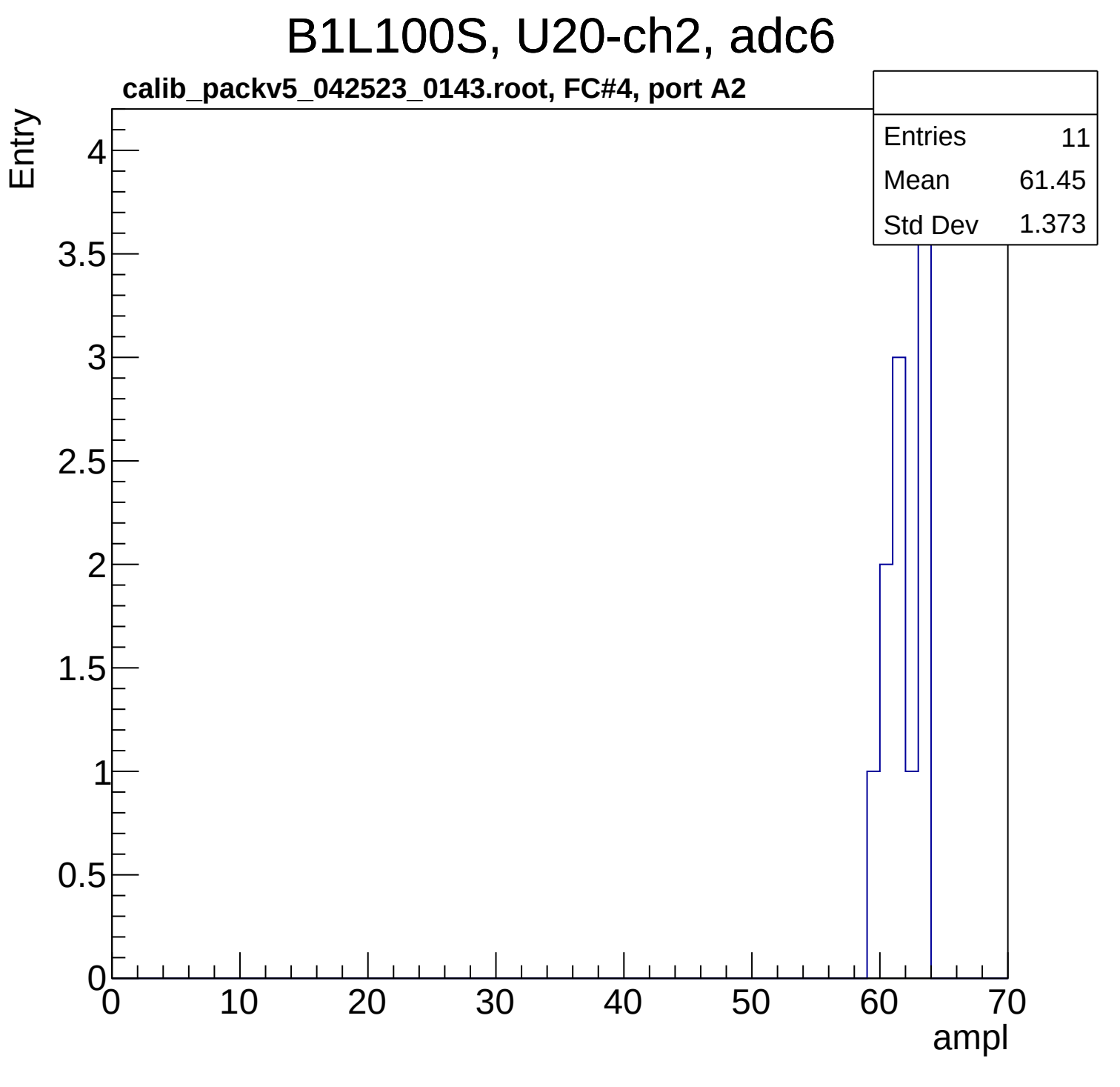
B1L100S, U20-ch2, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.45
Std Dev 1.373
Entry
ampl
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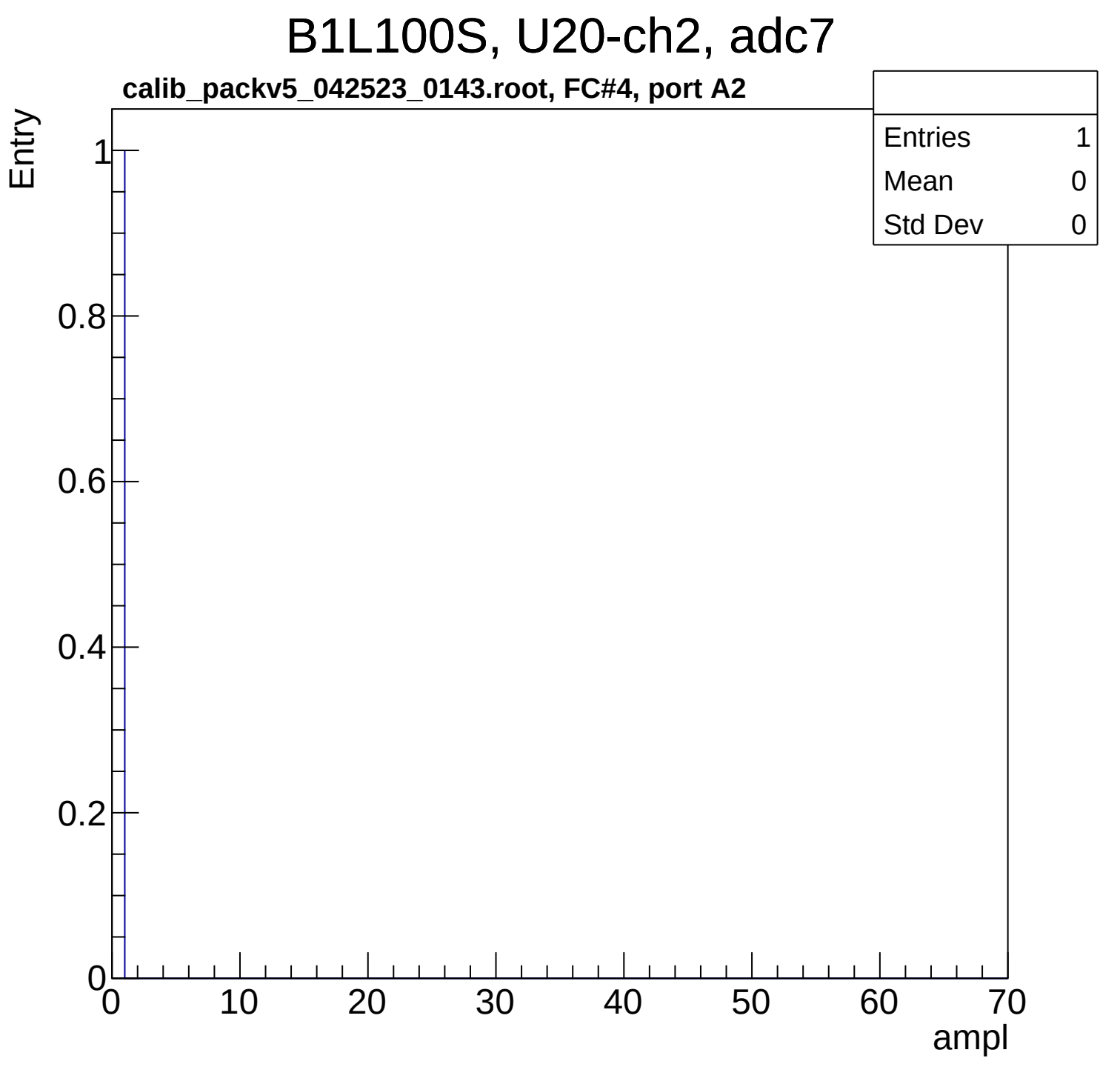
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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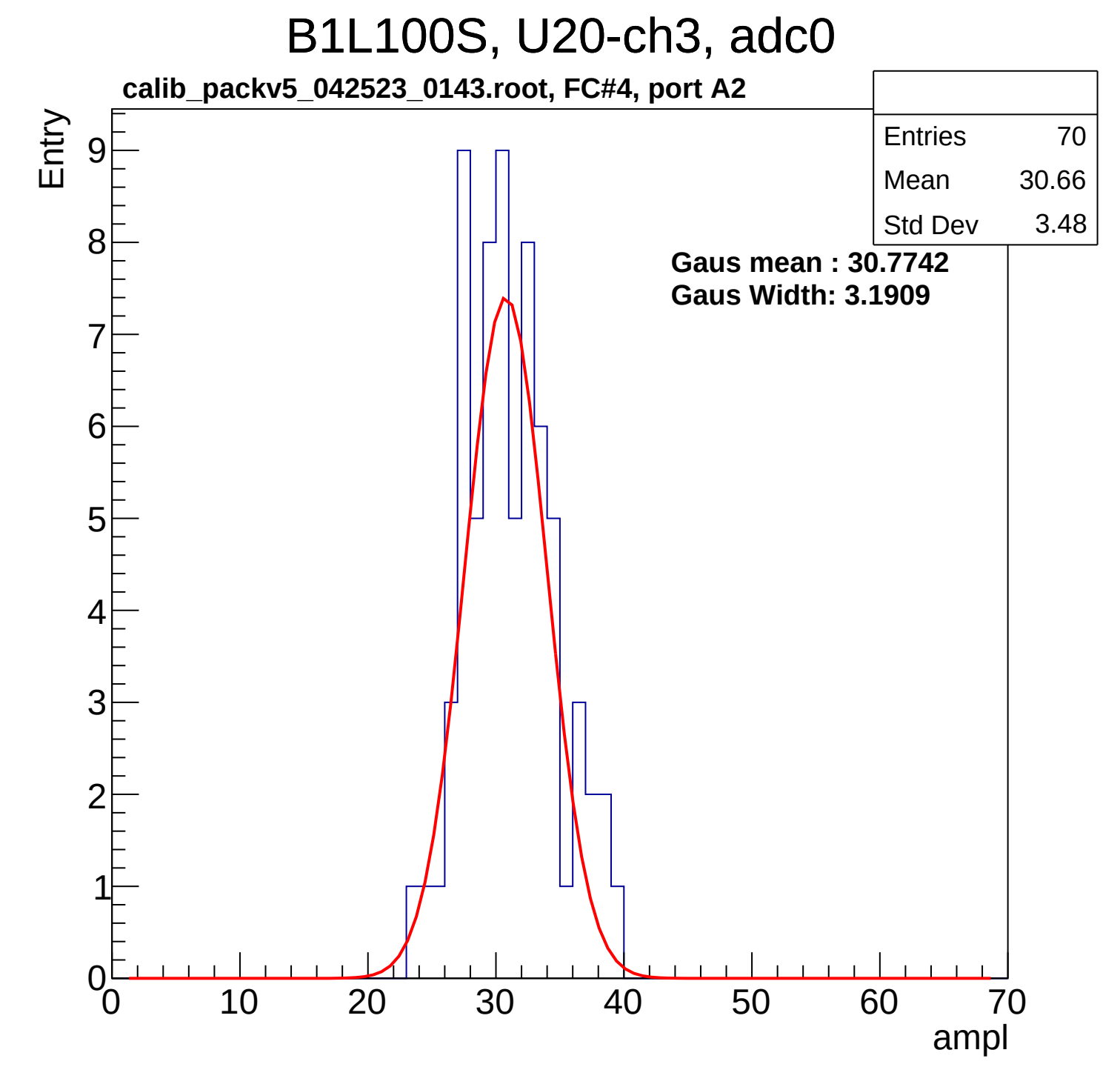
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 30.66
Std Dev 3.48
Gaus mean : 30.7742
Gaus Width: 3.1909
Entry
ampl
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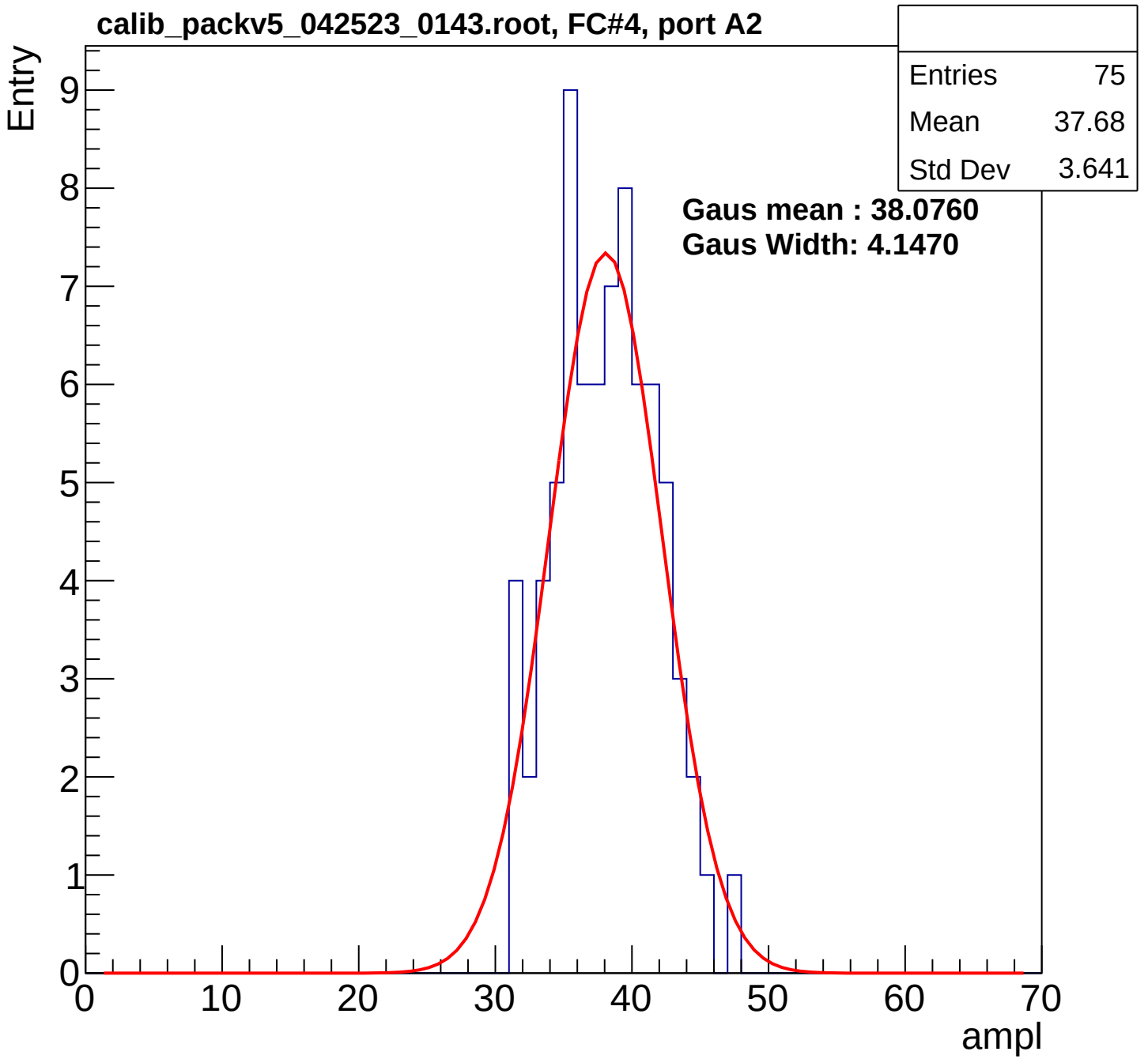
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.68
Std Dev 3.641
Gaus mean : 38.0760
Gaus Width: 4.1470
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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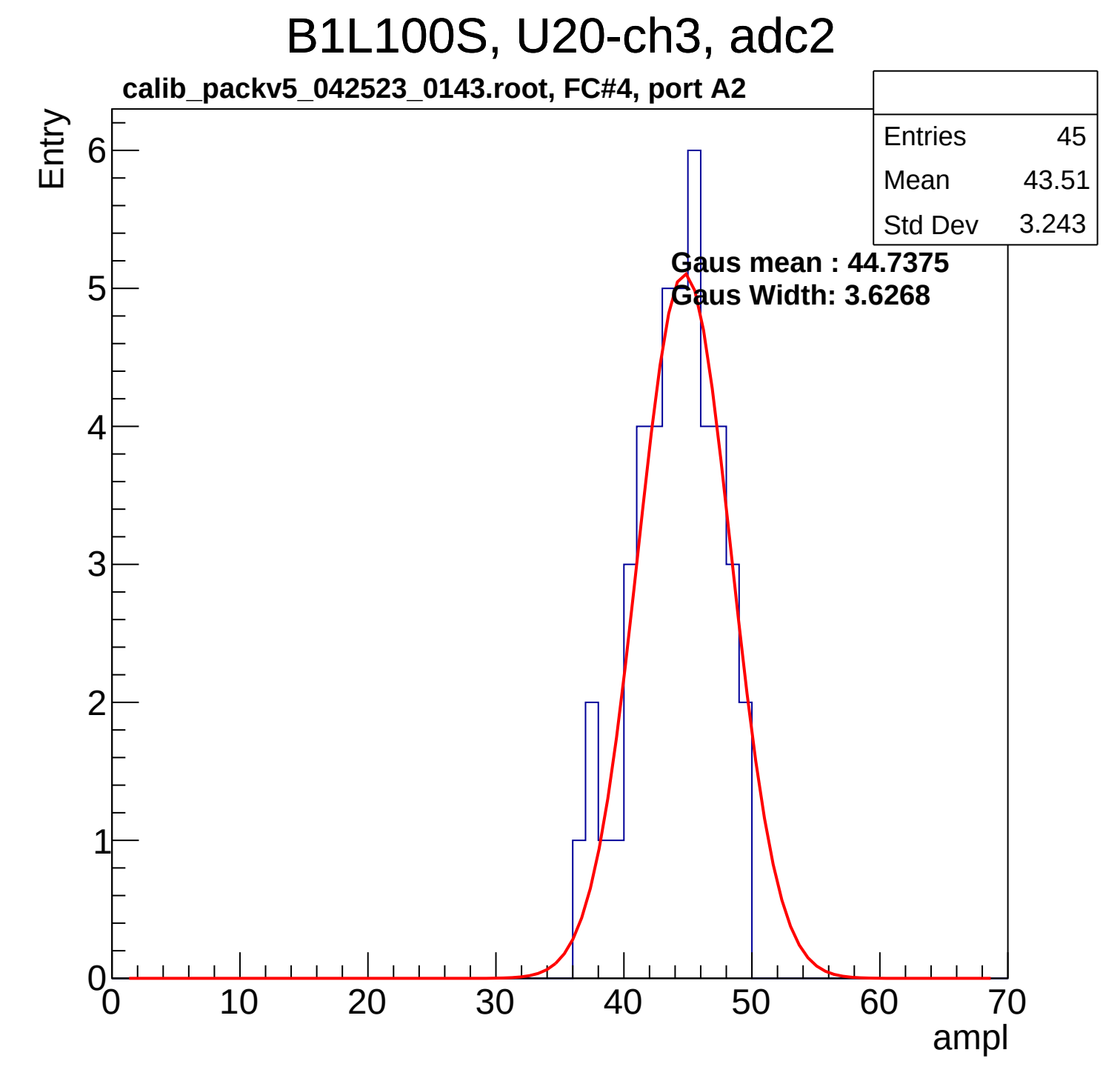
B1L100S, U20-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 43.51
Std Dev 3.243
Gaus mean : 44.7375
Gaus Width: 3.6268
Entry
ampl
0
1
2
3
4
5
6
0
10
20
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40
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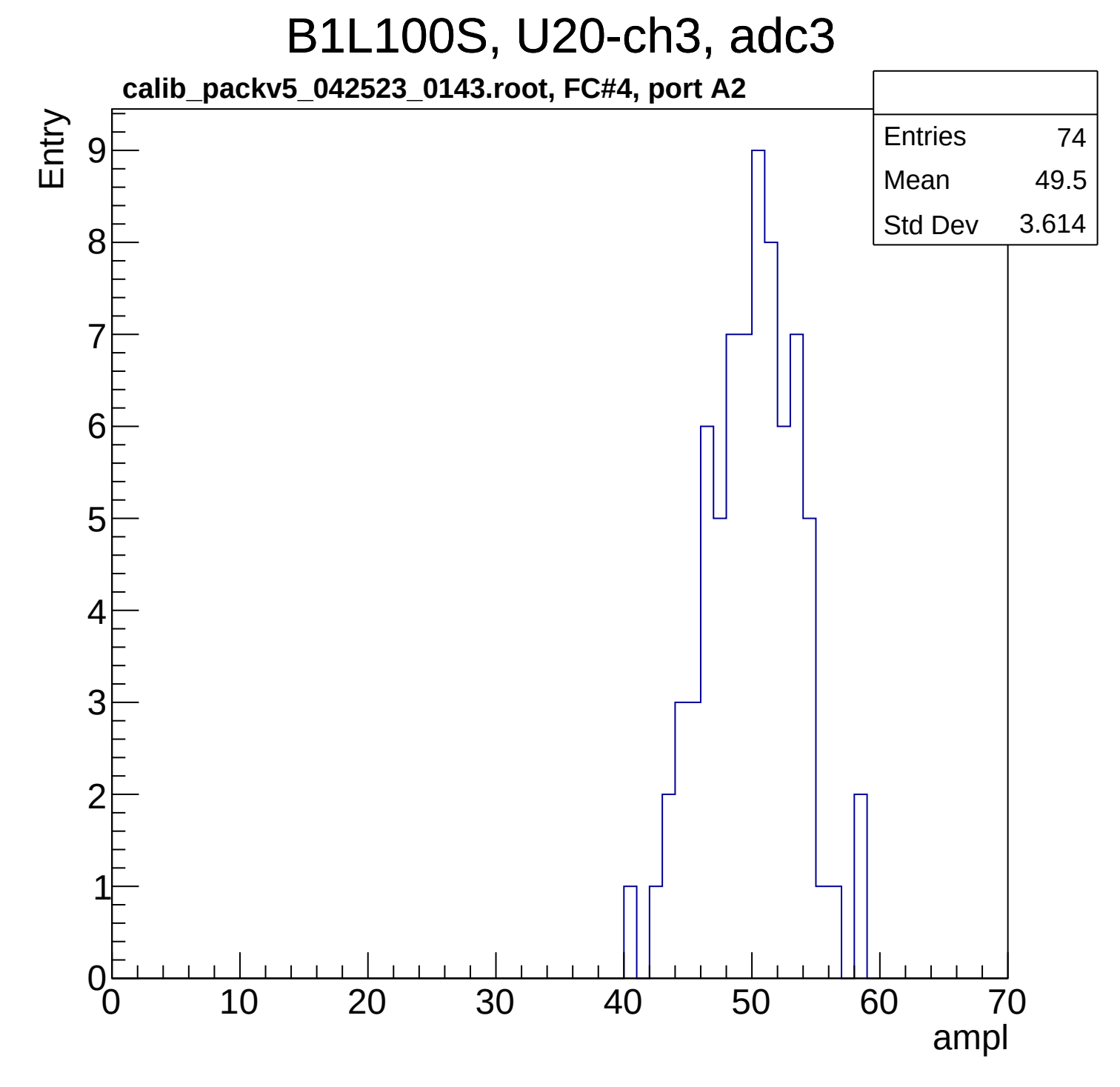
60
70


B1L100S, U20-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.5
Std Dev 3.614
Entry
ampl
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3
4
5
6
7
8
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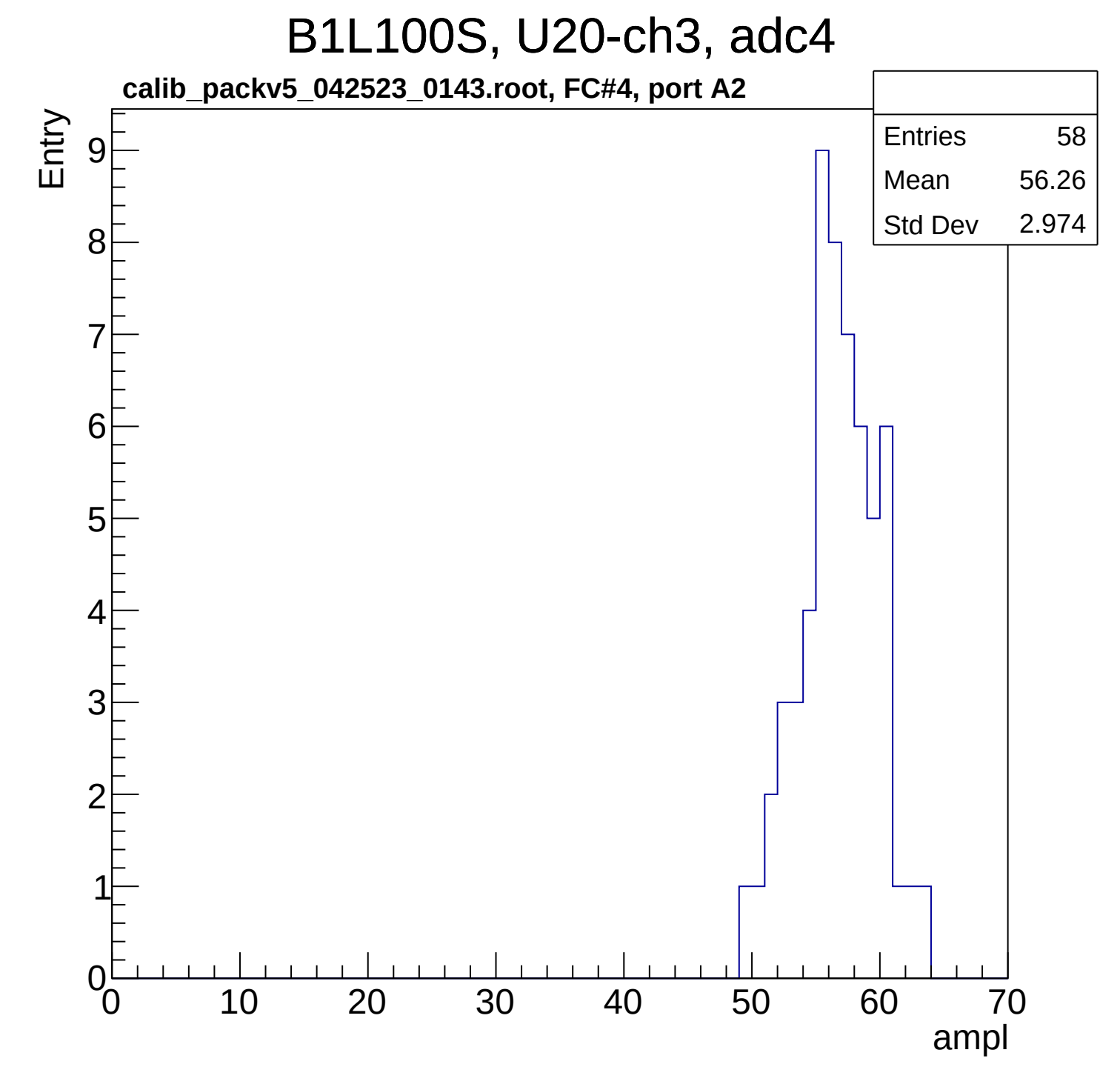
B1L100S, U20-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.26
Std Dev 2.974
Entry
ampl
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3
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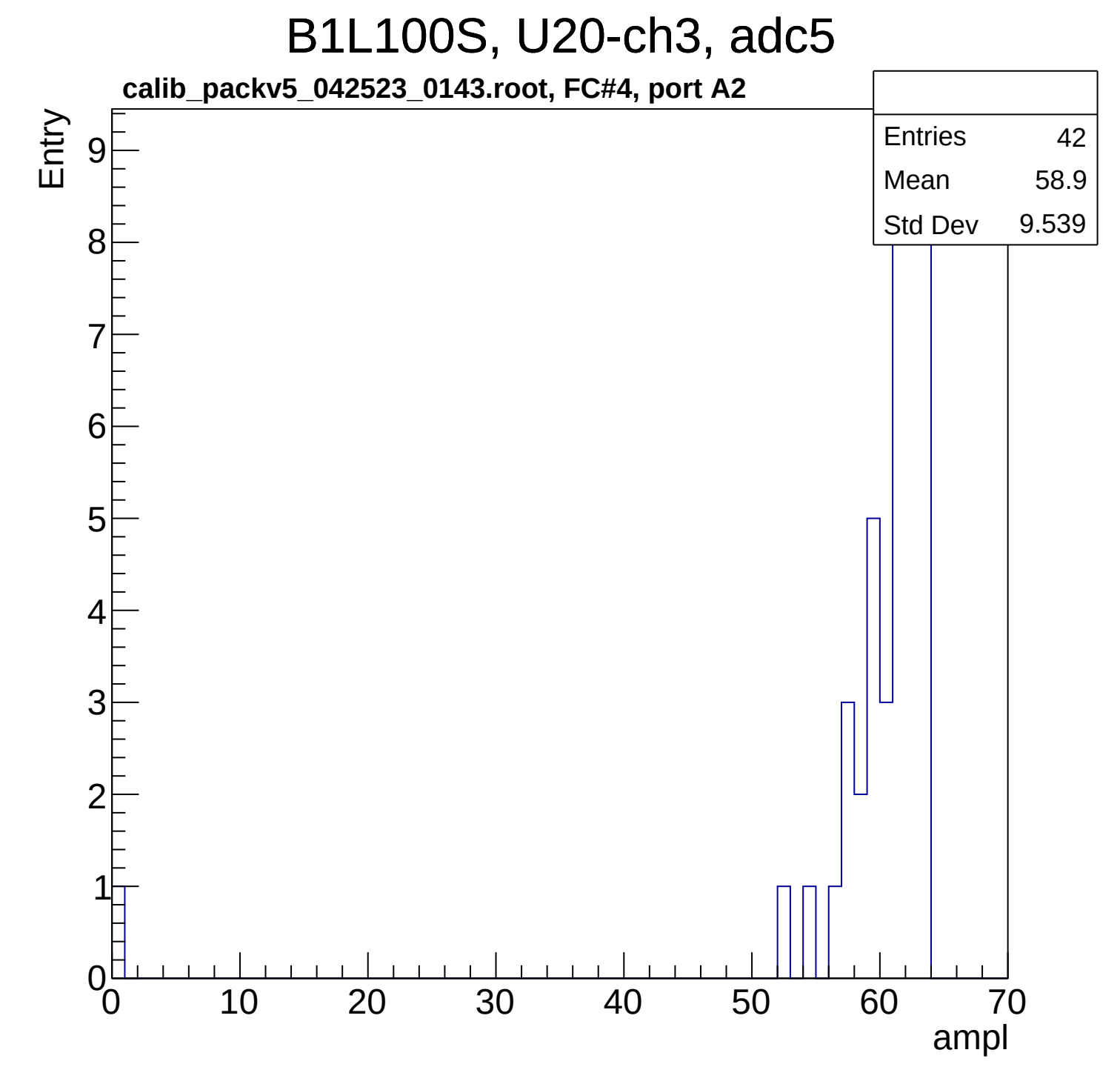
B1L100S, U20-ch3, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.9
Std Dev 9.539
Entry
ampl
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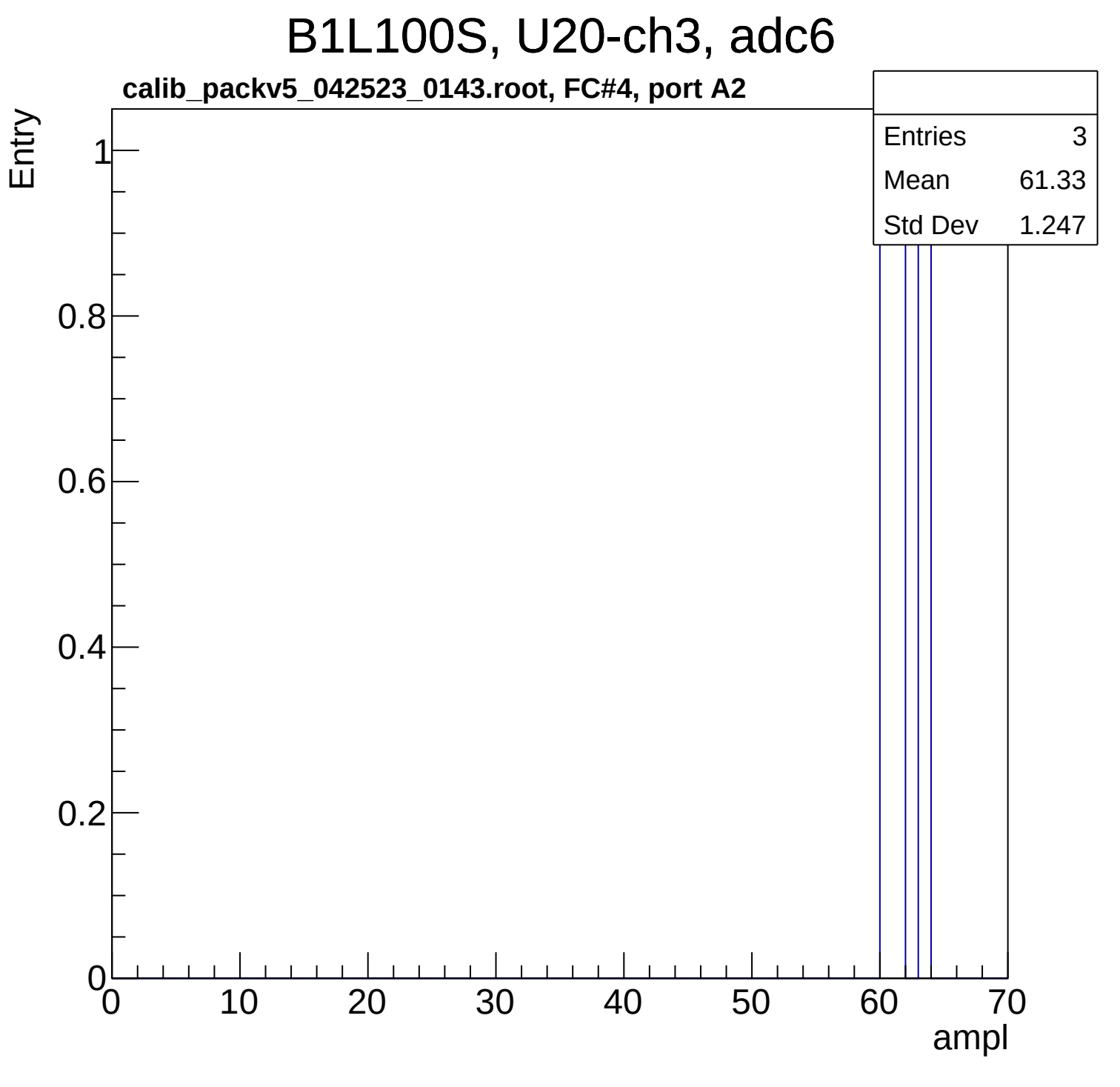
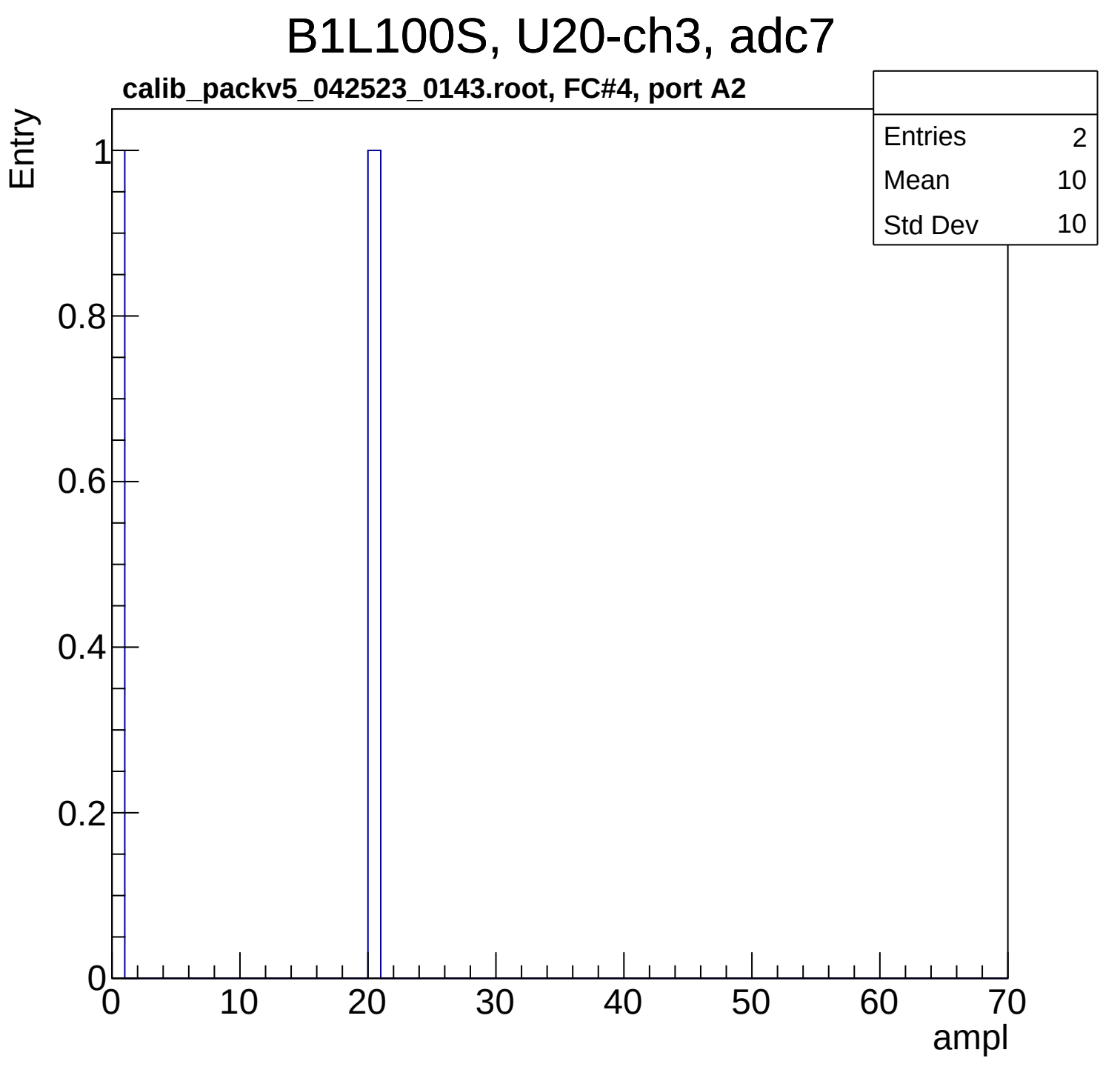

B1L100S, U20-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U20-ch3, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
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10
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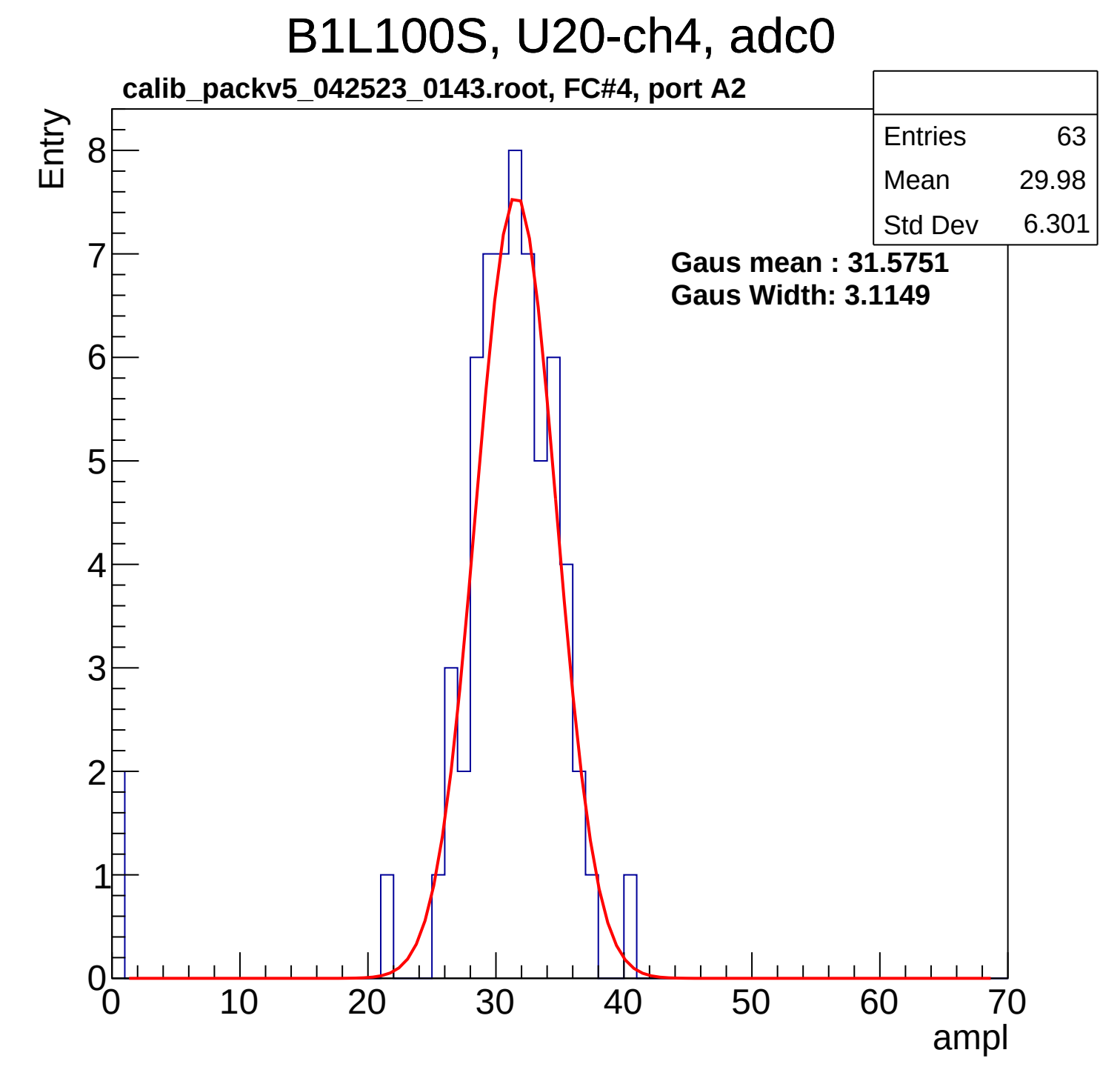
70
ampl


B1L100S, U20-ch4, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.98
Std Dev 6.301
Gaus mean : 31.5751
Gaus Width: 3.1149
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
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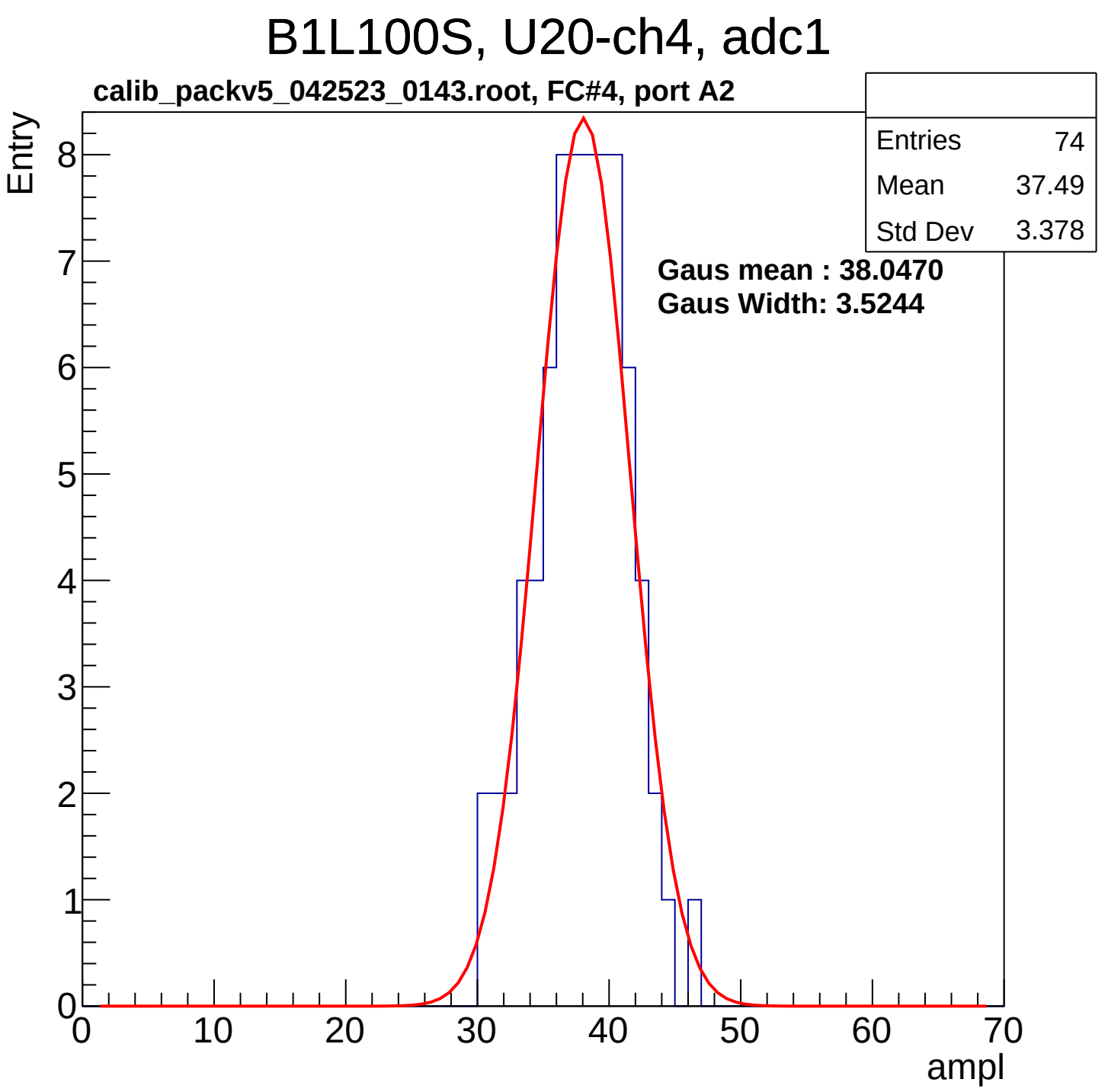
B1L100S, U20-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.49
Std Dev 3.378
Gaus mean : 38.0470
Gaus Width: 3.5244
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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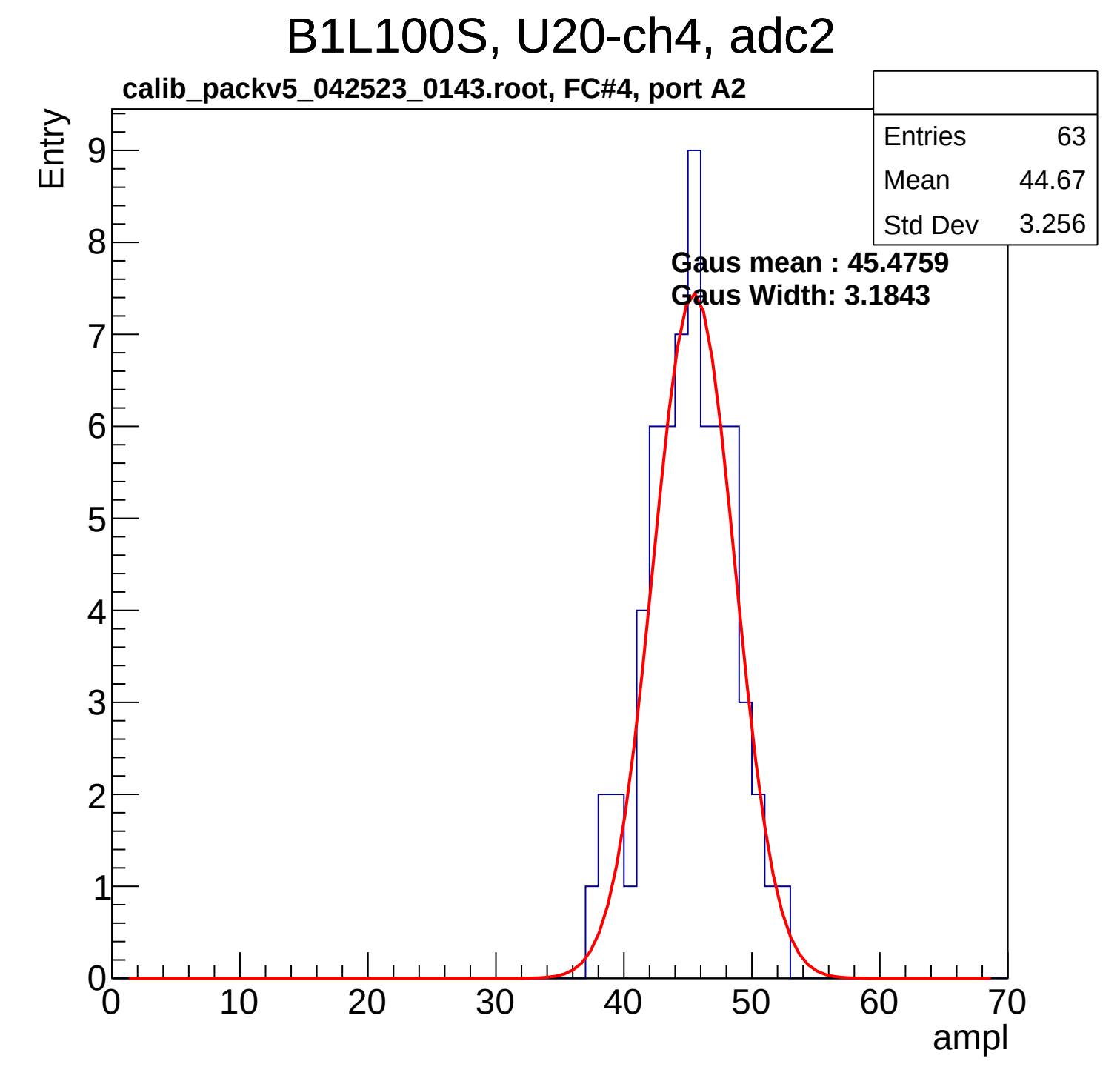
B1L100S, U20-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.67
Std Dev 3.256
Gaus mean : 45.4759
Gaus Width: 3.1843
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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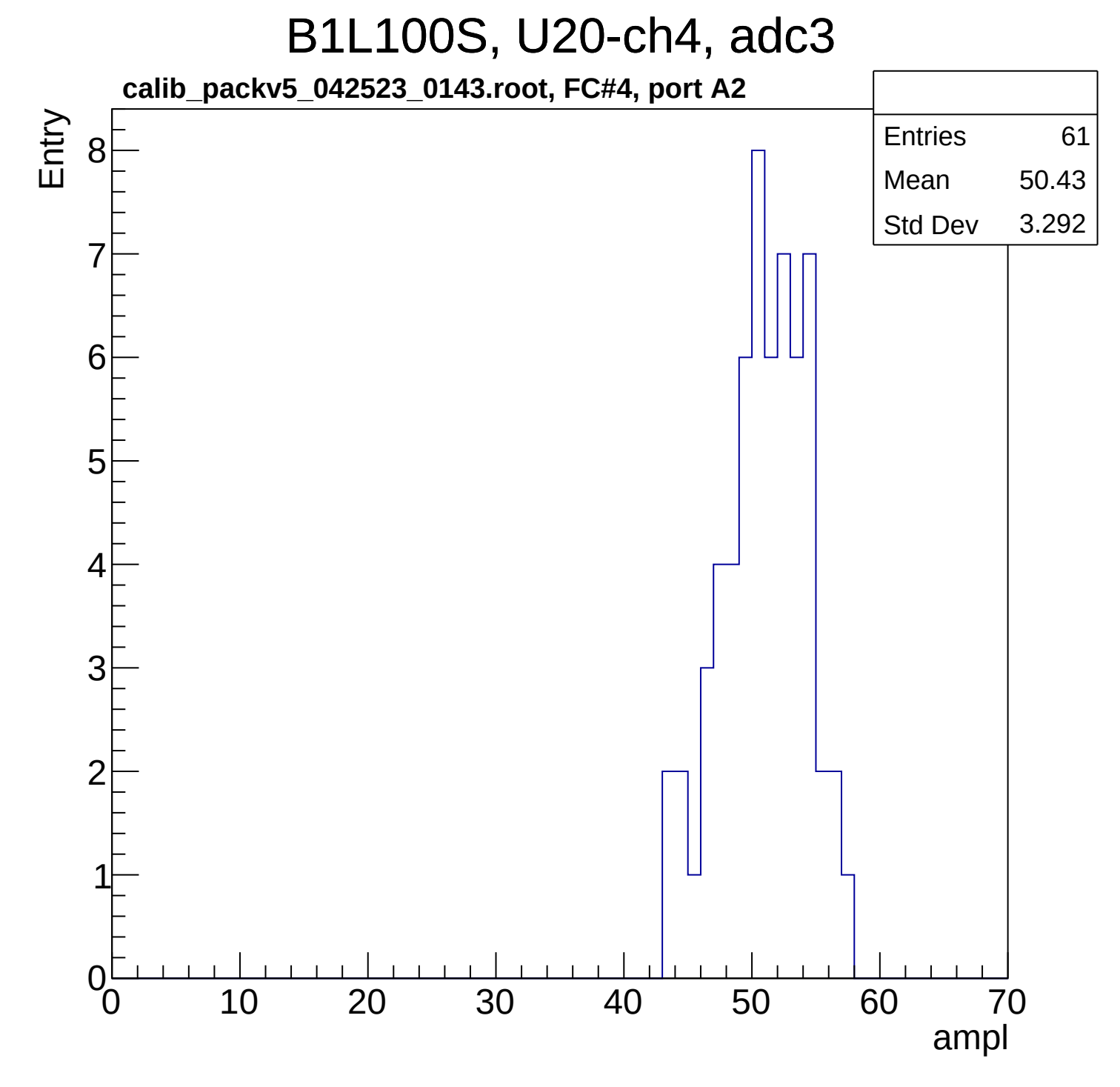
B1L100S, U20-ch4, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.43
Std Dev 3.292
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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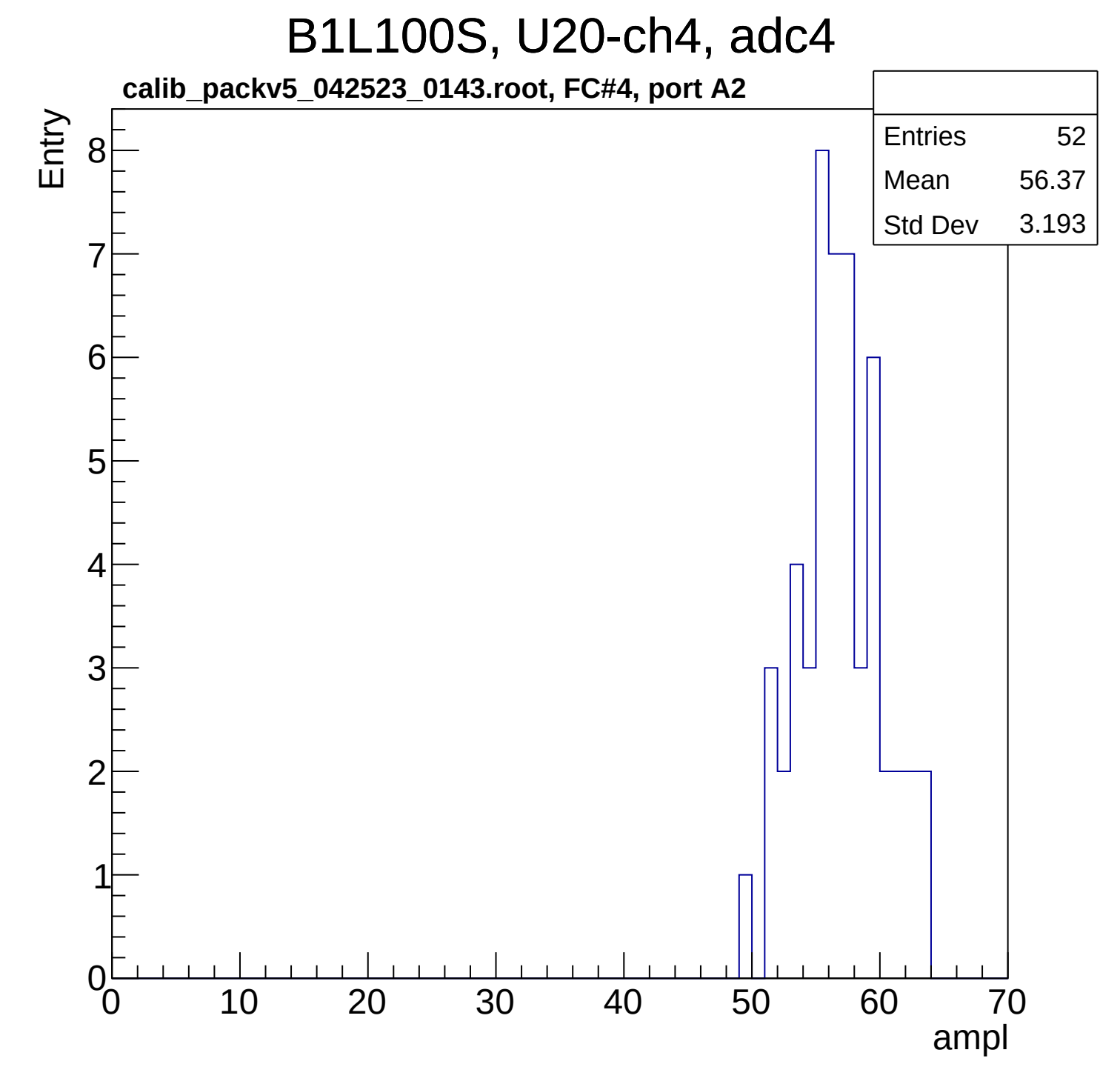
B1L100S, U20-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.37
Std Dev 3.193
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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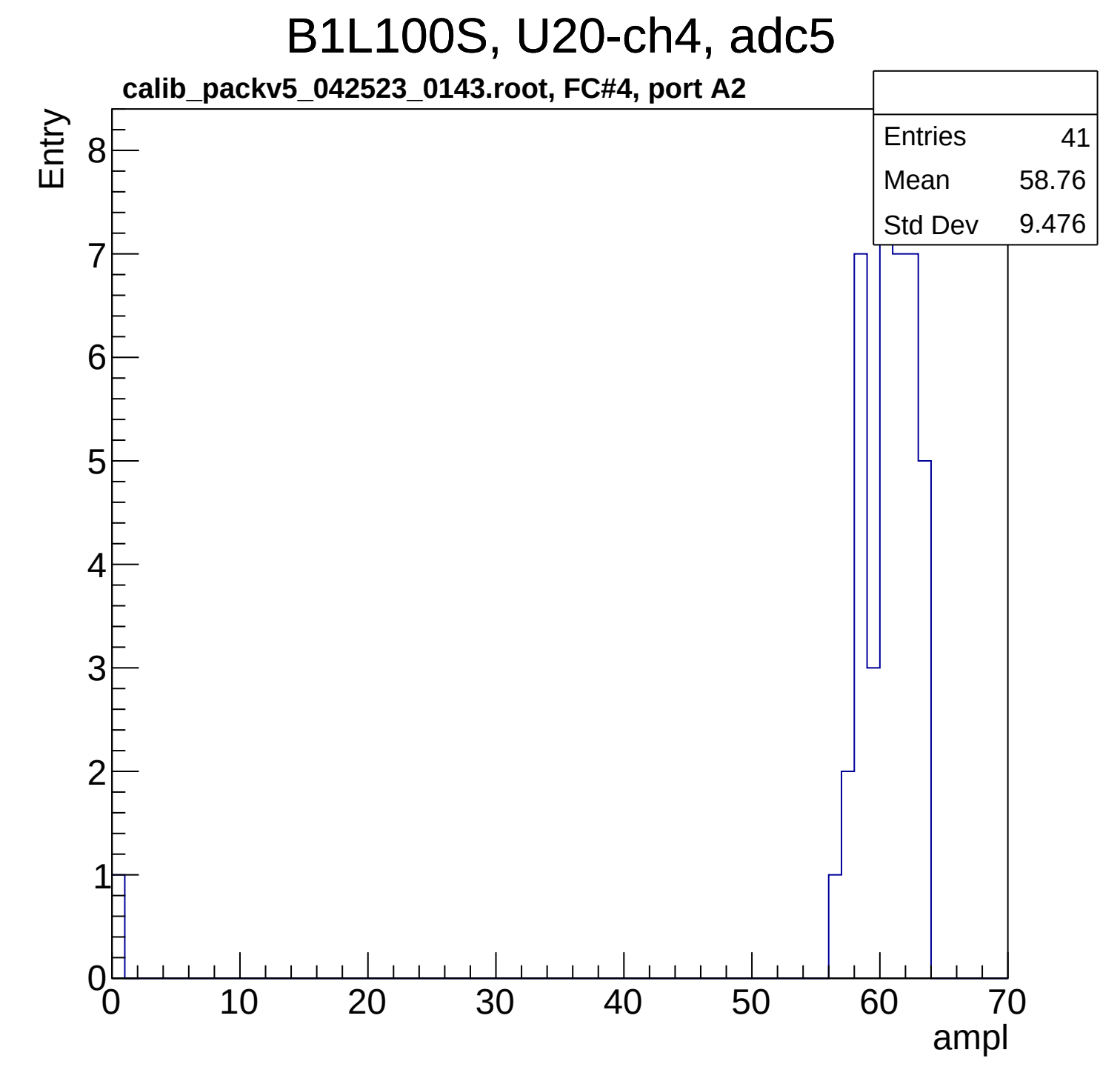
B1L100S, U20-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.76
Std Dev 9.476
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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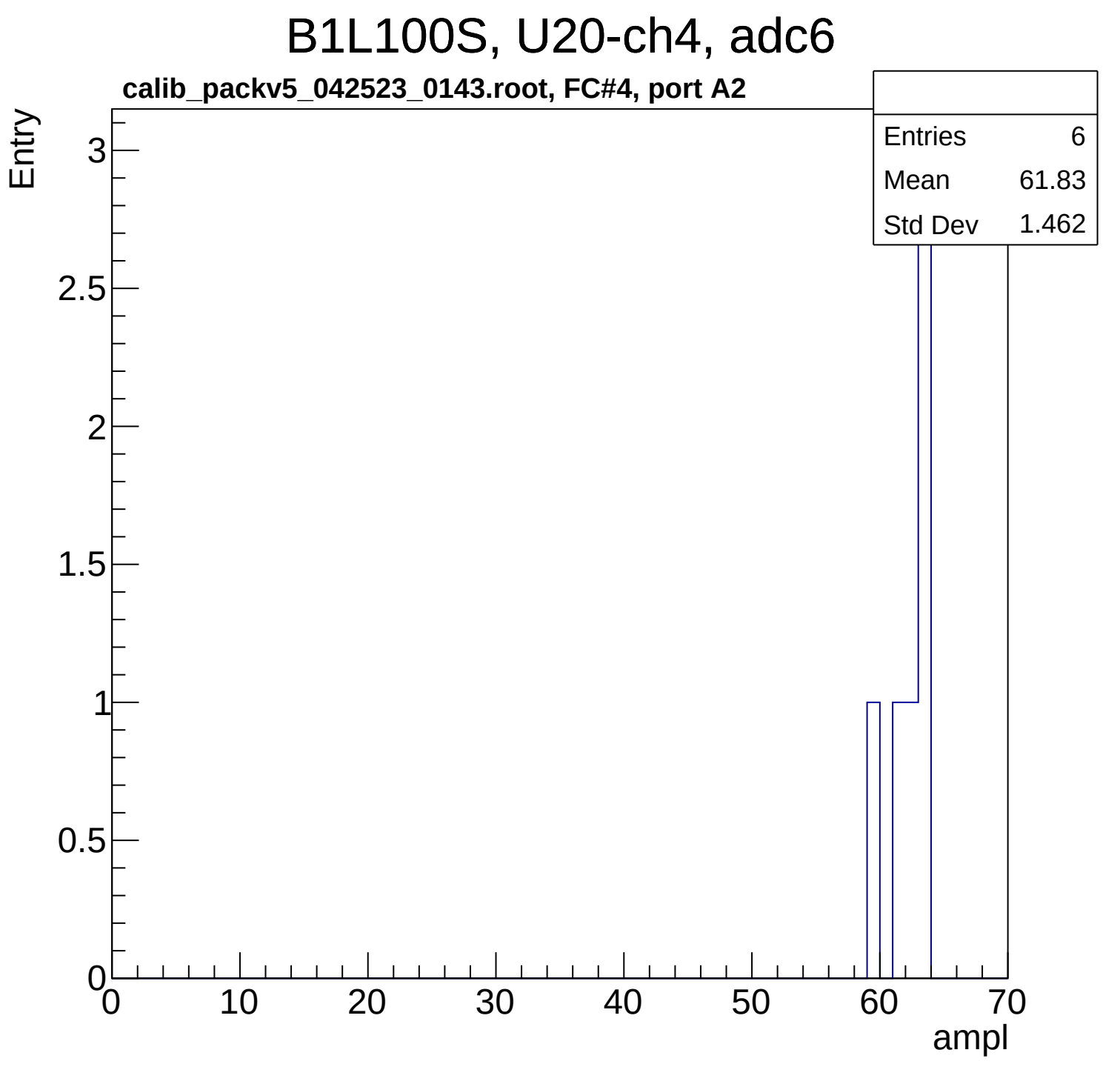
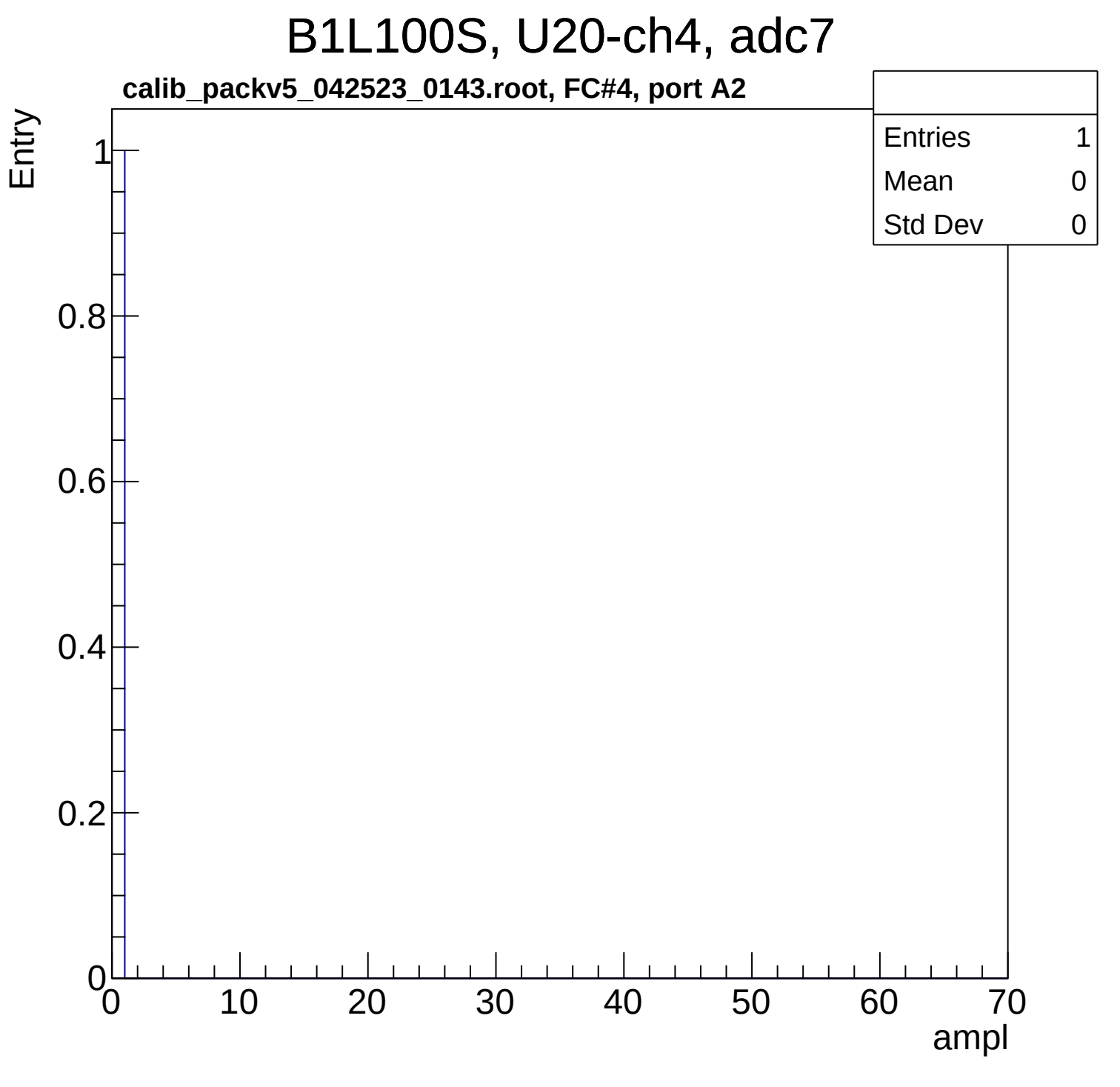
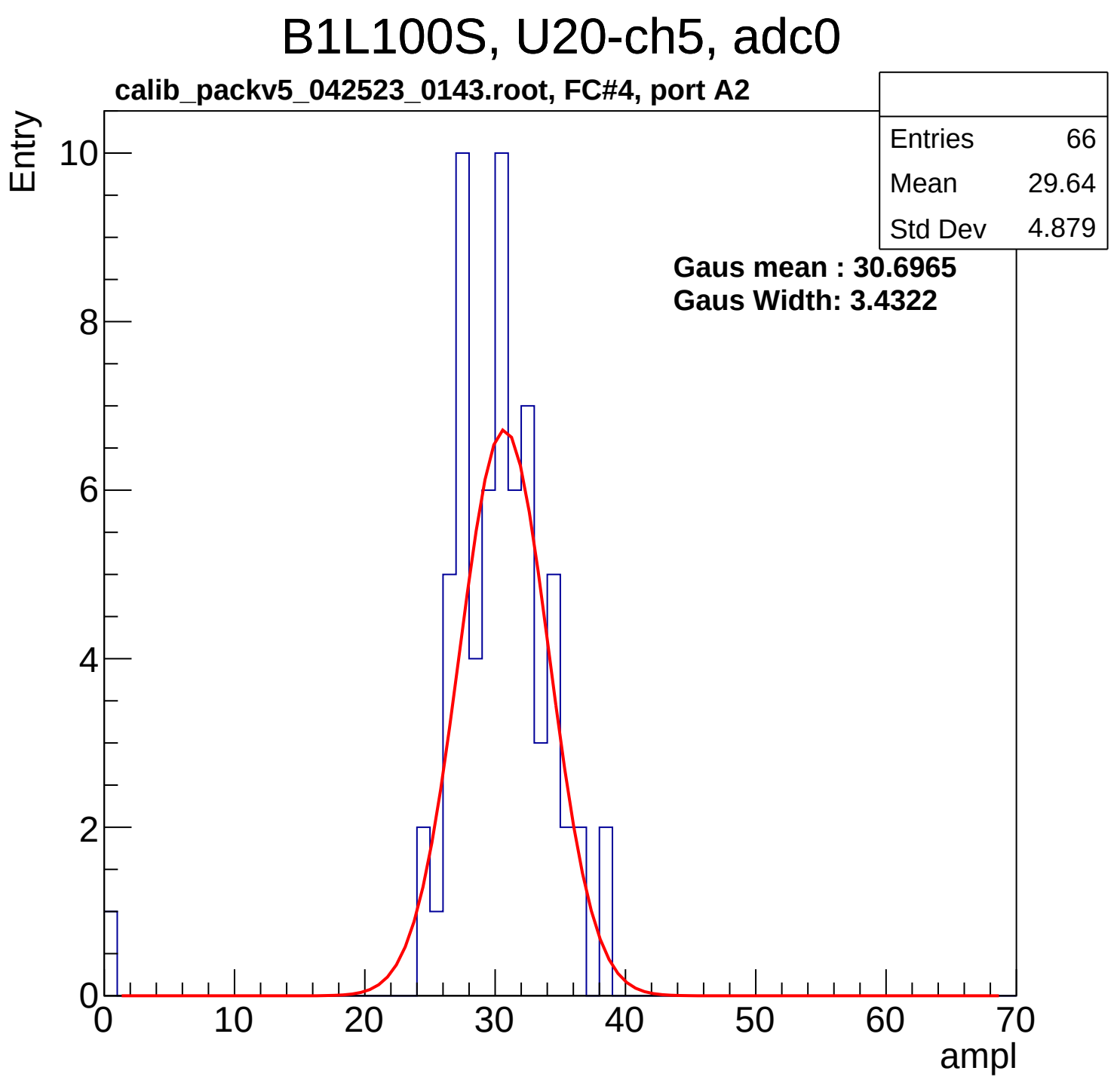
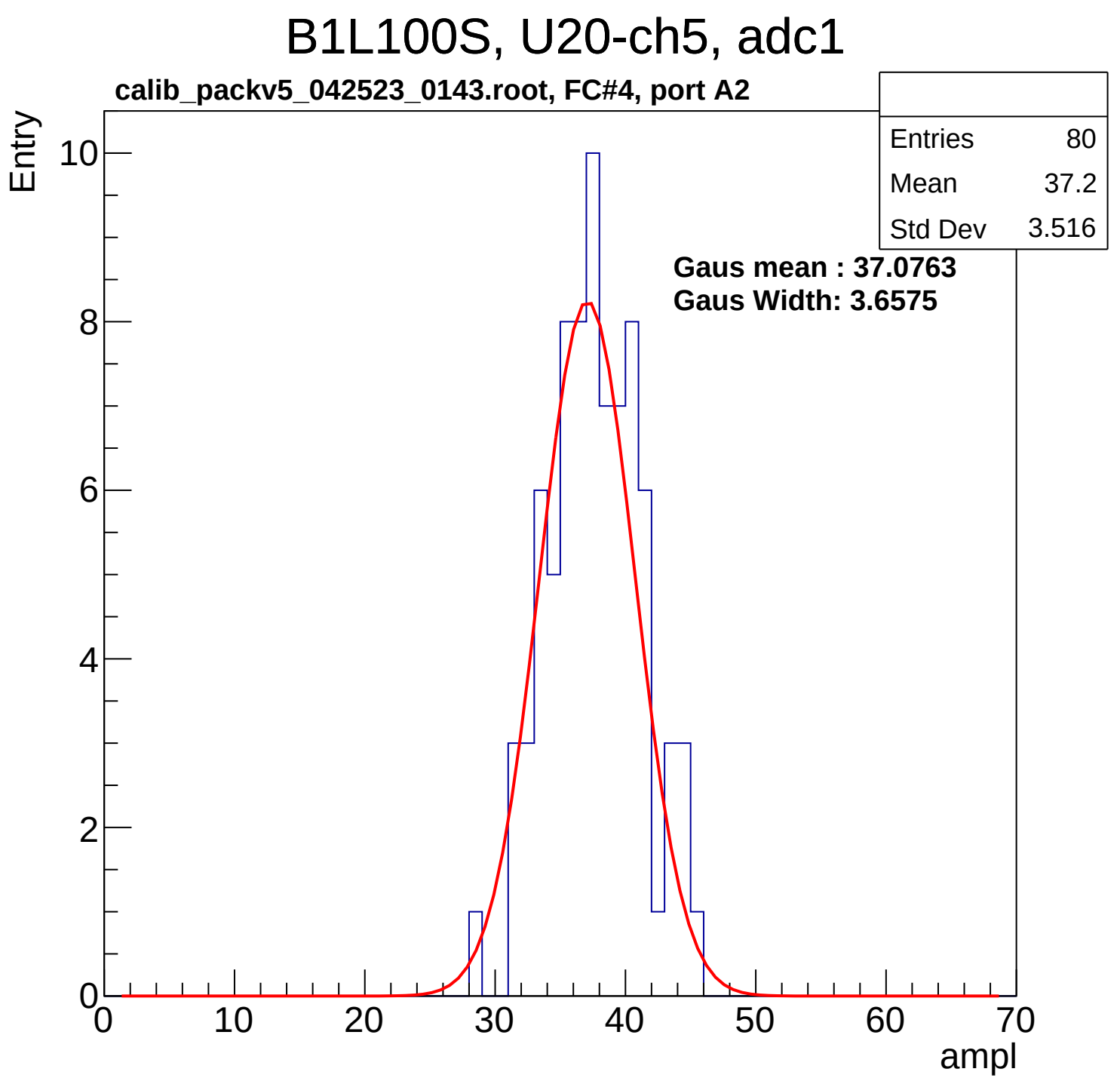

B1L100S, U20-ch4, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.64
Std Dev 4.879
Gaus mean : 30.6965
Gaus Width: 3.4322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.2
Std Dev 3.516
Gaus mean : 37.0763
Gaus Width: 3.6575
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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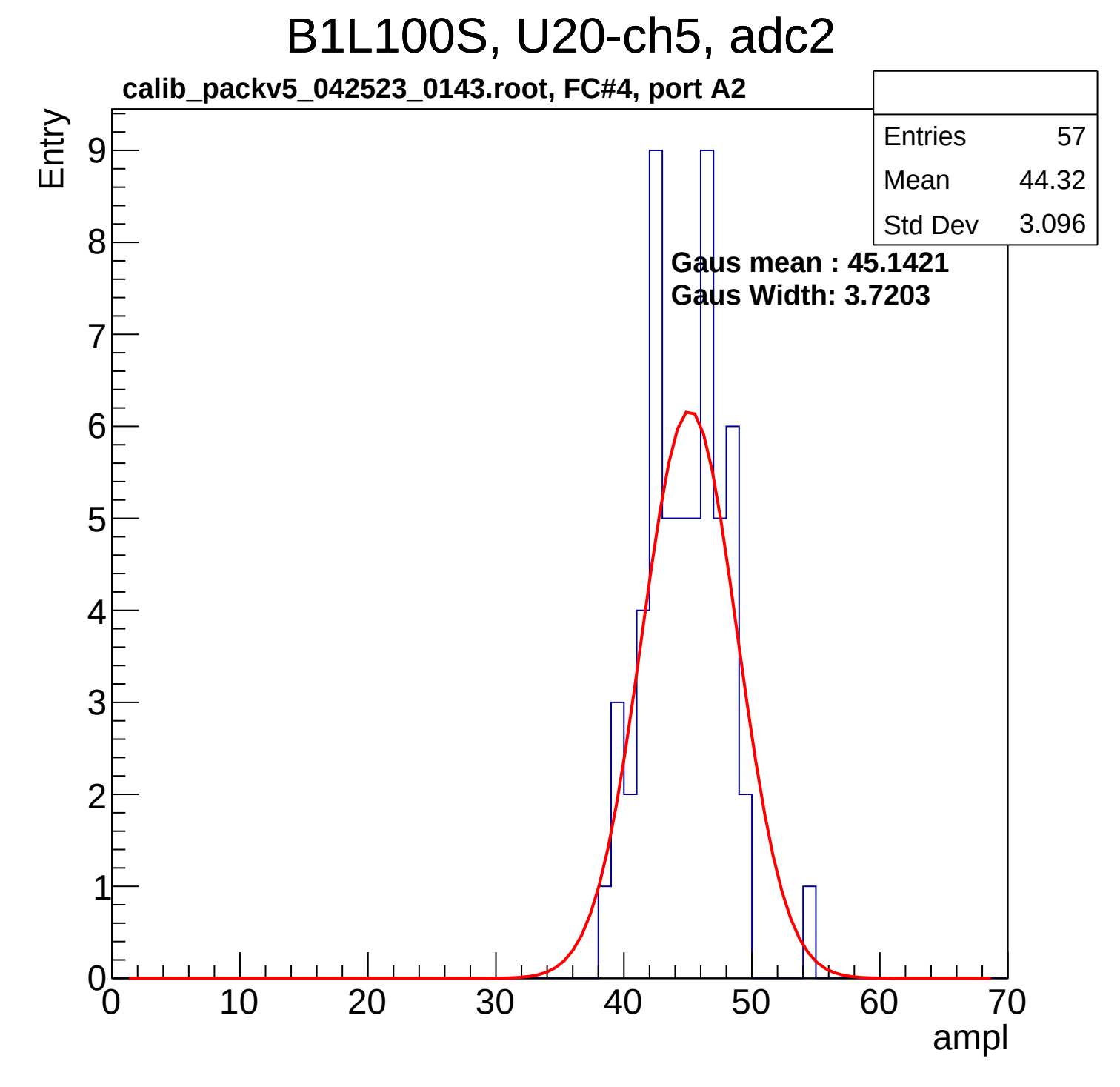
B1L100S, U20-ch5, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.32
Std Dev 3.096
Gaus mean : 45.1421
Gaus Width: 3.7203
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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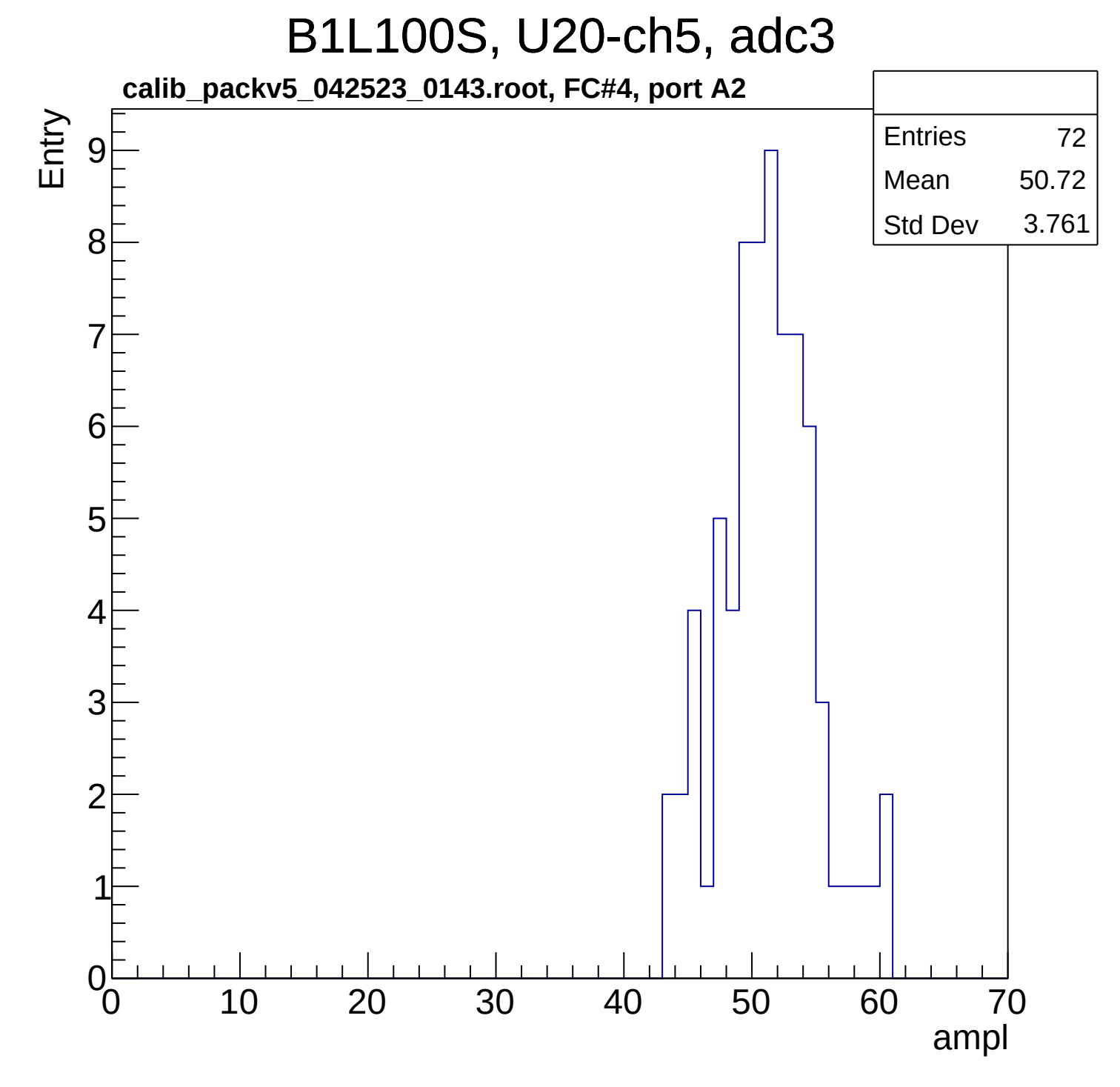
B1L100S, U20-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.72
Std Dev 3.761
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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50
60
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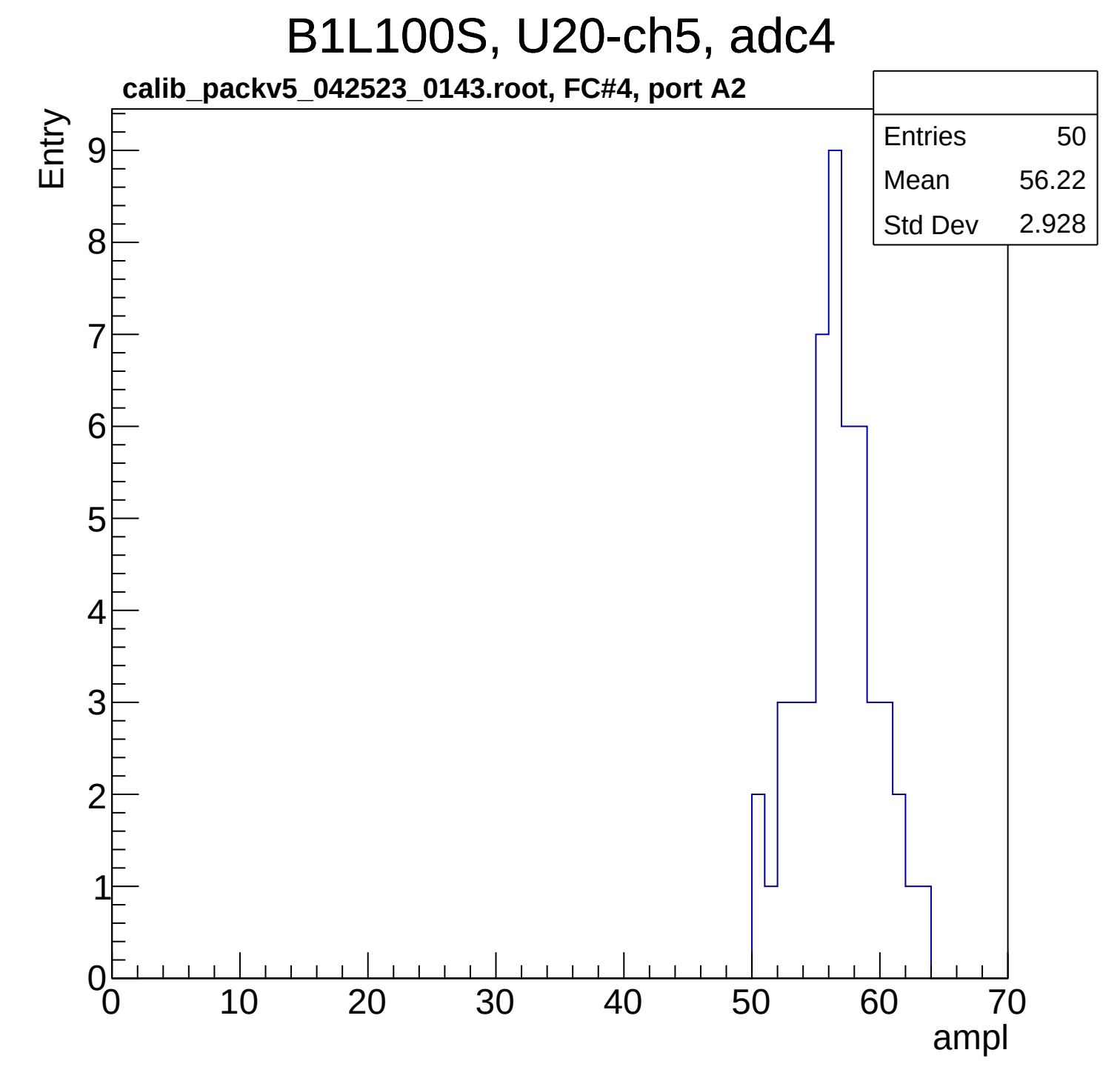
B1L100S, U20-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 56.22
Std Dev 2.928
Entry
ampl
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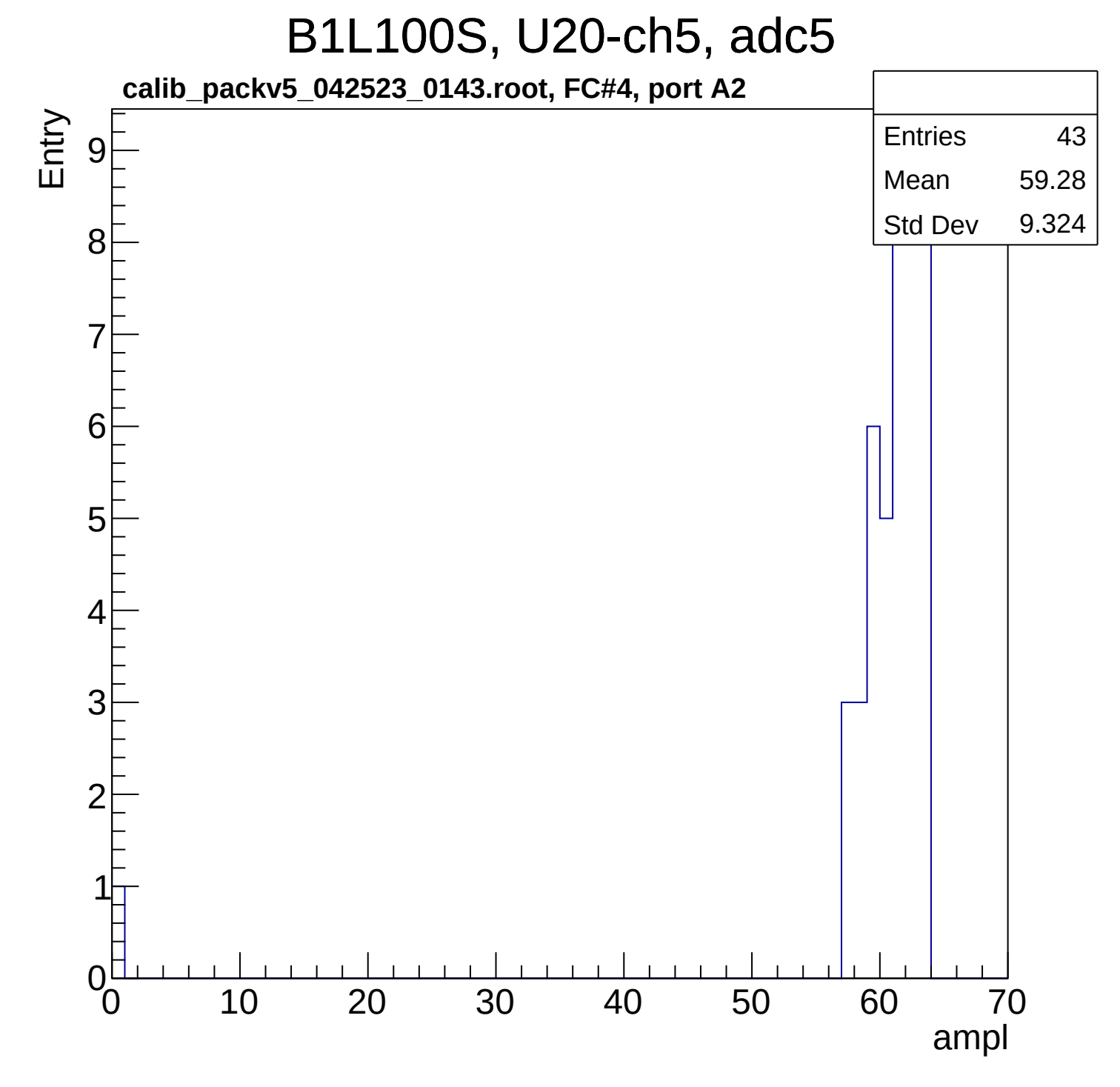
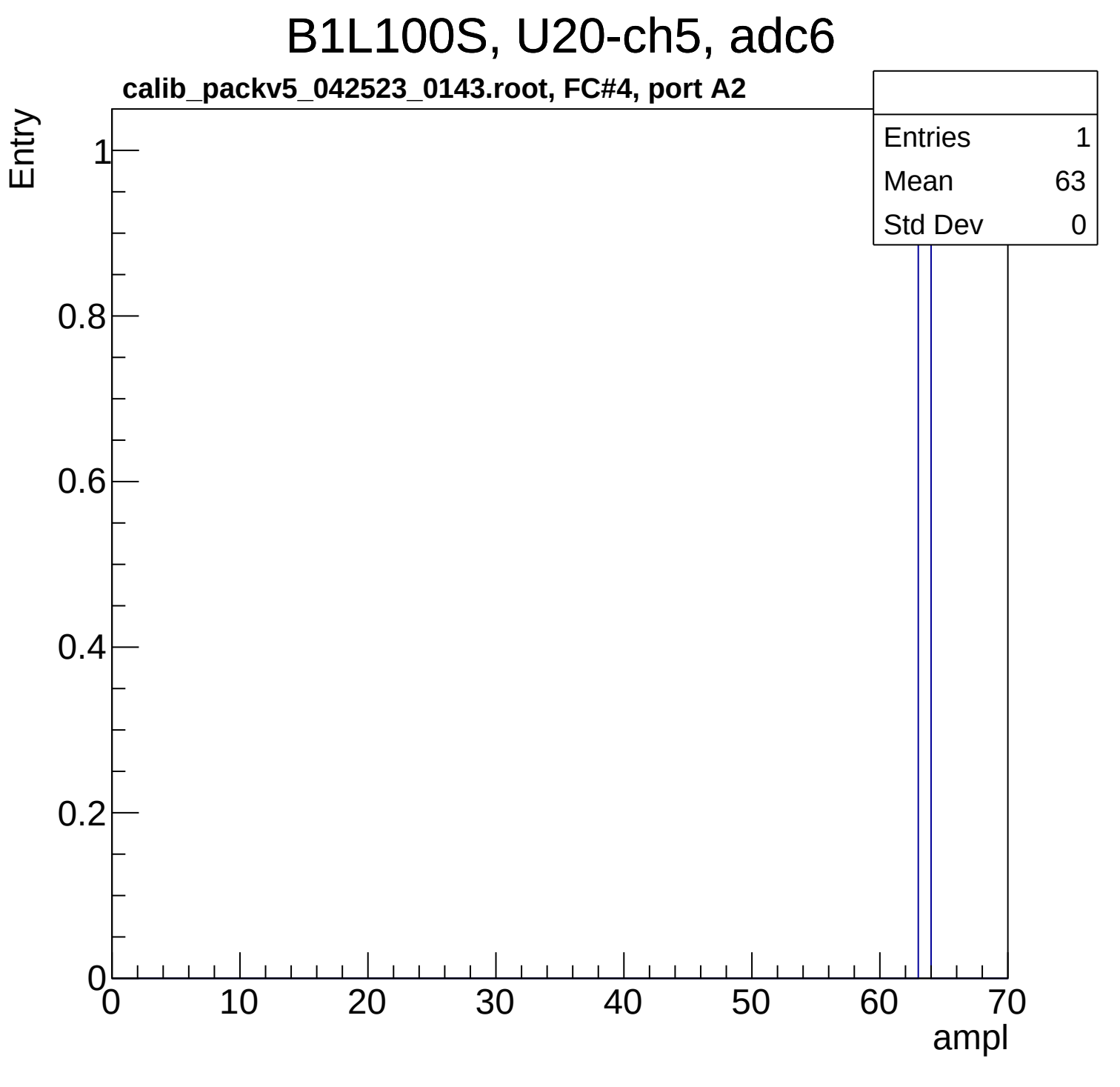
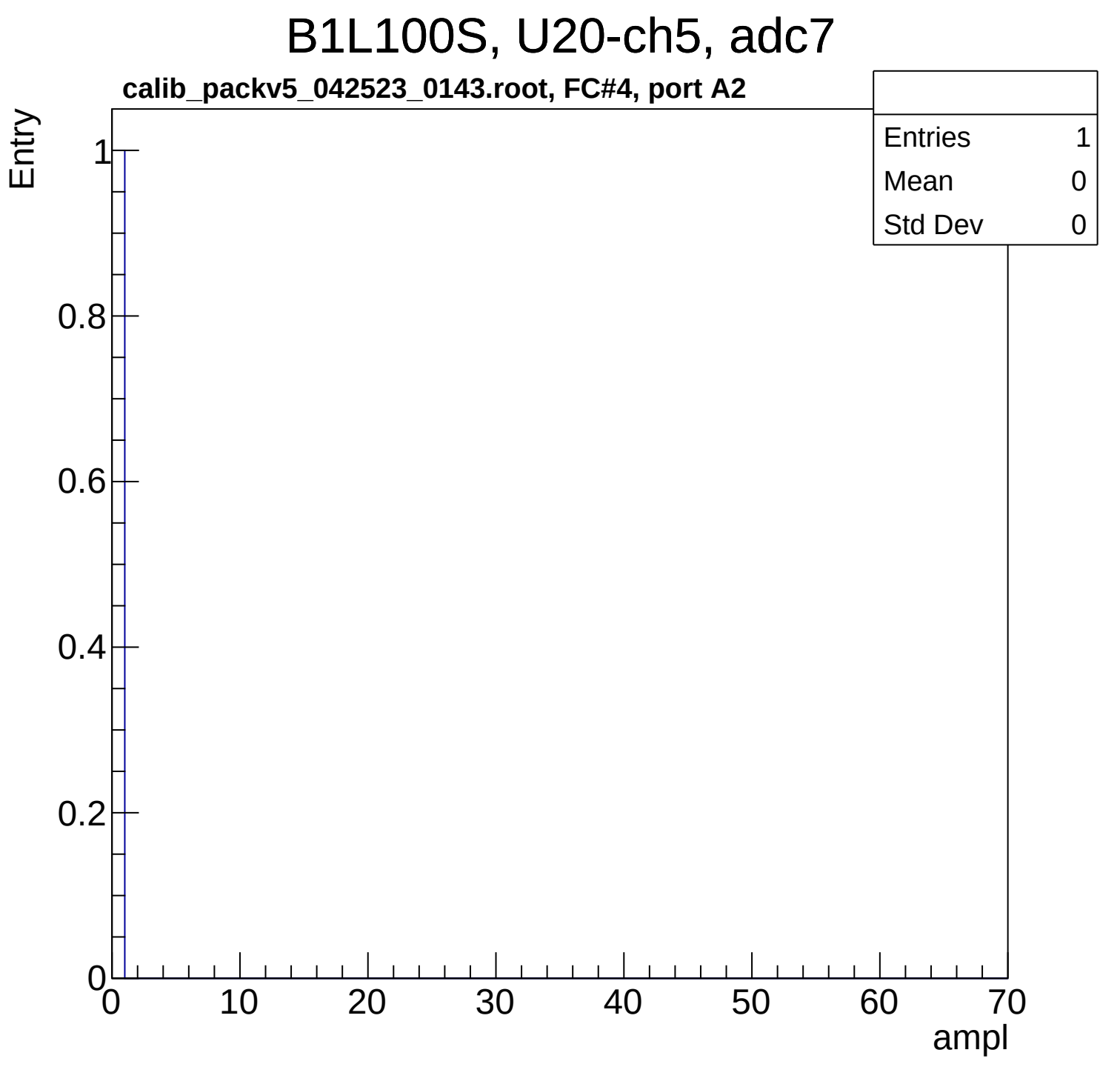
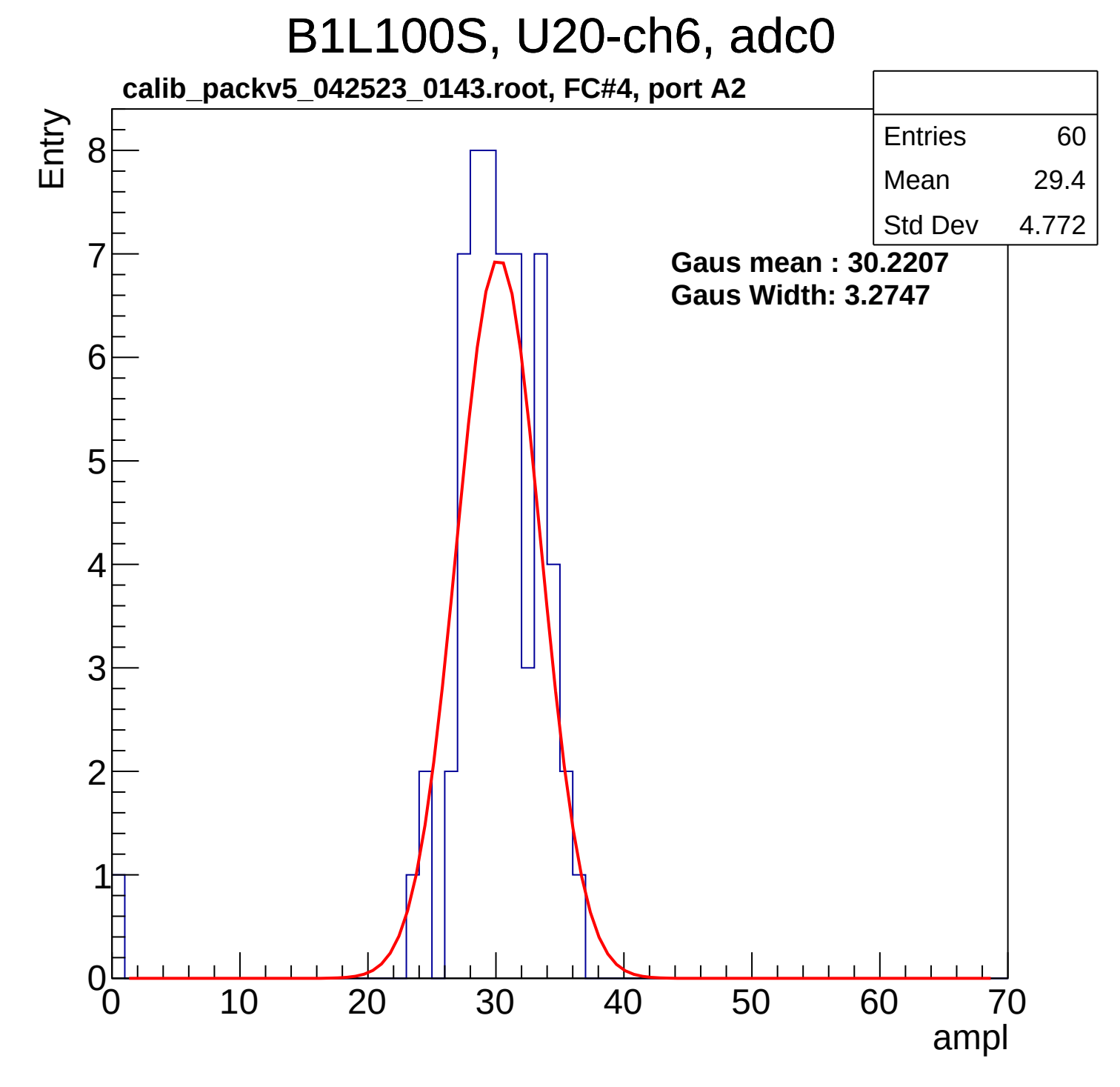
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch5, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59.28
Std Dev 9.324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch5, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 29.4
Std Dev 4.772
Gaus mean : 30.2207
Gaus Width: 3.2747
Entry
ampl
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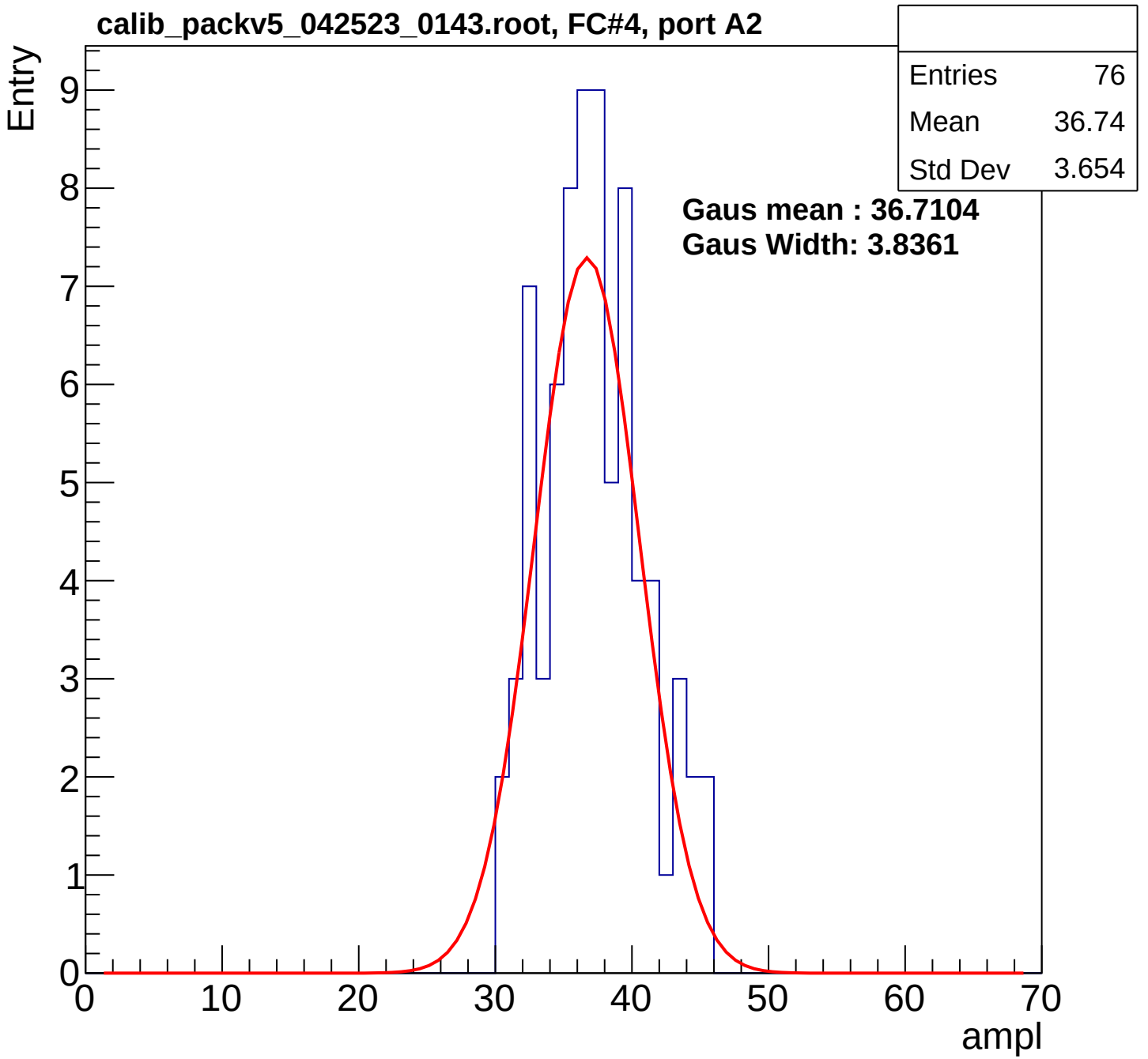
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.74
Std Dev 3.654
Gaus mean : 36.7104
Gaus Width: 3.8361
Entry
ampl
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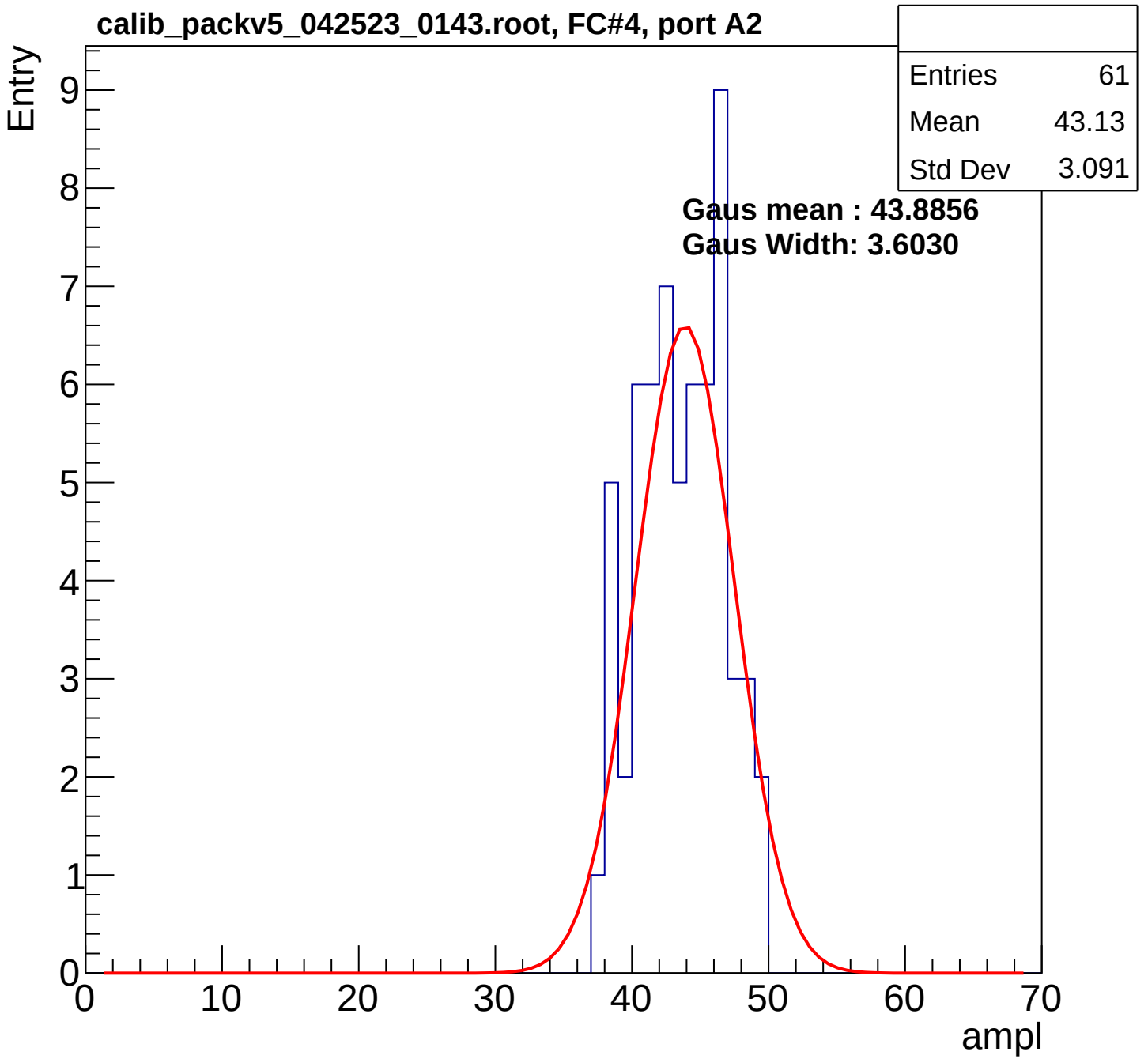
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch6, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.13
Std Dev 3.091
Gaus mean : 43.8856
Gaus Width: 3.6030
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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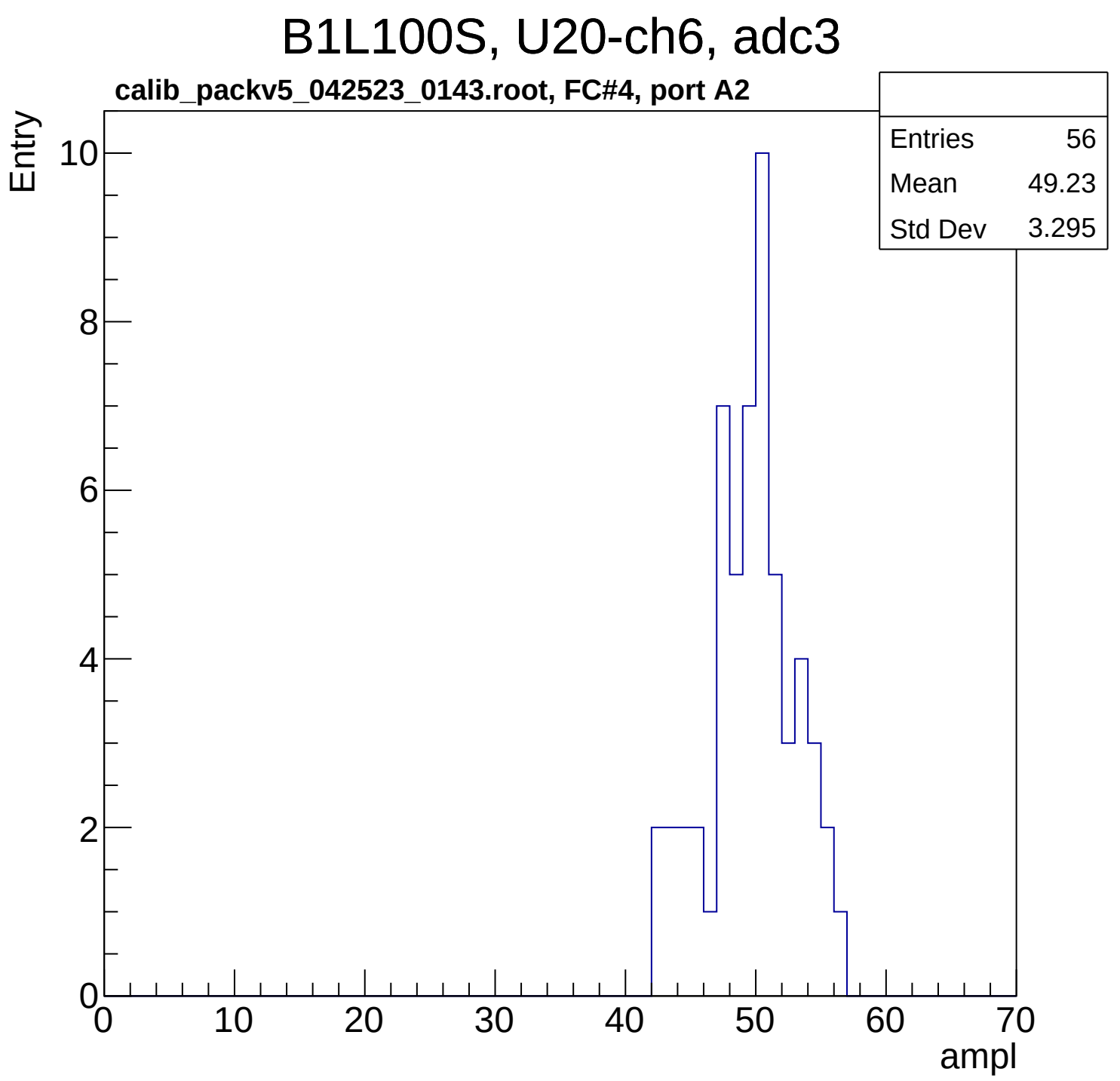
60
70


B1L100S, U20-ch6, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 49.23
Std Dev 3.295
Entry
ampl
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10
0
10
20
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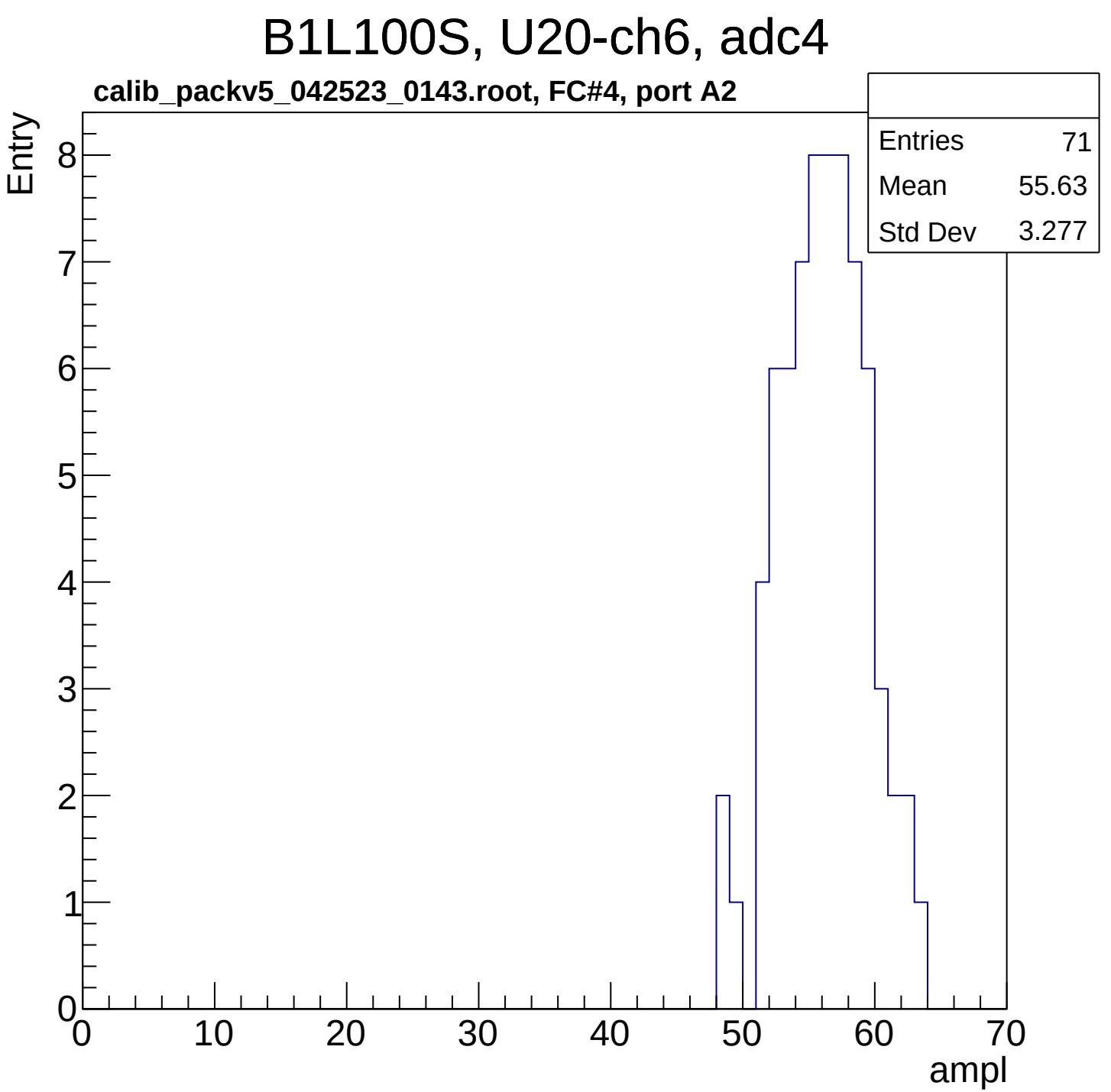
40
50
60
70


B1L100S, U20-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 55.63
Std Dev 3.277
Entry
ampl
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5
6
7
8
0
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20
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40
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60
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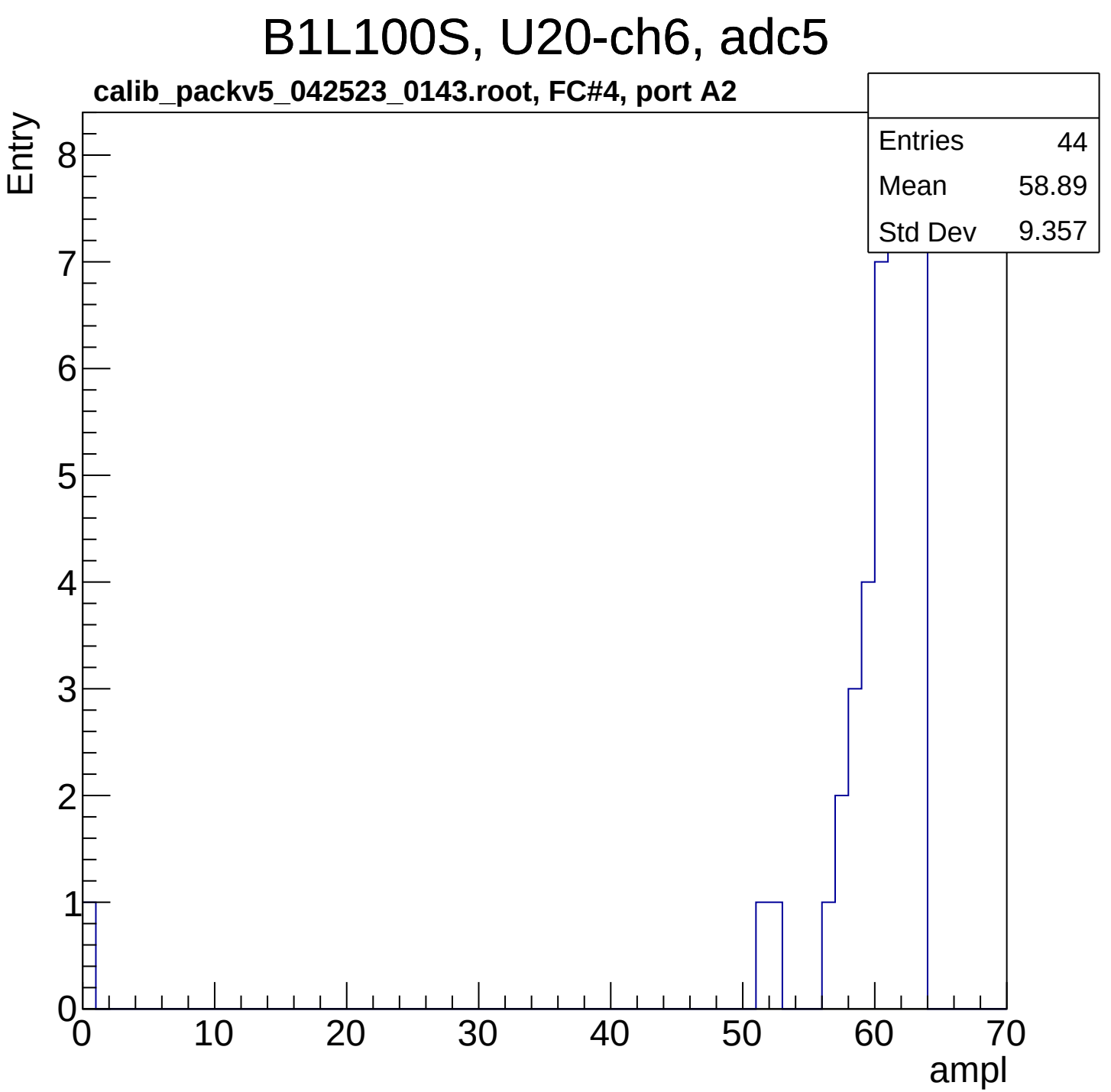
B1L100S, U20-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.89
Std Dev 9.357
Entry
ampl
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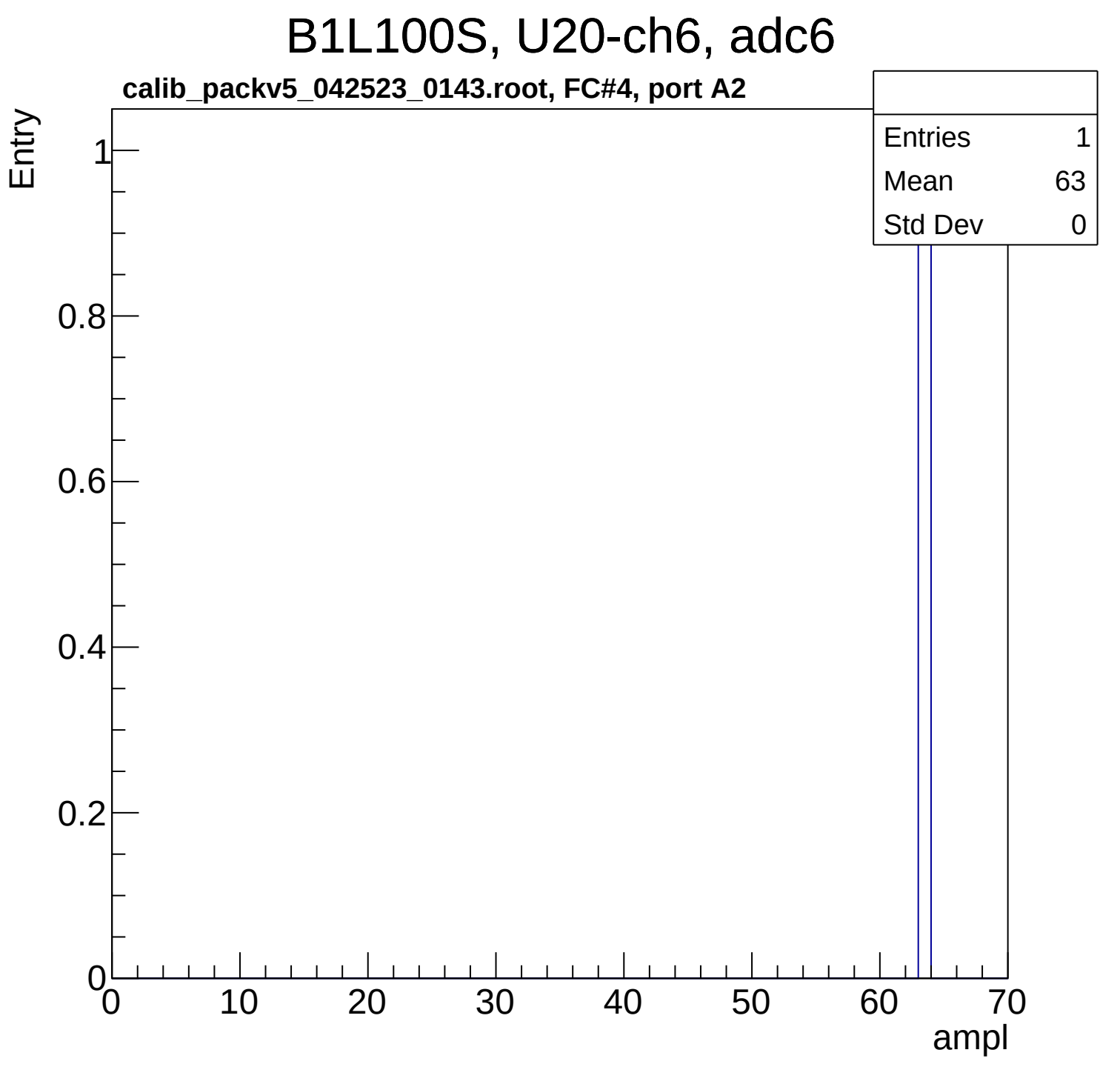
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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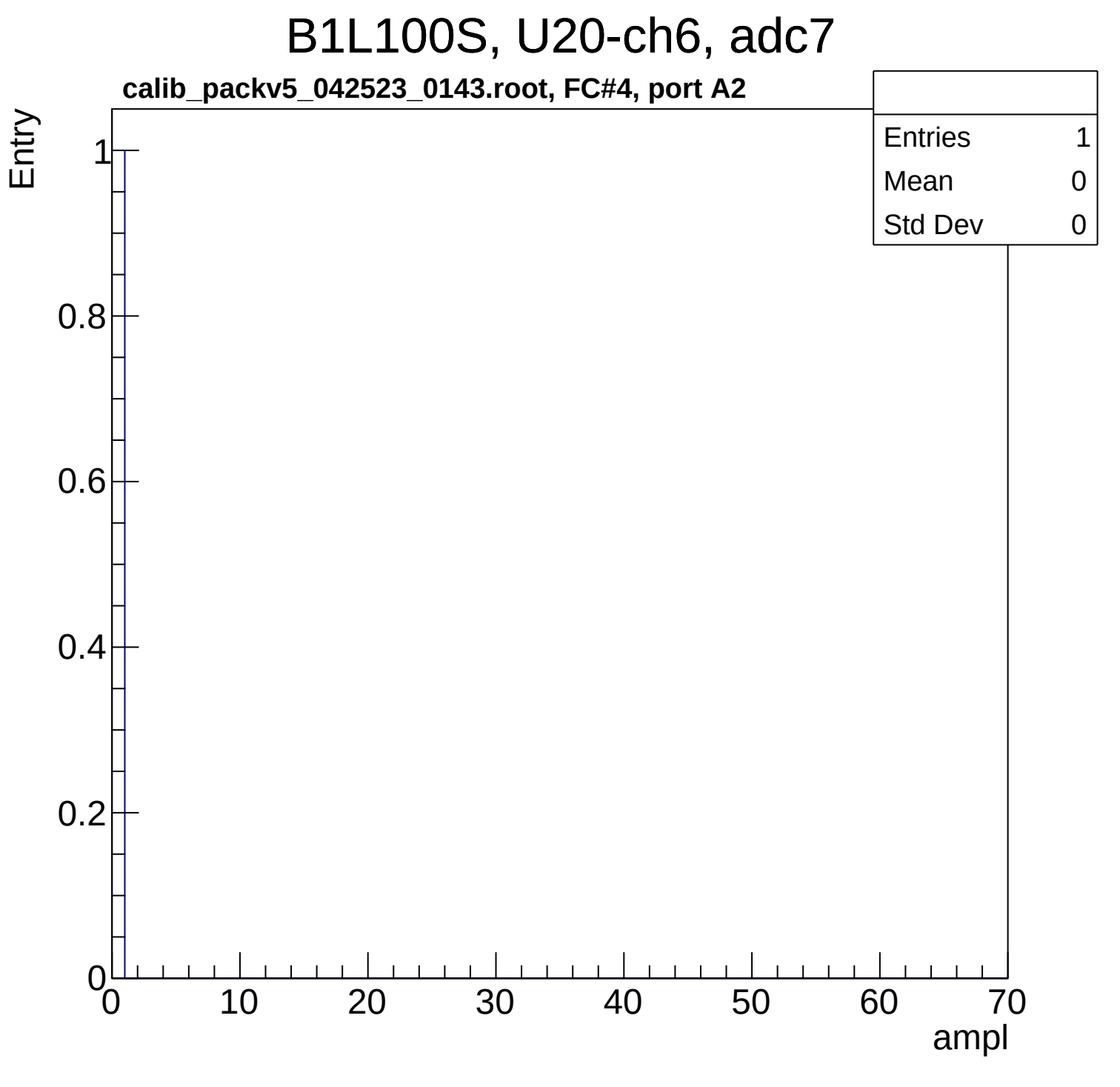
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch6, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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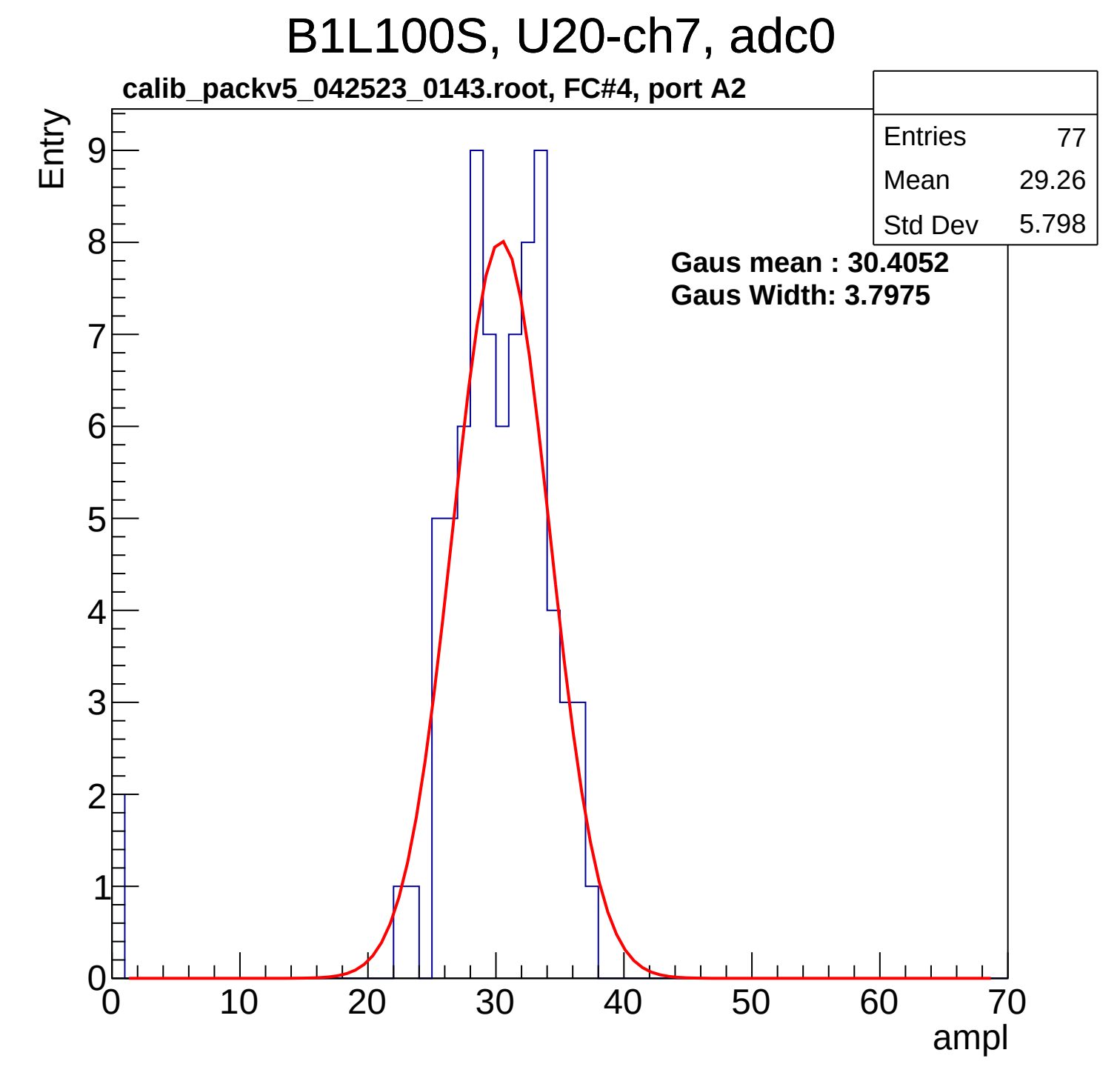
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.26
Std Dev 5.798
Gaus mean : 30.4052
Gaus Width: 3.7975
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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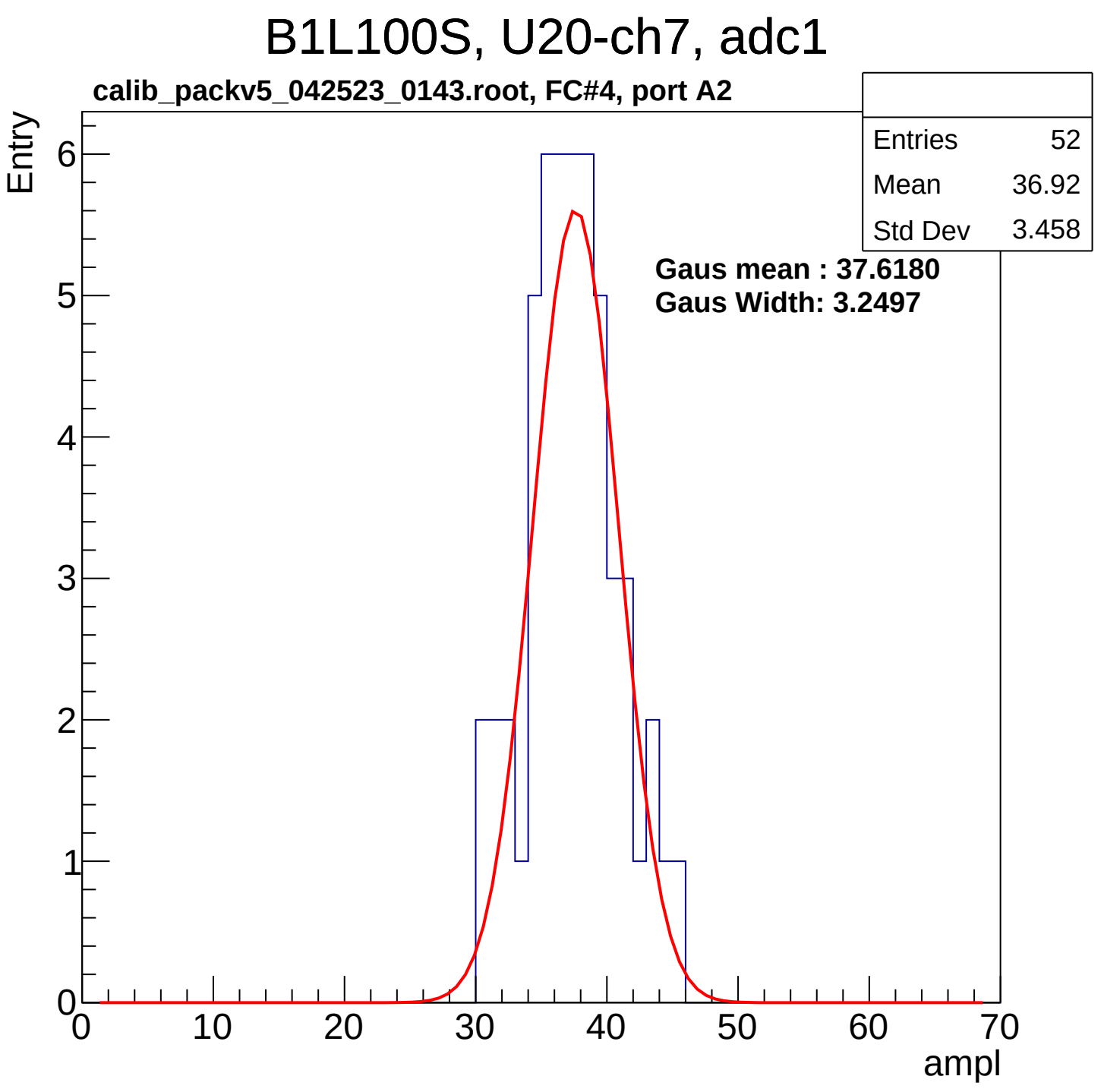
B1L100S, U20-ch7, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 36.92
Std Dev 3.458
Gaus mean : 37.6180
Gaus Width: 3.2497
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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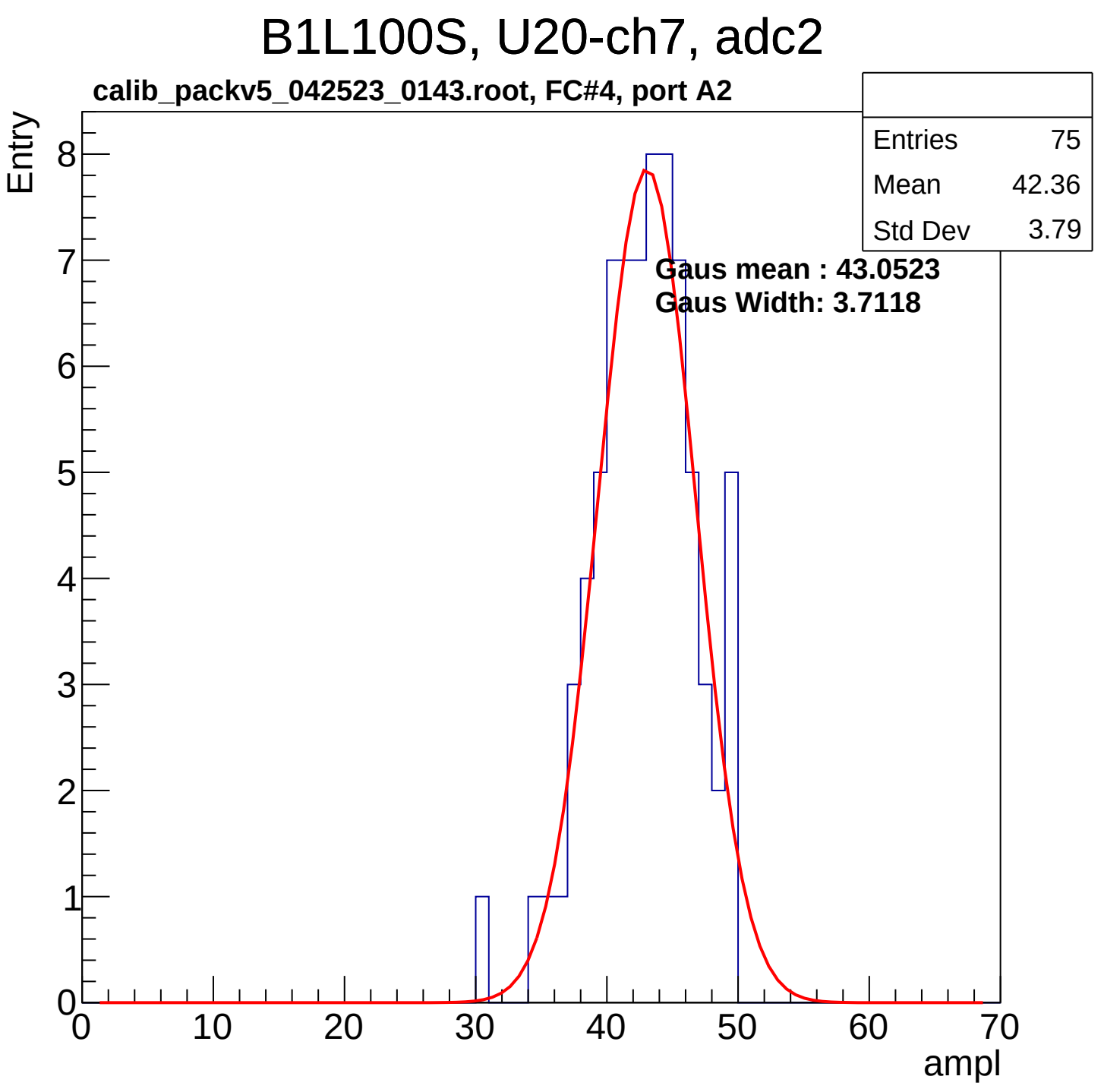
B1L100S, U20-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 42.36
Std Dev 3.79
Gaus mean : 43.0523
Gaus Width: 3.7118
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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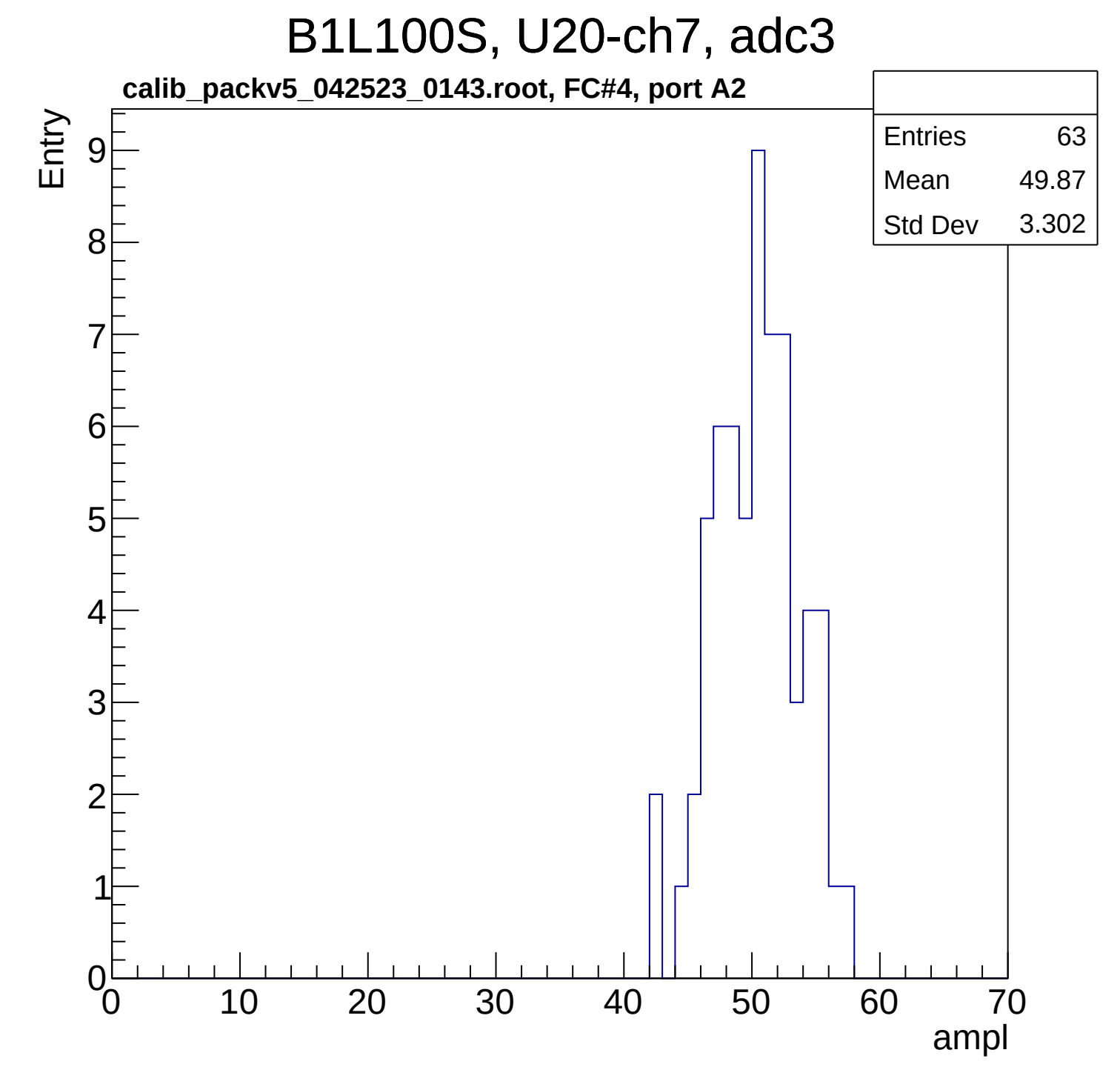
B1L100S, U20-ch7, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.87
Std Dev 3.302
Entry
ampl
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3
4
5
6
7
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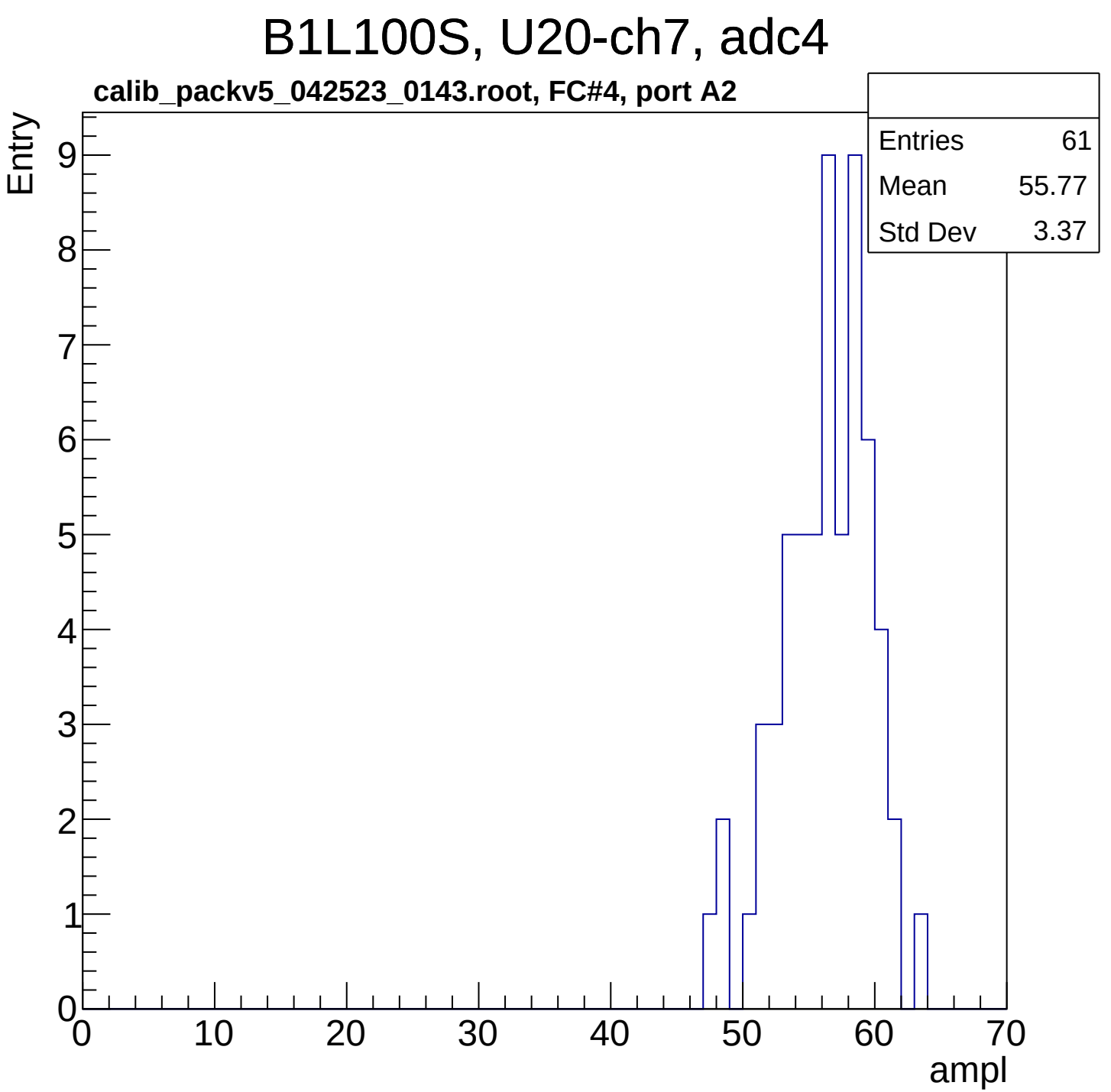
B1L100S, U20-ch7, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.77
Std Dev 3.37
Entry
ampl
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3
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5
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7
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0
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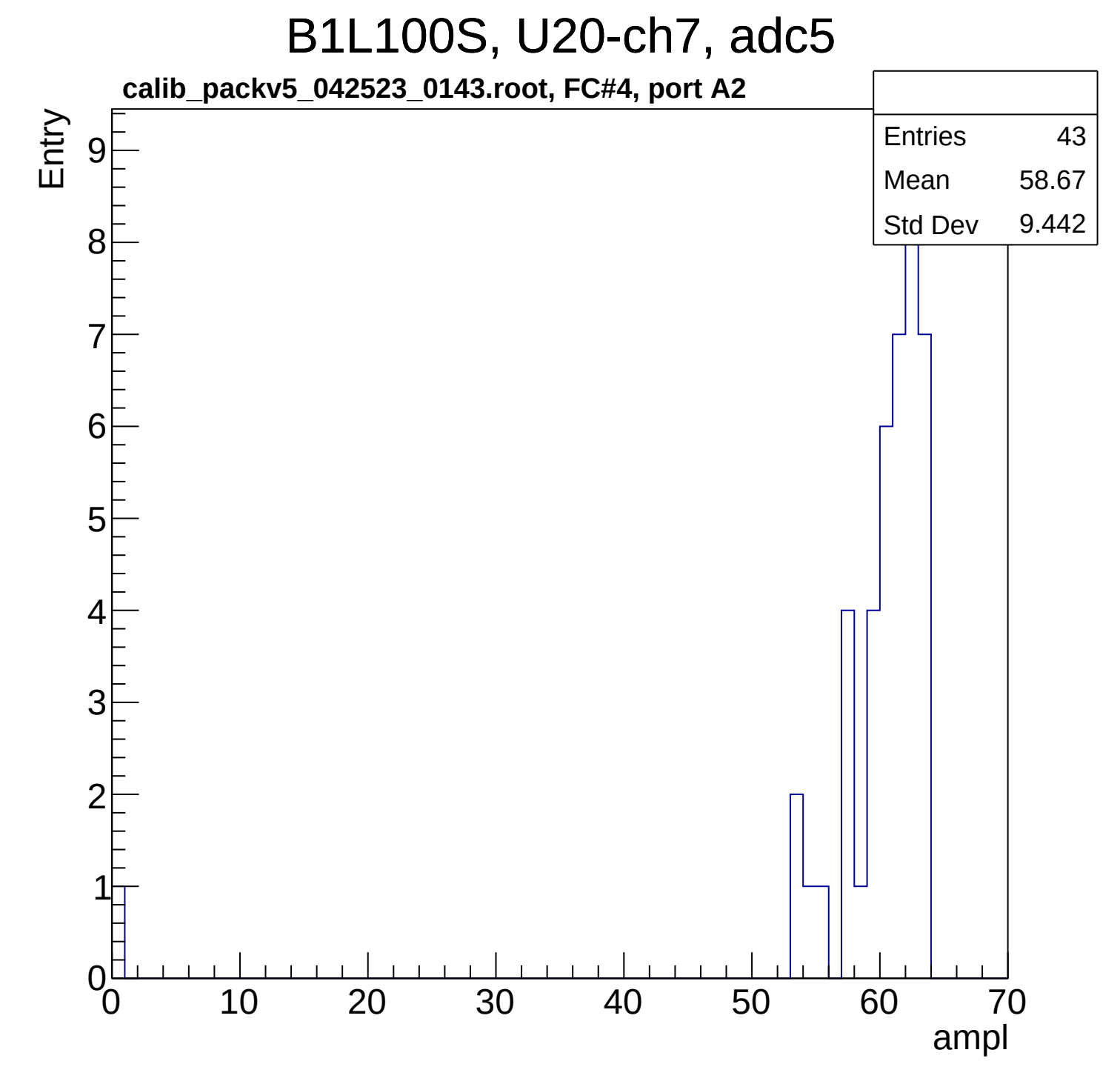
B1L100S, U20-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.67
Std Dev 9.442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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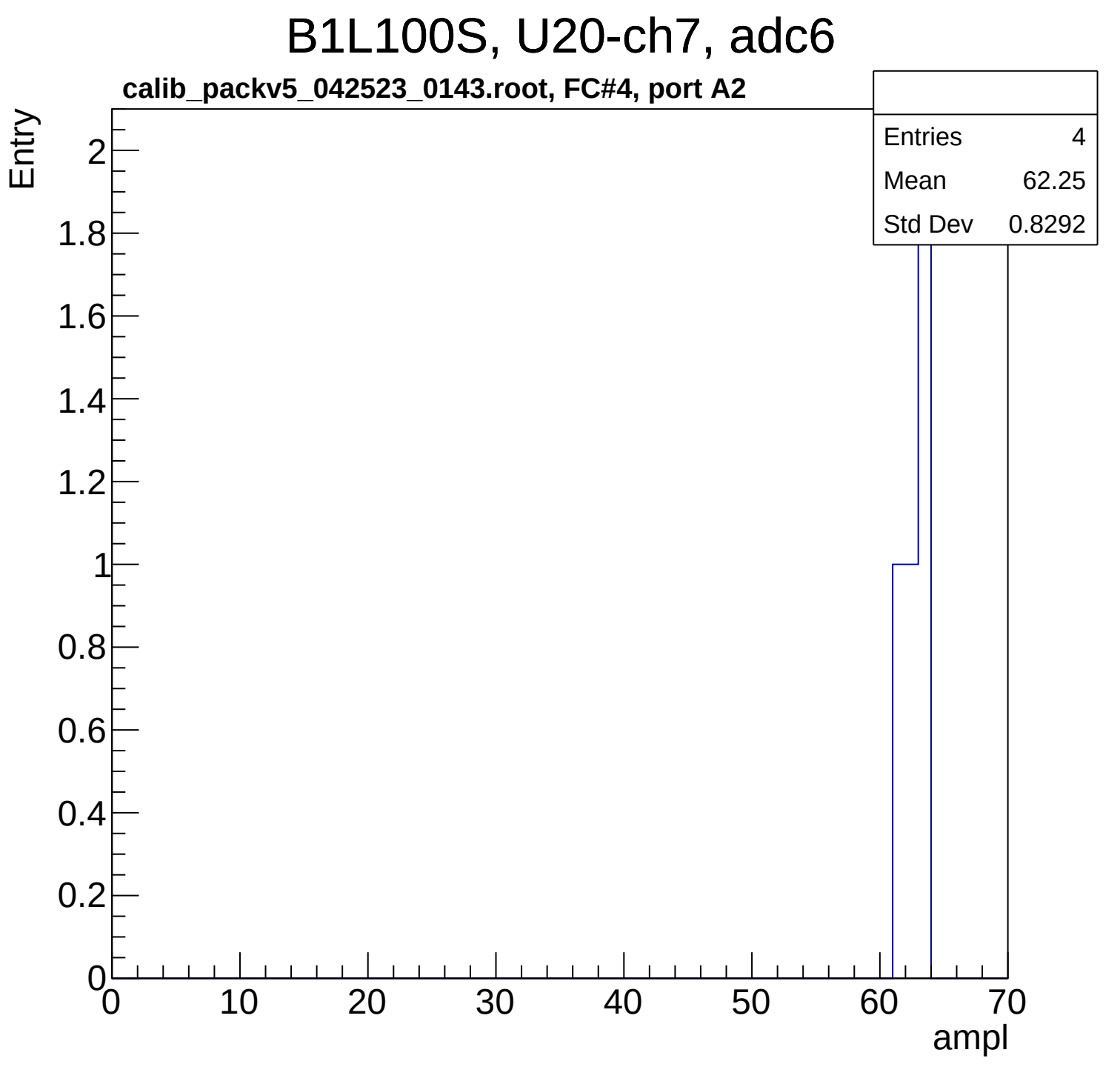
40
50
60
70


B1L100S, U20-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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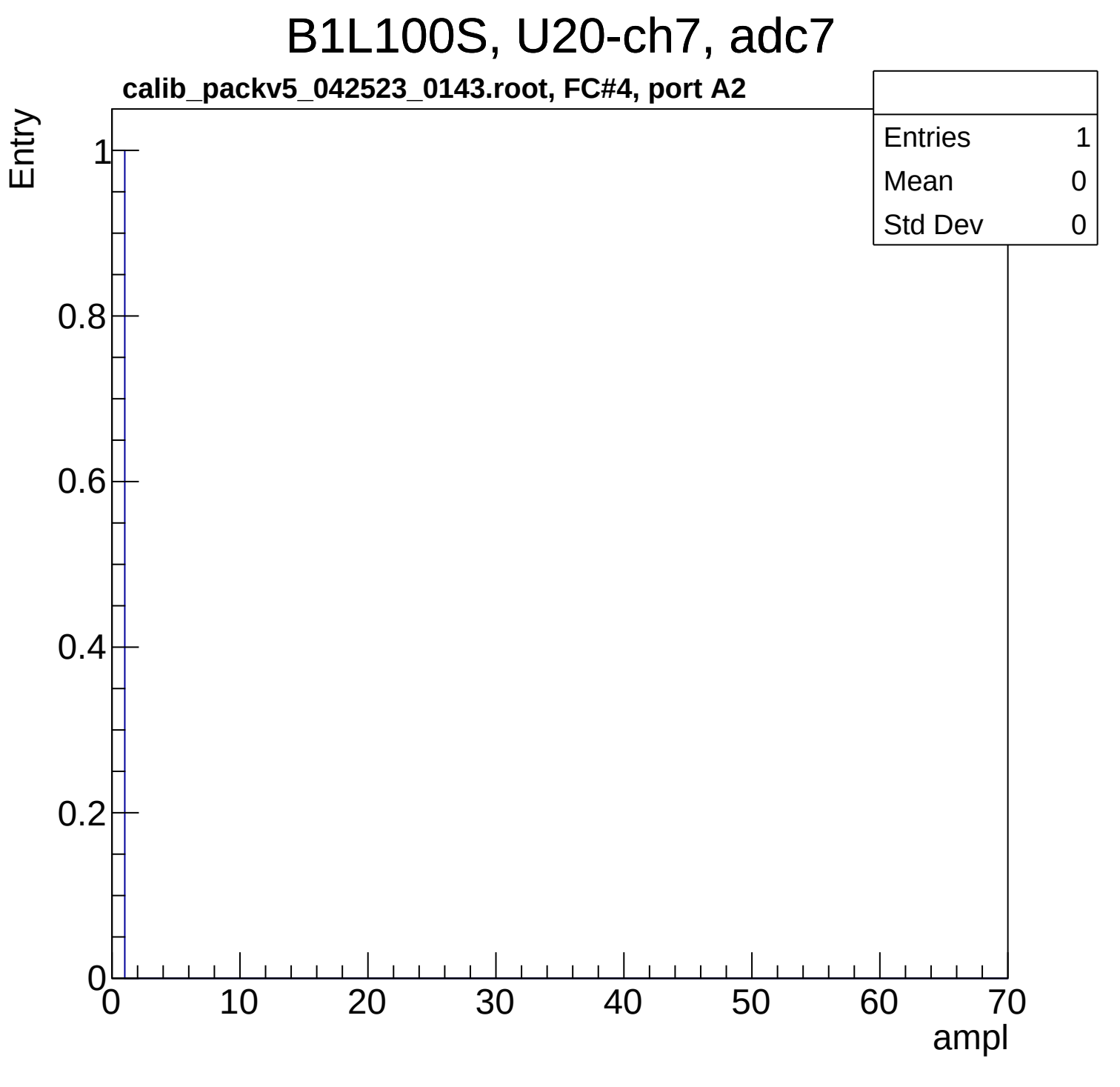
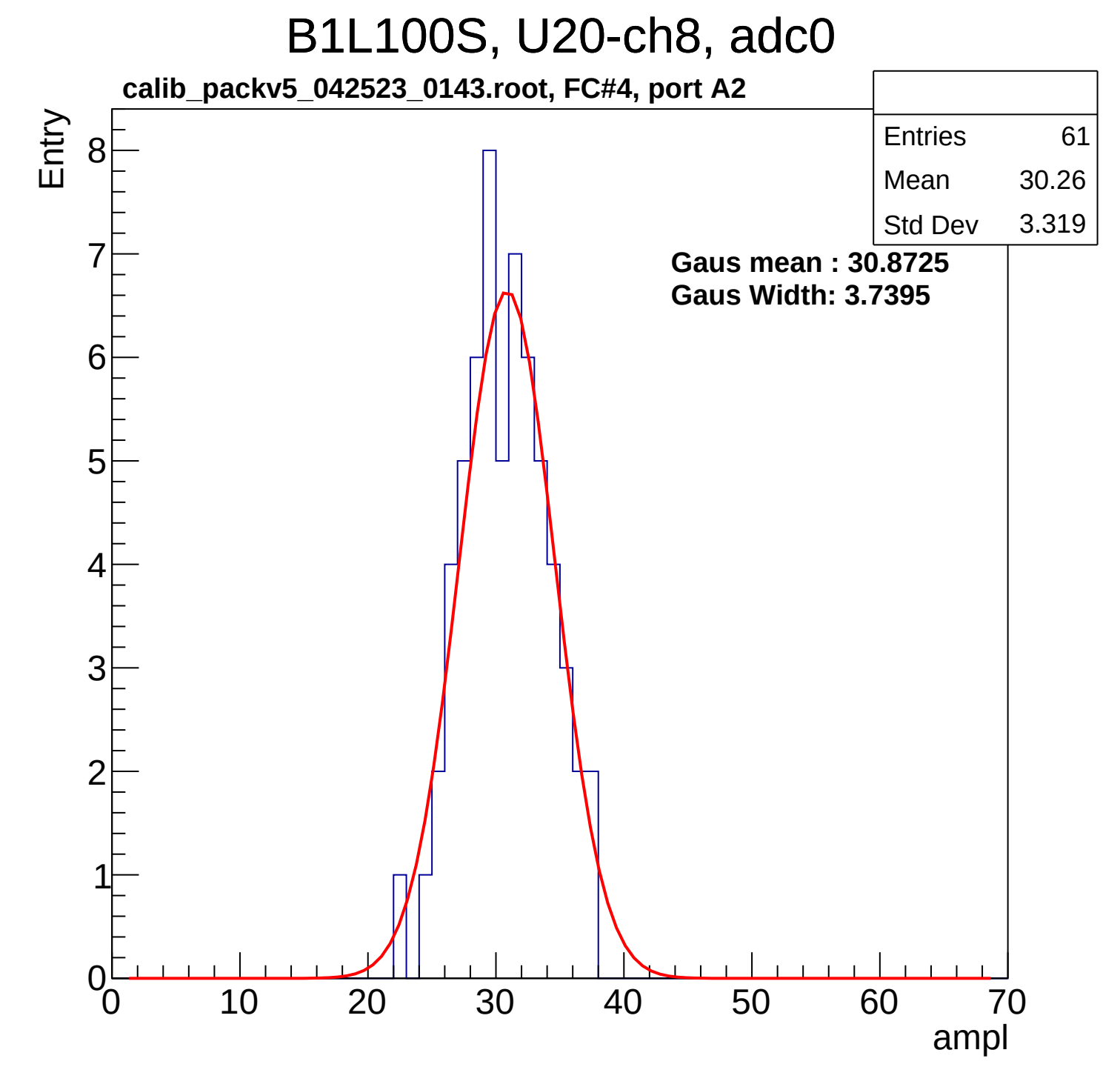
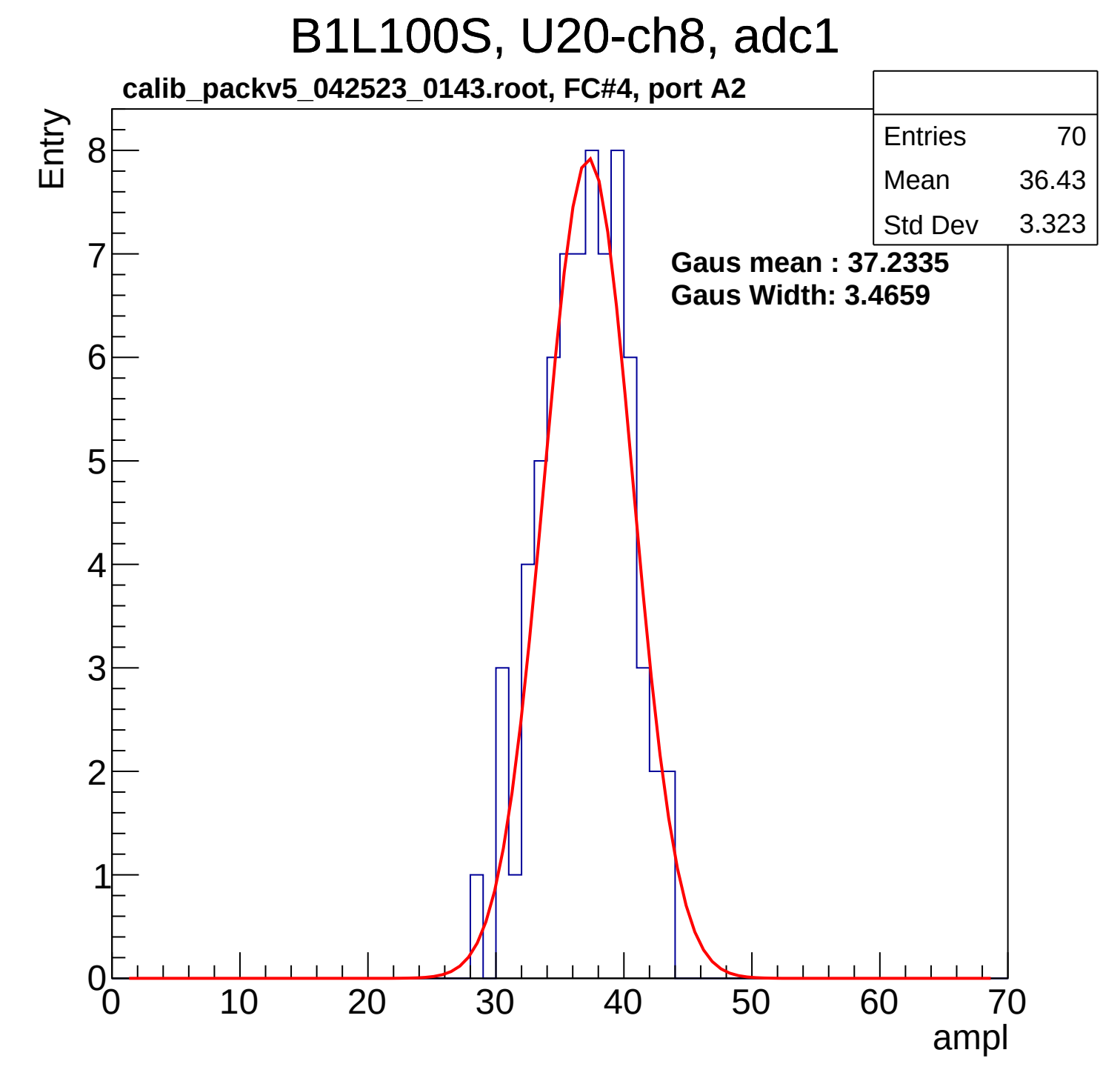

B1L100S, U20-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 30.26
Std Dev 3.319
Gaus mean : 30.8725
Gaus Width: 3.7395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.43
Std Dev 3.323
Gaus mean : 37.2335
Gaus Width: 3.4659
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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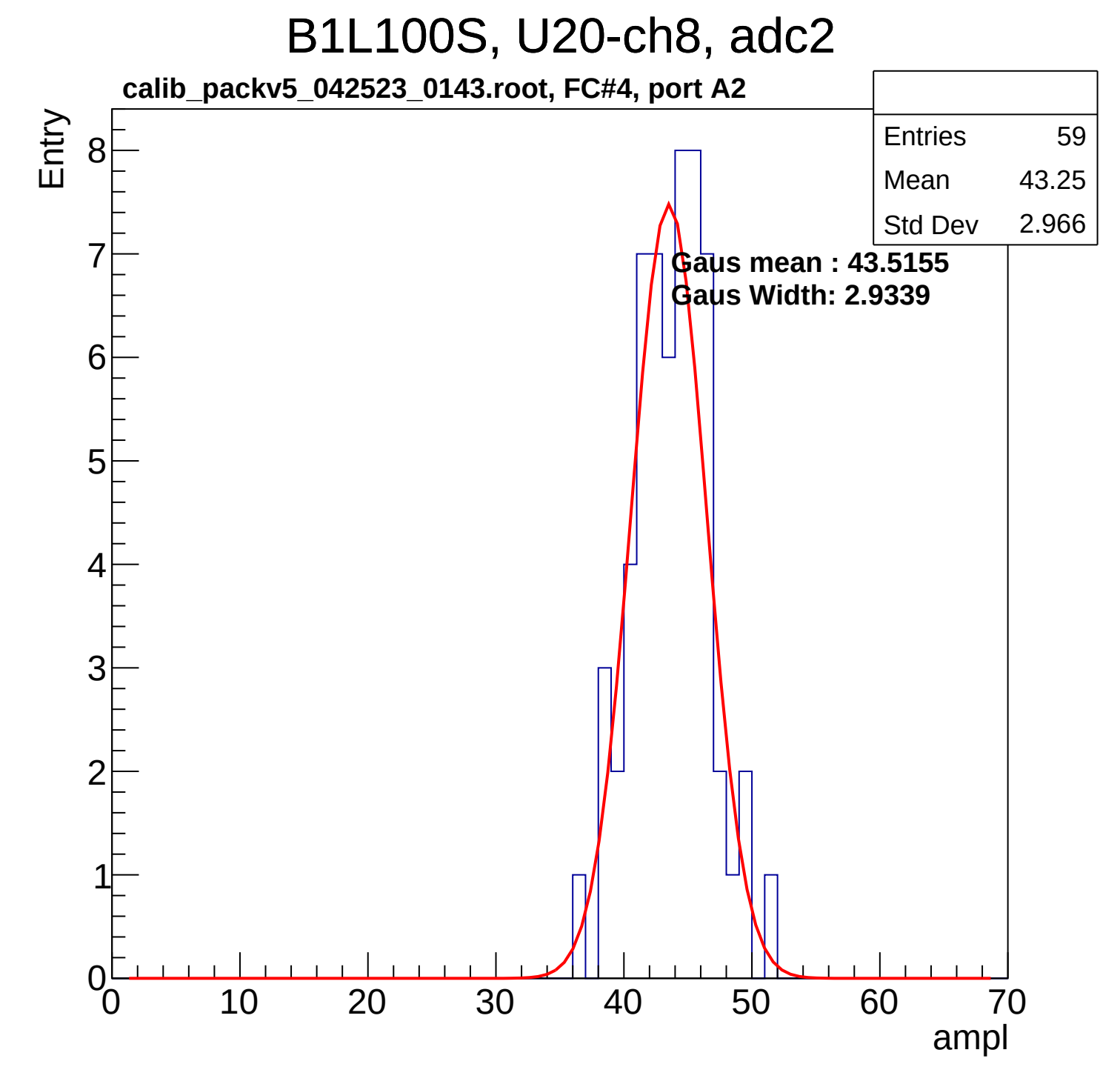
B1L100S, U20-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.25
Std Dev 2.966
Gaus mean : 43.5155
Gaus Width: 2.9339
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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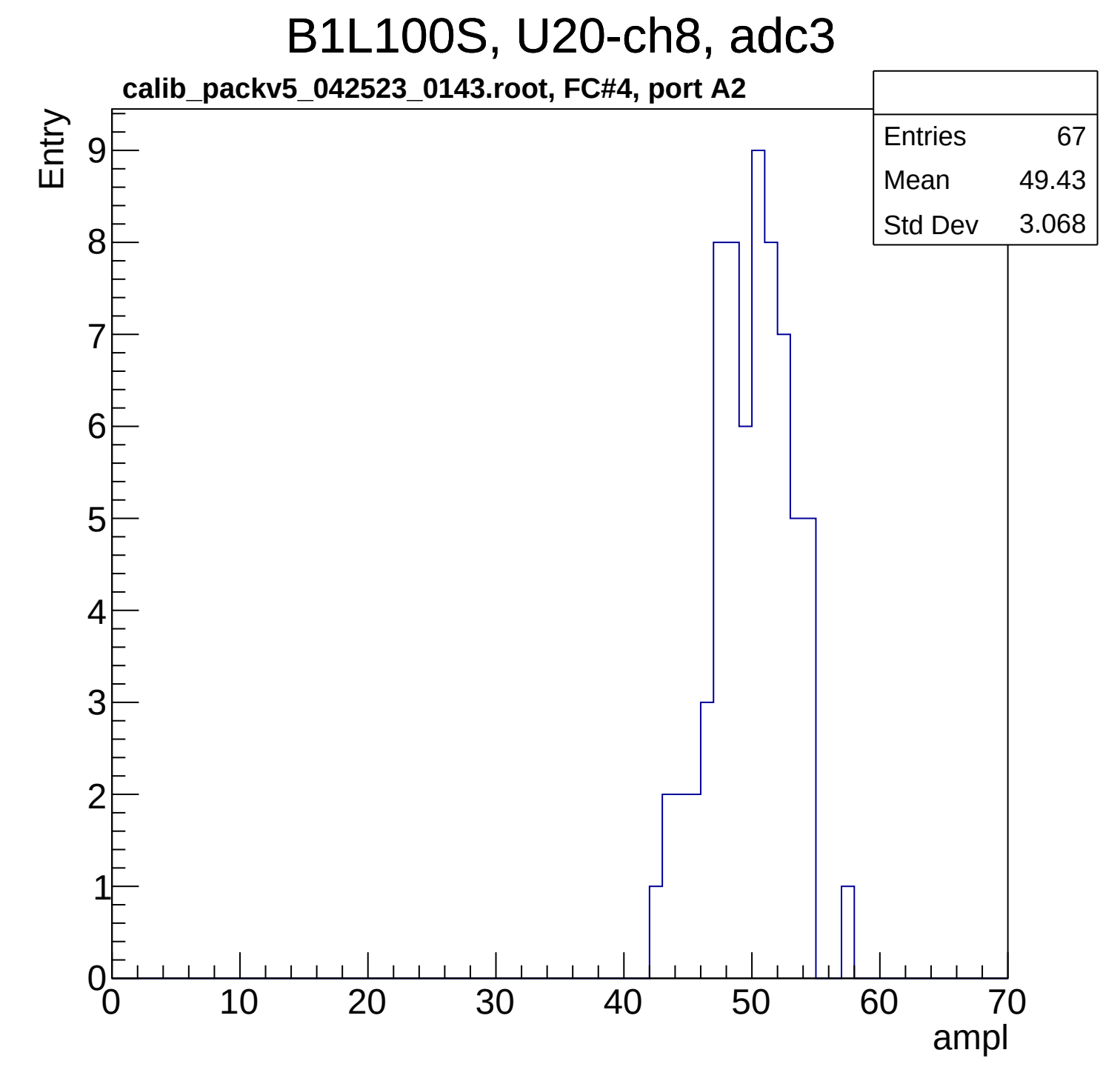
B1L100S, U20-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.43
Std Dev 3.068
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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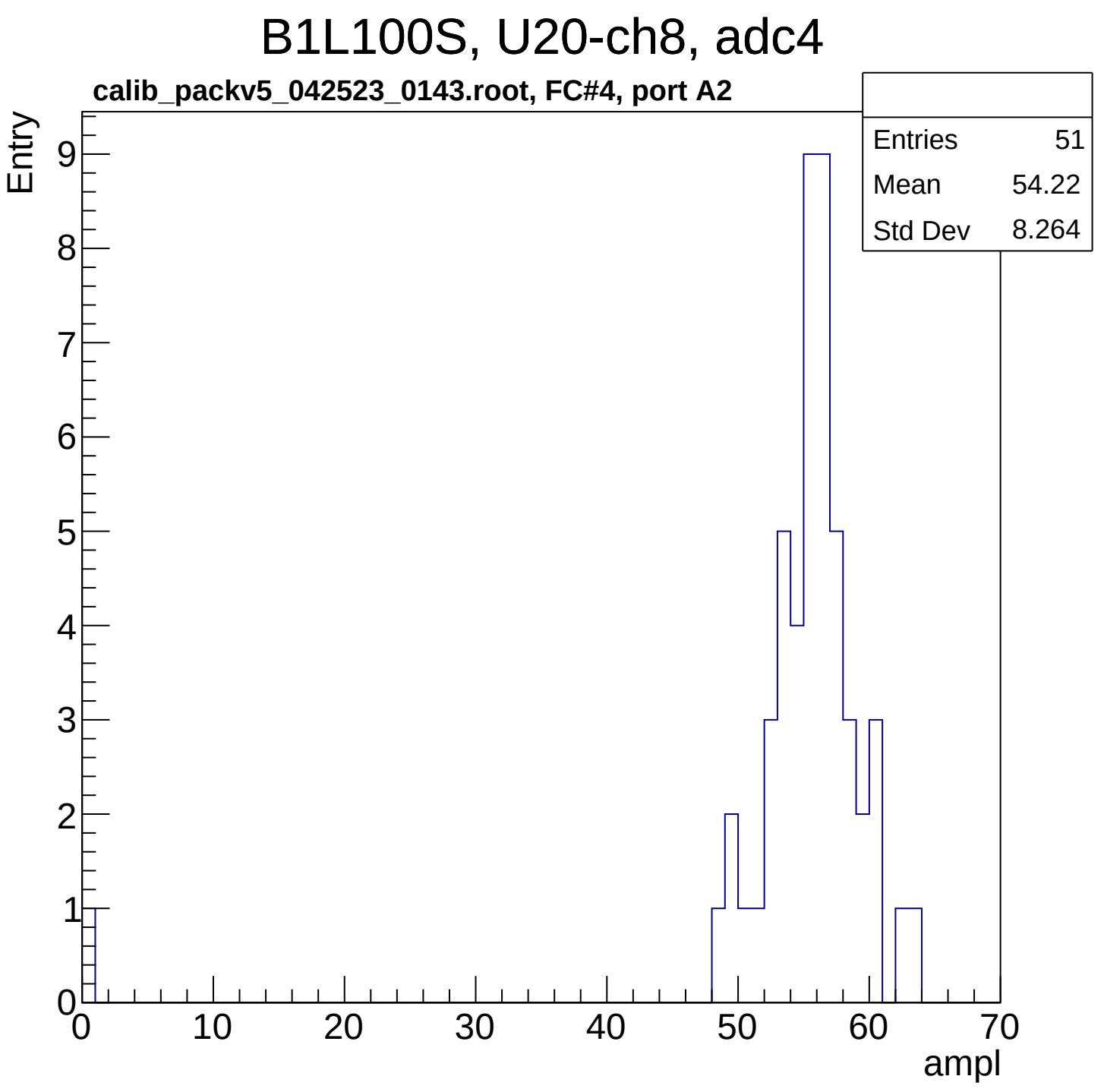
B1L100S, U20-ch8, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.22
Std Dev 8.264
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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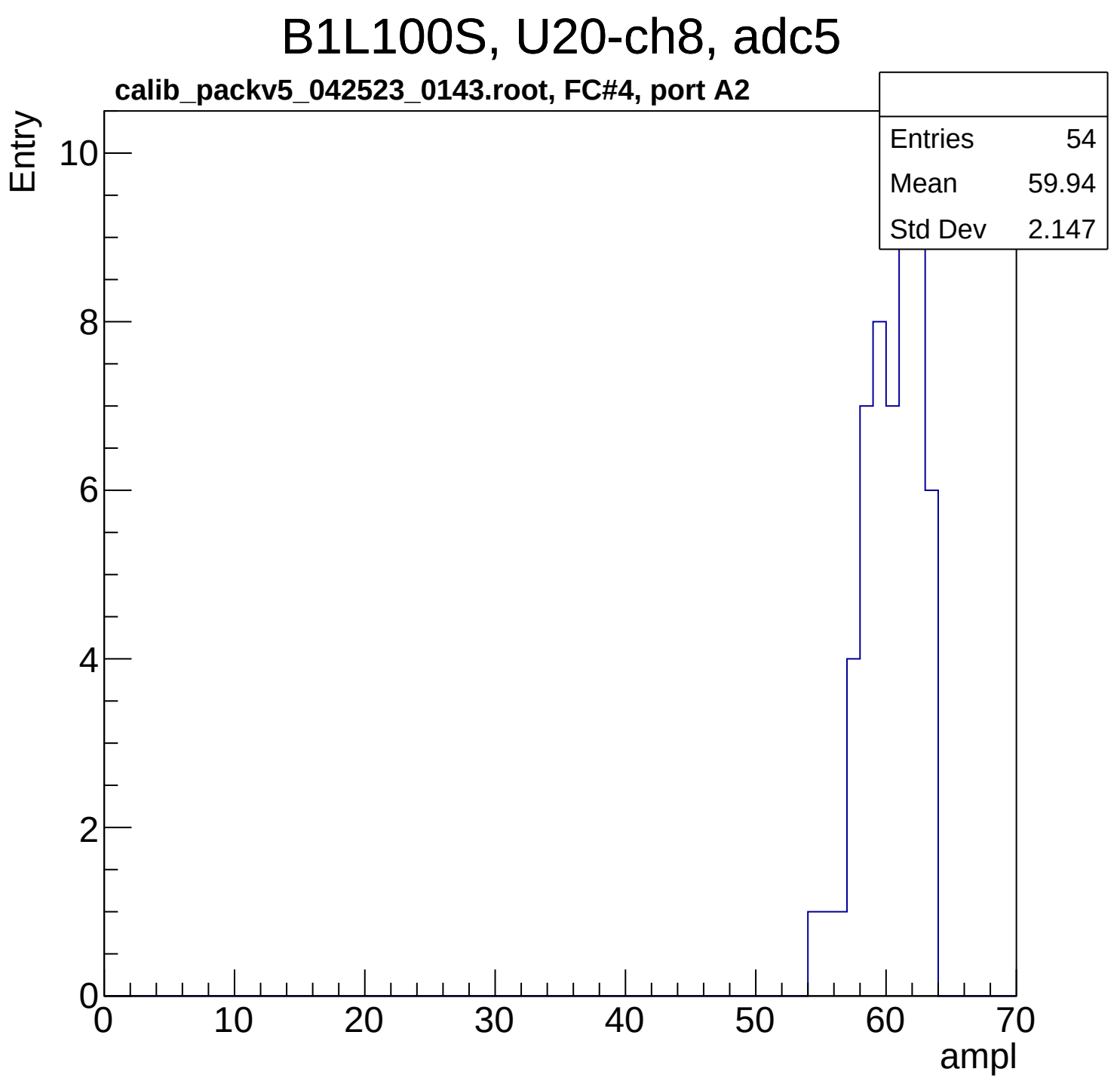
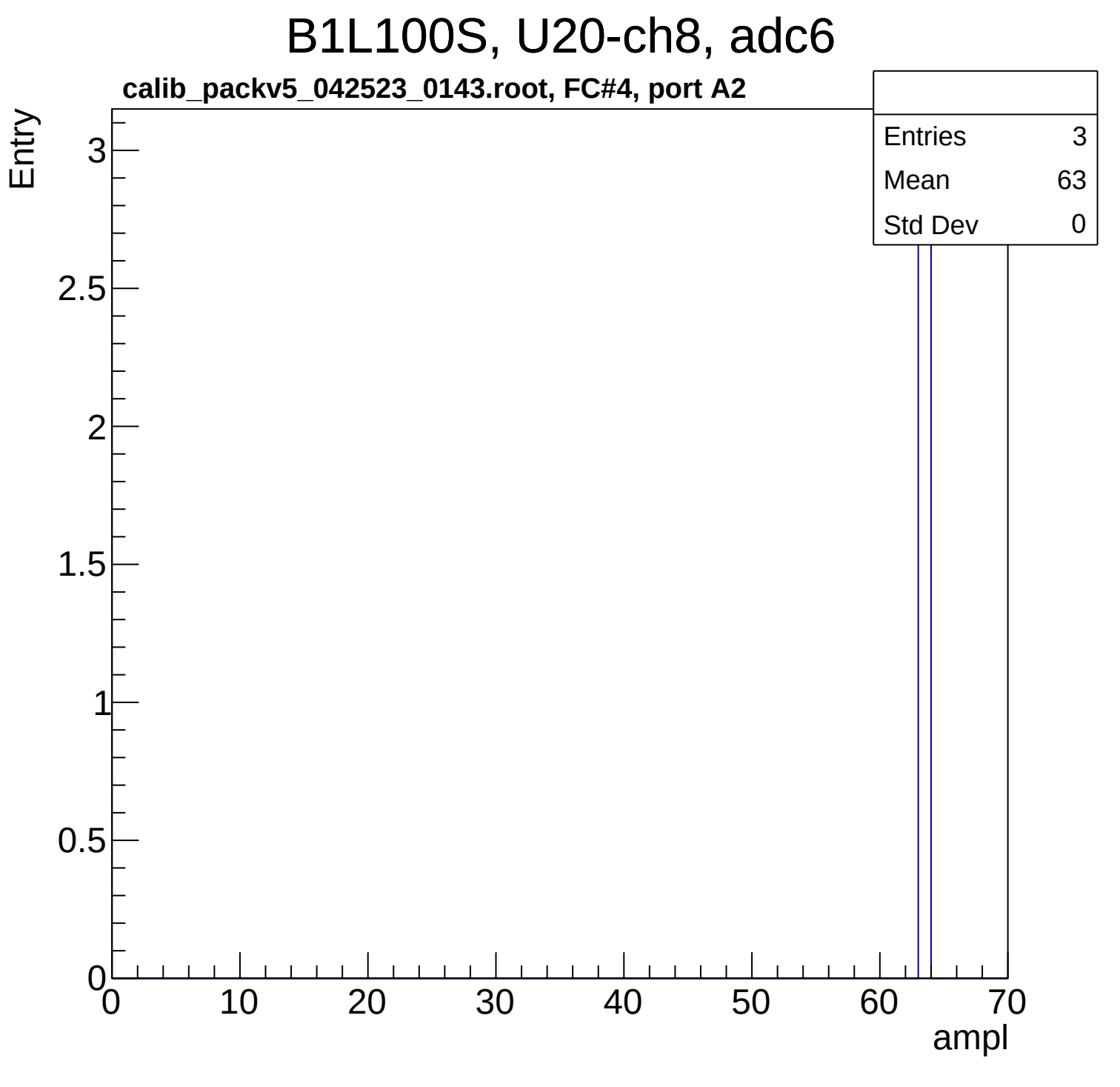
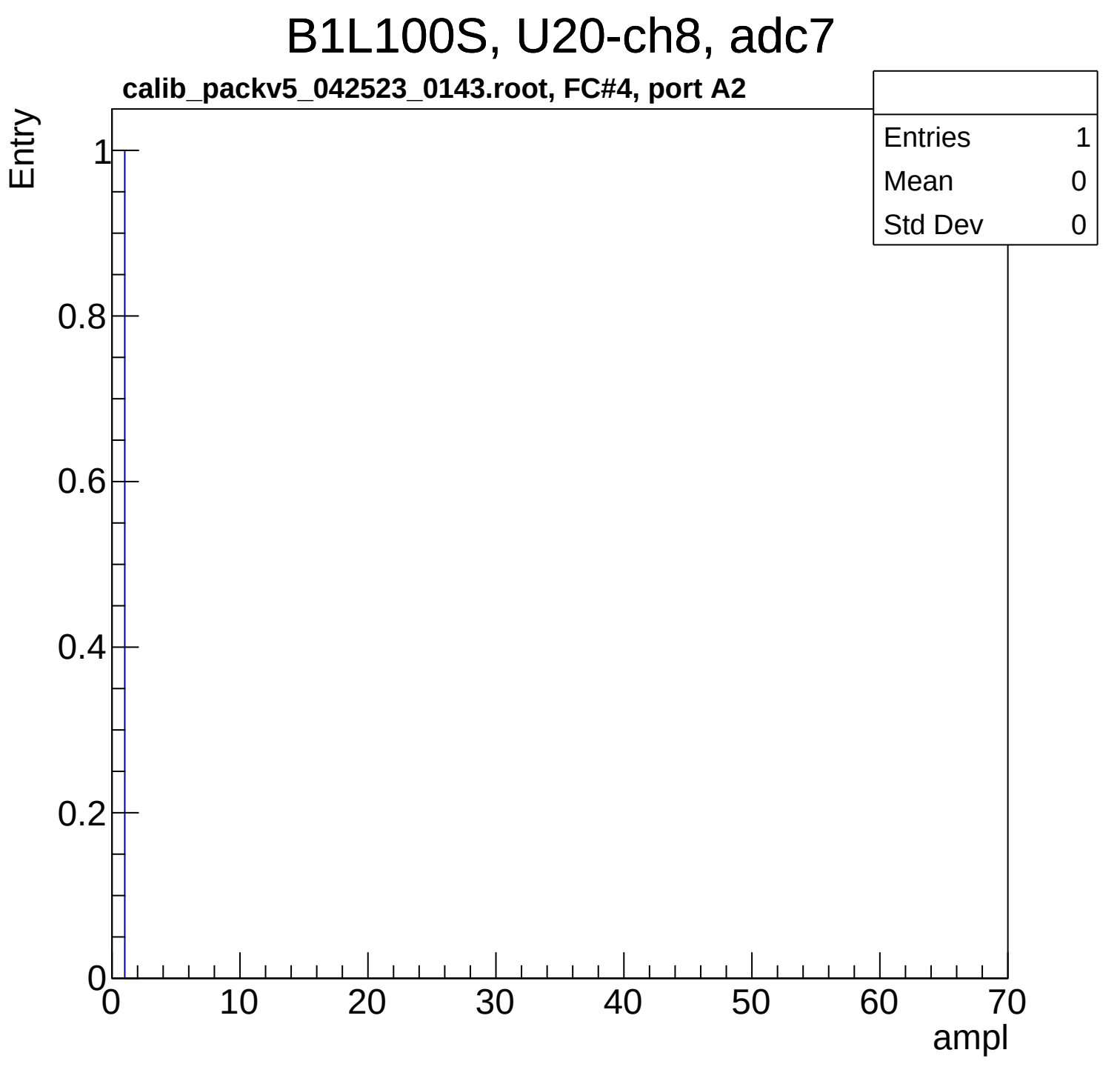
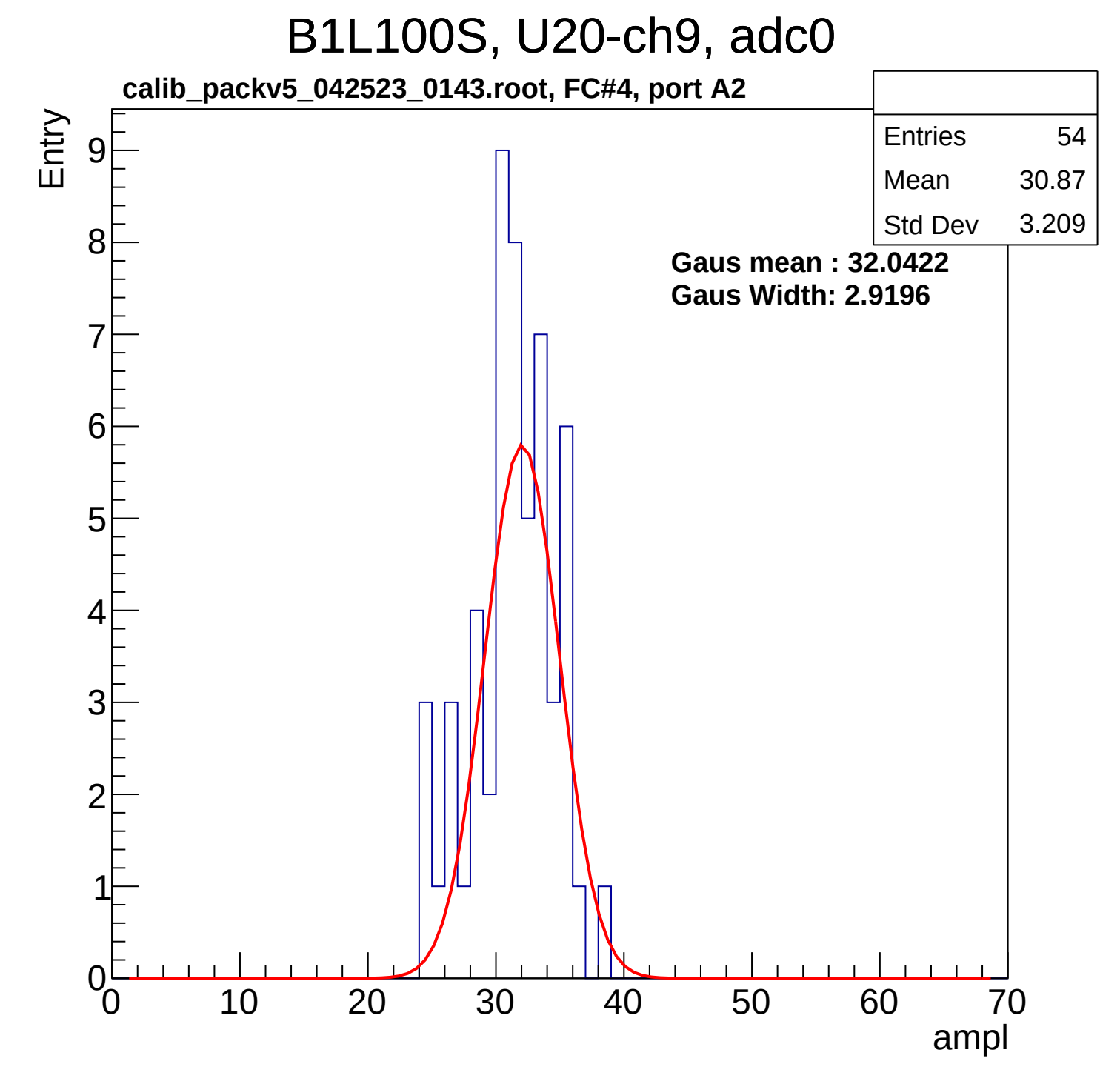
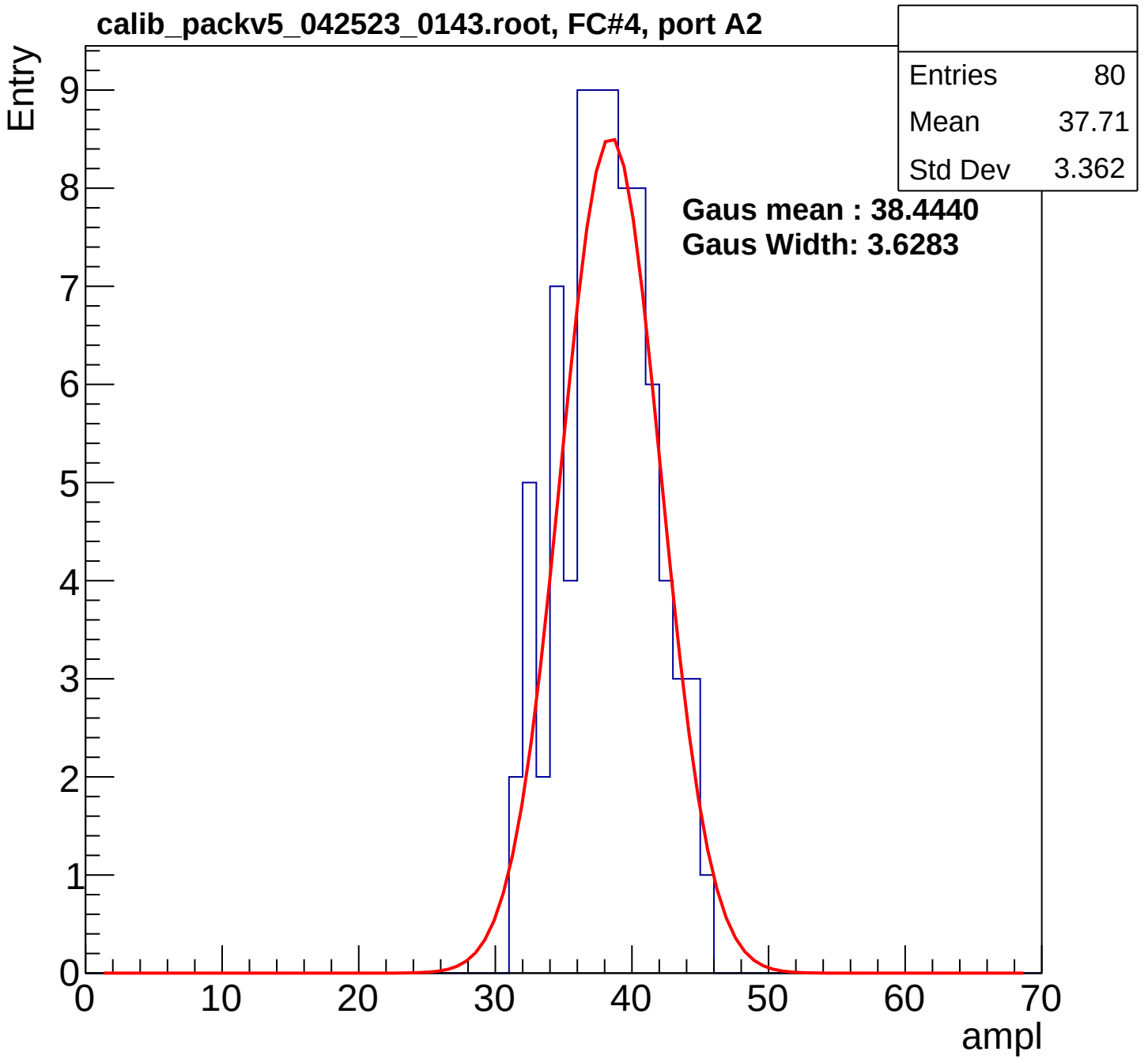

B1L100S, U20-ch8, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 59.94
Std Dev 2.147
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch8, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 63
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 30.87
Std Dev 3.209
Gaus mean : 32.0422
Gaus Width: 2.9196
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.71
Std Dev 3.362
Gaus mean : 38.4440
Gaus Width: 3.6283
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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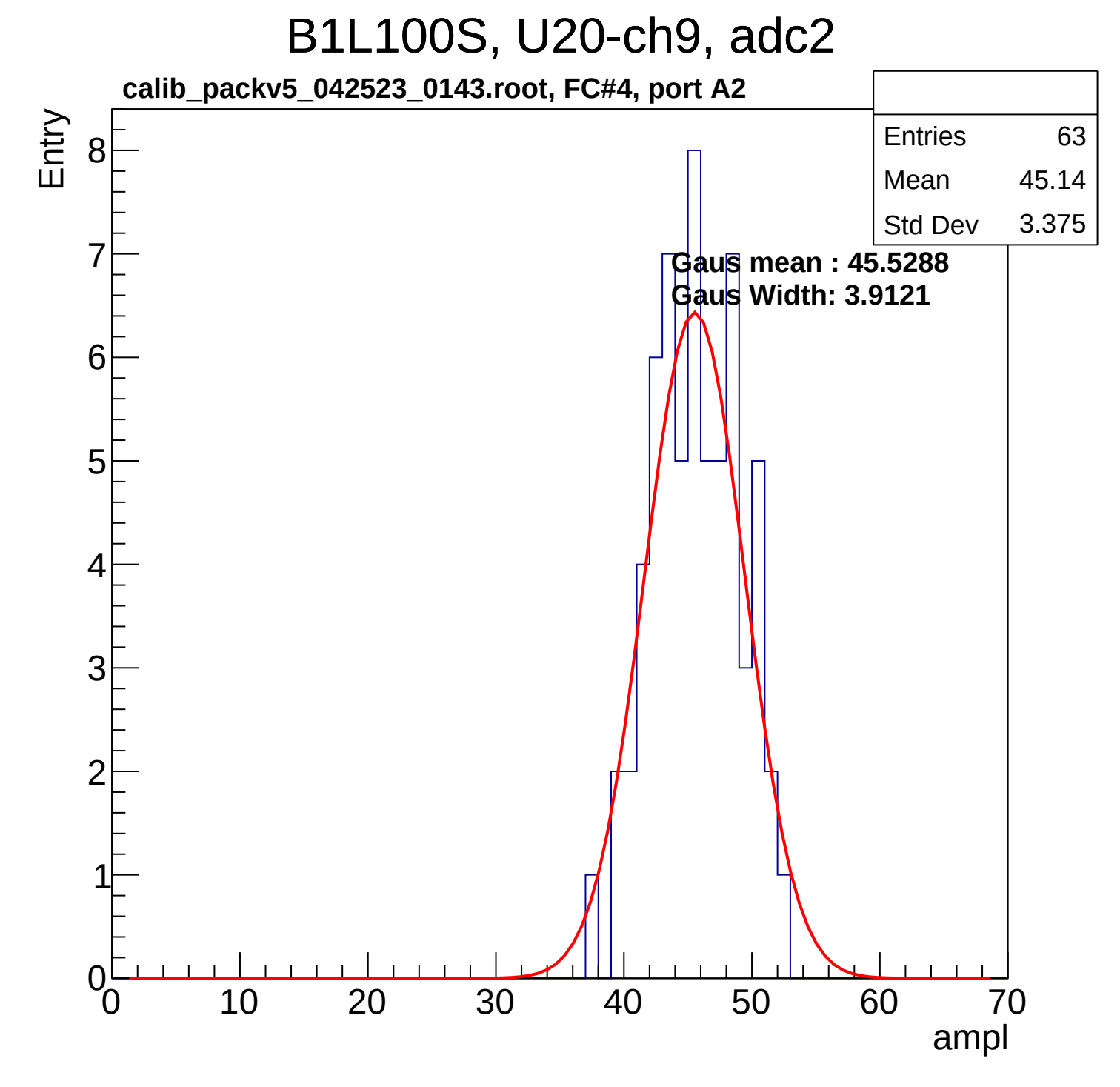
B1L100S, U20-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 45.14
Std Dev 3.375
Gaus mean : 45.5288
Gaus Width: 3.9121
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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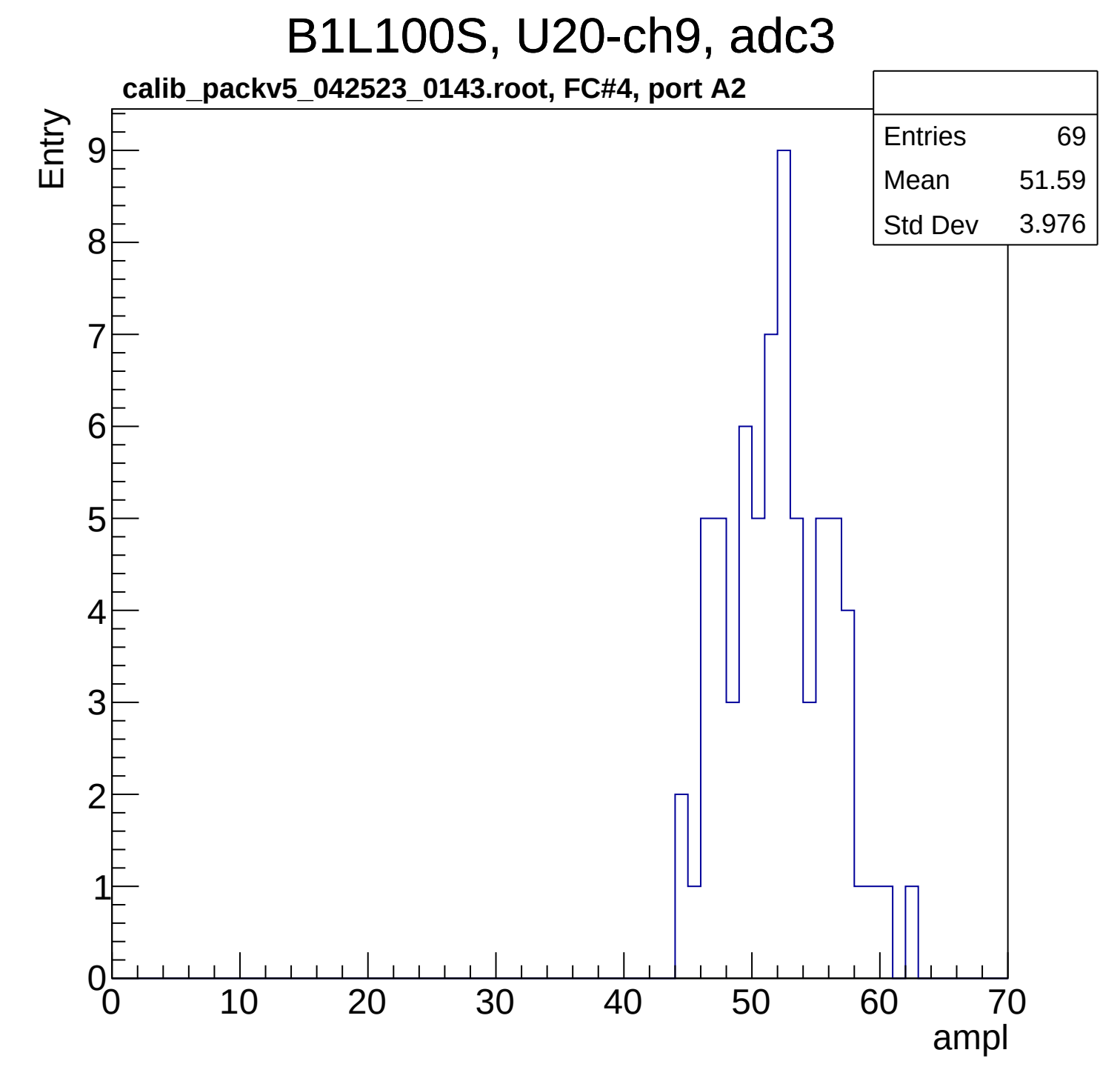
B1L100S, U20-ch9, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.59
Std Dev 3.976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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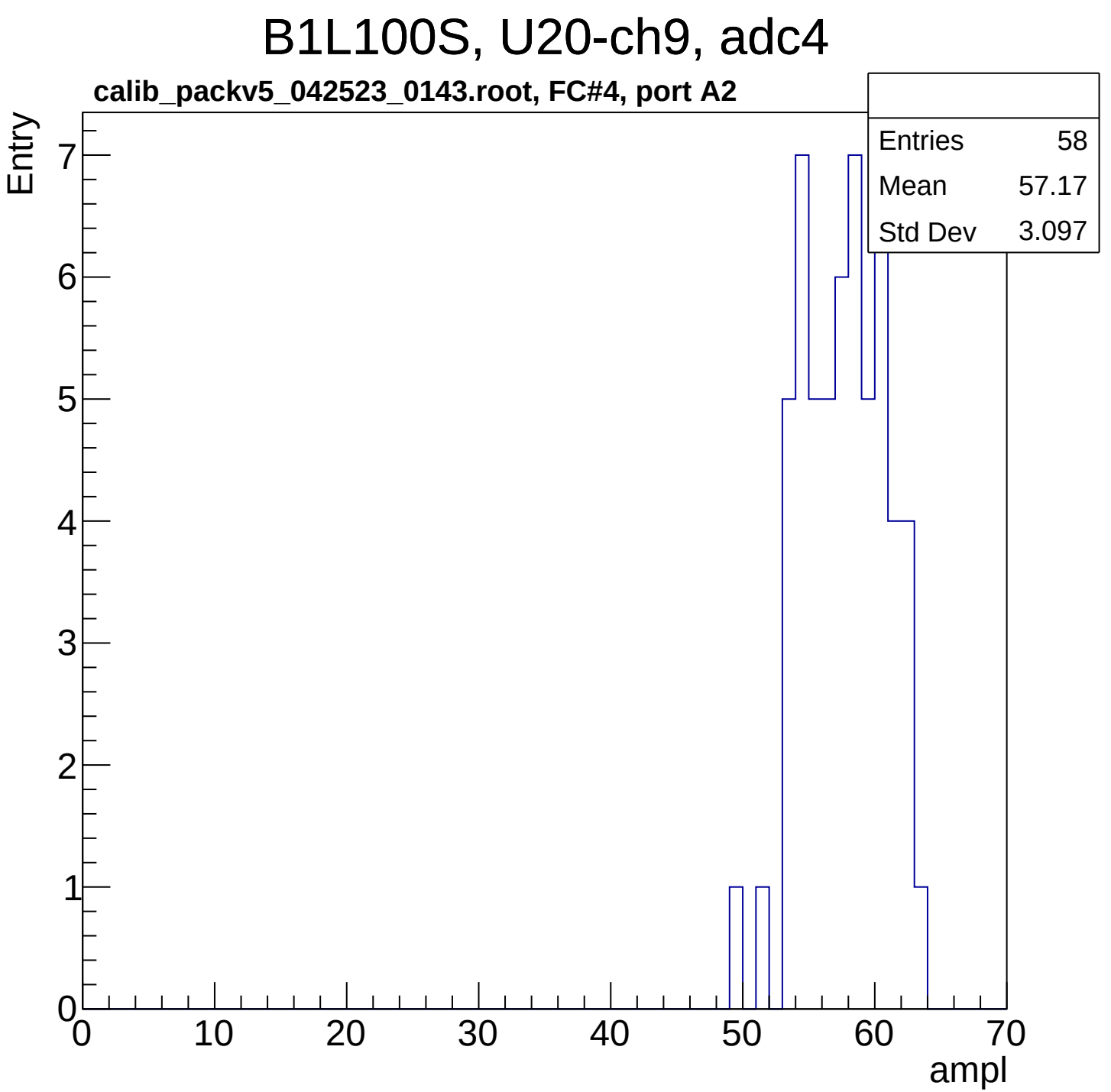
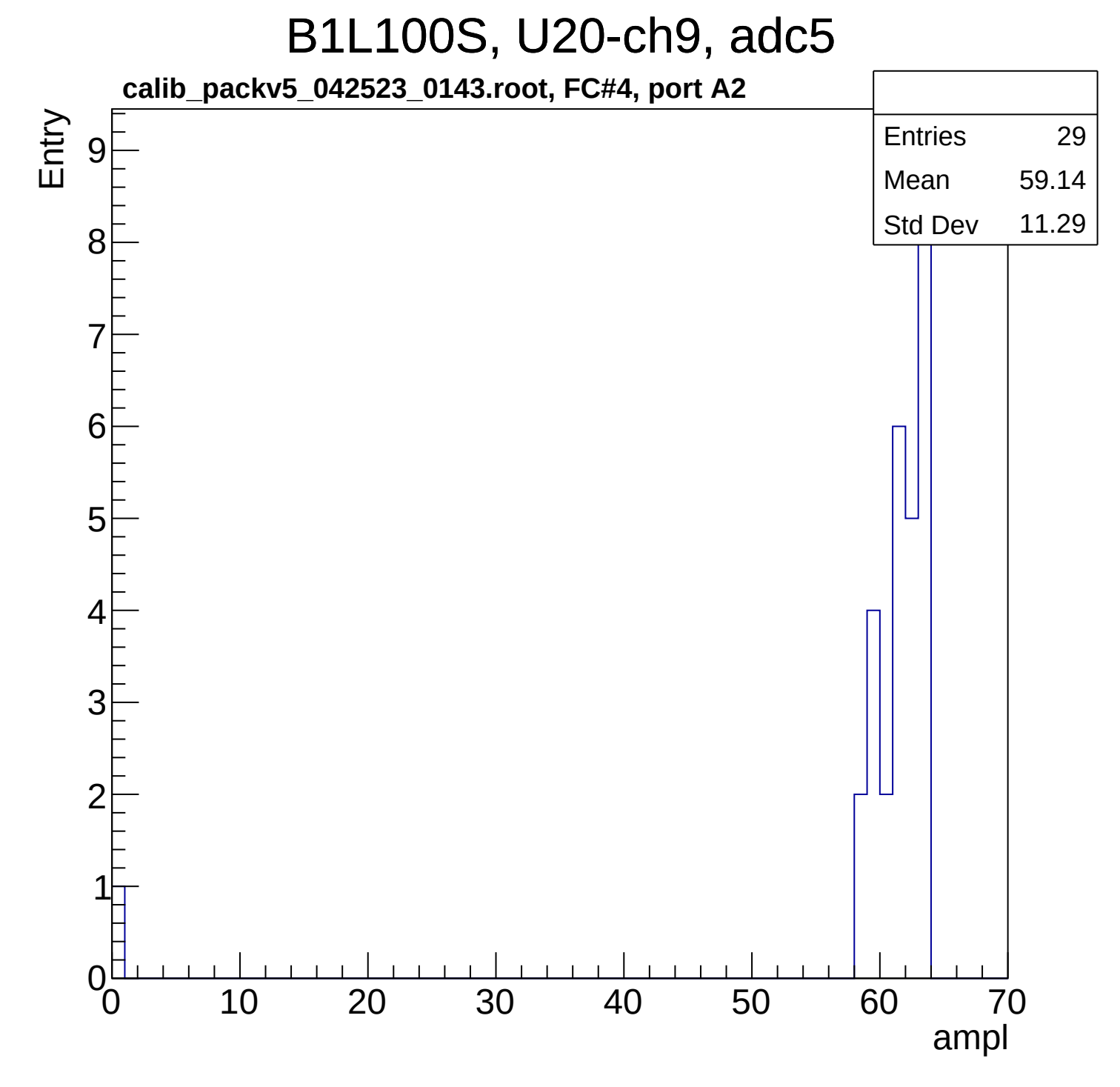
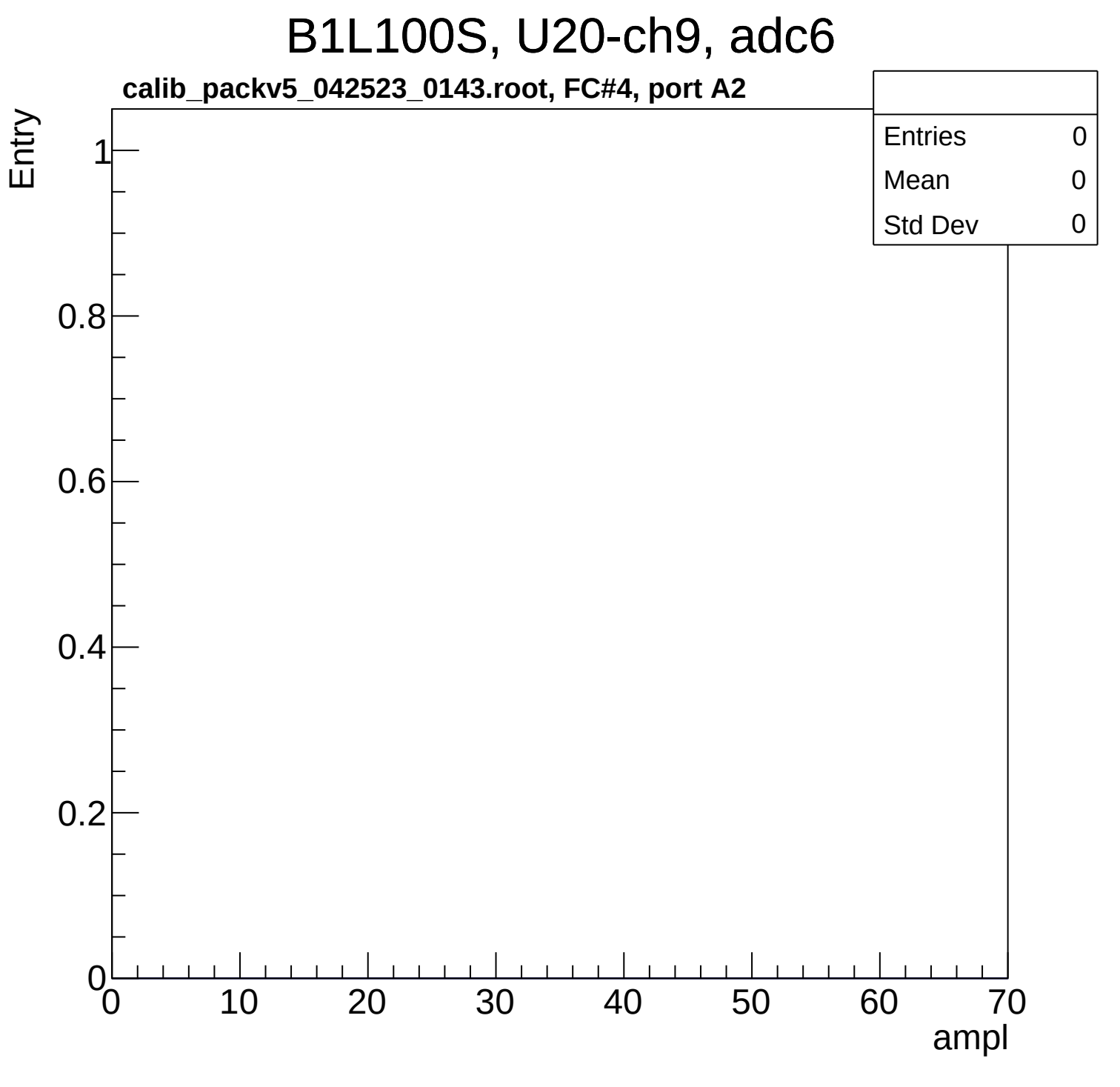
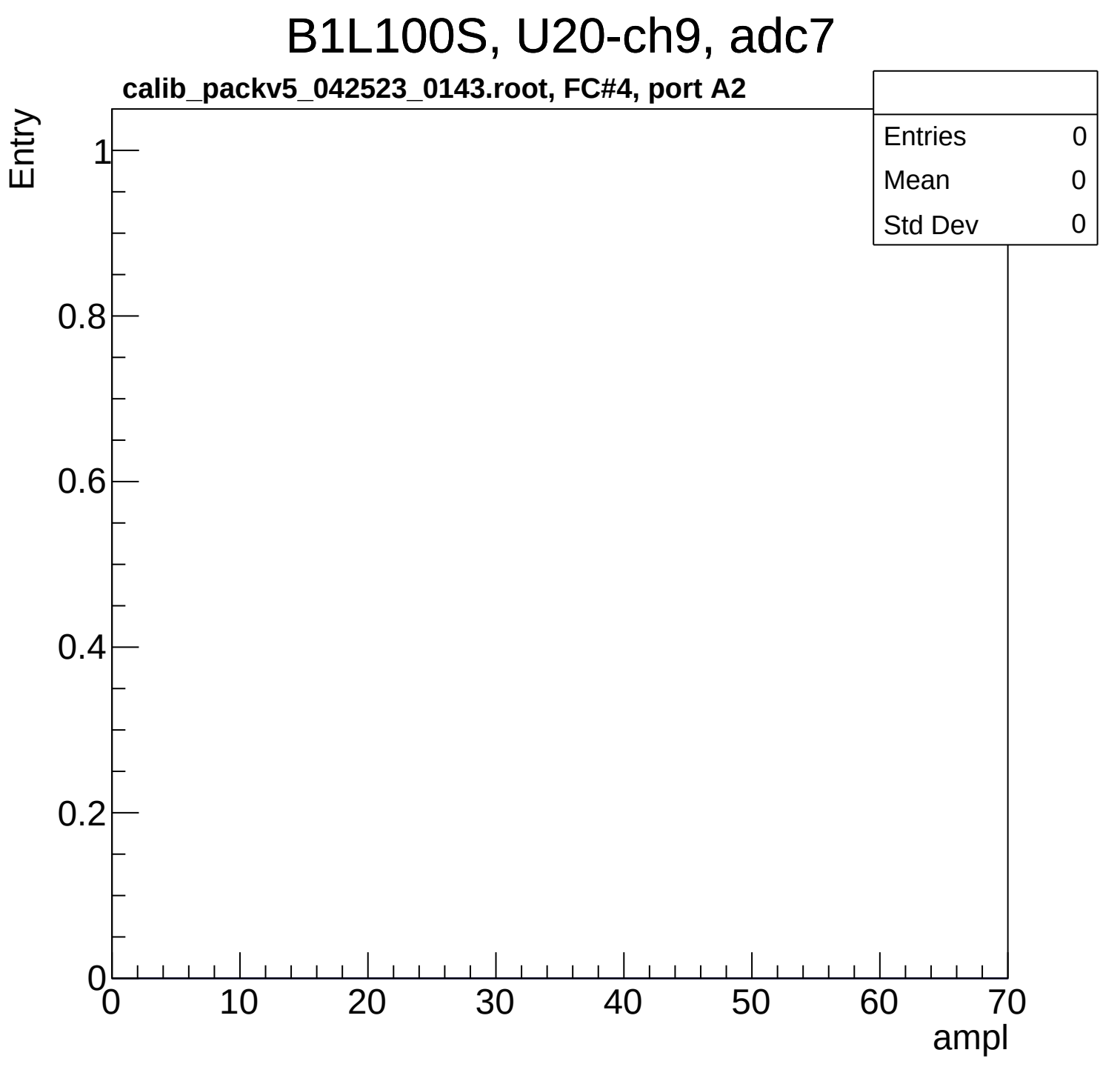
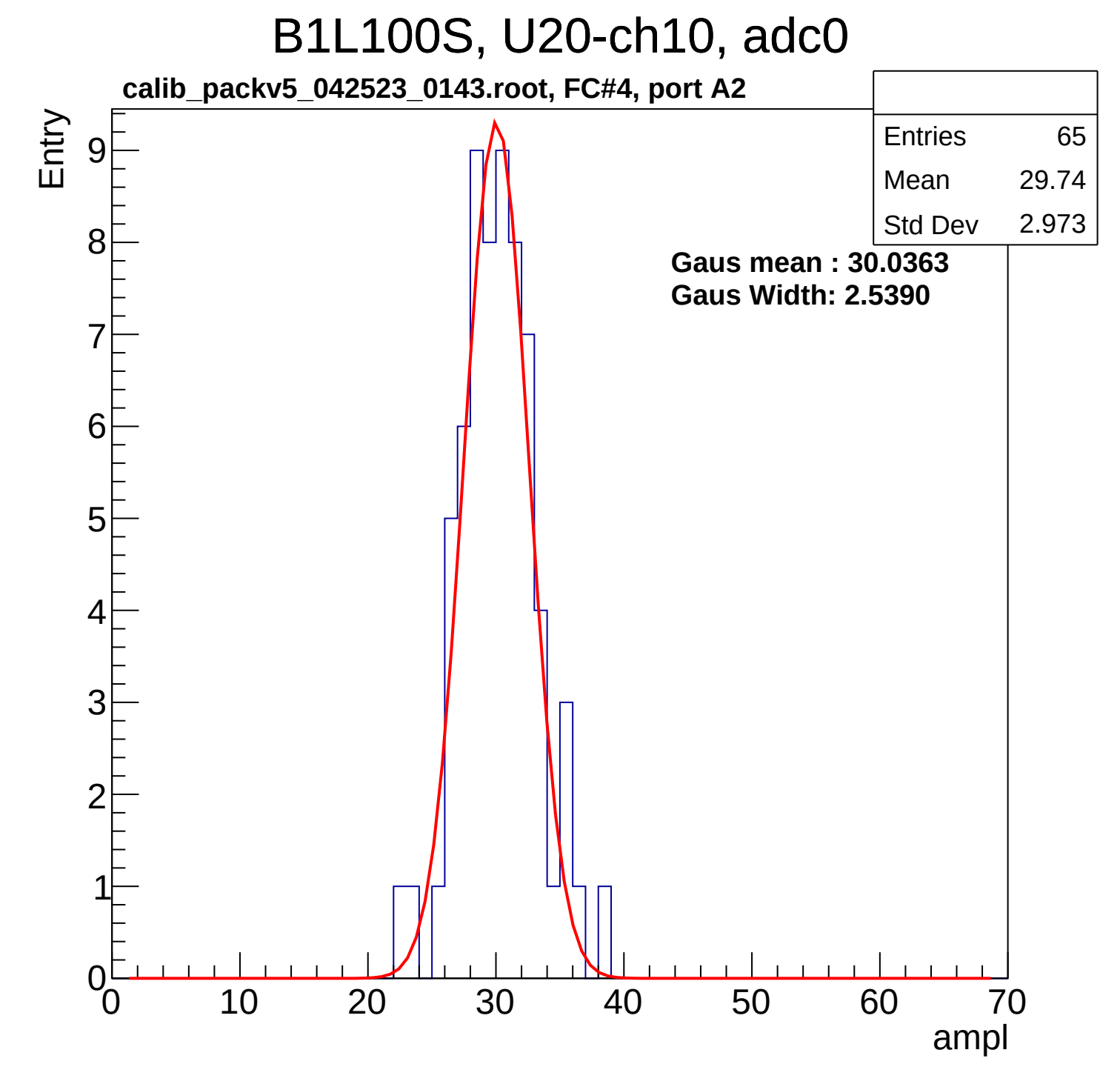
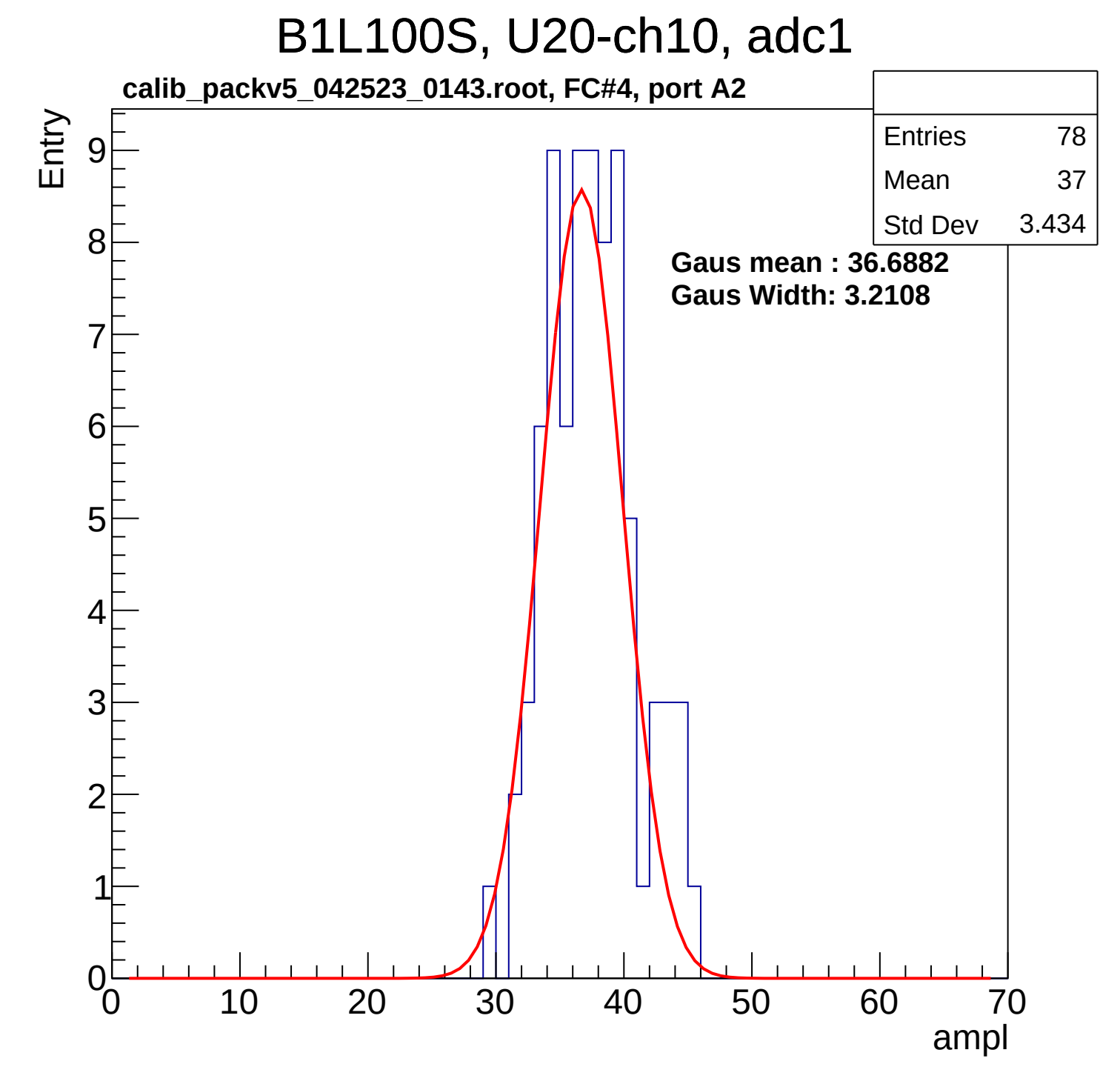

B1L100S, U20-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.17
Std Dev 3.097
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

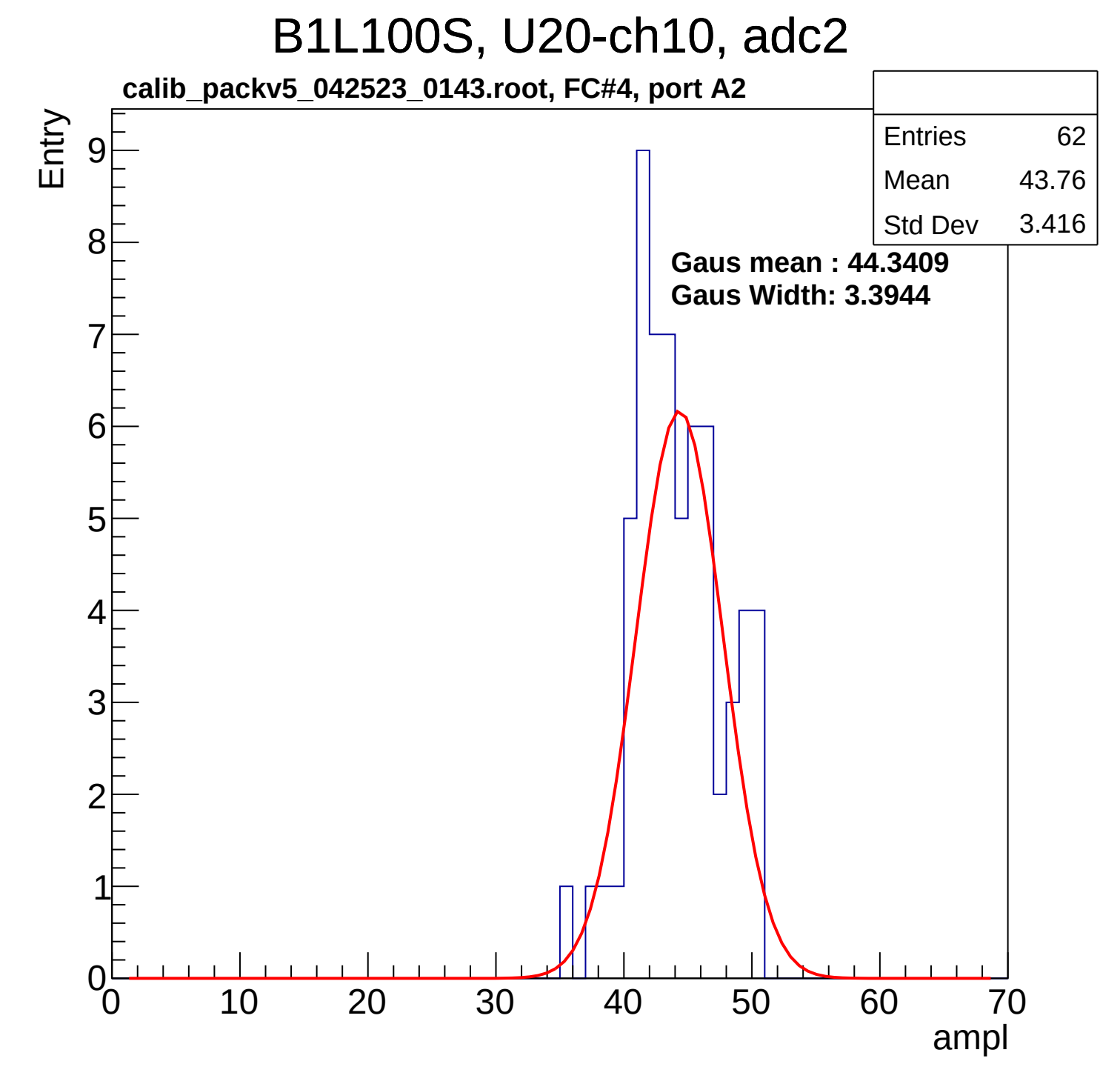

B1L100S, U20-ch9, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 59.14
Std Dev 11.29
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

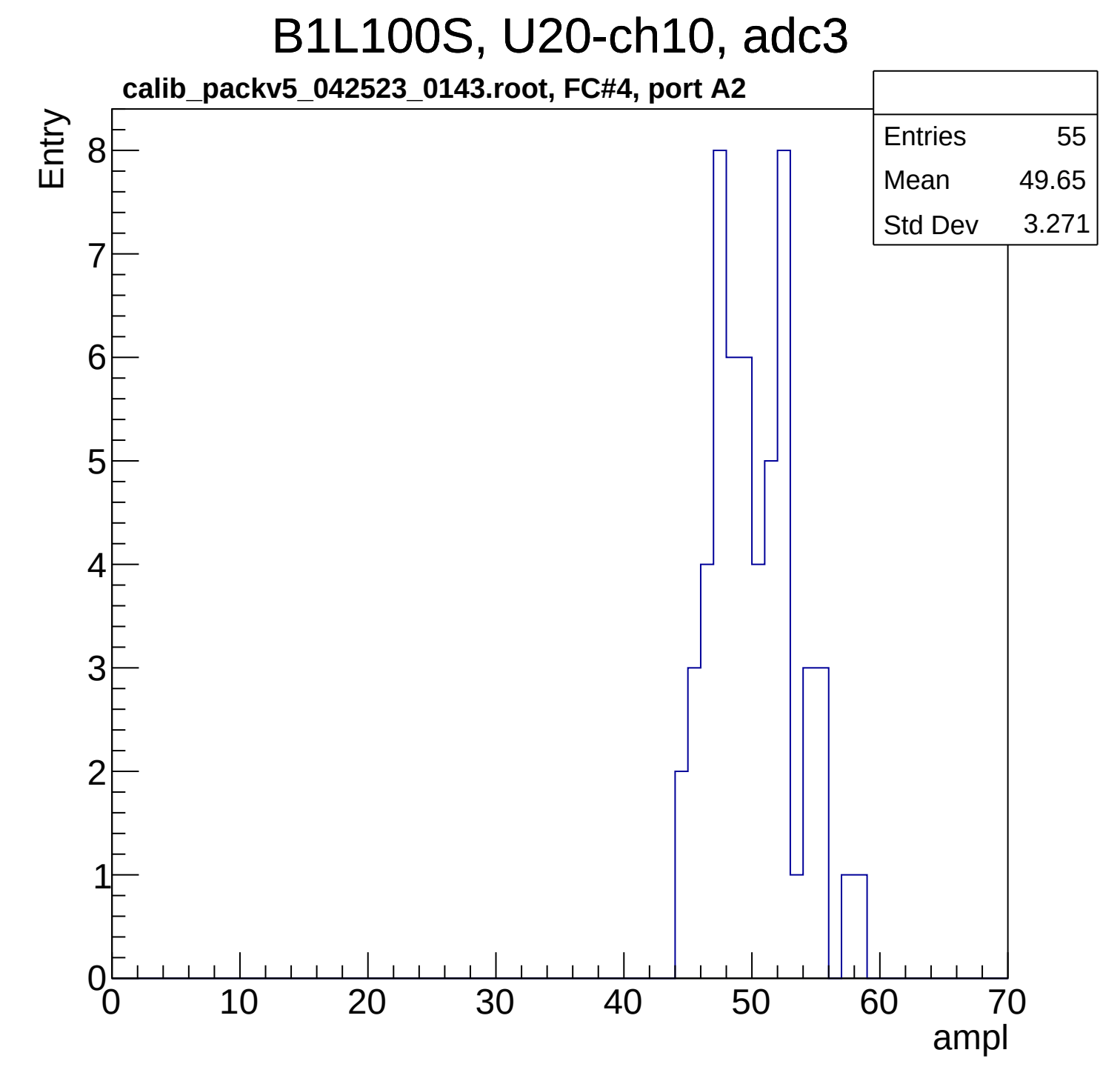

B1L100S, U20-ch9, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.74
Std Dev 2.973
Gaus mean : 30.0363
Gaus Width: 2.5390
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 37
Std Dev 3.434
Gaus mean : 36.6882
Gaus Width: 3.2108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.76
Std Dev 3.416
Gaus mean : 44.3409
Gaus Width: 3.3944
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 49.65
Std Dev 3.271
Entry
ampl
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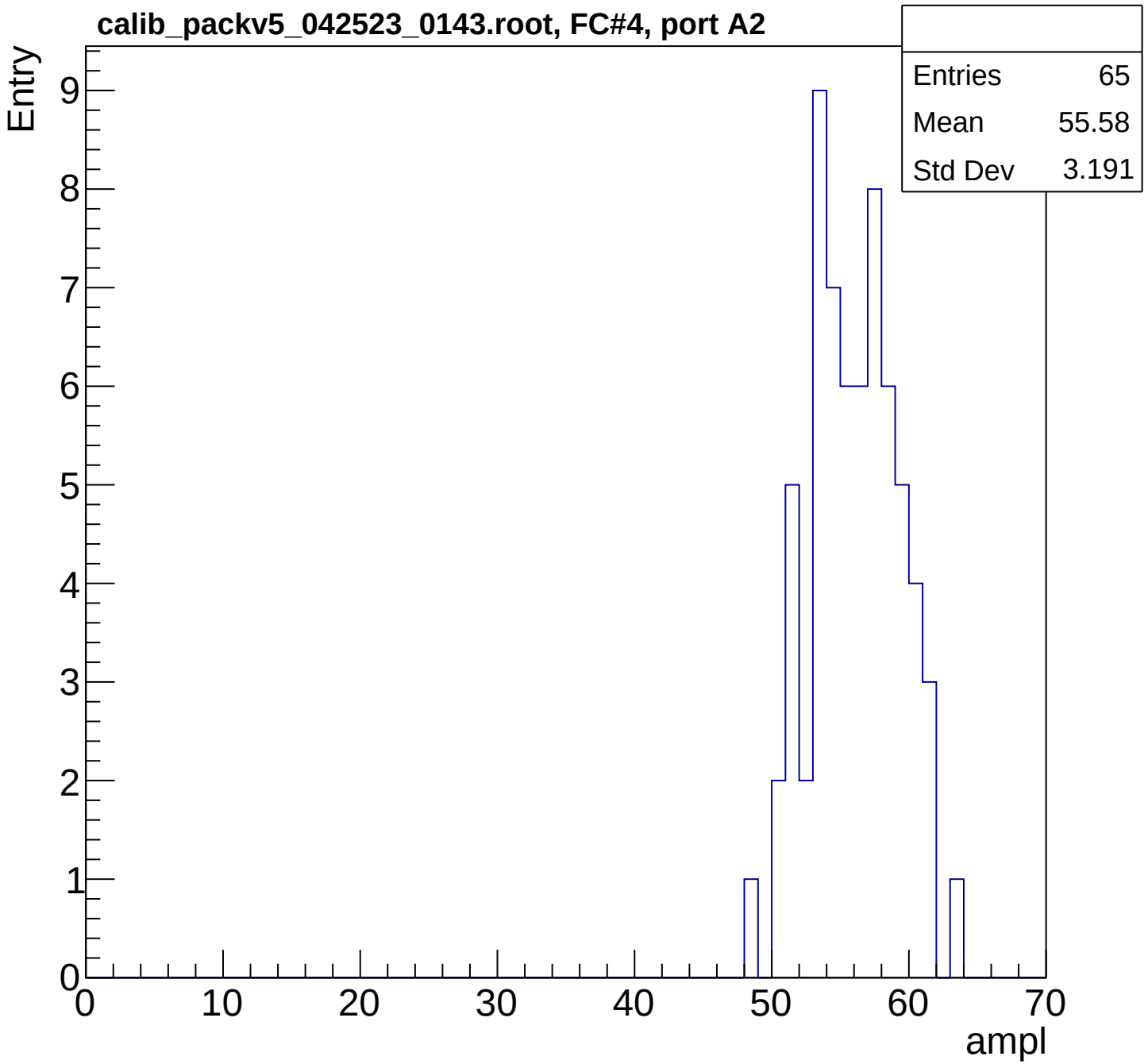
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.58
Std Dev 3.191
Entry
ampl
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2
3
4
5
6
7
8
9
0
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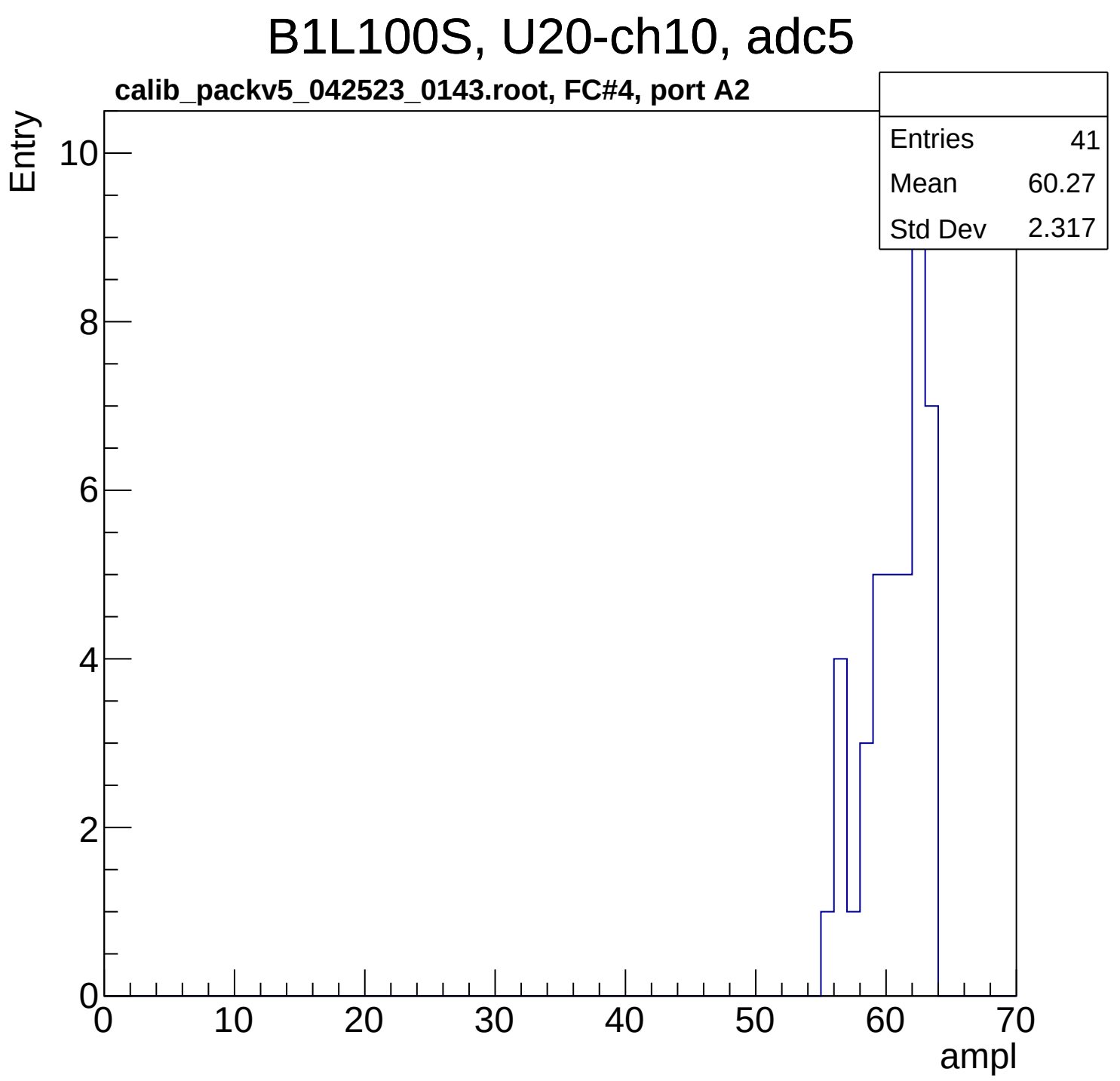
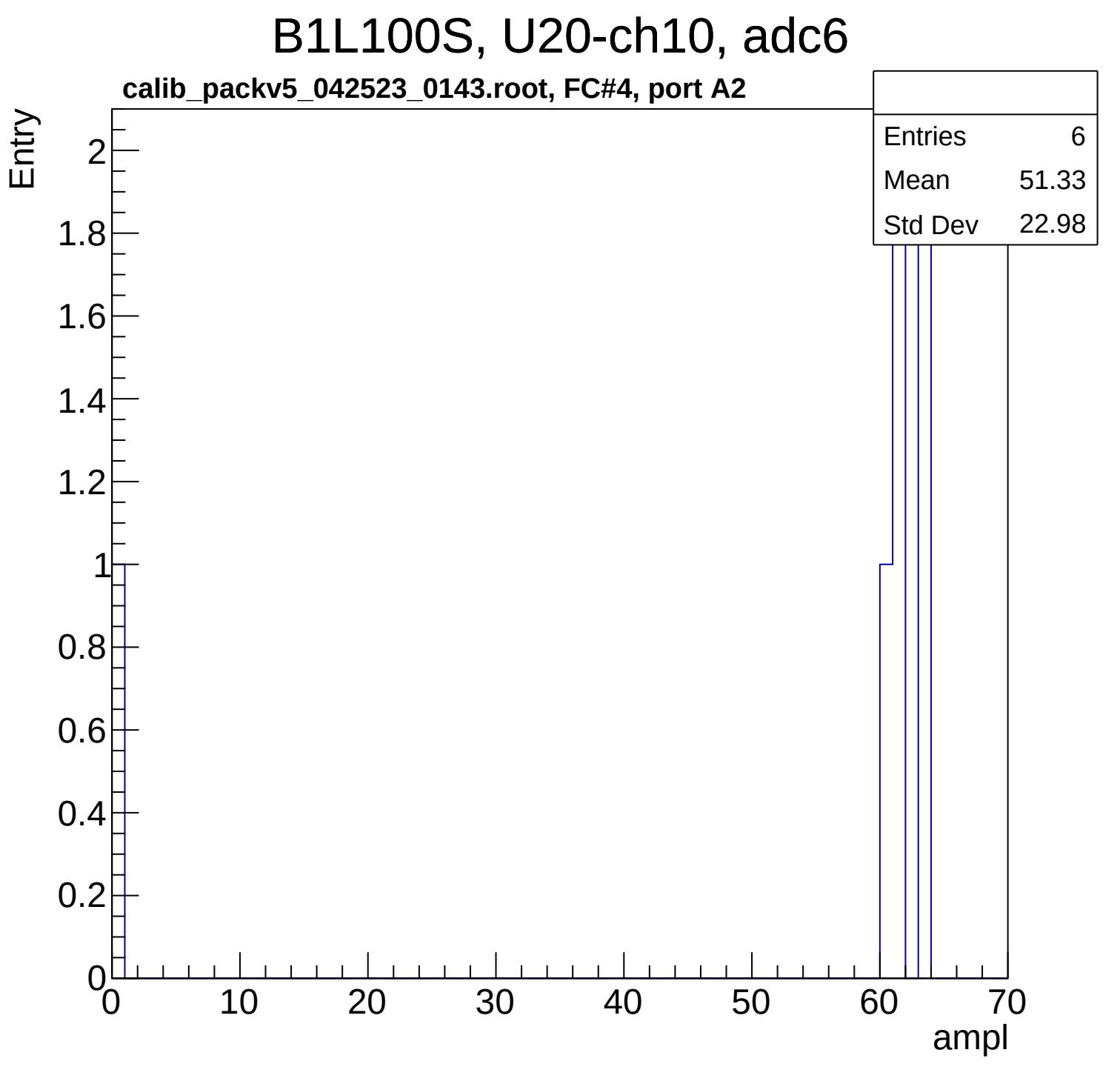
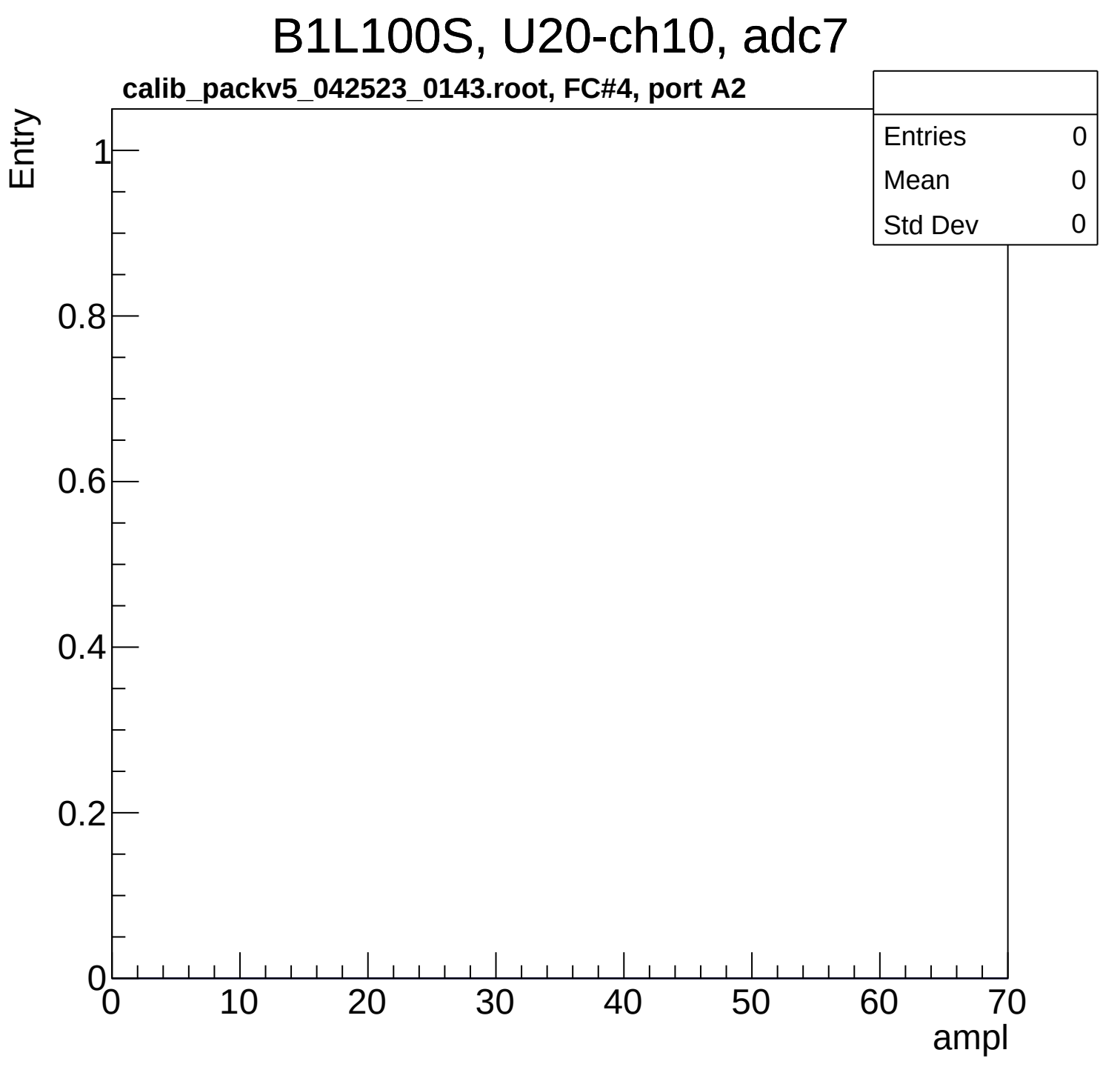
20
30
40
50
60
70


B1L100S, U20-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 60.27
Std Dev 2.317
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch10, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.33
Std Dev 22.98
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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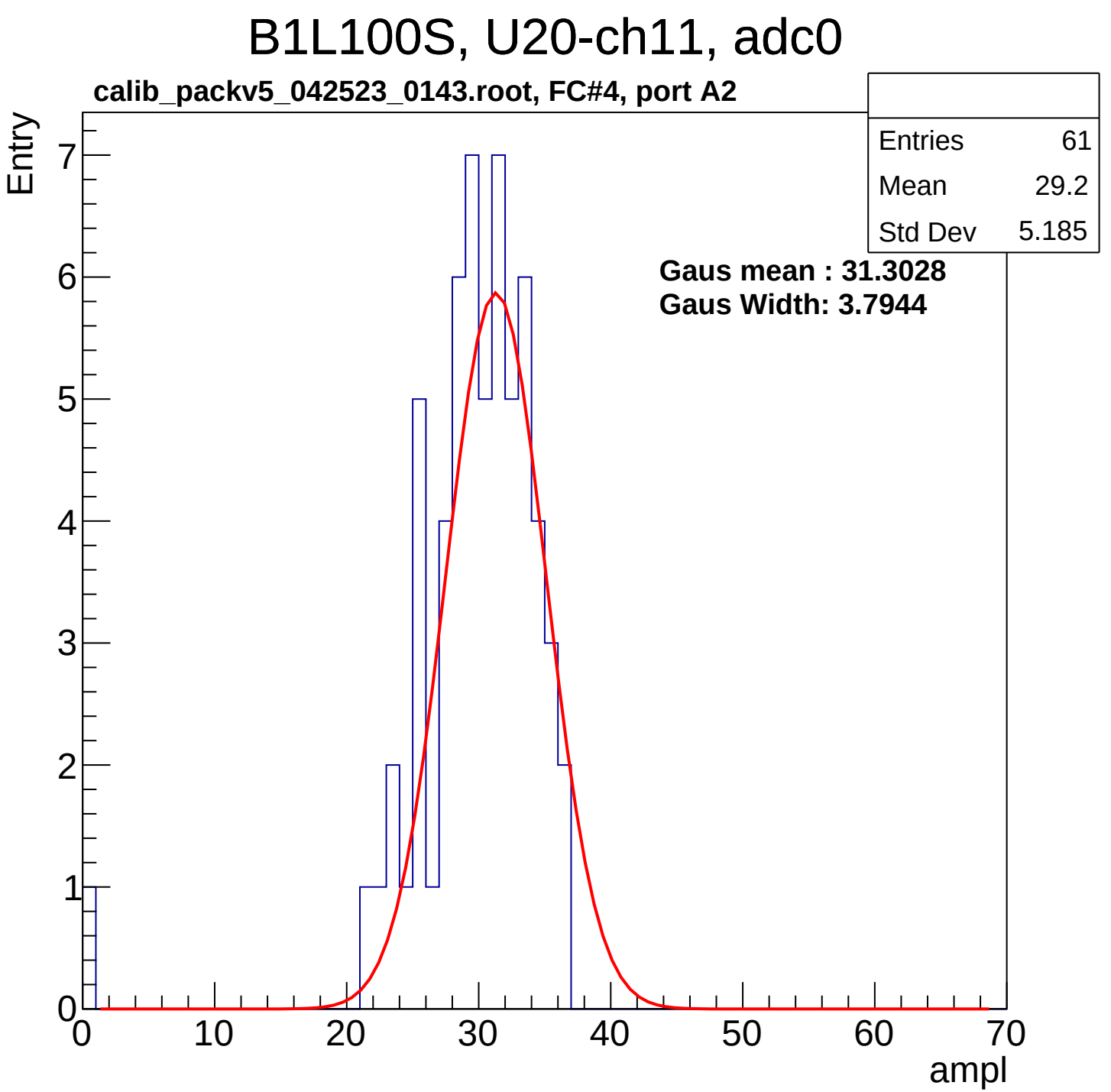
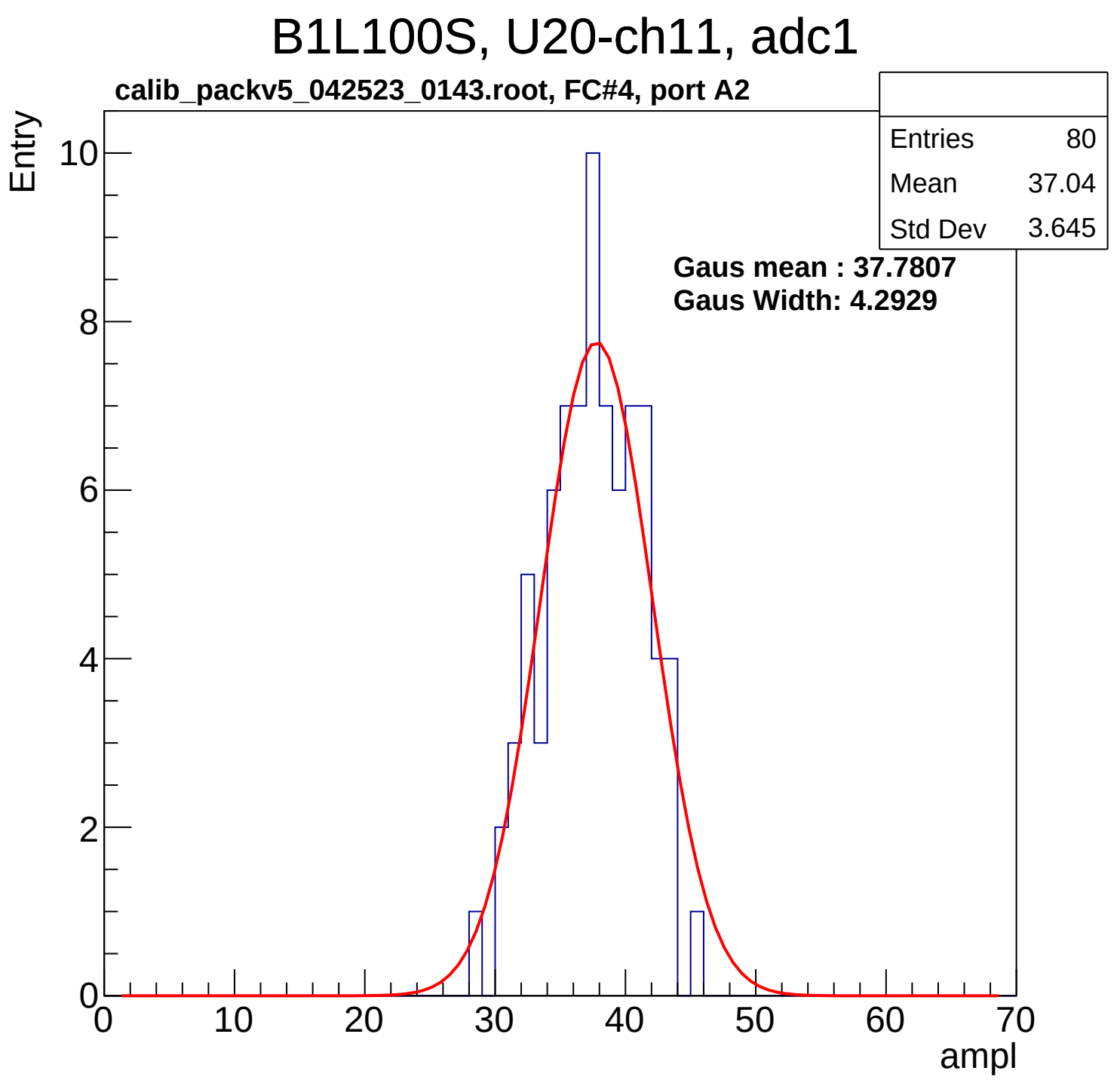
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.2
Std Dev 5.185
Gaus mean : 31.3028
Gaus Width: 3.7944
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.04
Std Dev 3.645
Gaus mean : 37.7807
Gaus Width: 4.2929
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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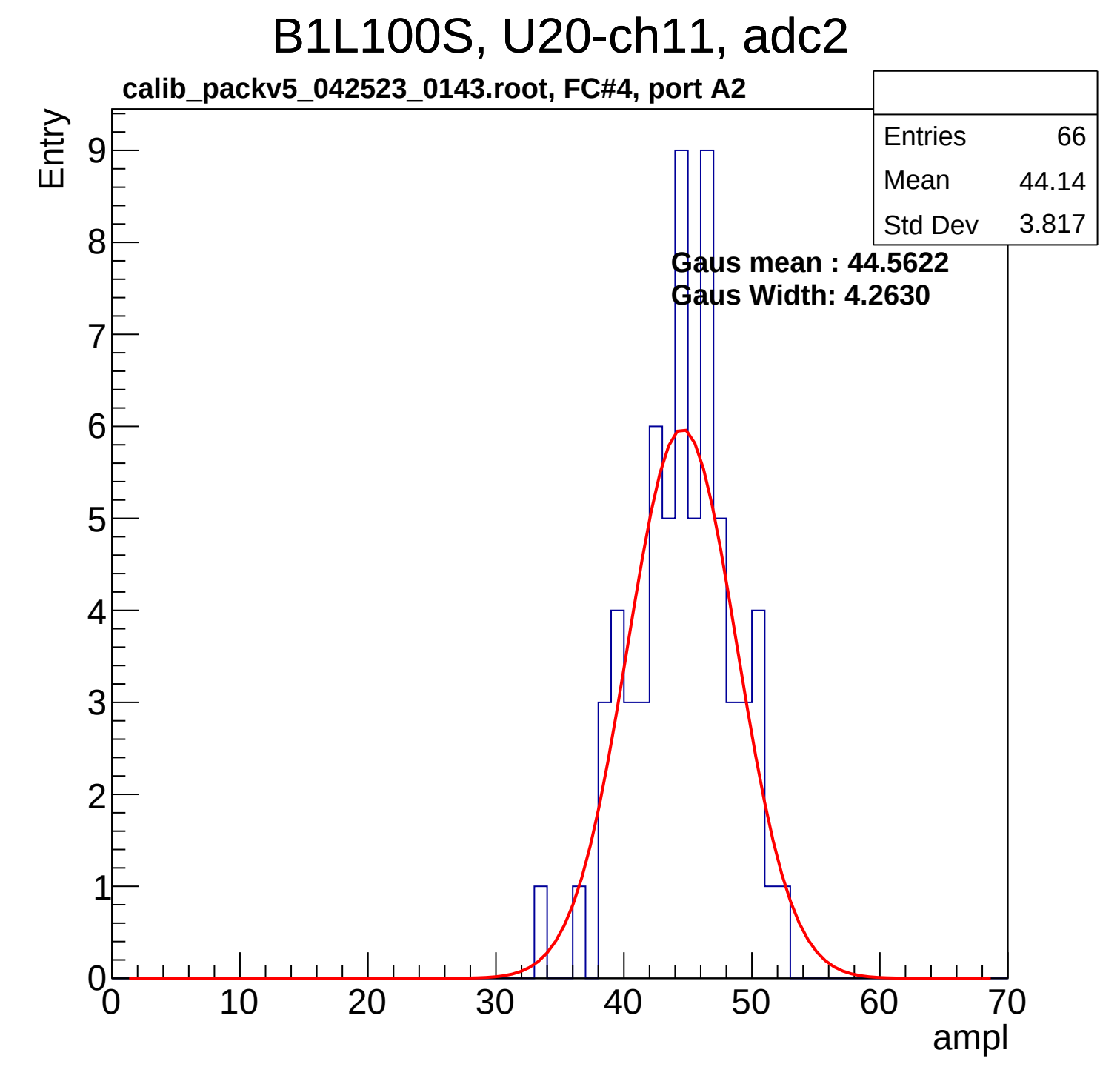
B1L100S, U20-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 44.14
Std Dev 3.817
Gaus mean : 44.5622
Gaus Width: 4.2630
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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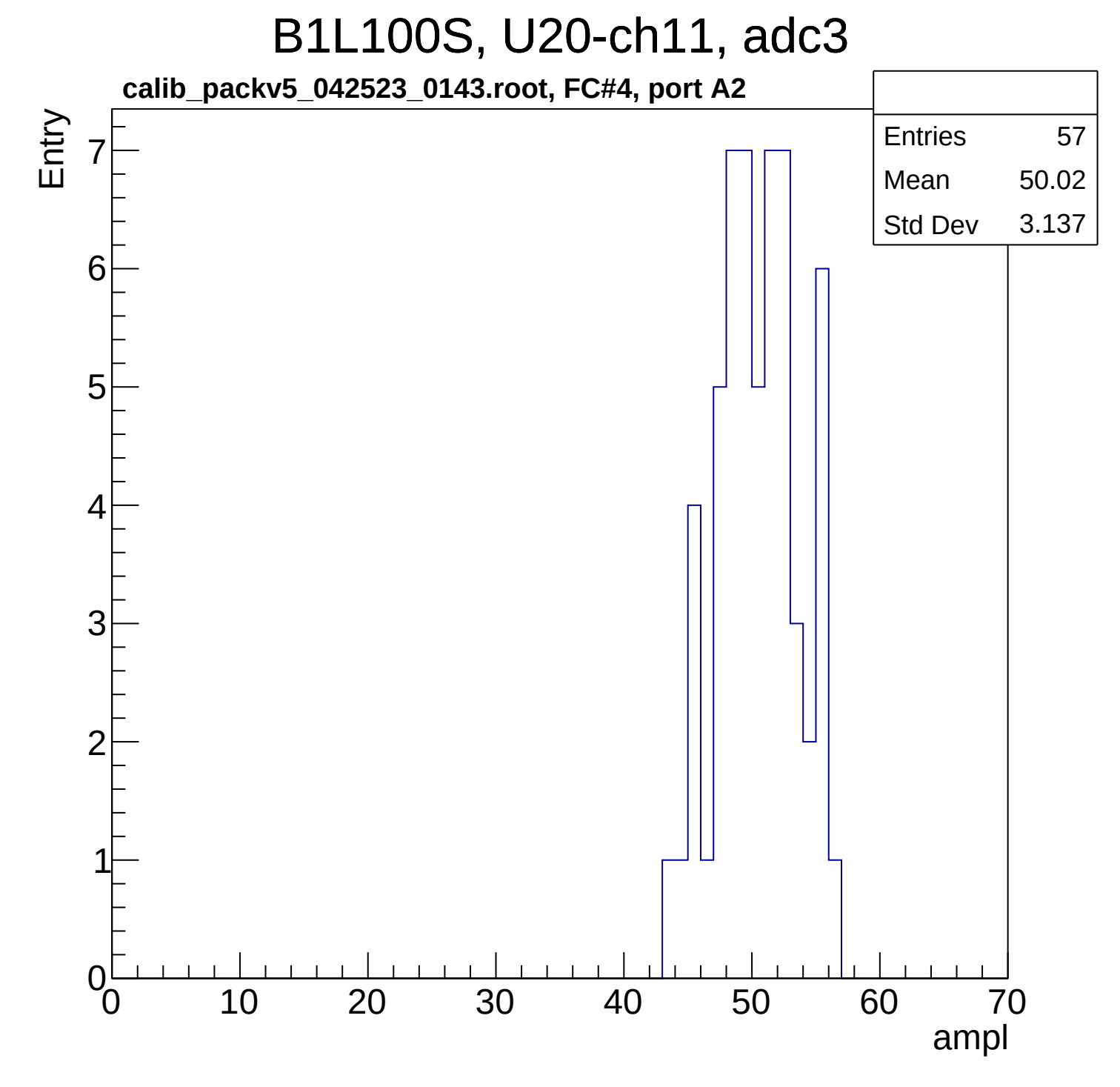
B1L100S, U20-ch11, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50.02
Std Dev 3.137
Entry
ampl
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3
4
5
6
7
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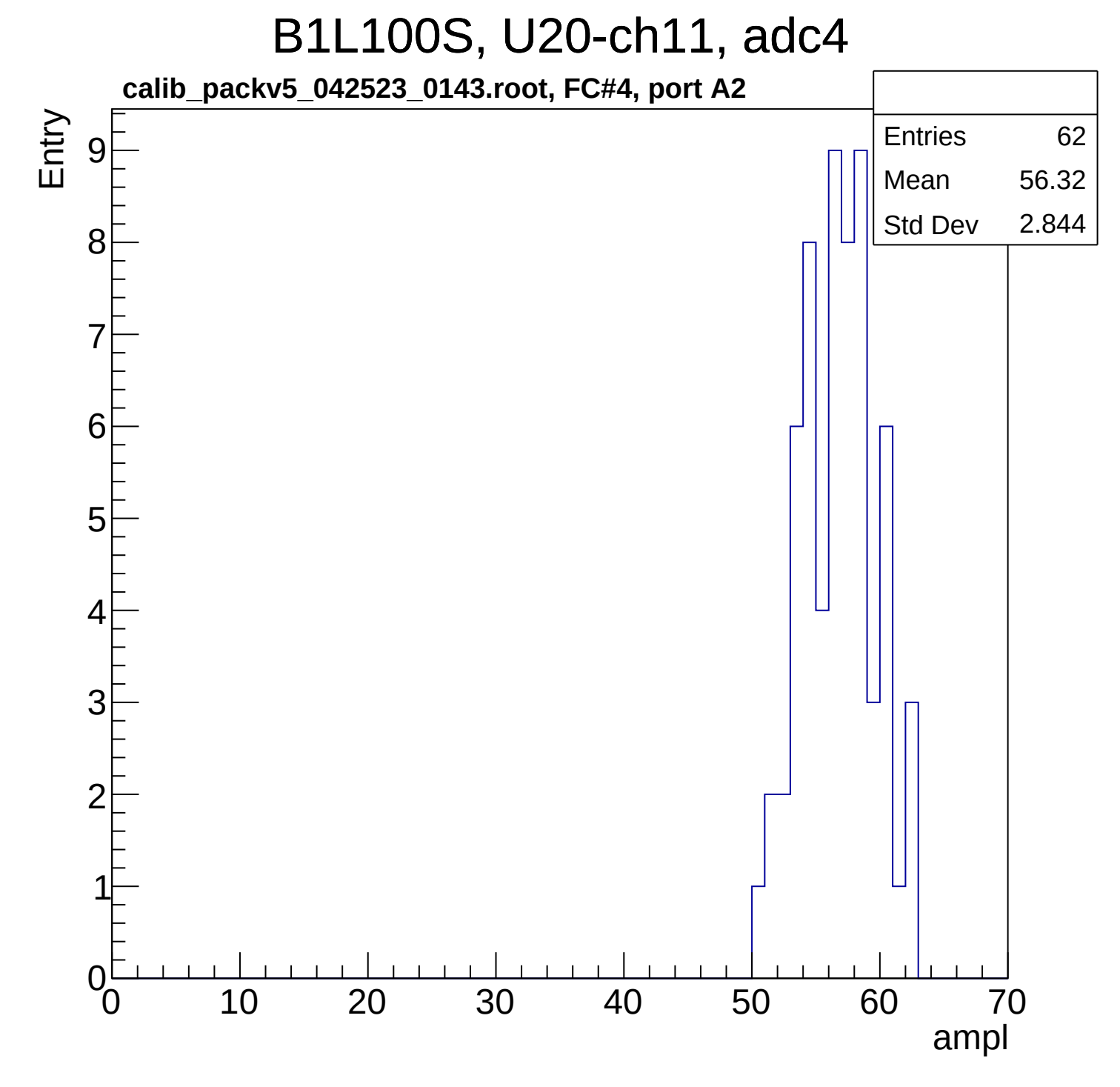
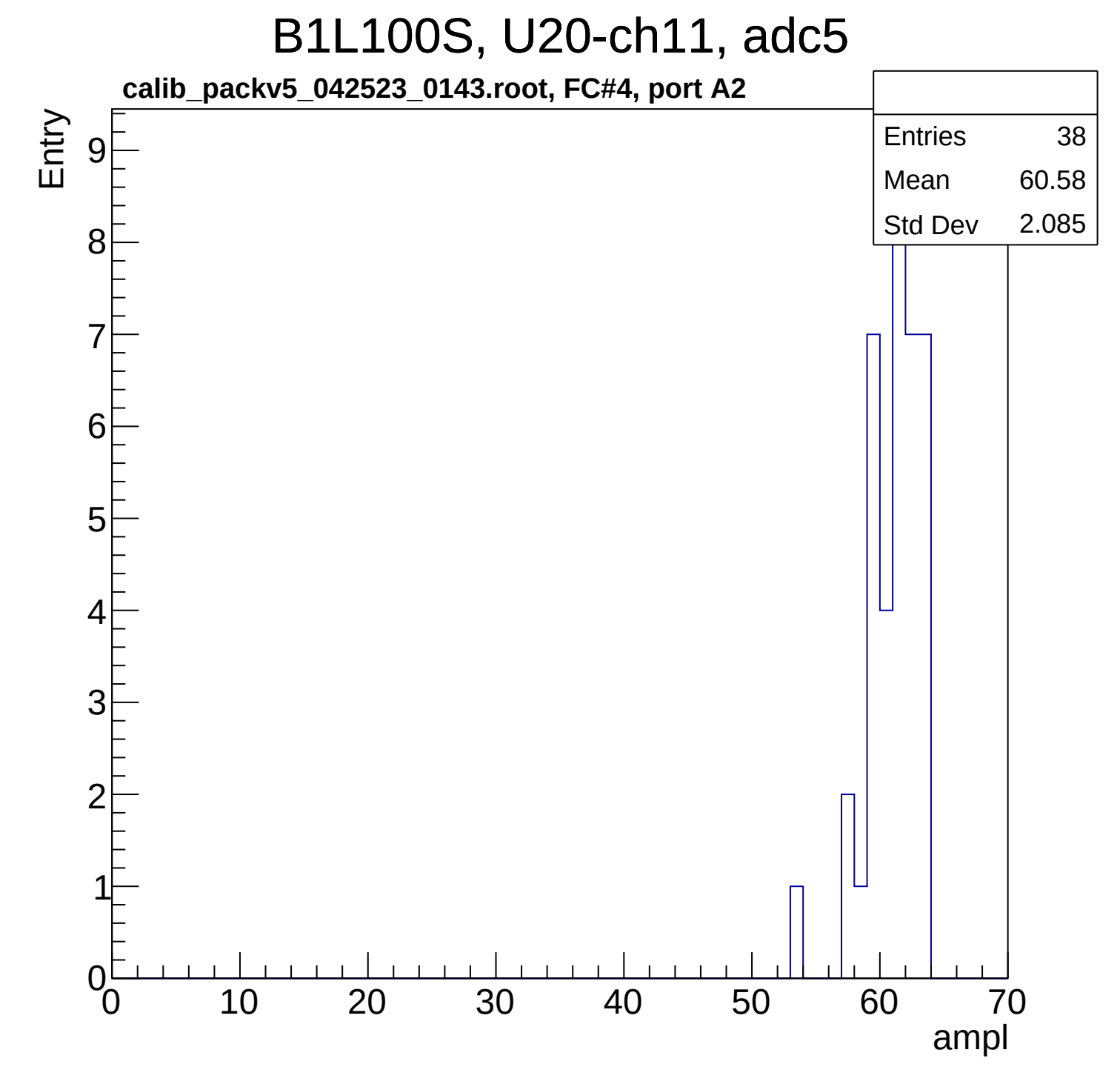
40
50
60
70


B1L100S, U20-ch11, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 56.32
Std Dev 2.844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch11, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 60.58
Std Dev 2.085
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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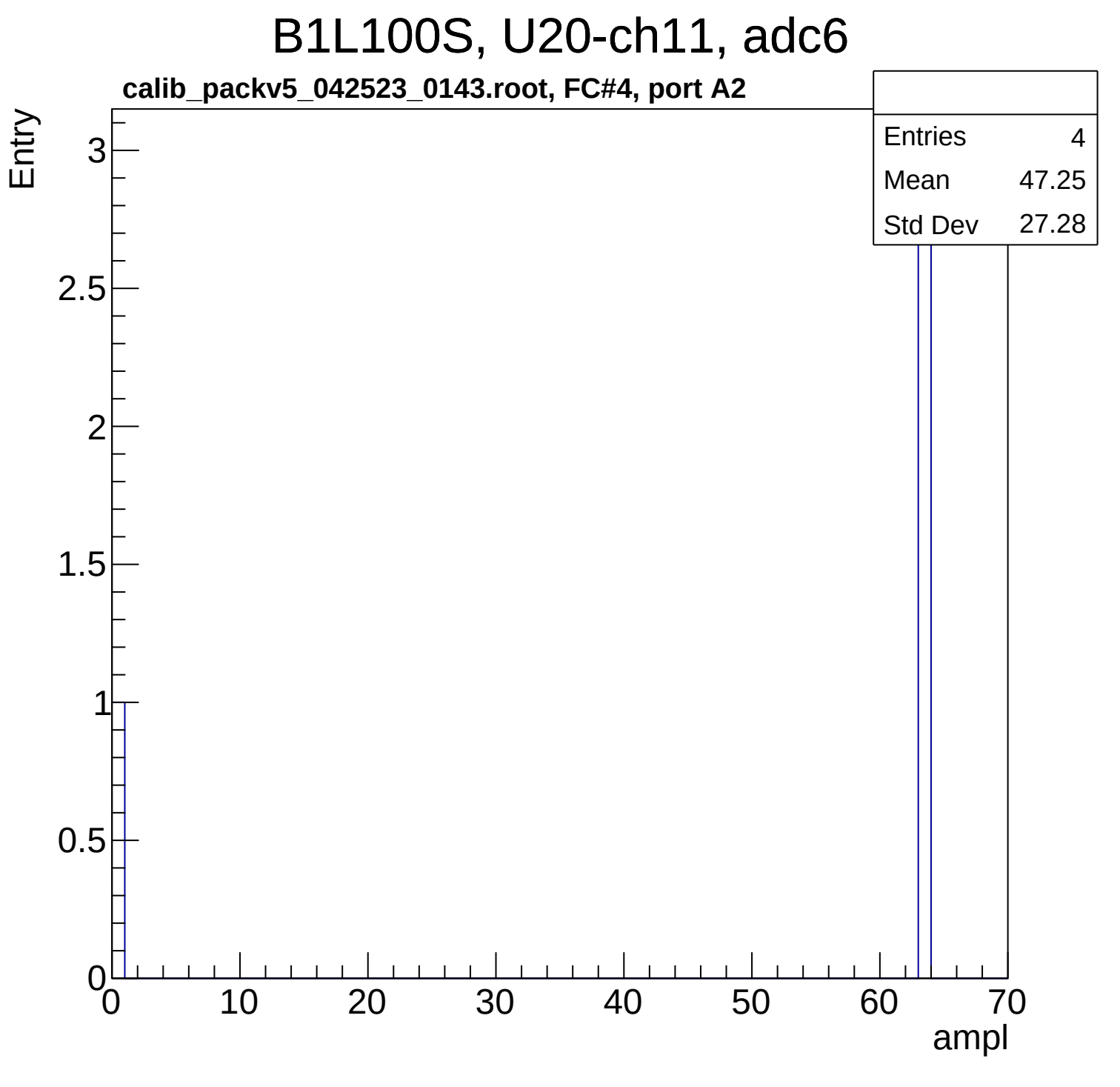
B1L100S, U20-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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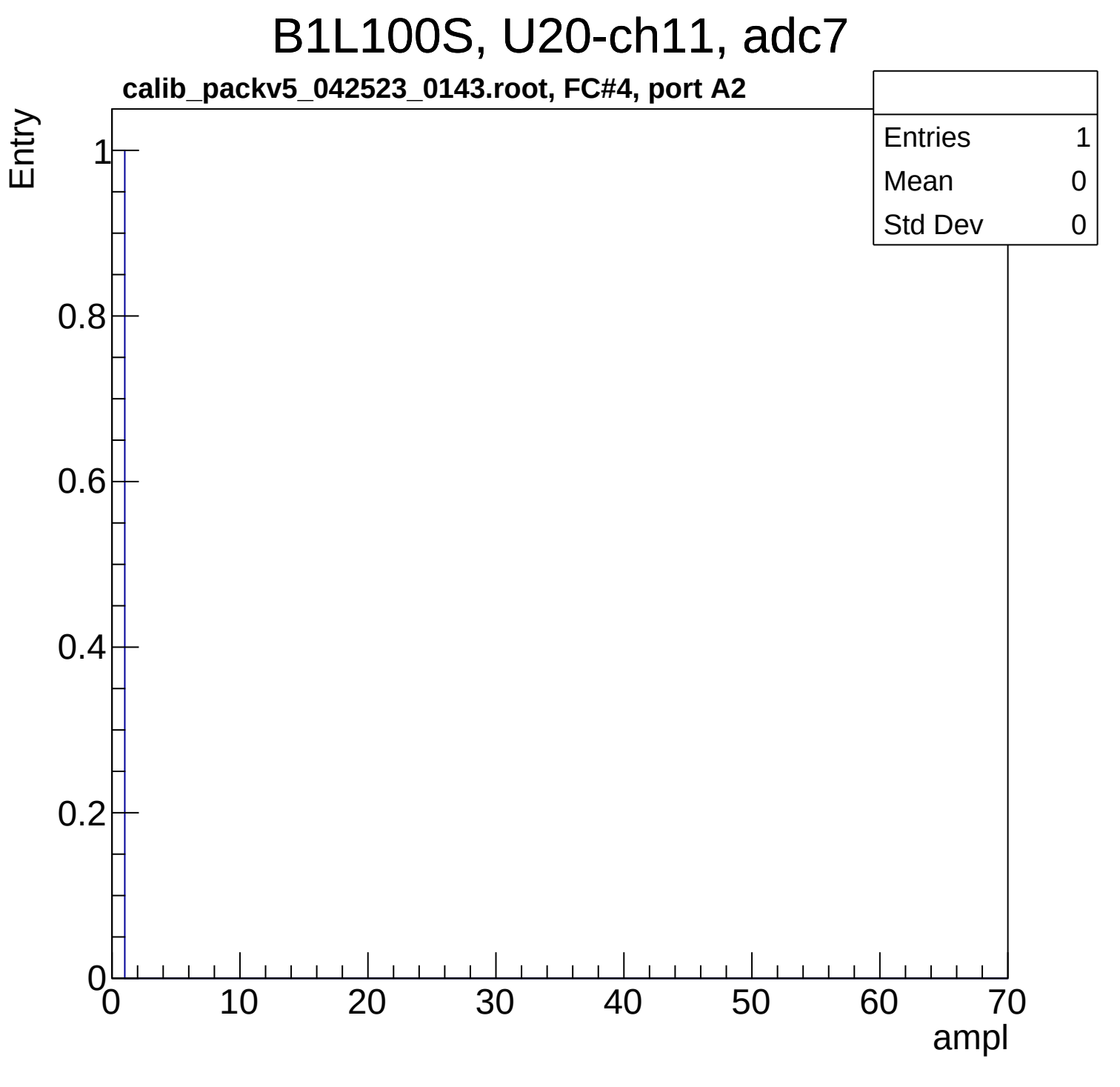
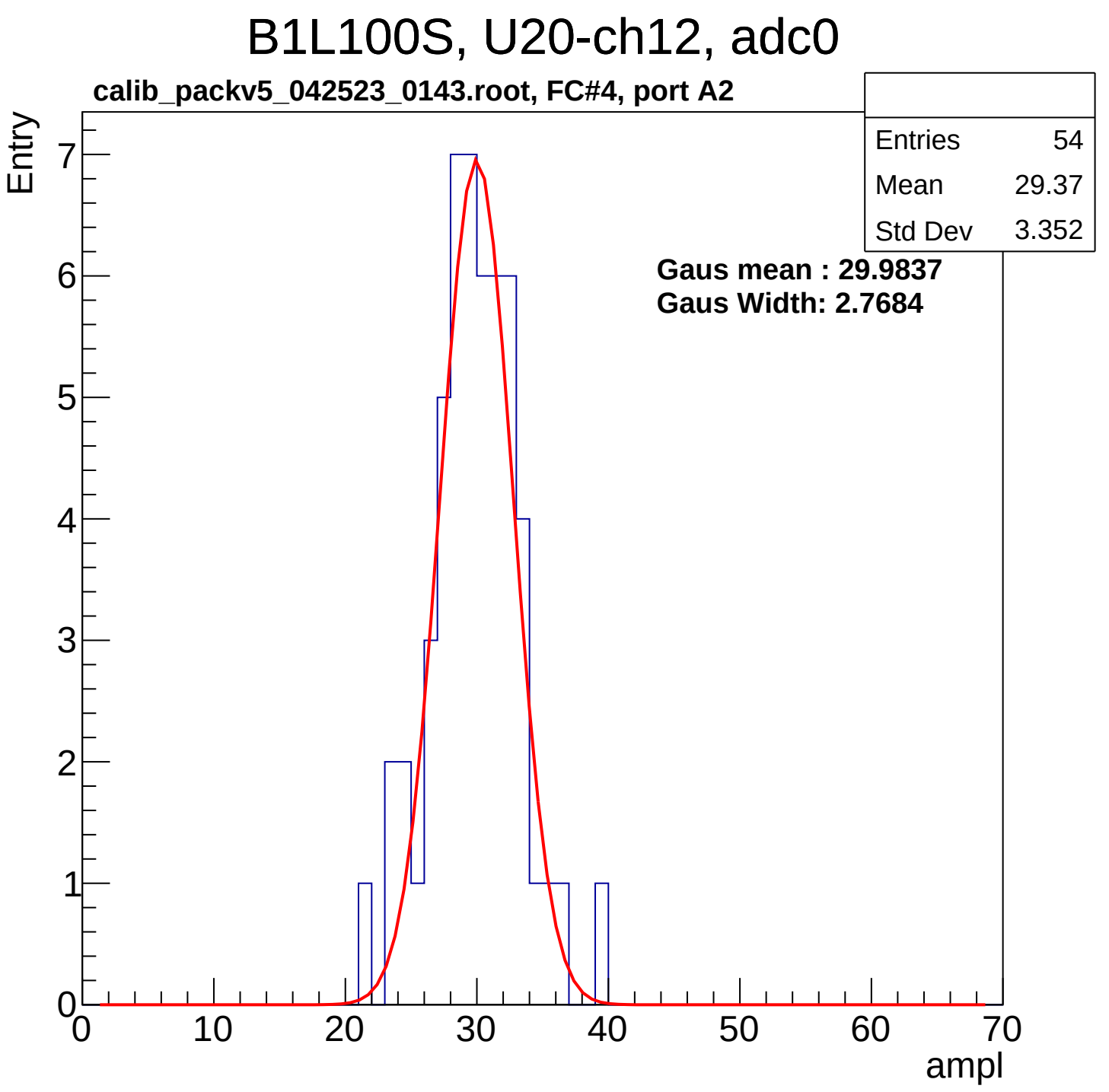
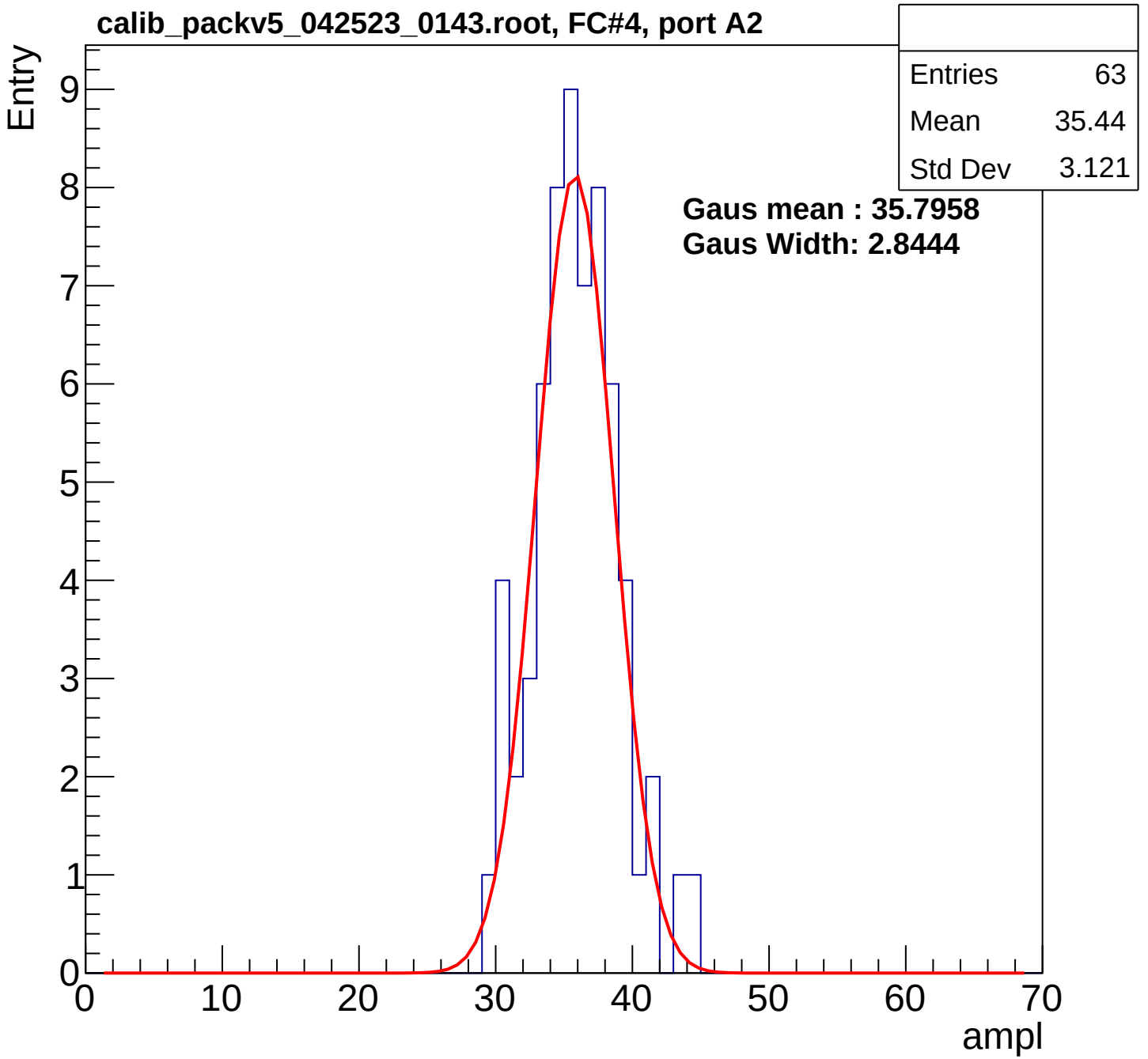

B1L100S, U20-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 29.37
Std Dev 3.352
Gaus mean : 29.9837
Gaus Width: 2.7684
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.44
Std Dev 3.121
Gaus mean : 35.7958
Gaus Width: 2.8444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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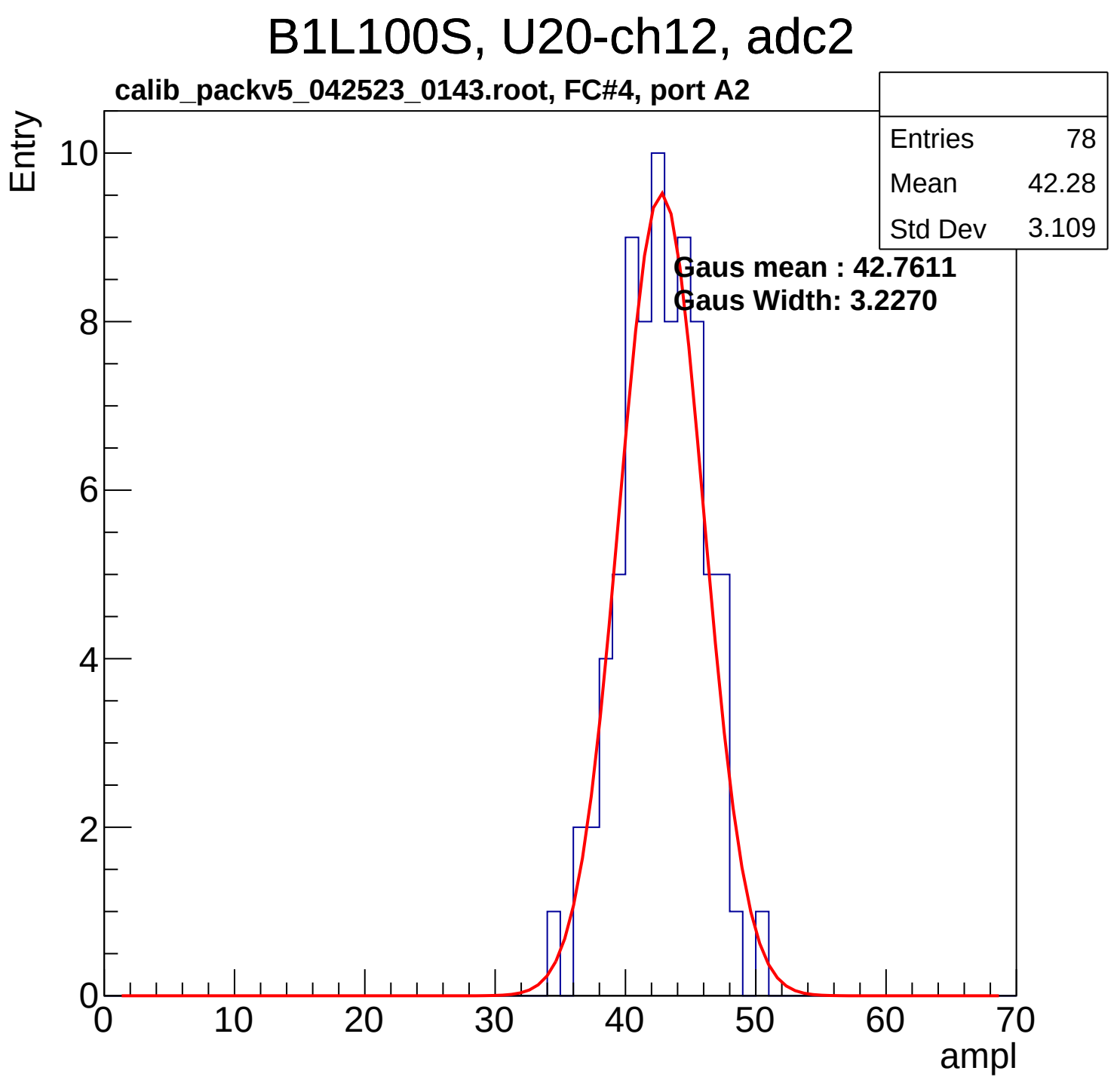
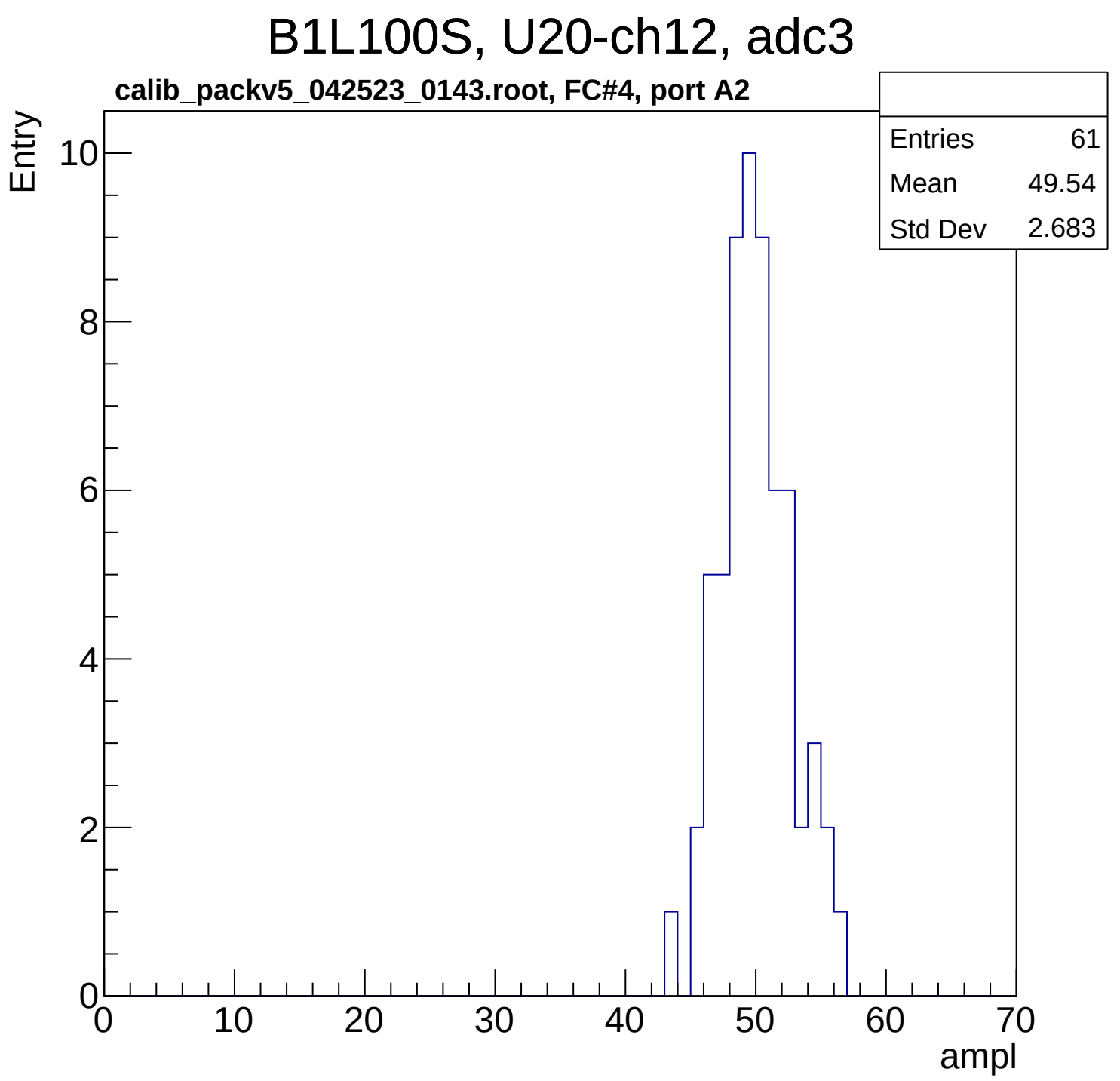
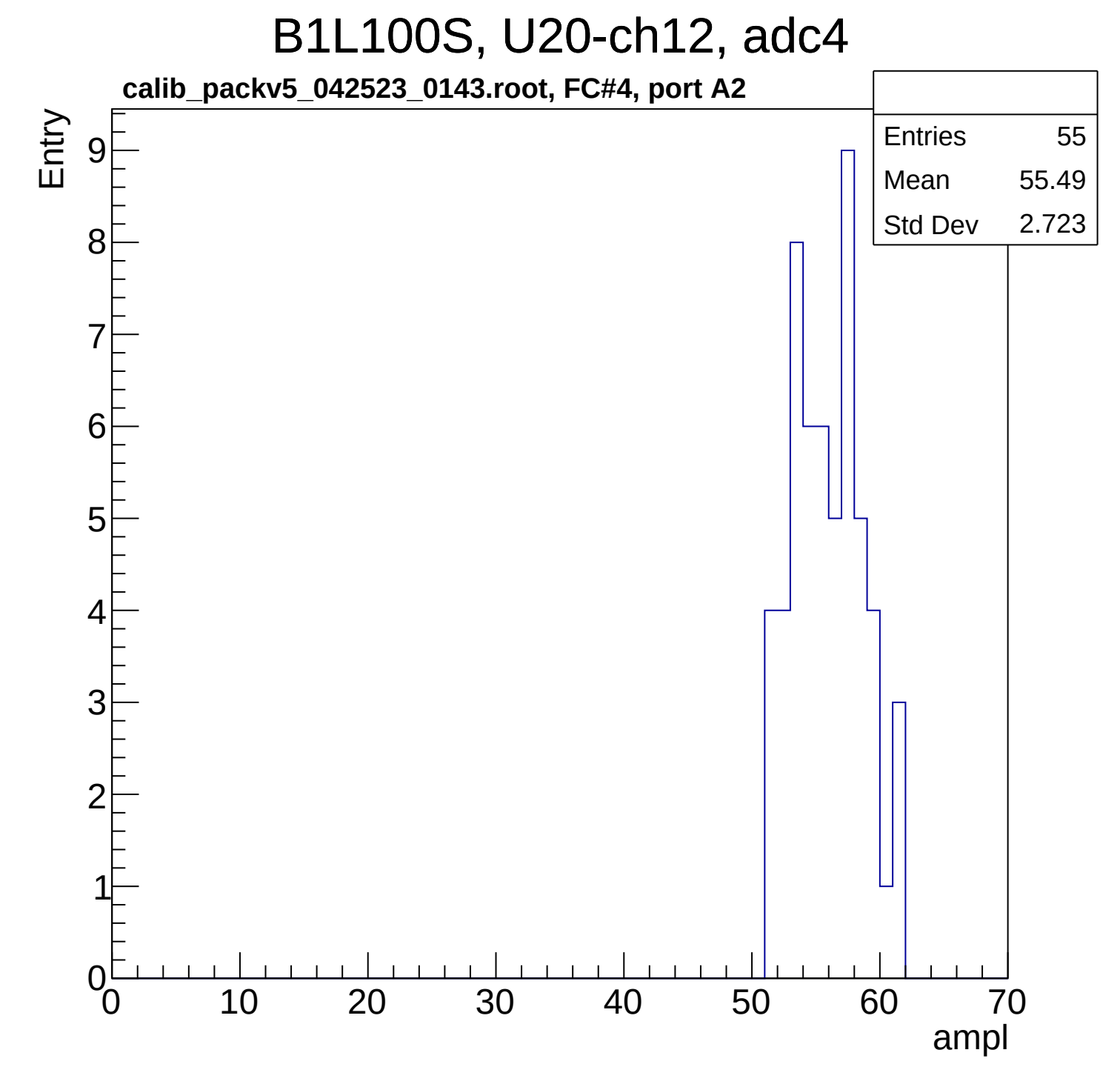
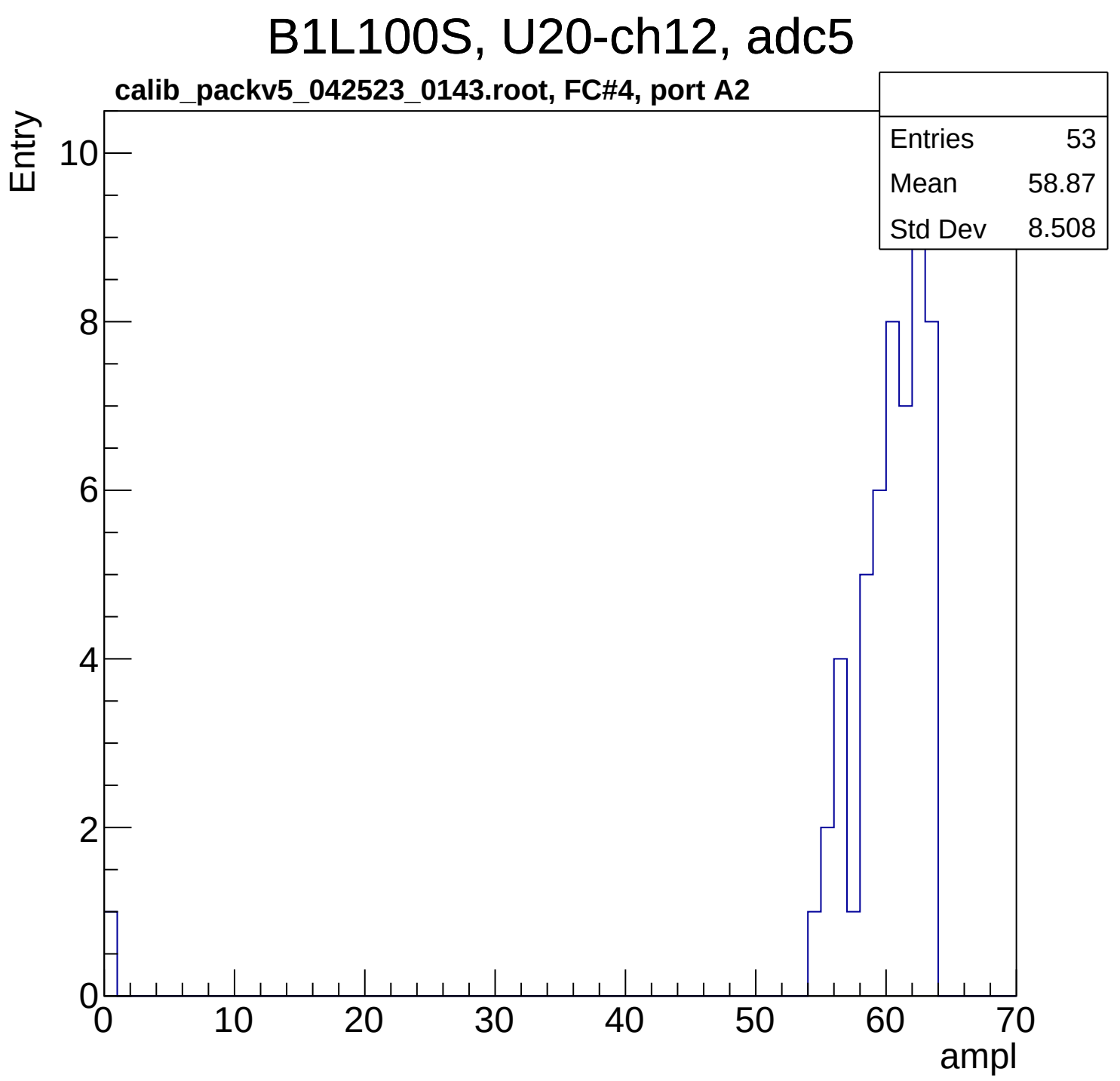
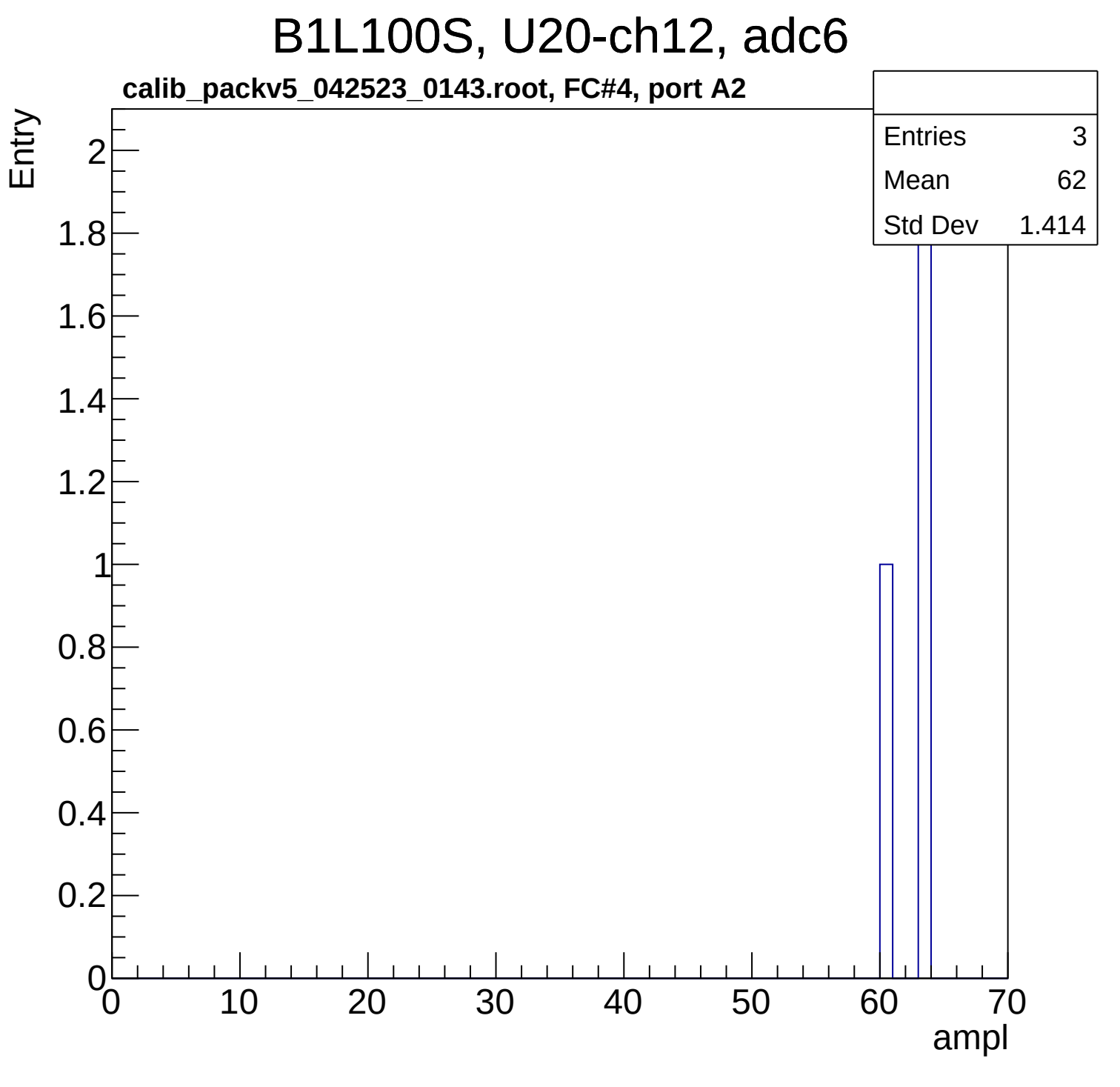
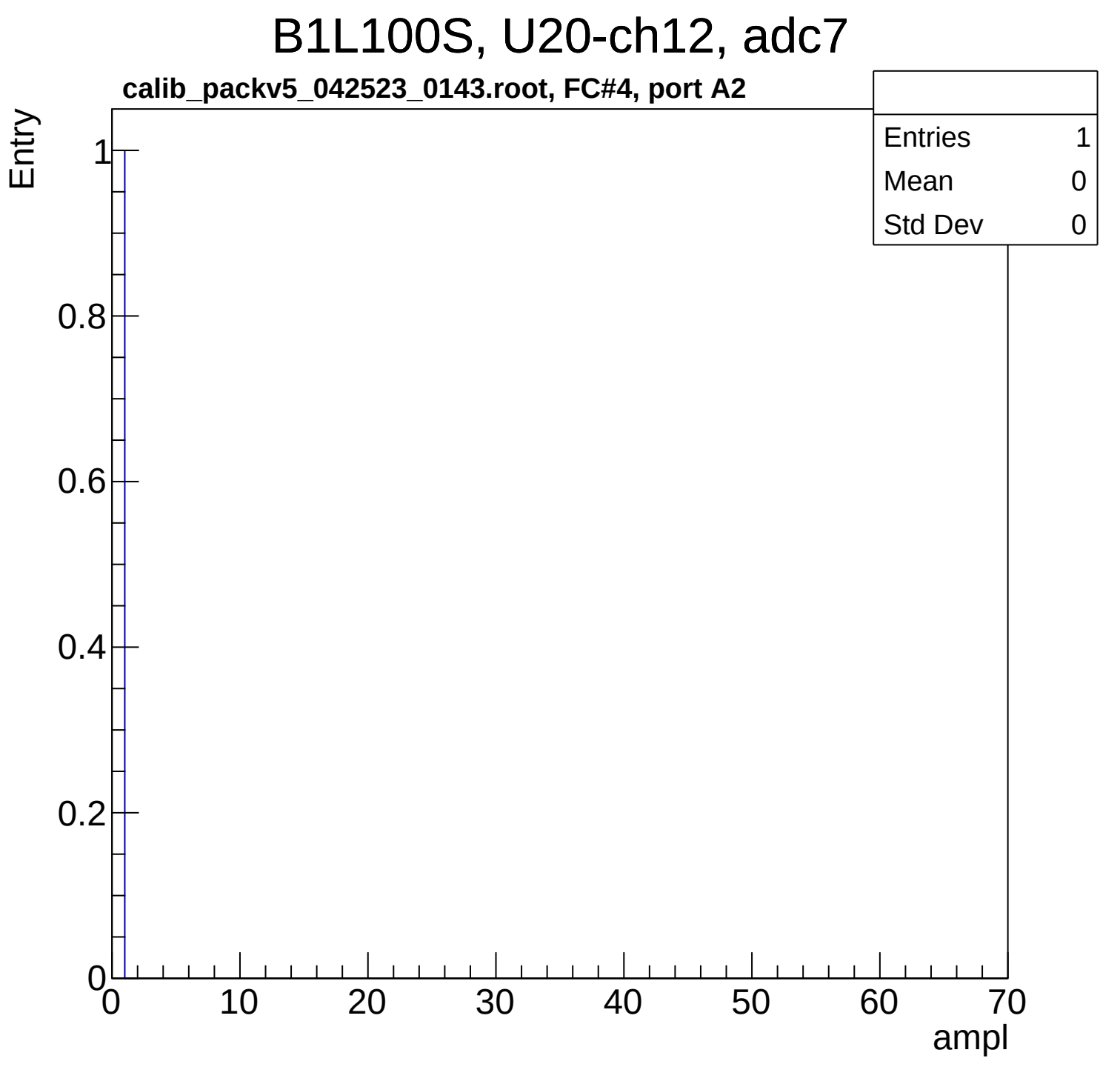

B1L100S, U20-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 42.28
Std Dev 3.109
Gaus mean : 42.7611
Gaus Width: 3.2270
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

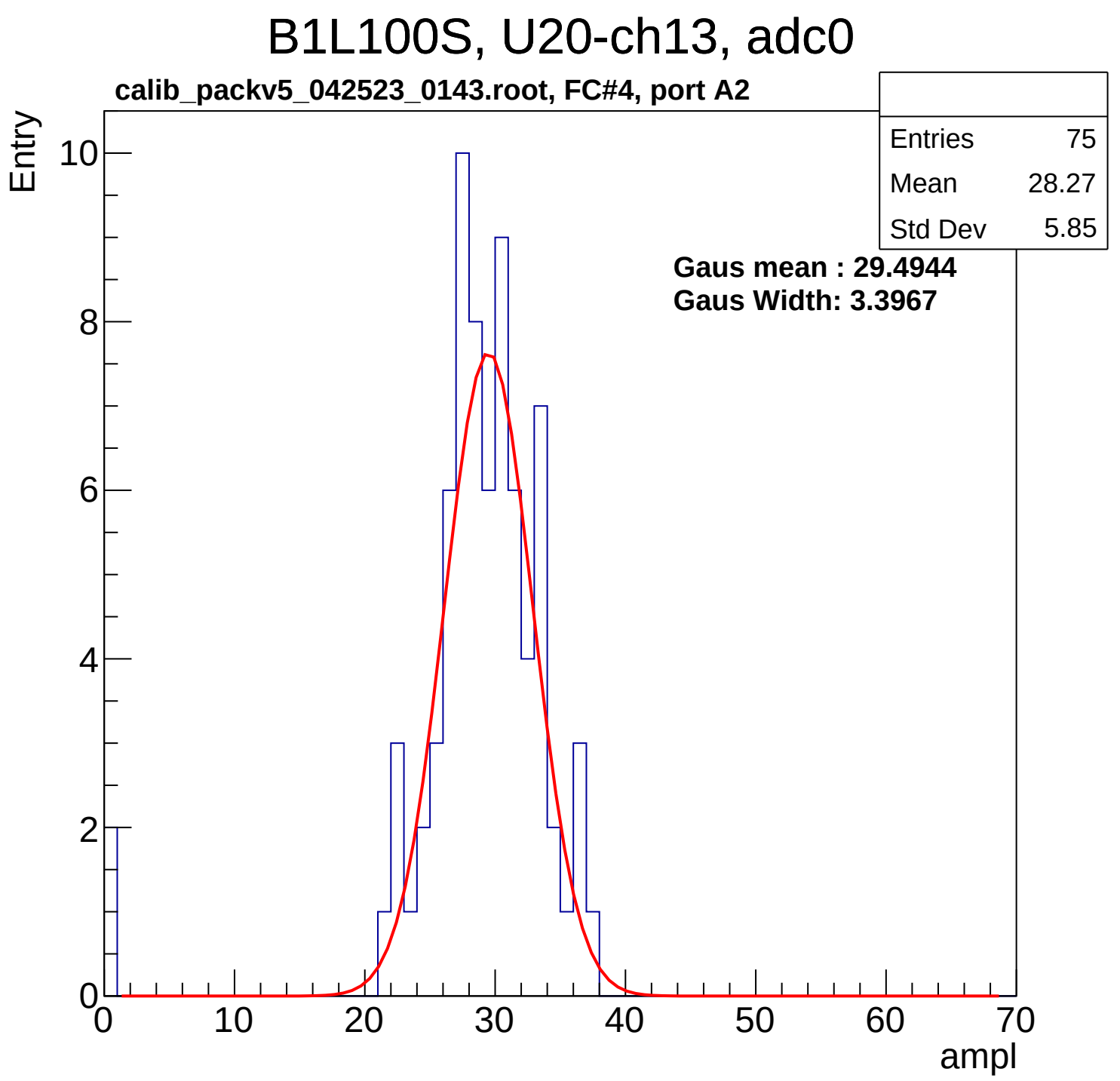

B1L100S, U20-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.54
Std Dev 2.683
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

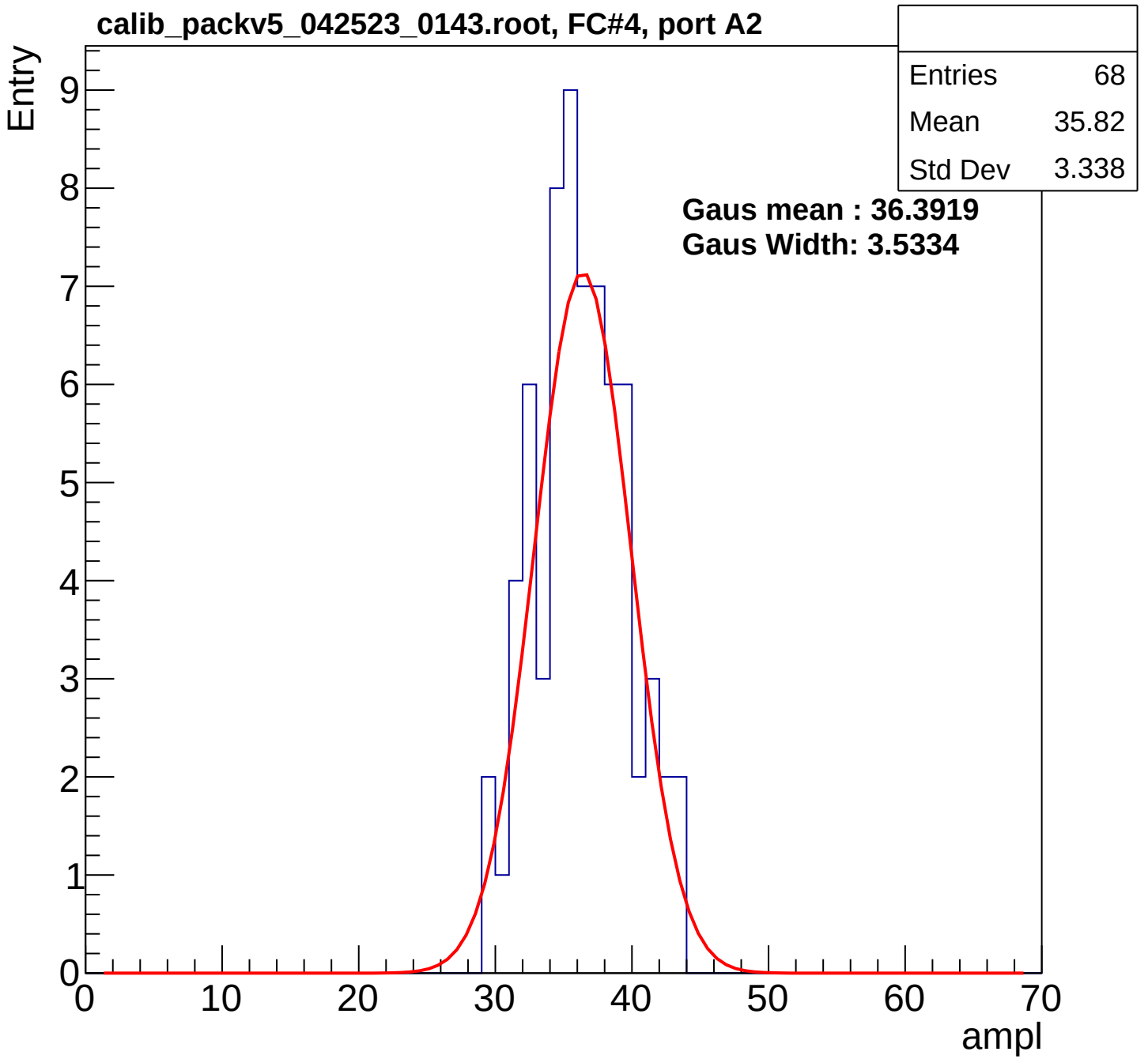

B1L100S, U20-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.49
Std Dev 2.723
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

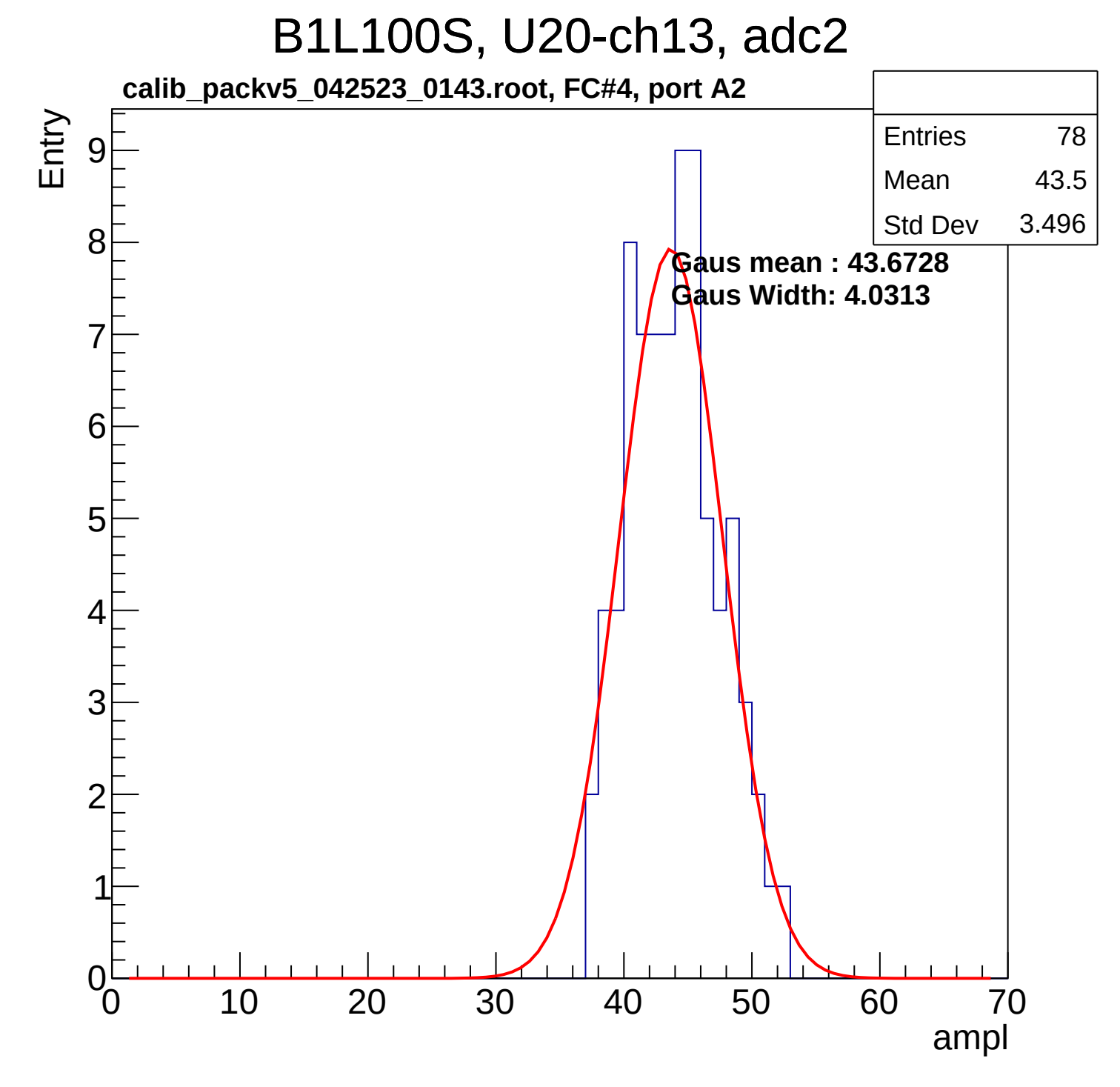

B1L100S, U20-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.87
Std Dev 8.508
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

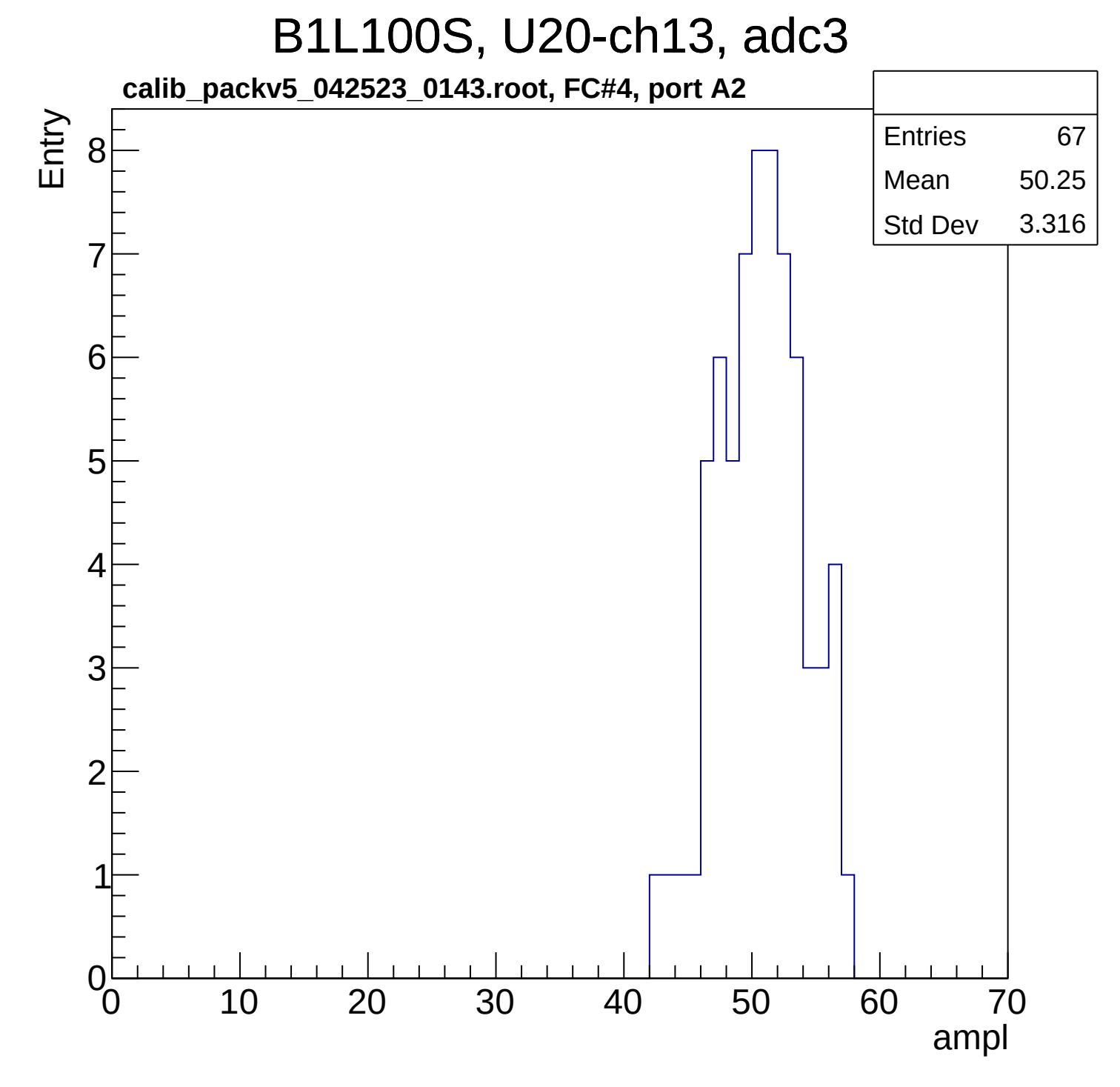

B1L100S, U20-ch12, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

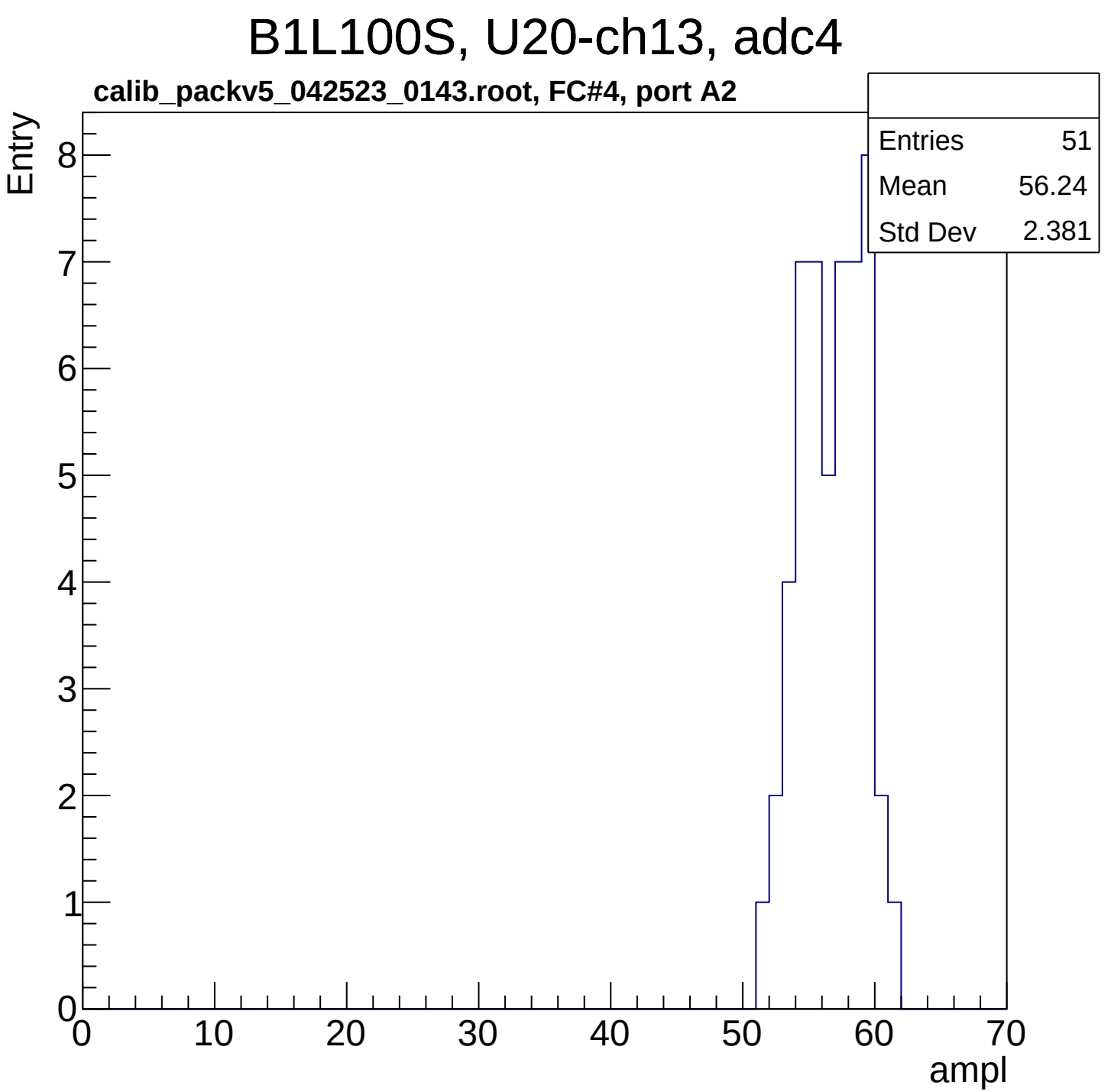

B1L100S, U20-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

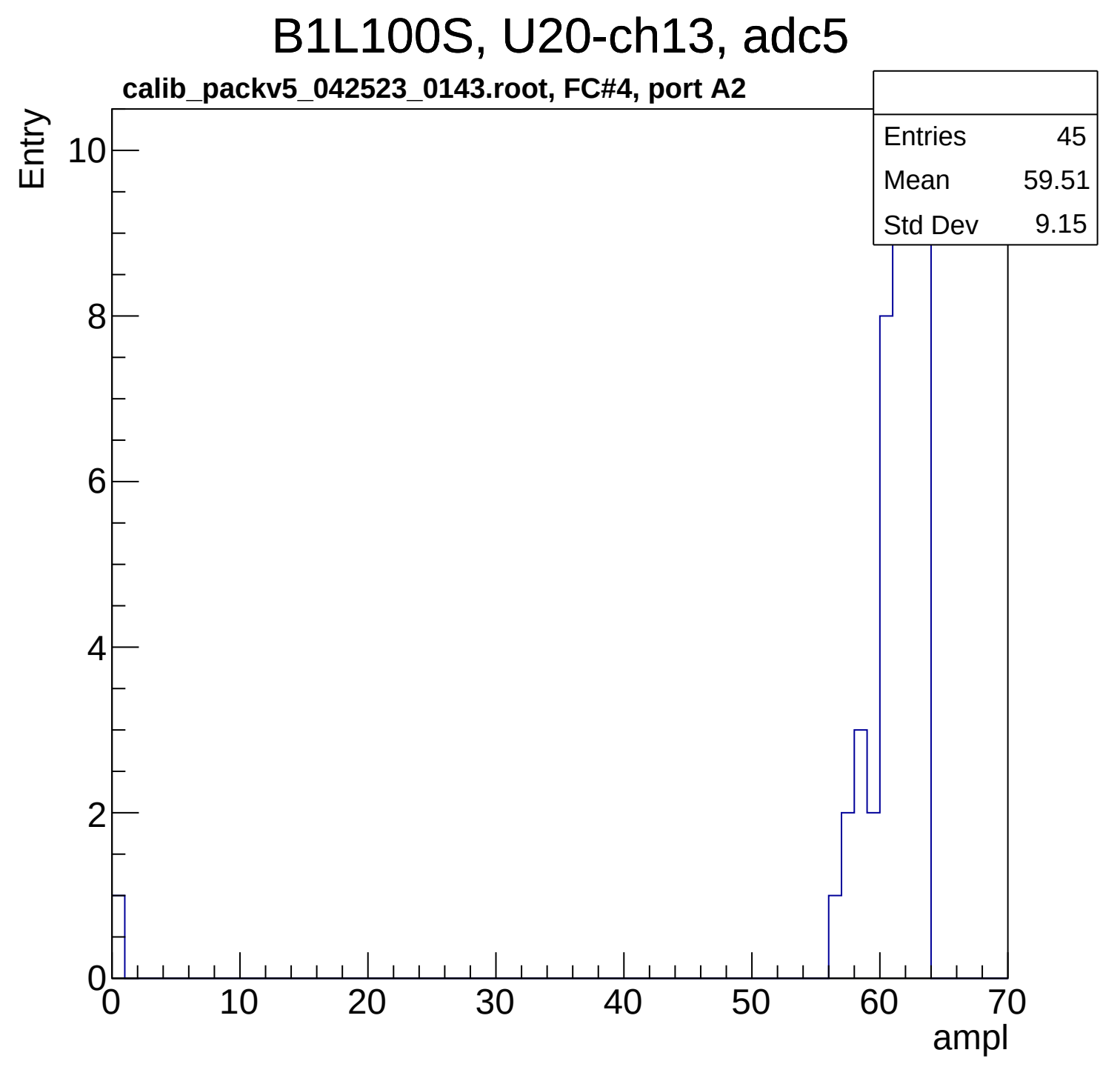

B1L100S, U20-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.27
Std Dev 5.85
Gaus mean : 29.4944
Gaus Width: 3.3967
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

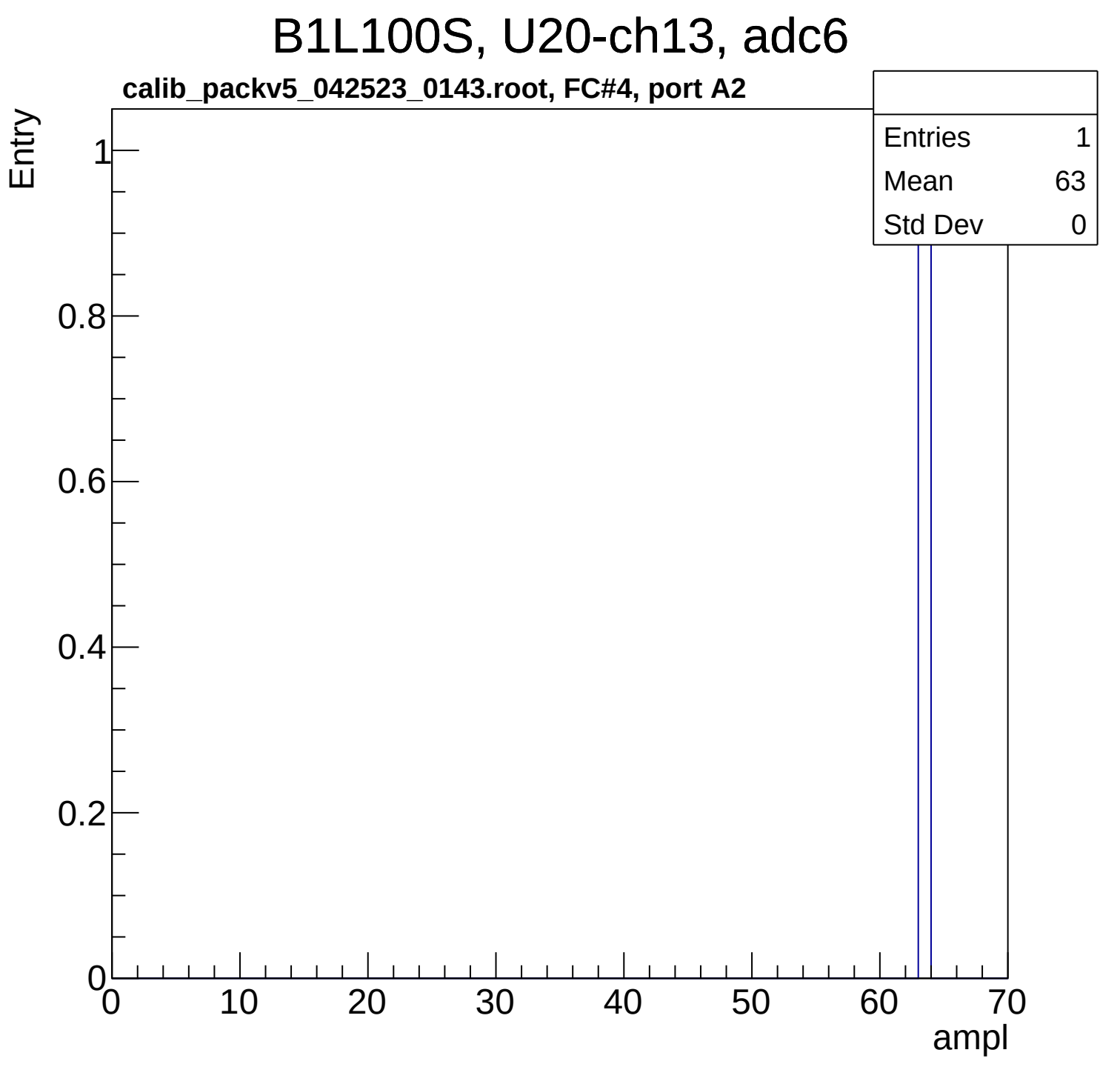

B1L100S, U20-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 35.82
Std Dev 3.338
Gaus mean : 36.3919
Gaus Width: 3.5334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

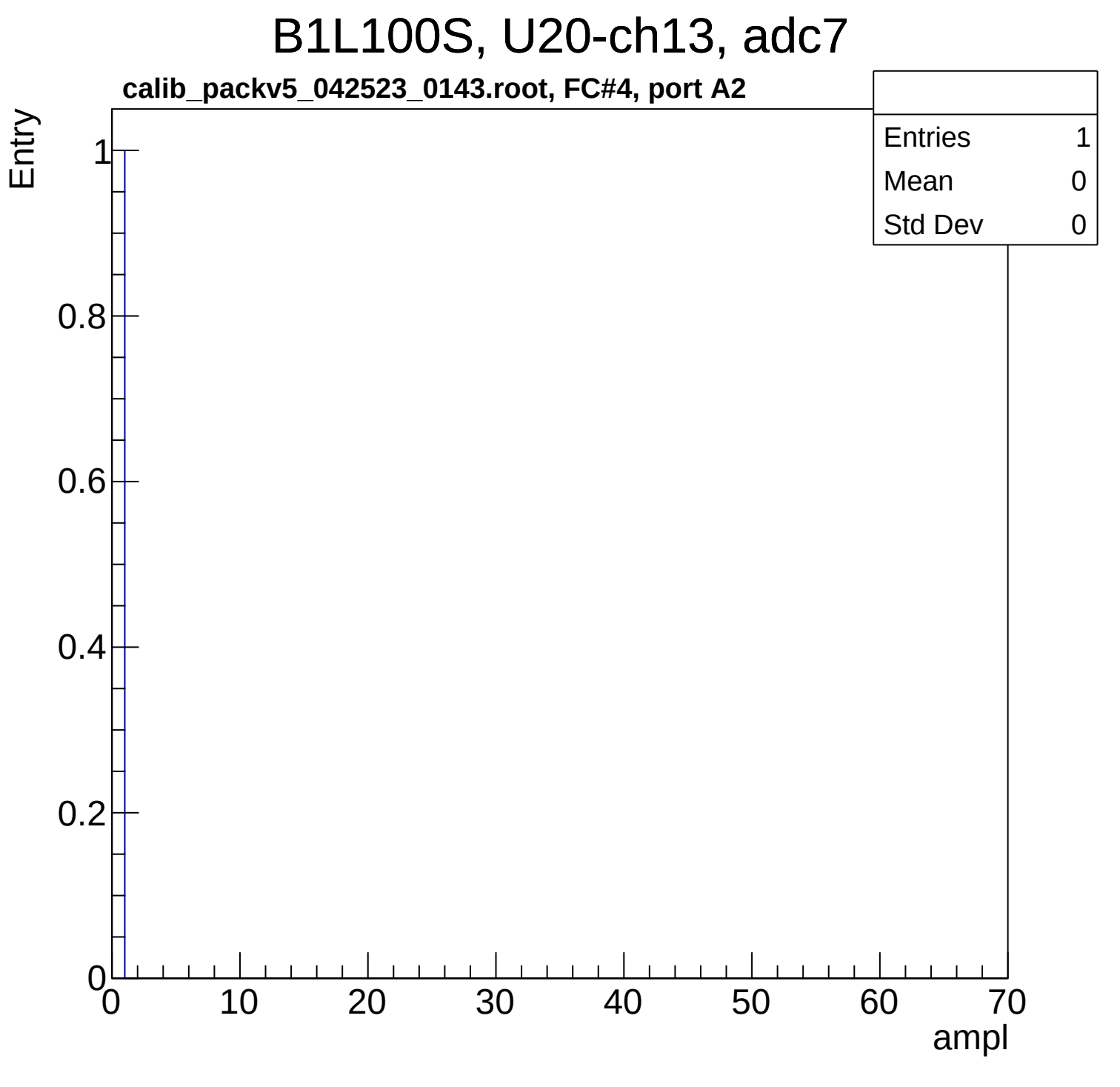

B1L100S, U20-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.5
Std Dev 3.496
Gaus mean : 43.6728
Gaus Width: 4.0313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.25
Std Dev 3.316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.24
Std Dev 2.381
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.51
Std Dev 9.15
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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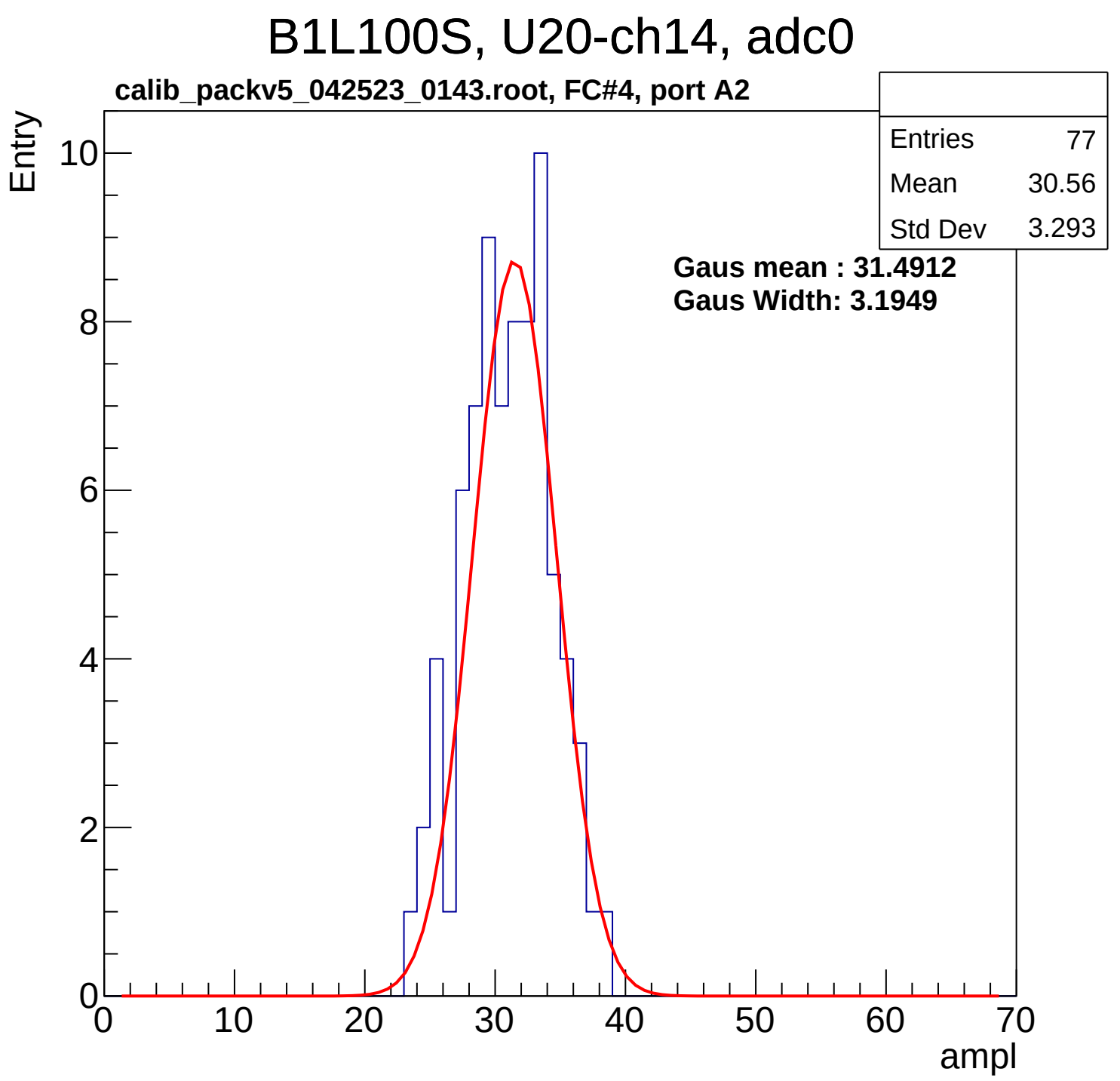
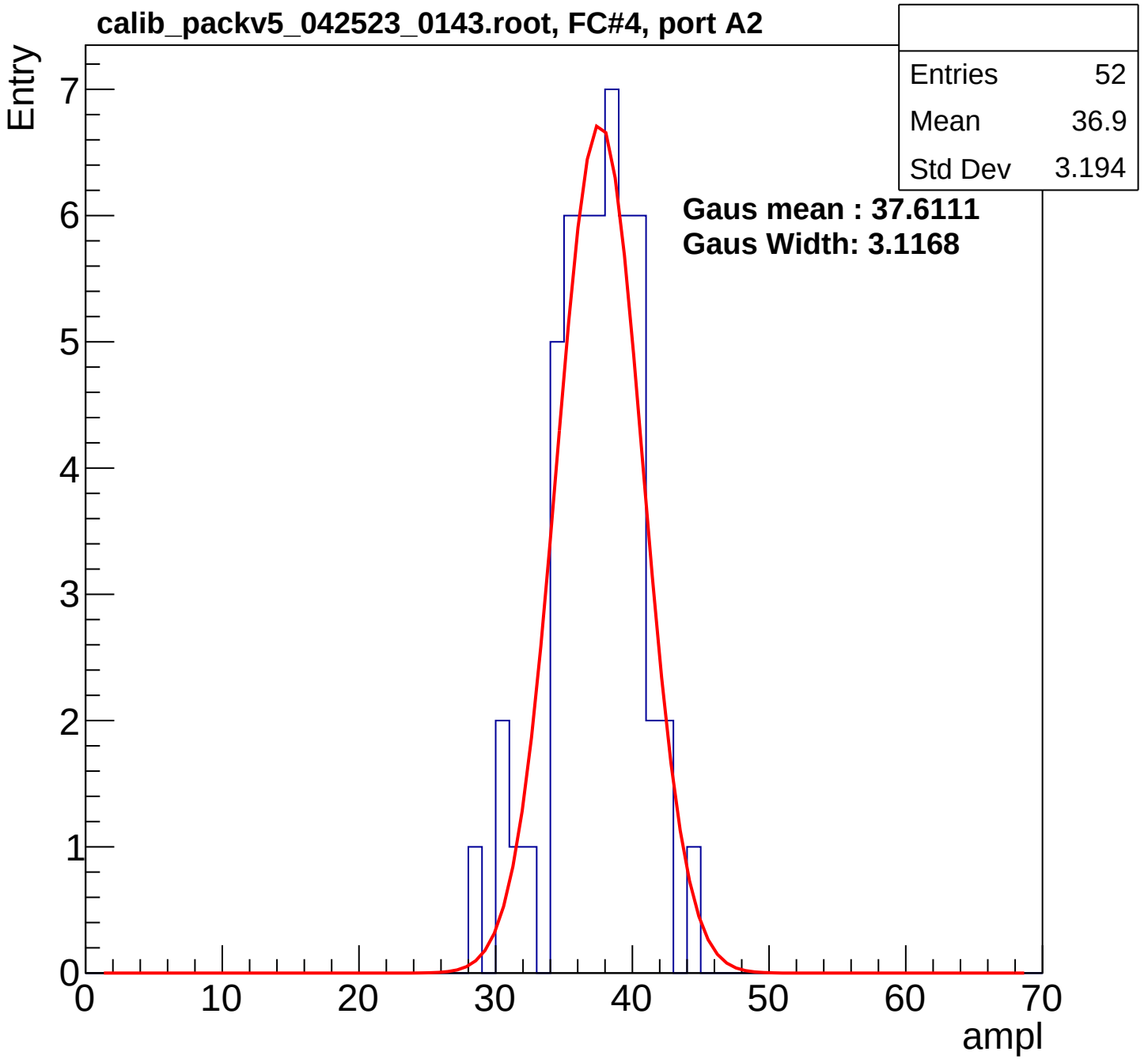
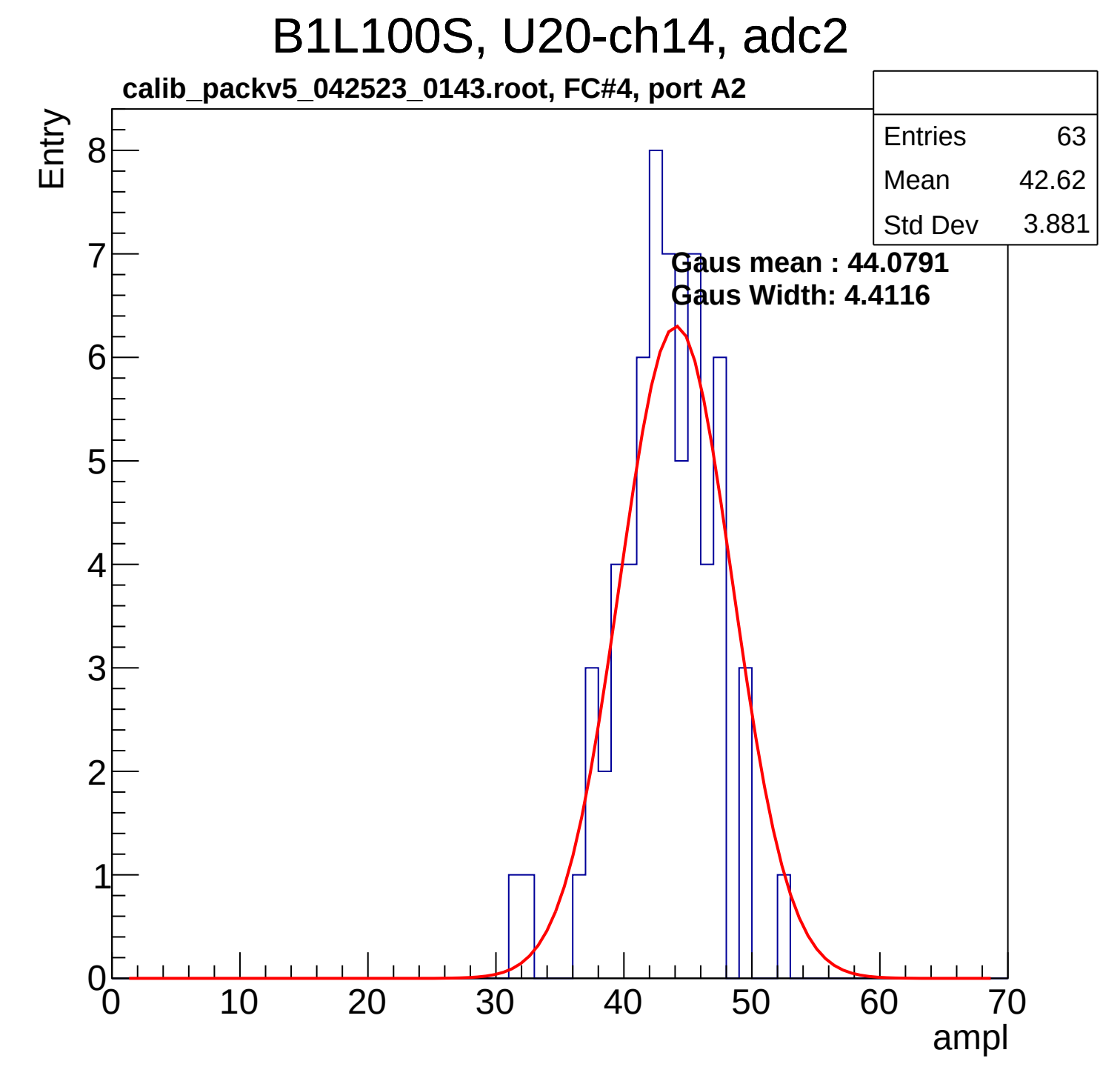
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 30.56
Std Dev 3.293
Gaus mean : 31.4912
Gaus Width: 3.1949
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 36.9
Std Dev 3.194
Gaus mean : 37.6111
Gaus Width: 3.1168
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.62
Std Dev 3.881
Gaus mean : 44.0791
Gaus Width: 4.4116
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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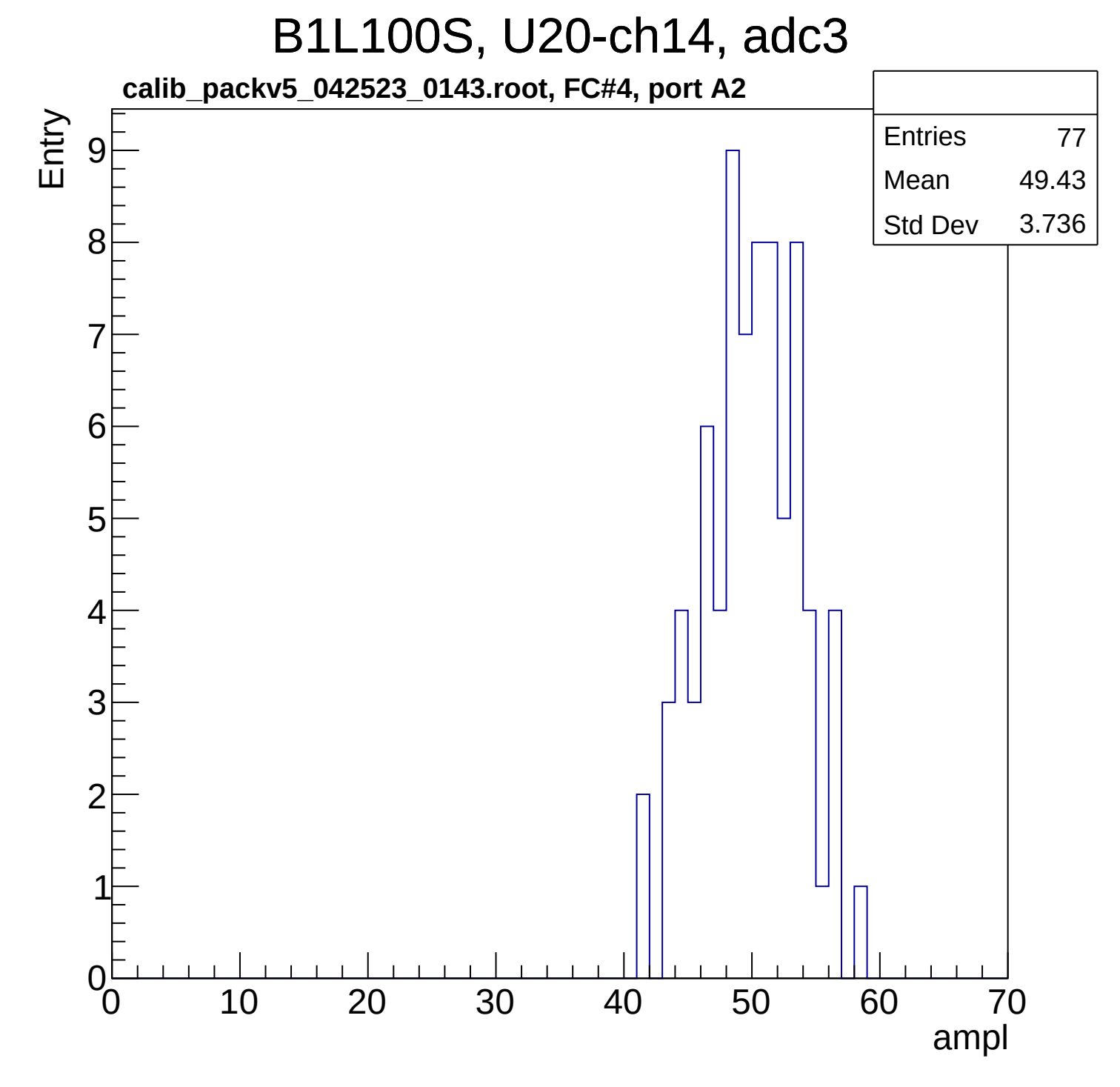
40
50
60
70


B1L100S, U20-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 49.43
Std Dev 3.736
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
50
60
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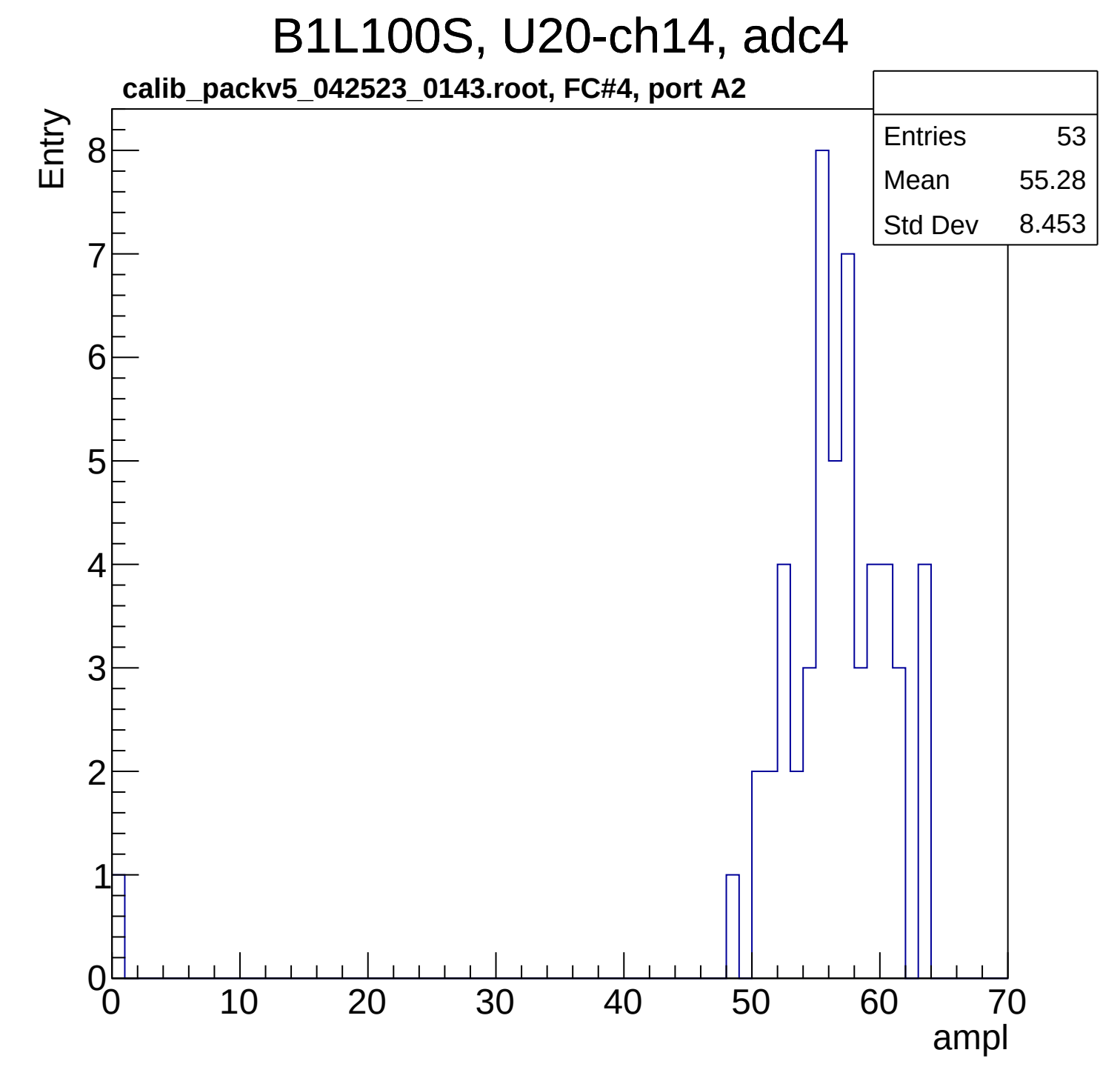
B1L100S, U20-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.28
Std Dev 8.453
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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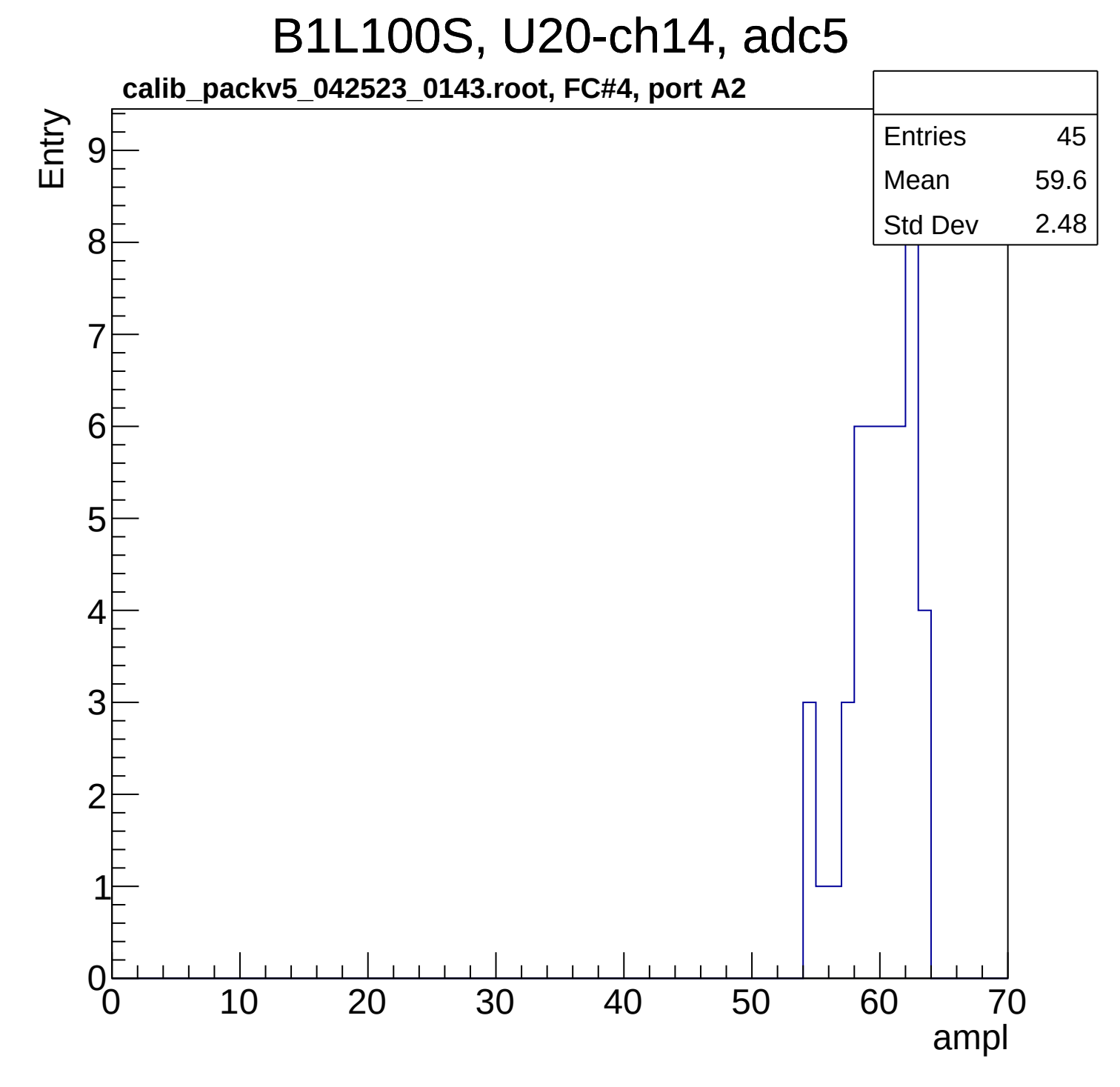
B1L100S, U20-ch14, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.6
Std Dev 2.48
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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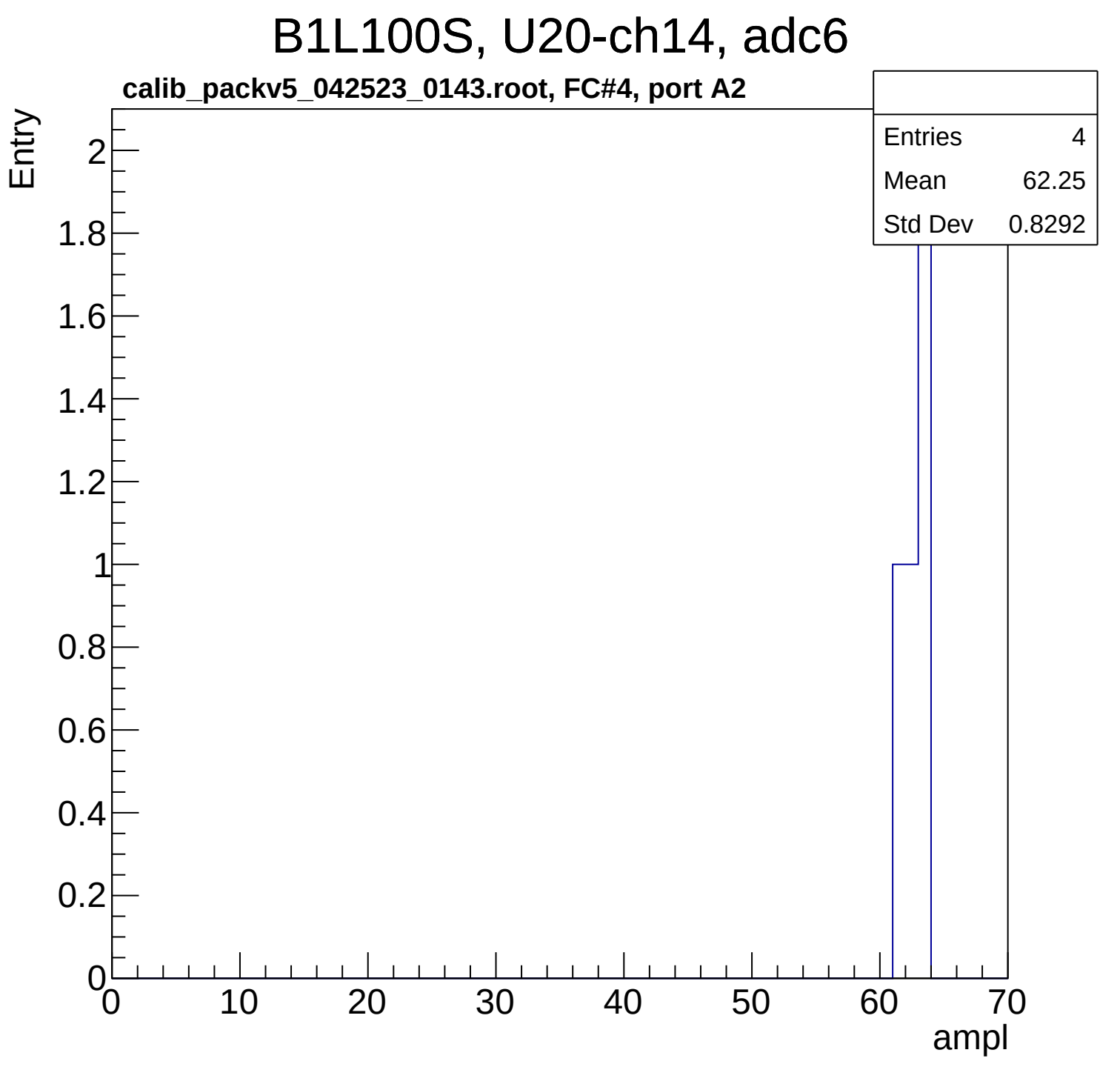
B1L100S, U20-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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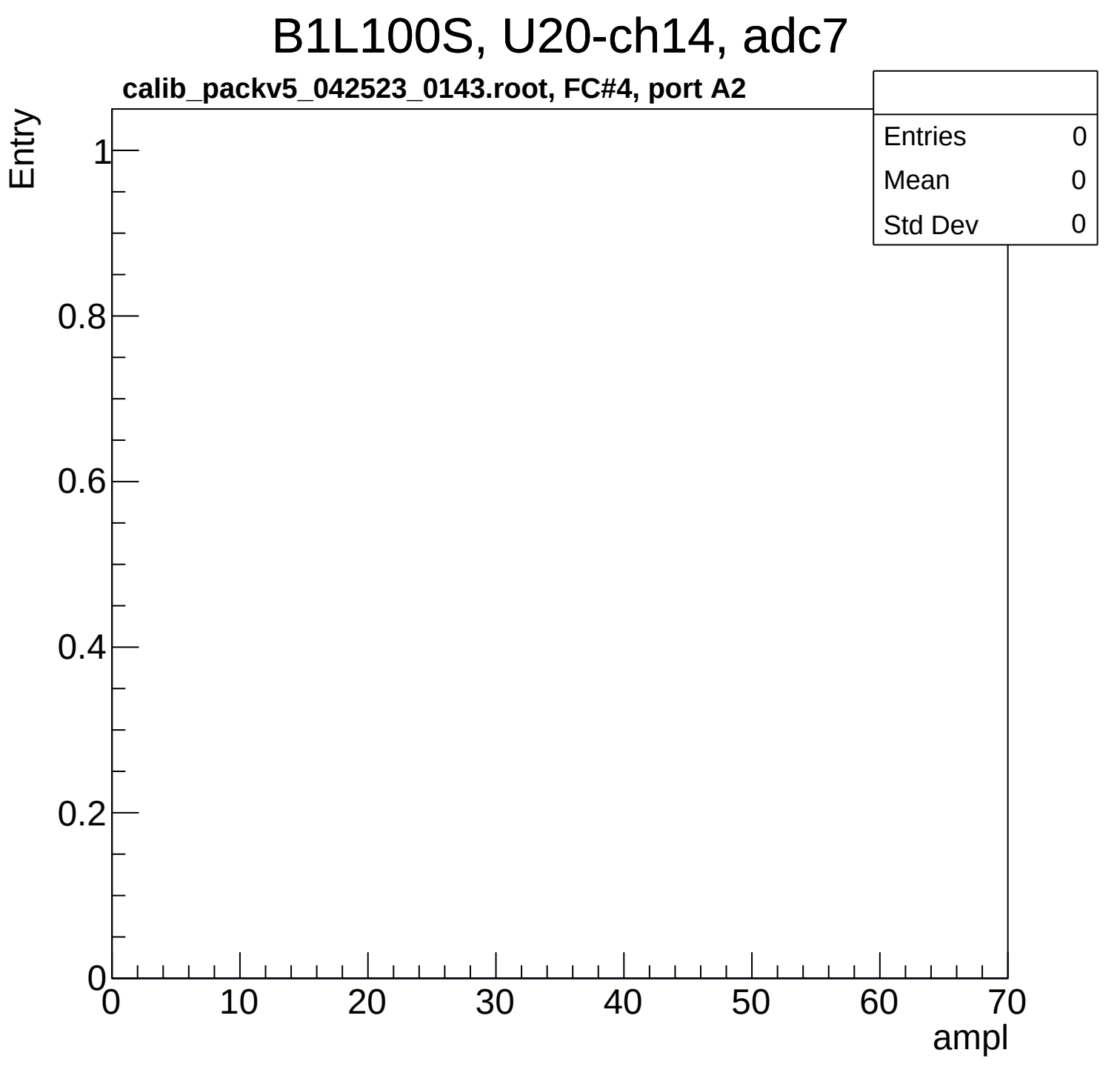
40
50
60
70


B1L100S, U20-ch14, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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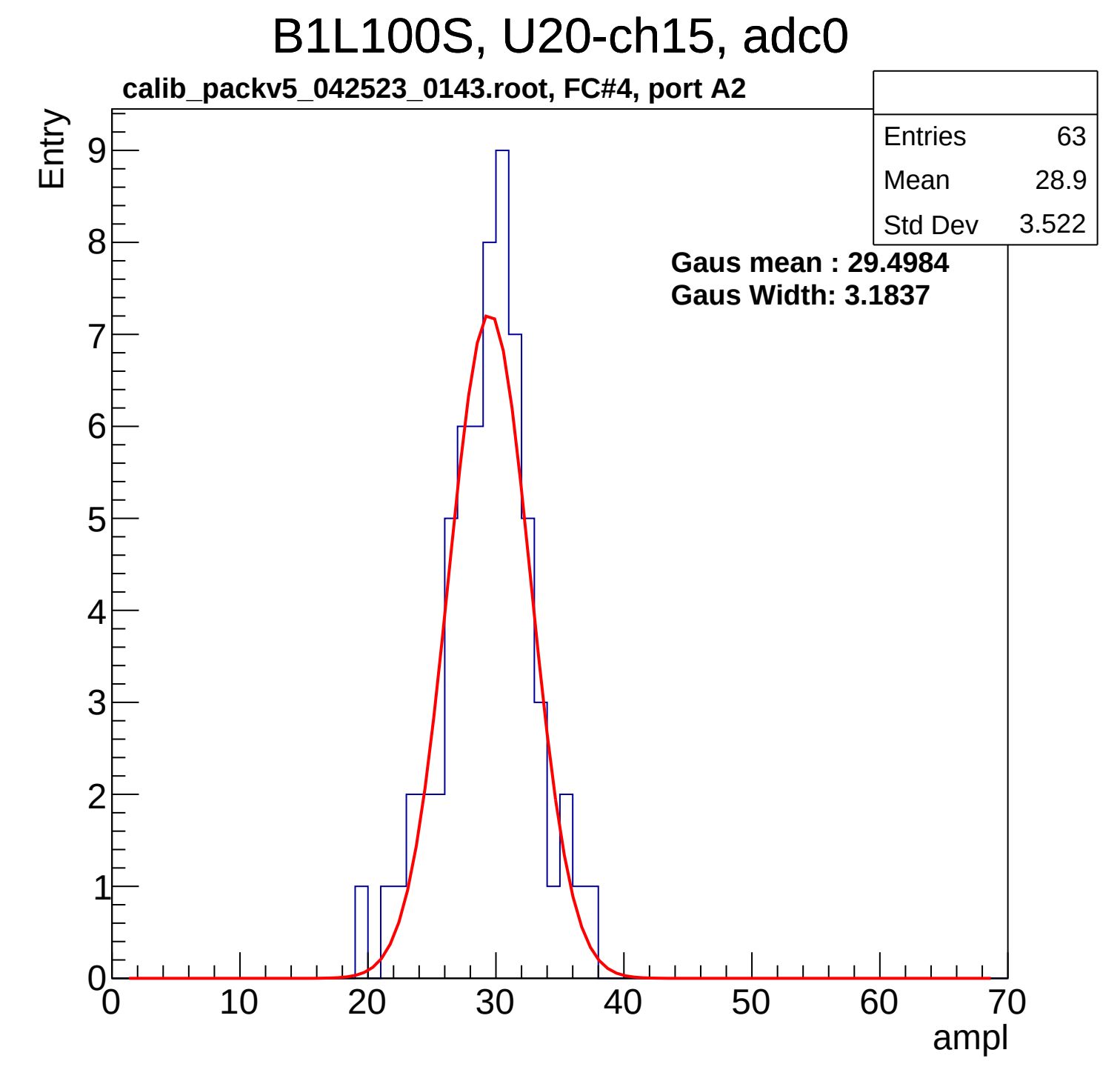
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.9
Std Dev 3.522
Gaus mean : 29.4984
Gaus Width: 3.1837
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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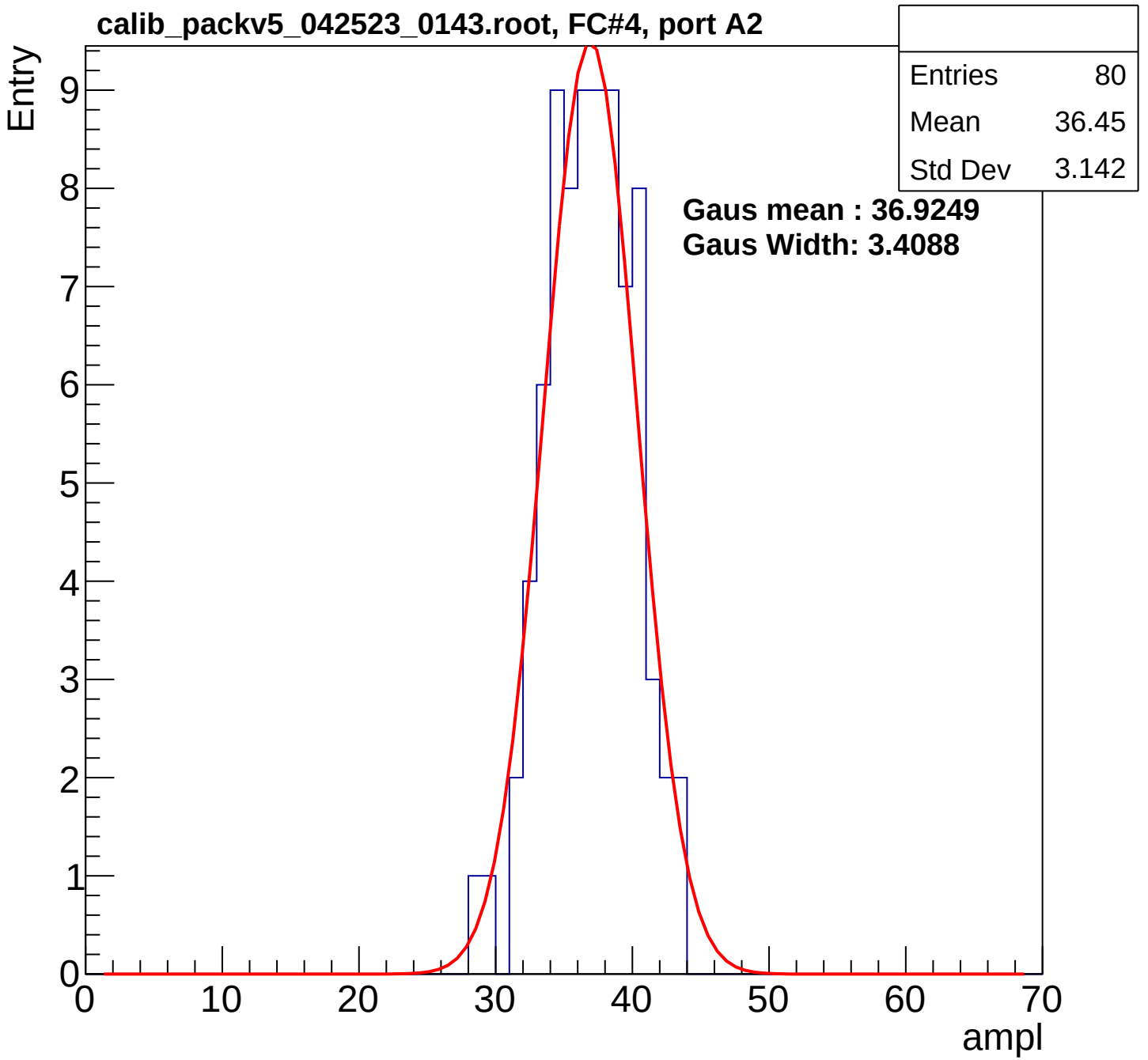
B1L100S, U20-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.45
Std Dev 3.142
Gaus mean : 36.9249
Gaus Width: 3.4088
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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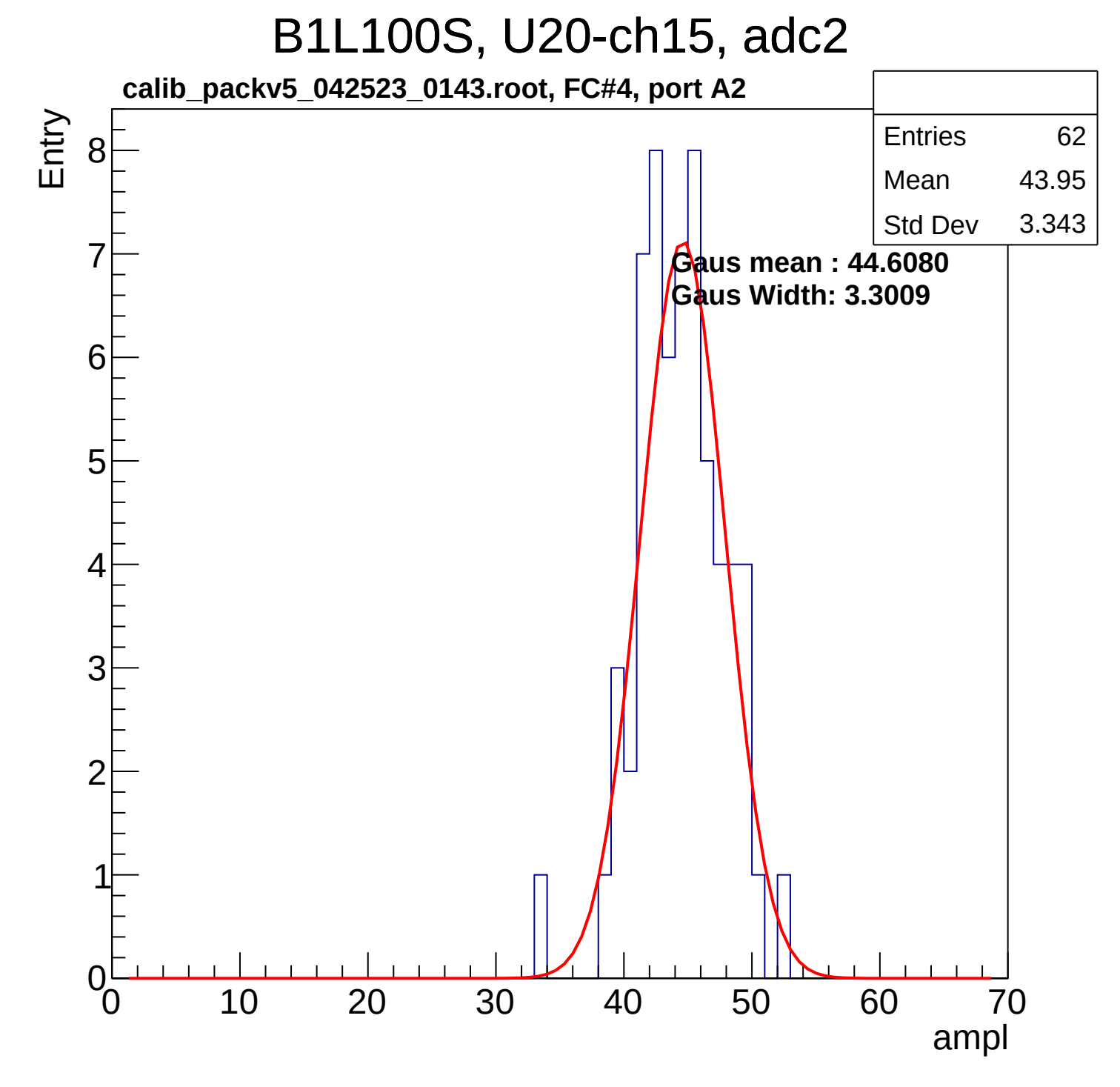
B1L100S, U20-ch15, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.95
Std Dev 3.343
Gaus mean : 44.6080
Gaus Width: 3.3009
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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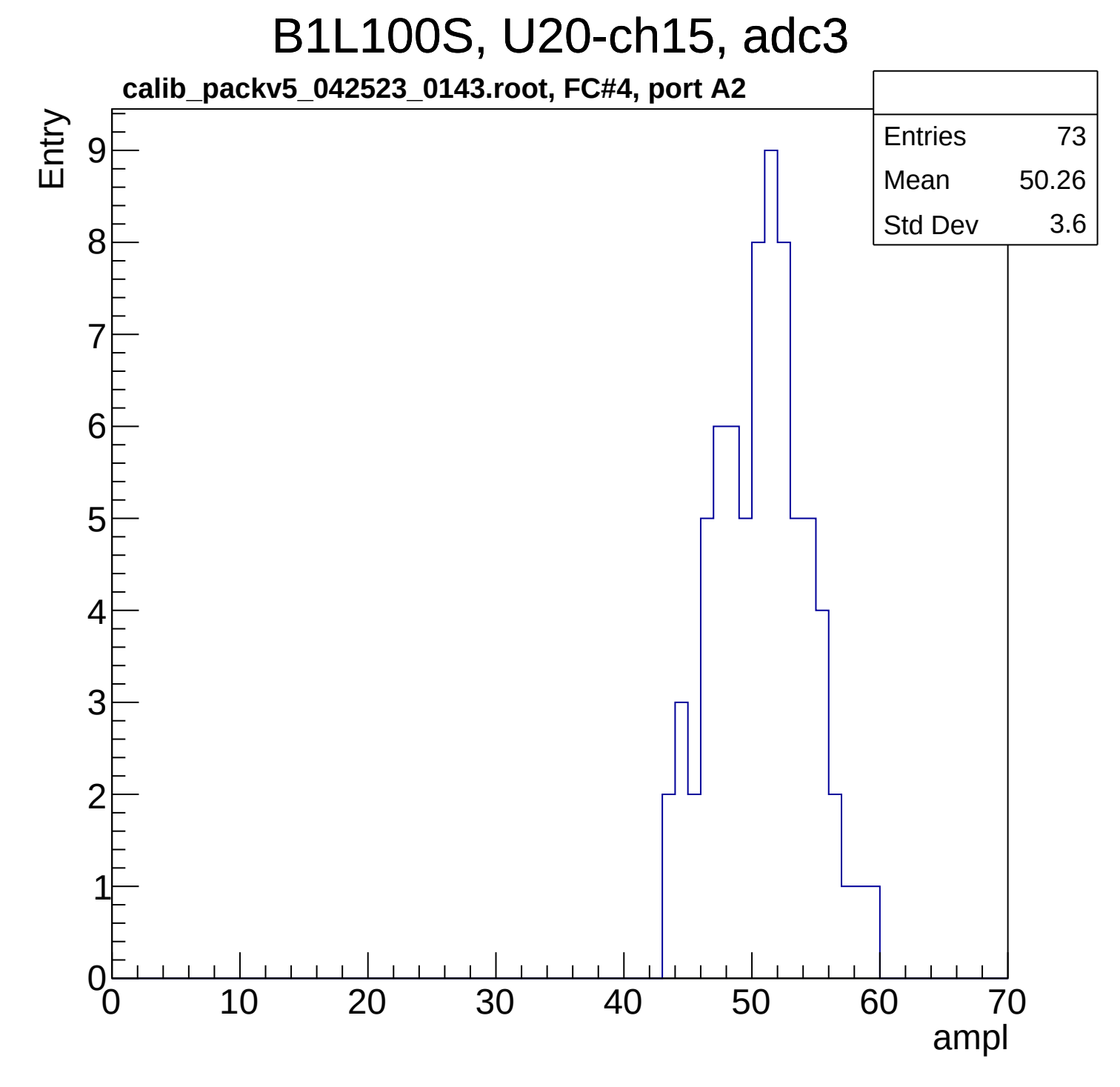
B1L100S, U20-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 50.26
Std Dev 3.6
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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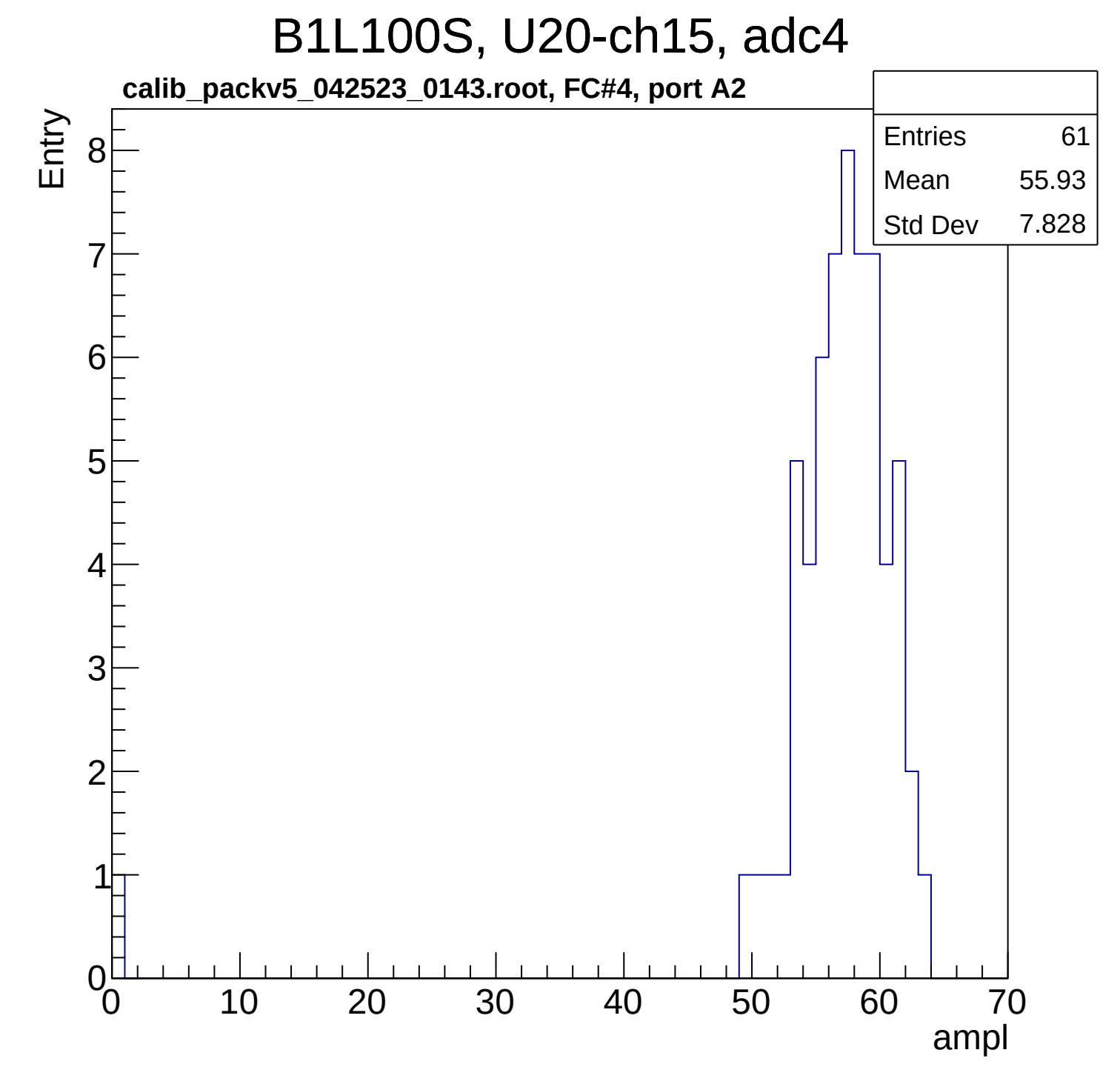
B1L100S, U20-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.93
Std Dev 7.828
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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30
40
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60
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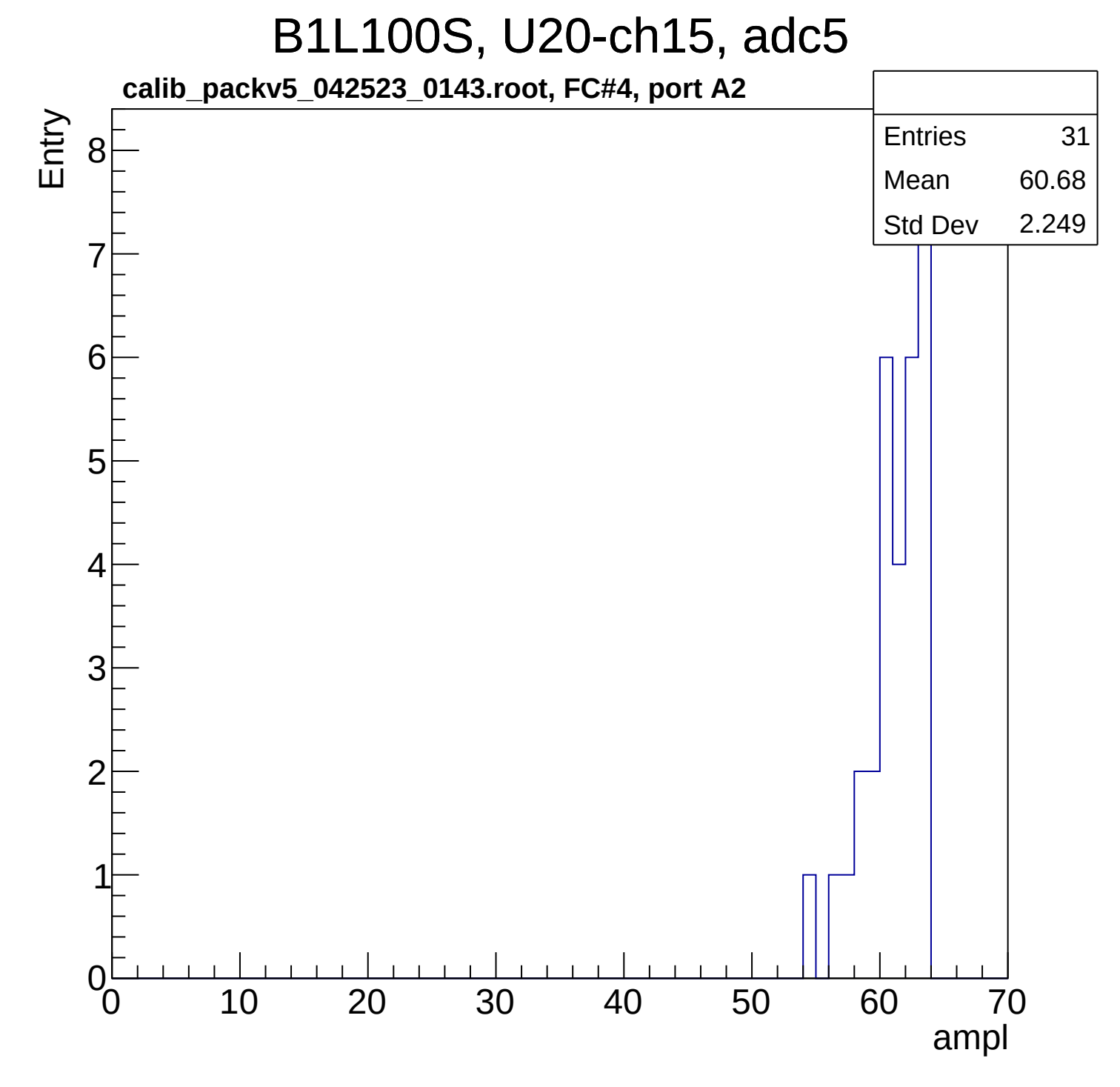
B1L100S, U20-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 60.68
Std Dev 2.249
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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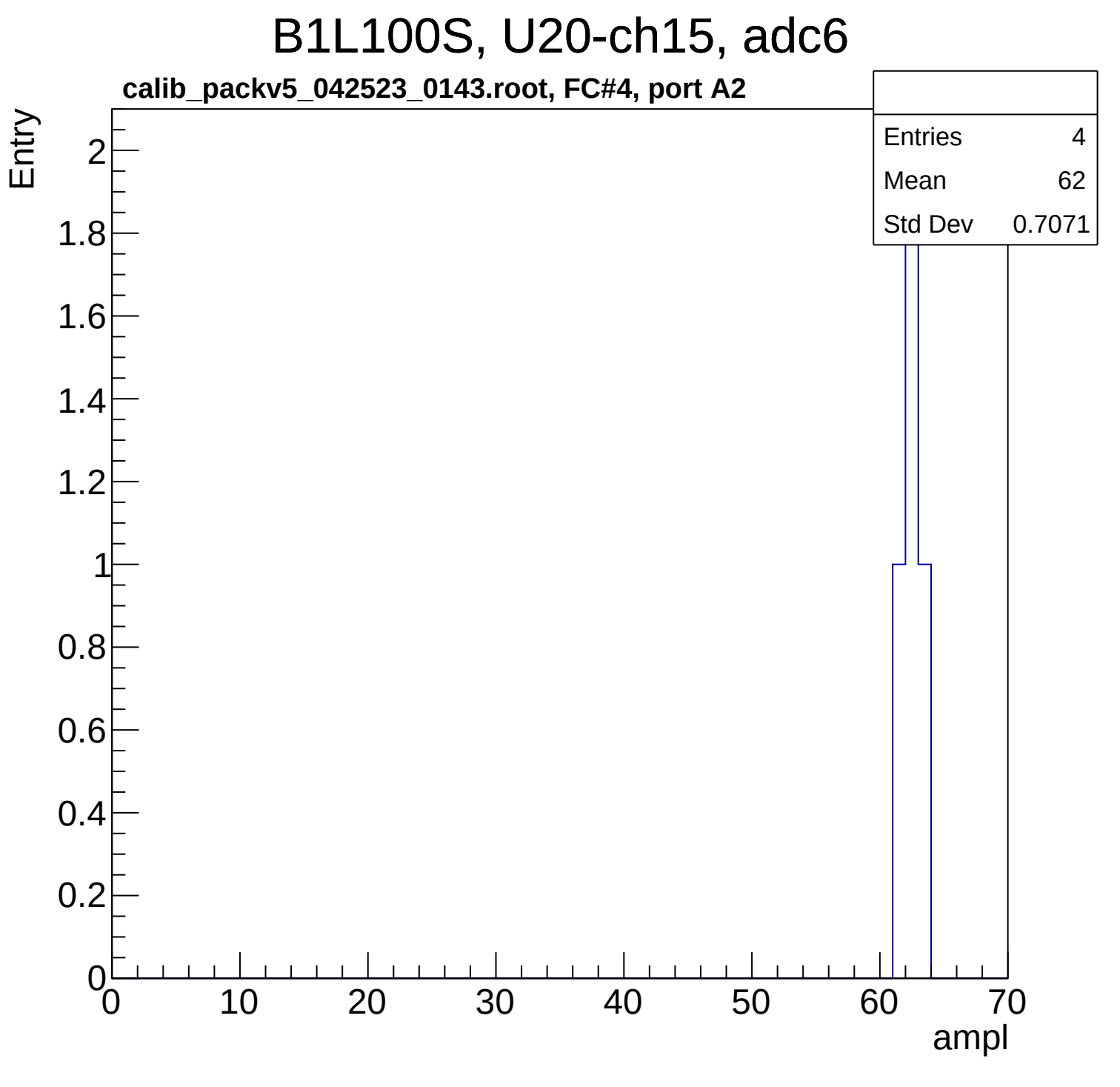
B1L100S, U20-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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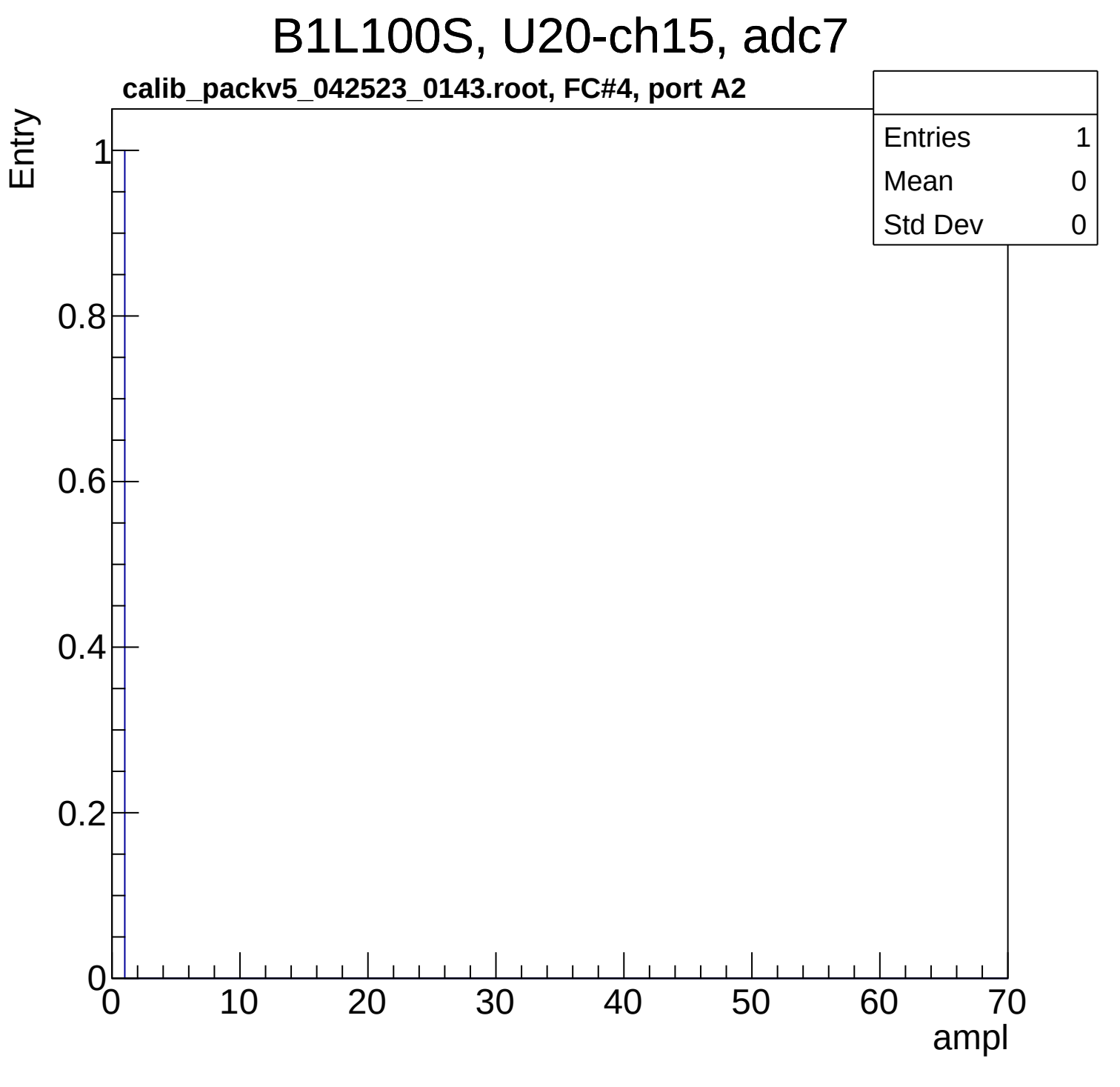
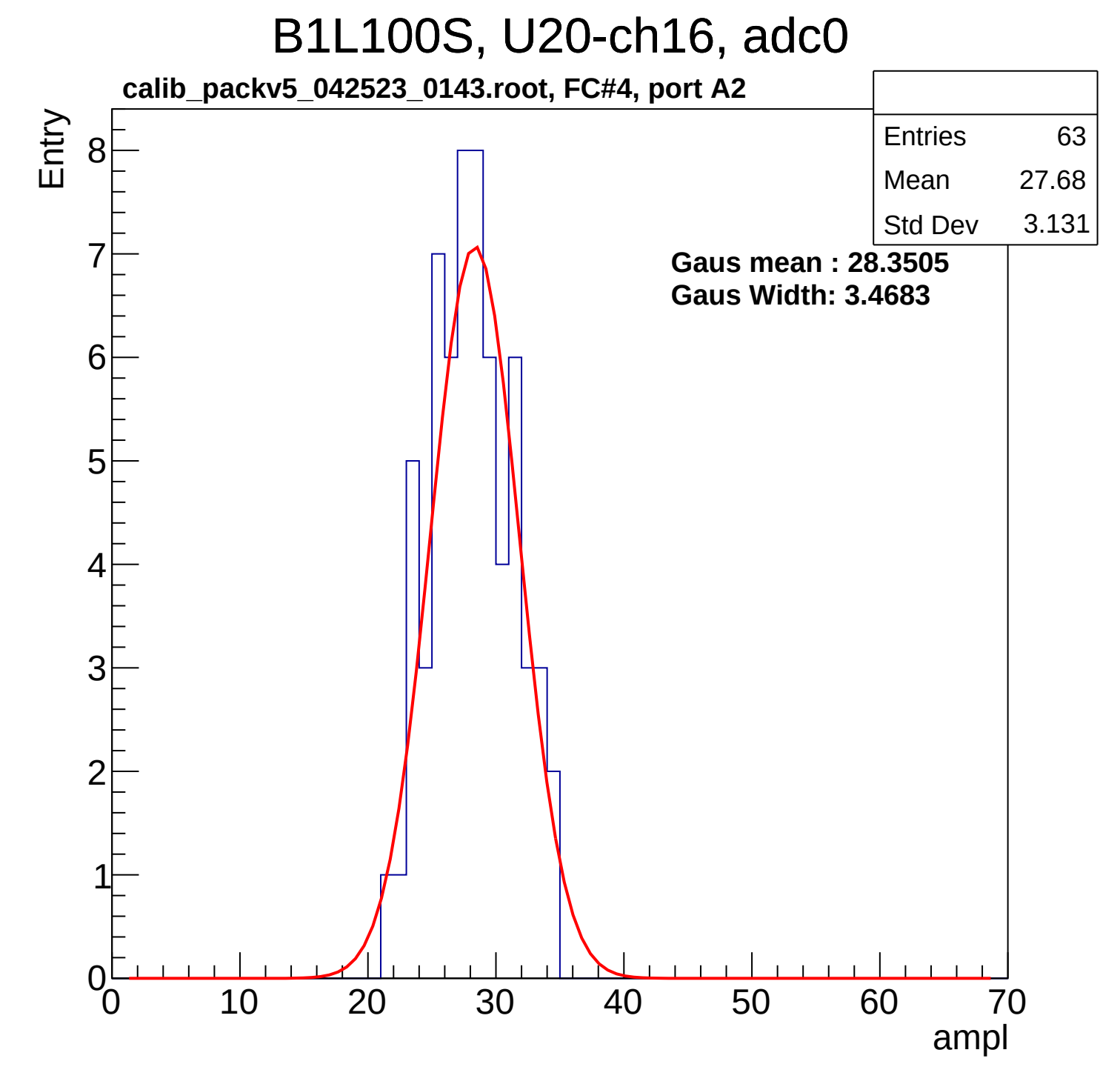

B1L100S, U20-ch15, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch16, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 27.68
Std Dev 3.131
Gaus mean : 28.3505
Gaus Width: 3.4683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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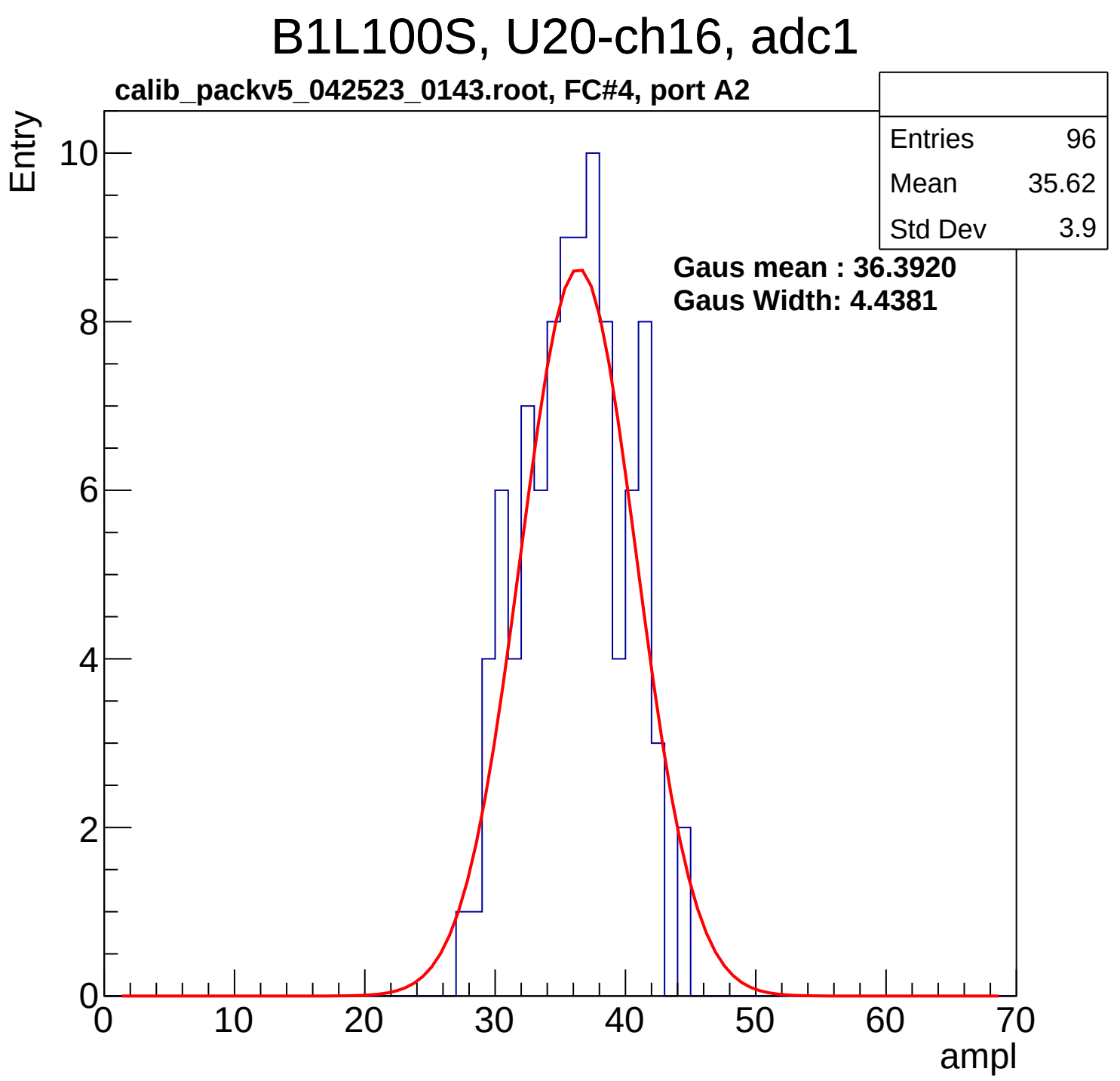
B1L100S, U20-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 96
Mean 35.62
Std Dev 3.9
Gaus mean : 36.3920
Gaus Width: 4.4381
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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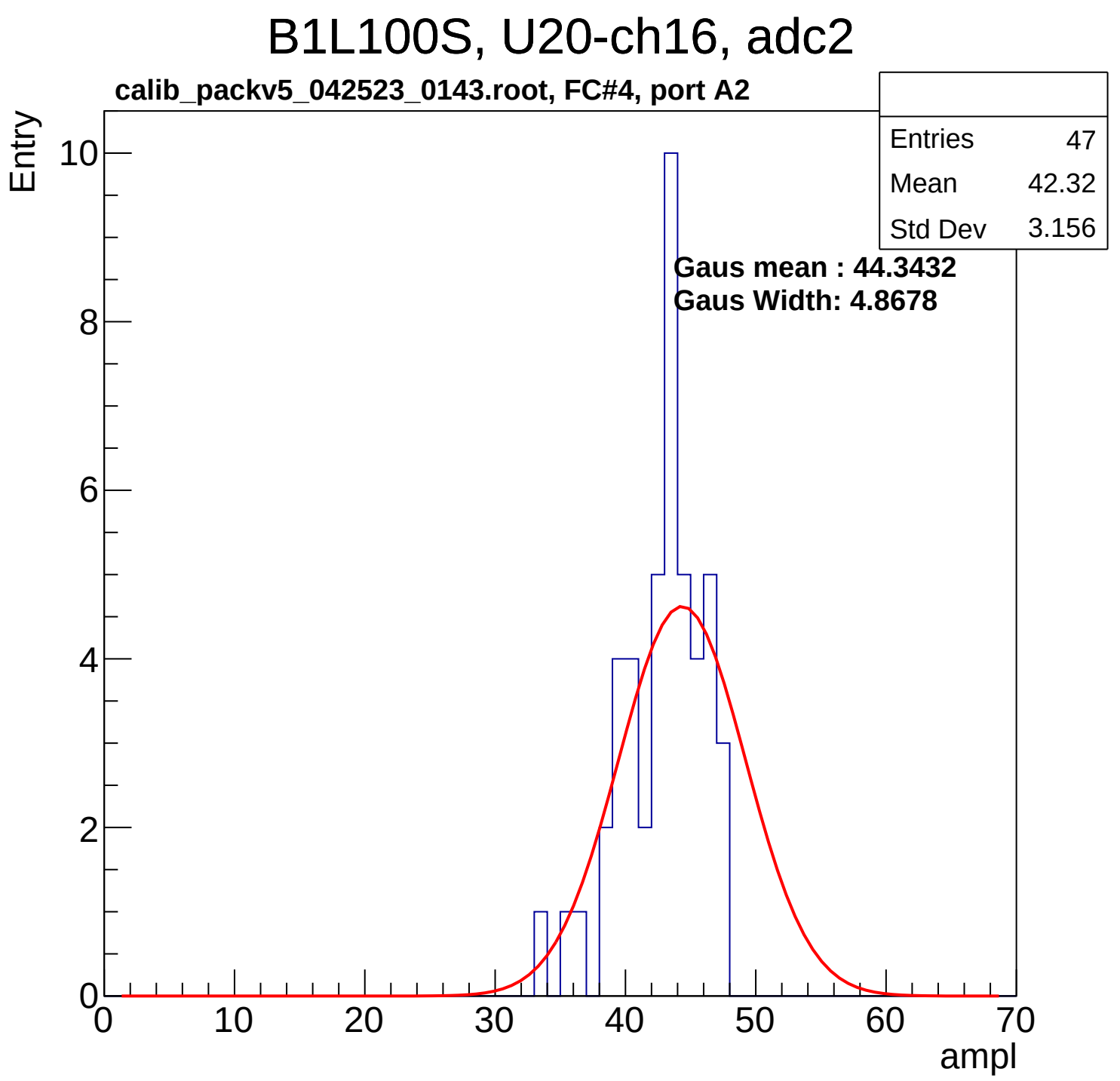
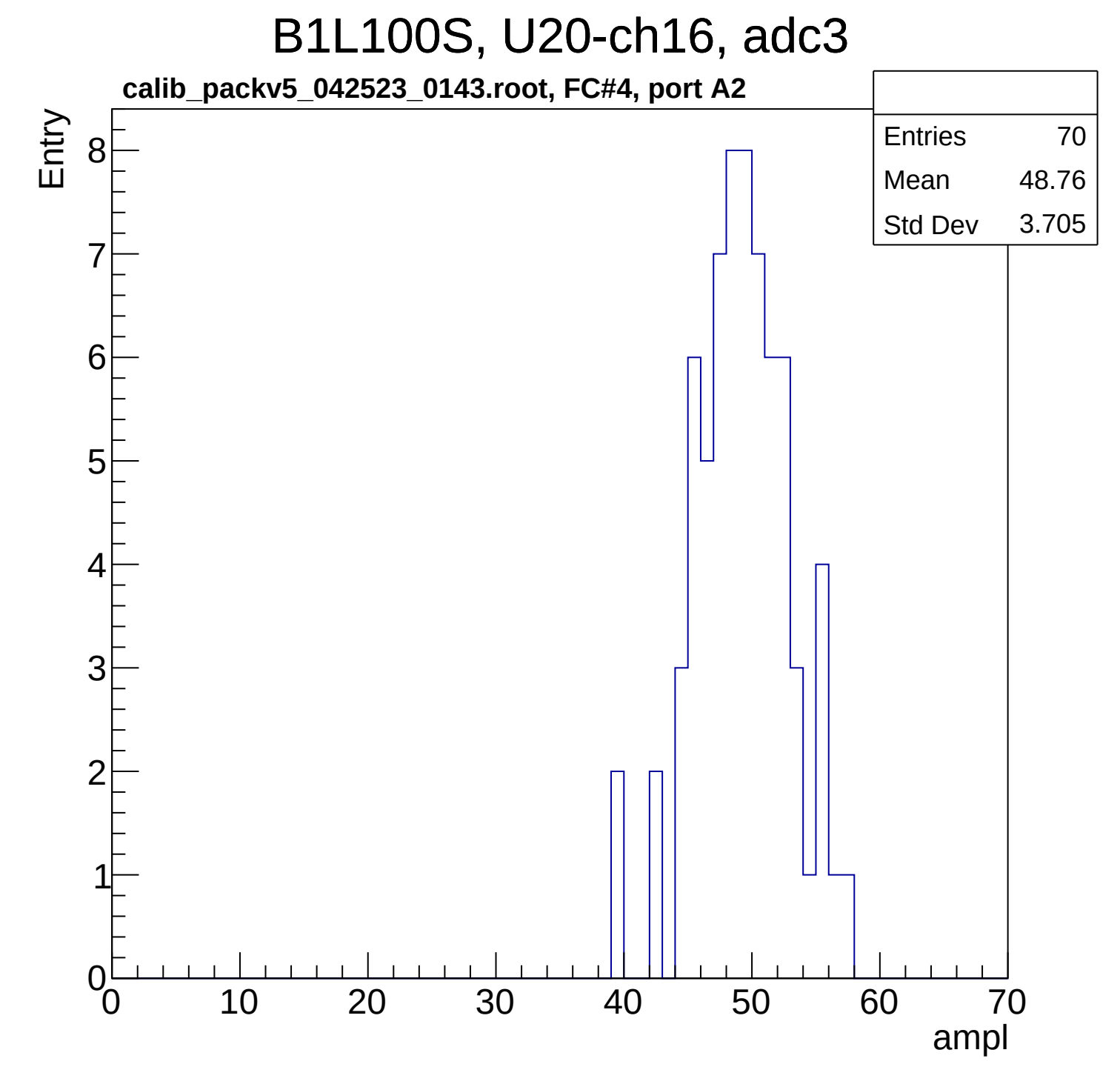
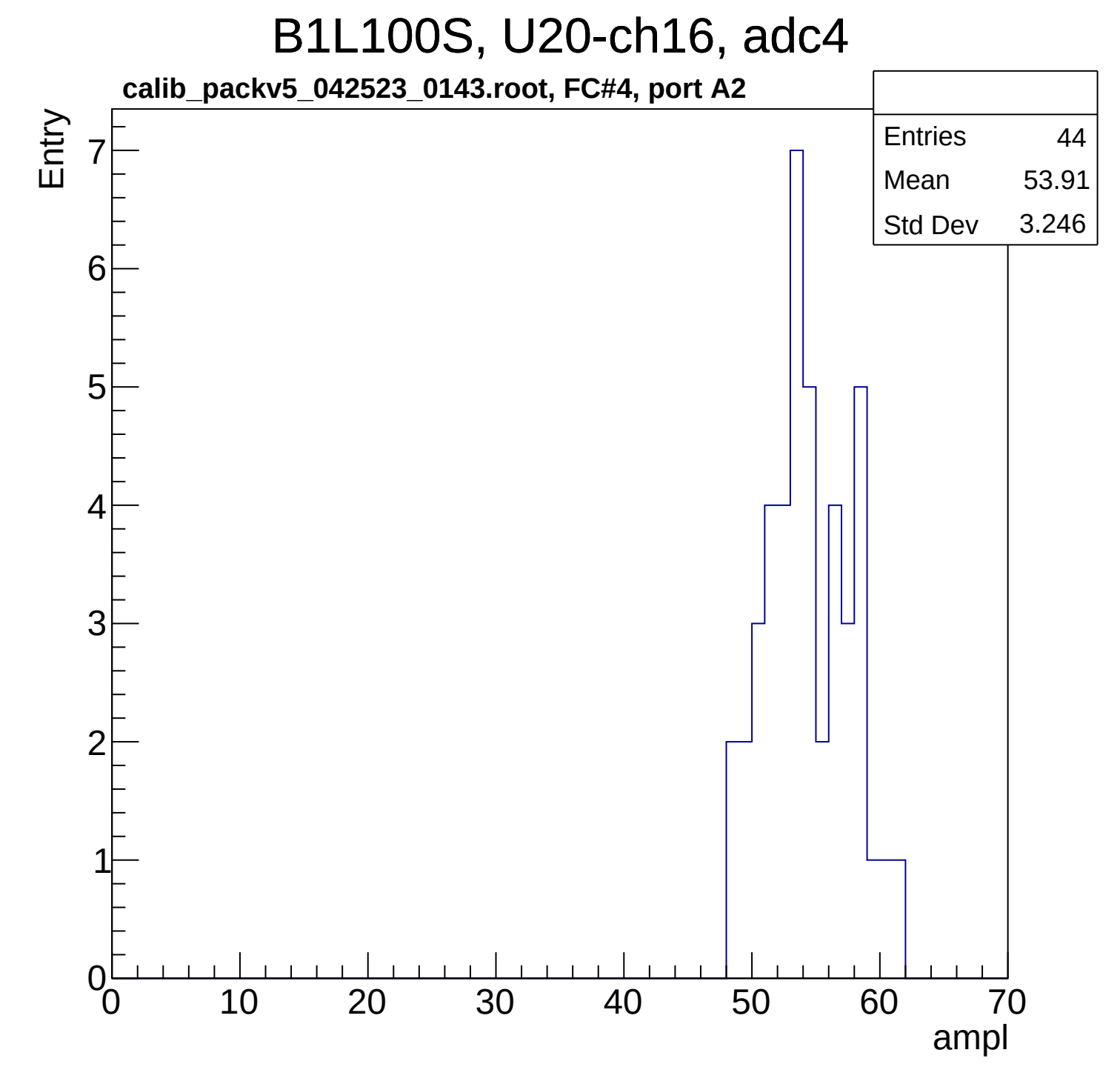
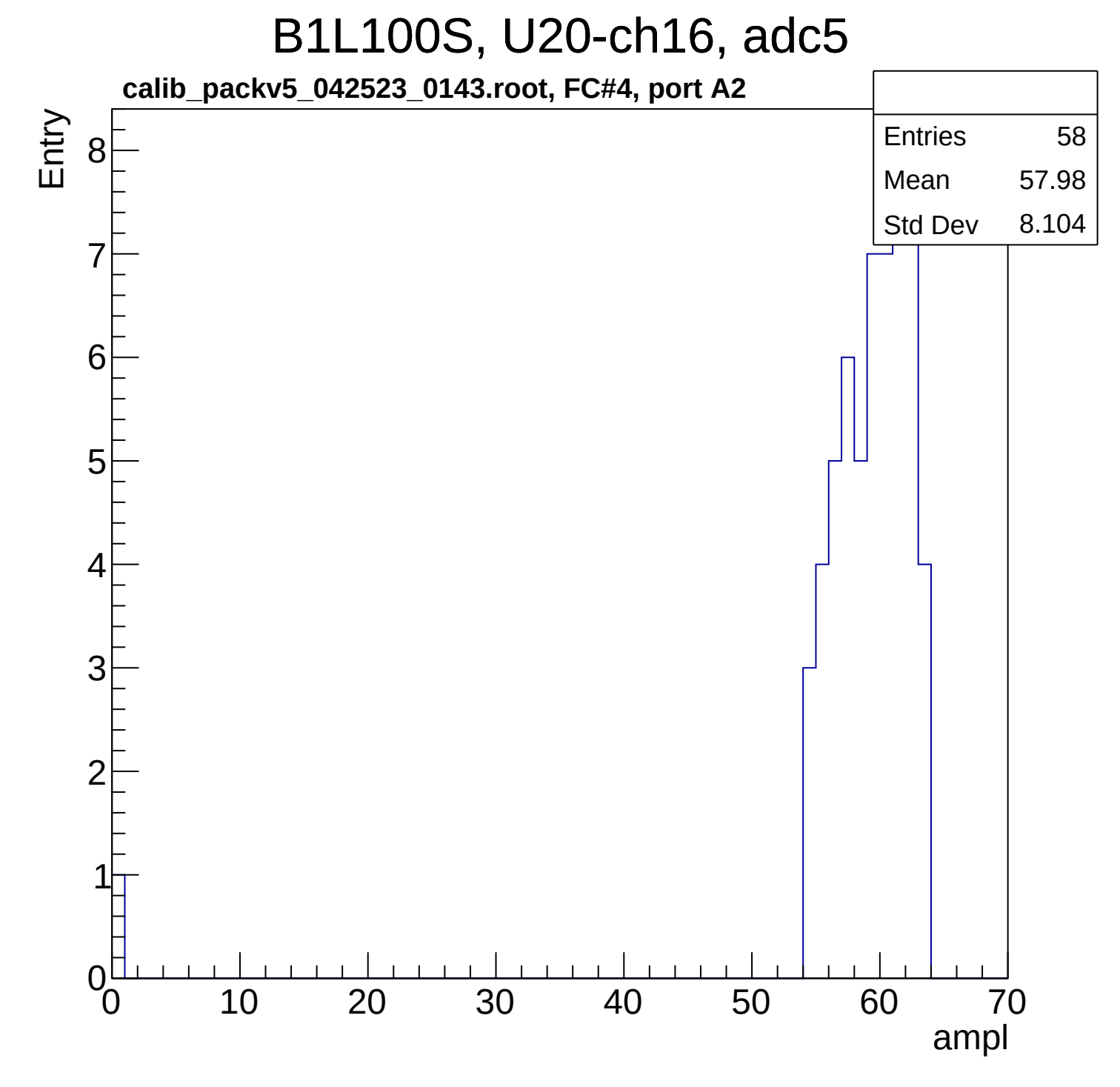

B1L100S, U20-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 42.32
Std Dev 3.156
Gaus mean : 44.3432
Gaus Width: 4.8678
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch16, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 48.76
Std Dev 3.705
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch16, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 53.91
Std Dev 3.246
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch16, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.98
Std Dev 8.104
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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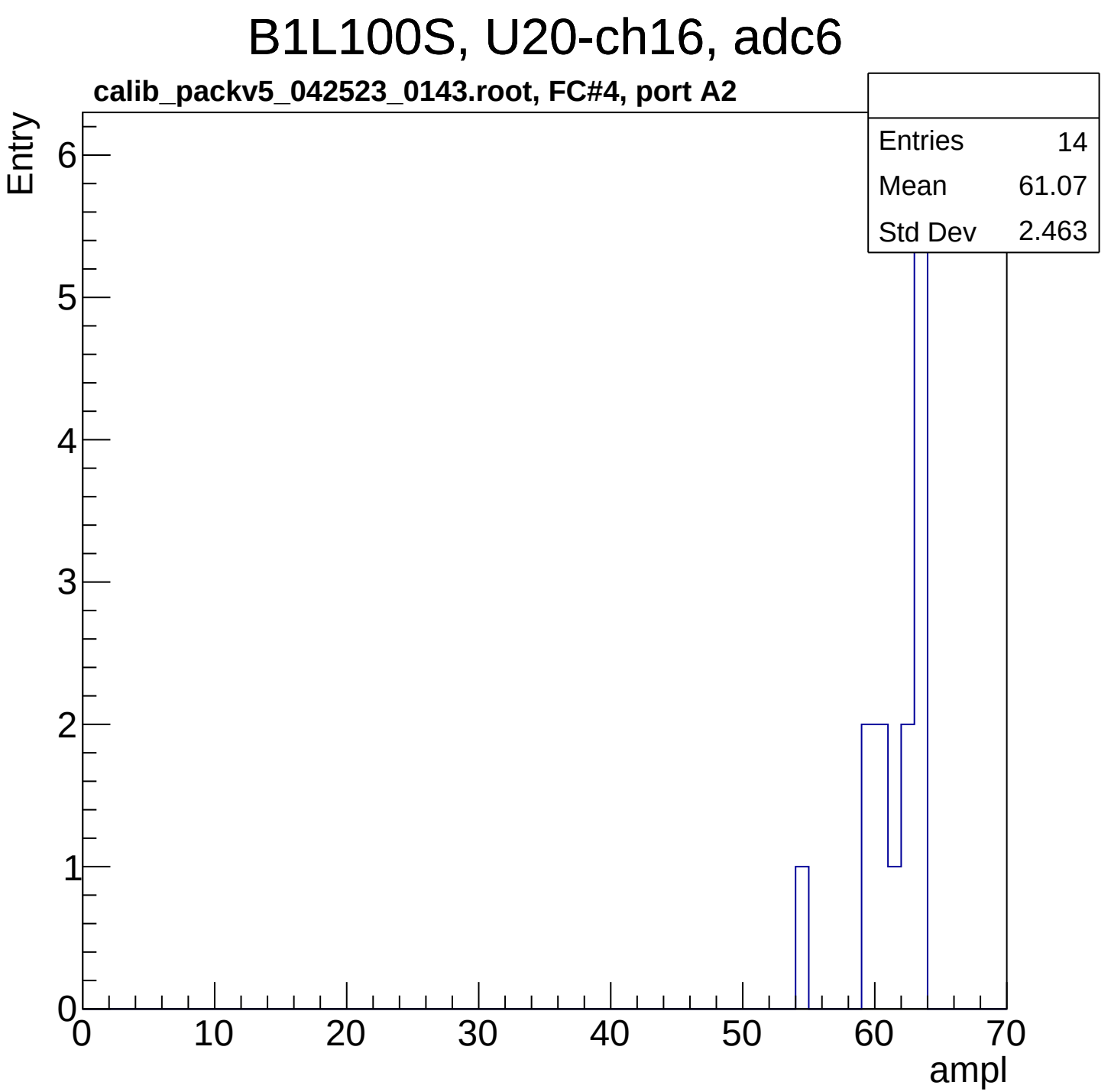
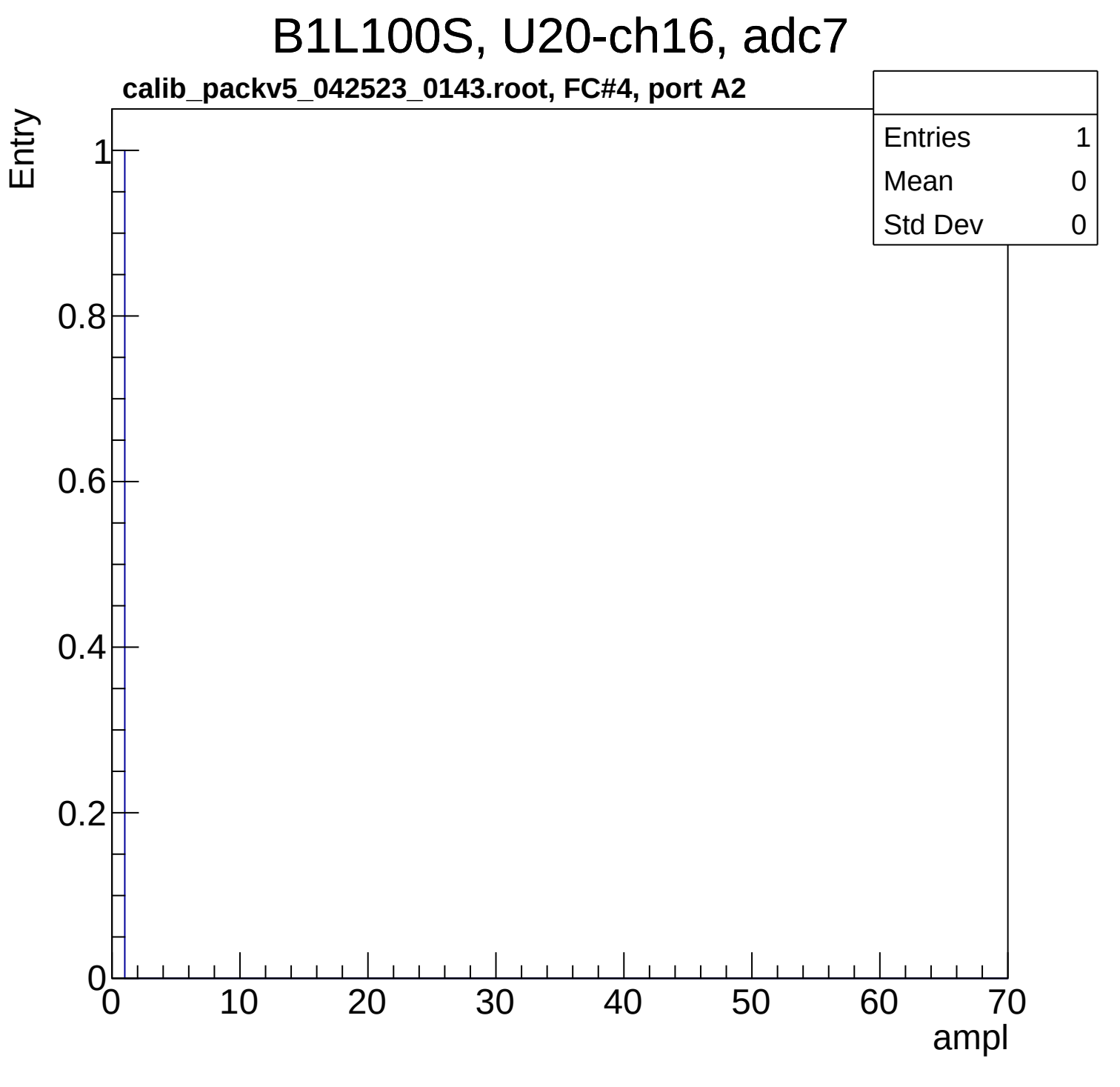
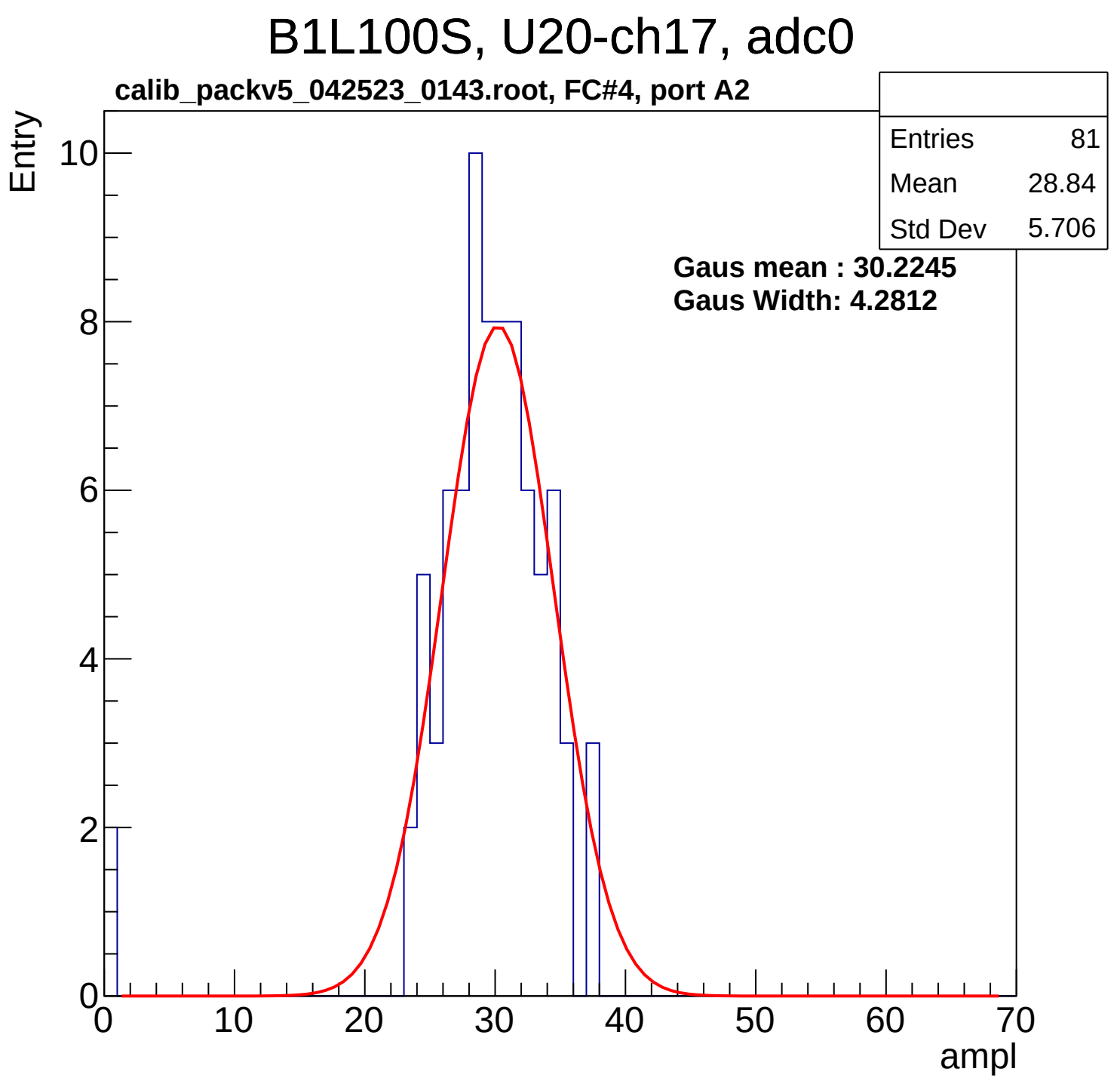
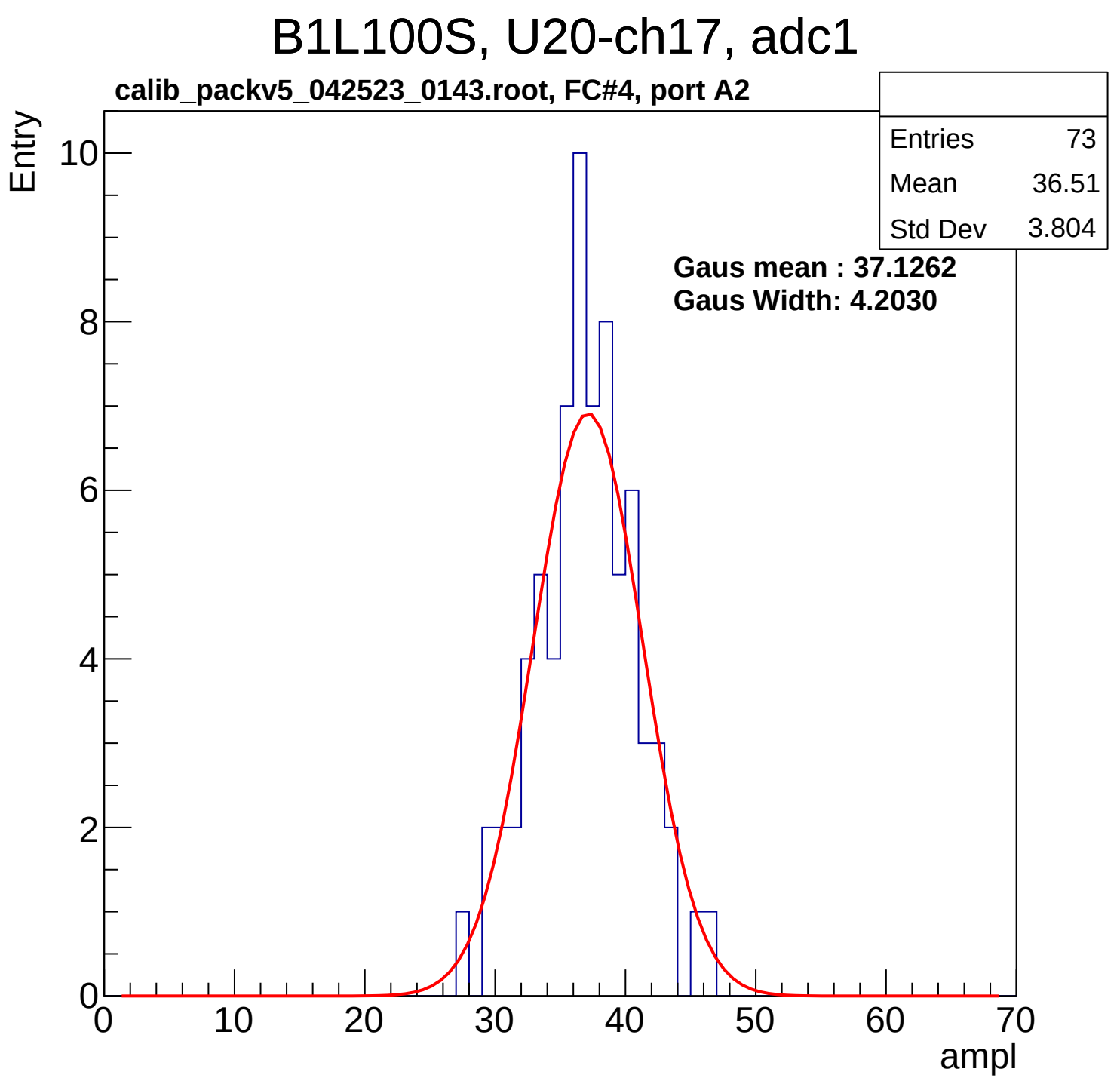
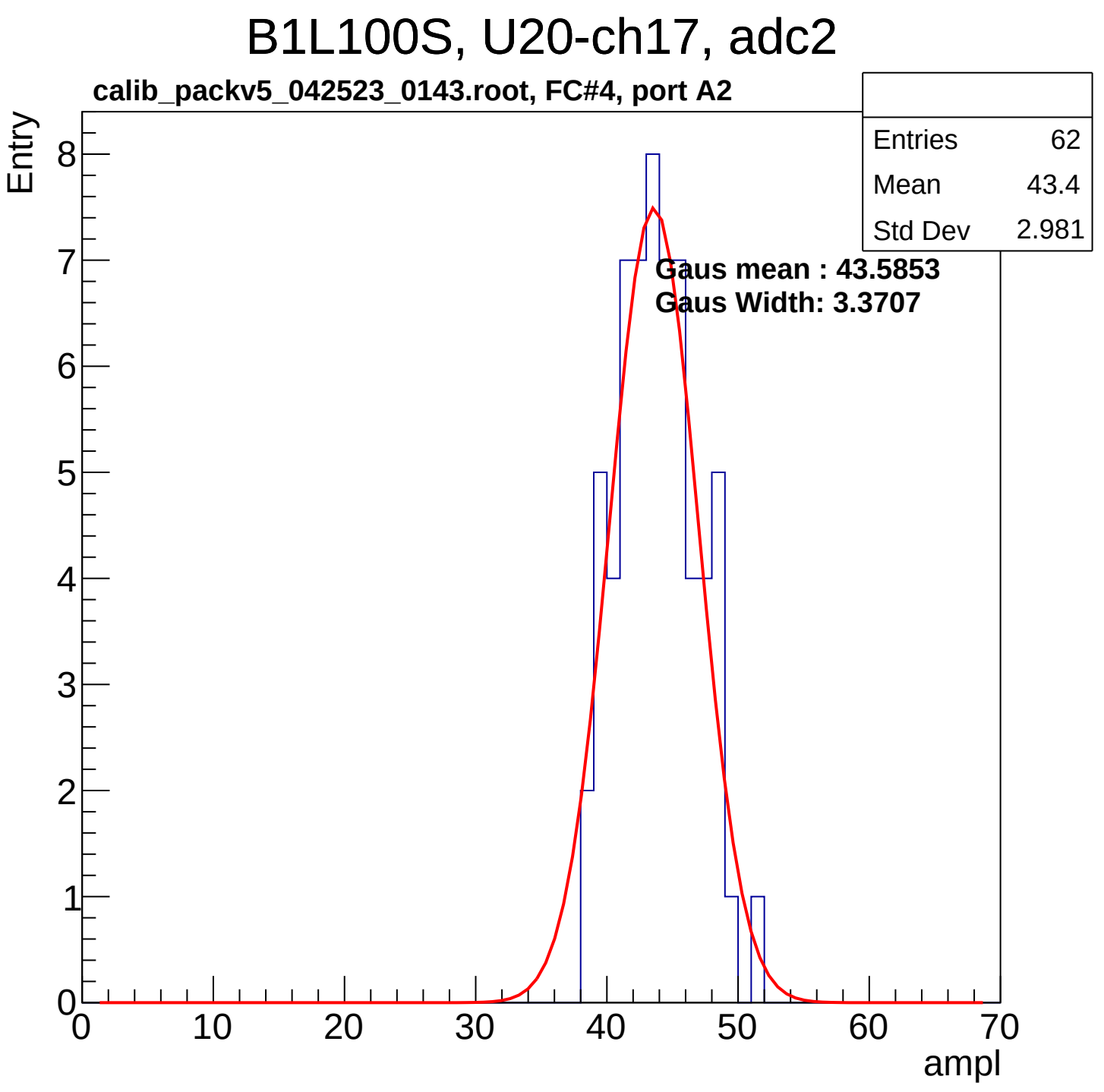

B1L100S, U20-ch16, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 61.07
Std Dev 2.463
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U20-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 28.84
Std Dev 5.706
Gaus mean : 30.2245
Gaus Width: 4.2812
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.51
Std Dev 3.804
Gaus mean : 37.1262
Gaus Width: 4.2030
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.4
Std Dev 2.981
Gaus mean : 43.5853
Gaus Width: 3.3707
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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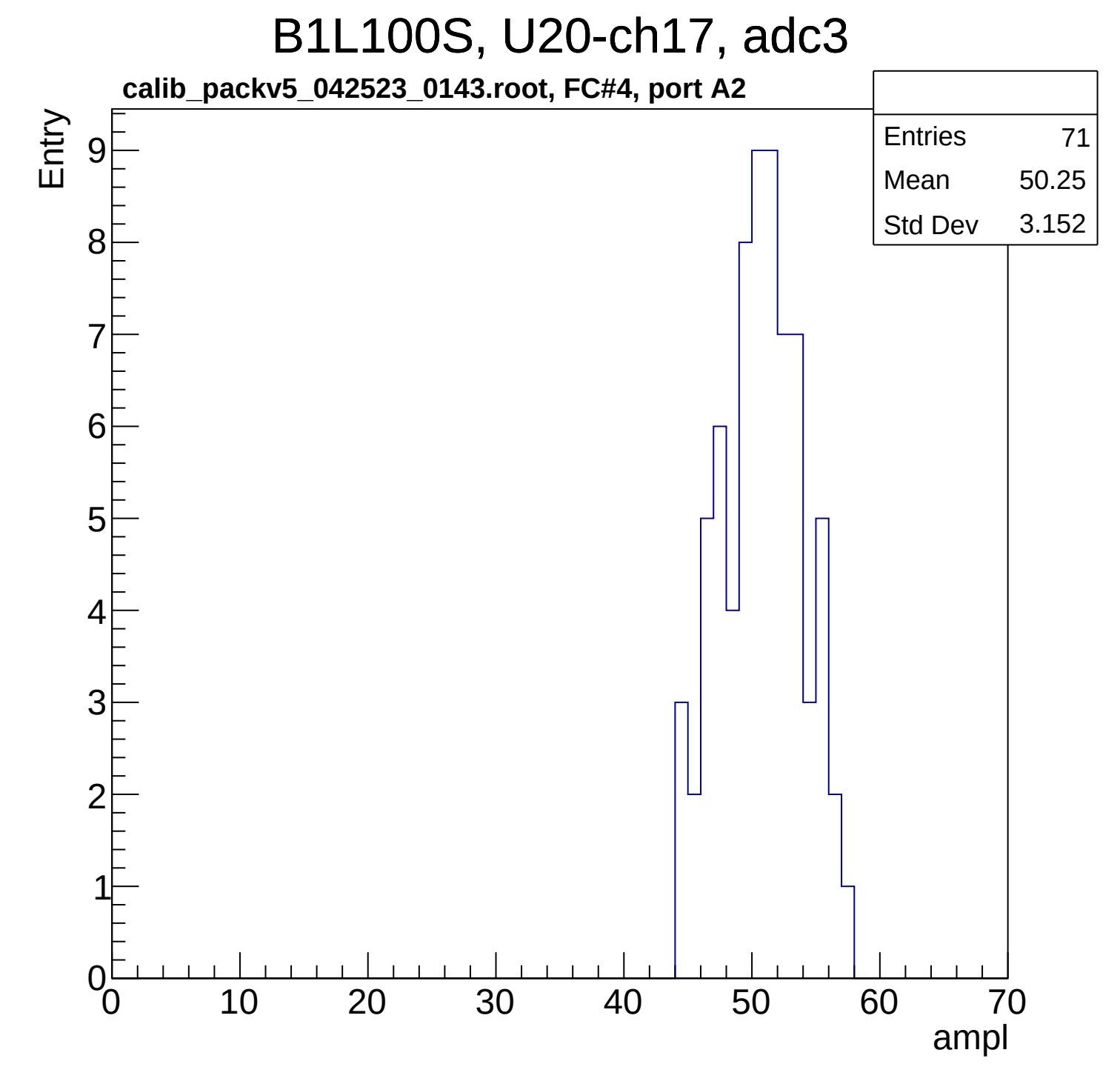
B1L100S, U20-ch17, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.25
Std Dev 3.152
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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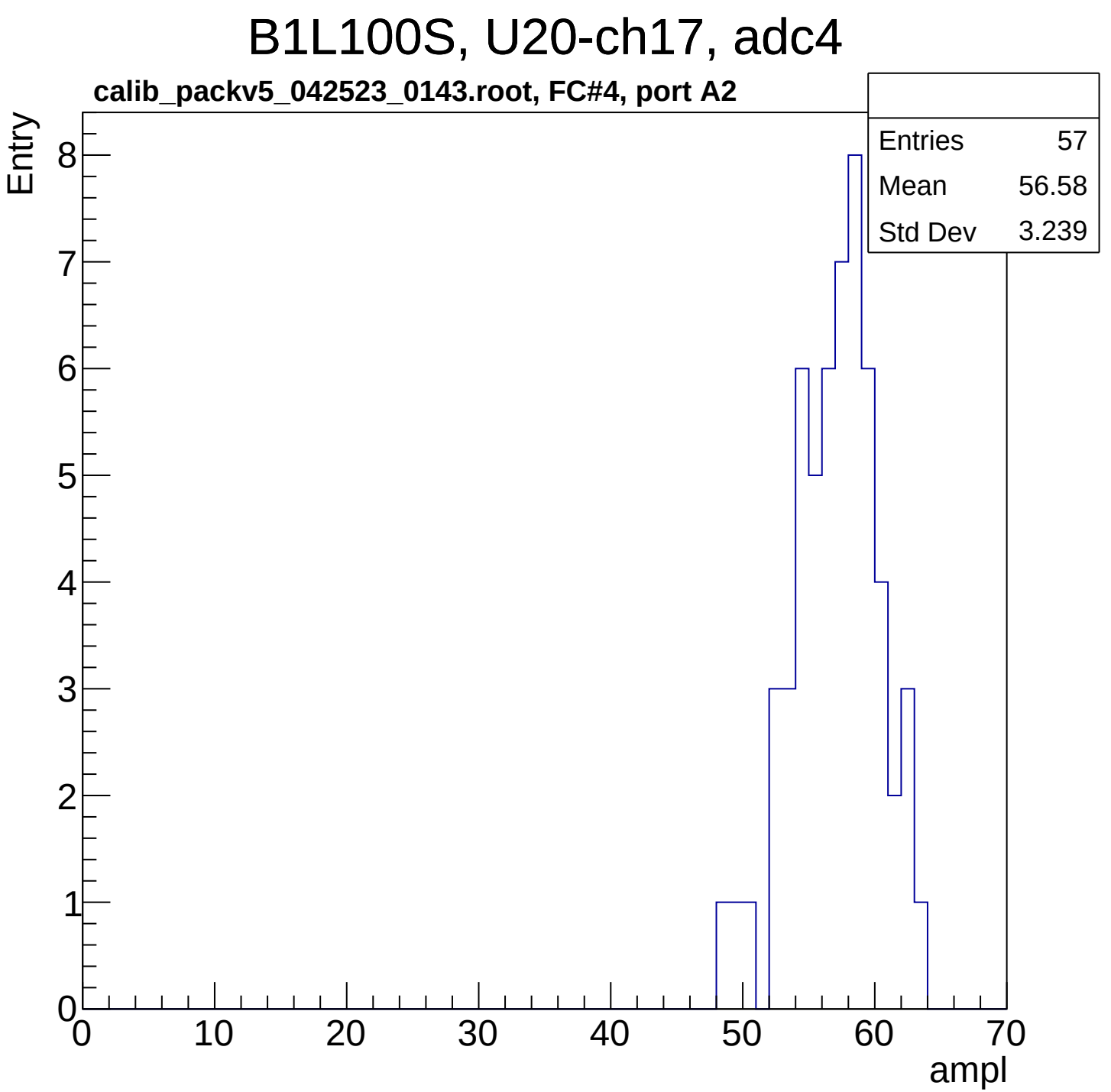
B1L100S, U20-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.58
Std Dev 3.239
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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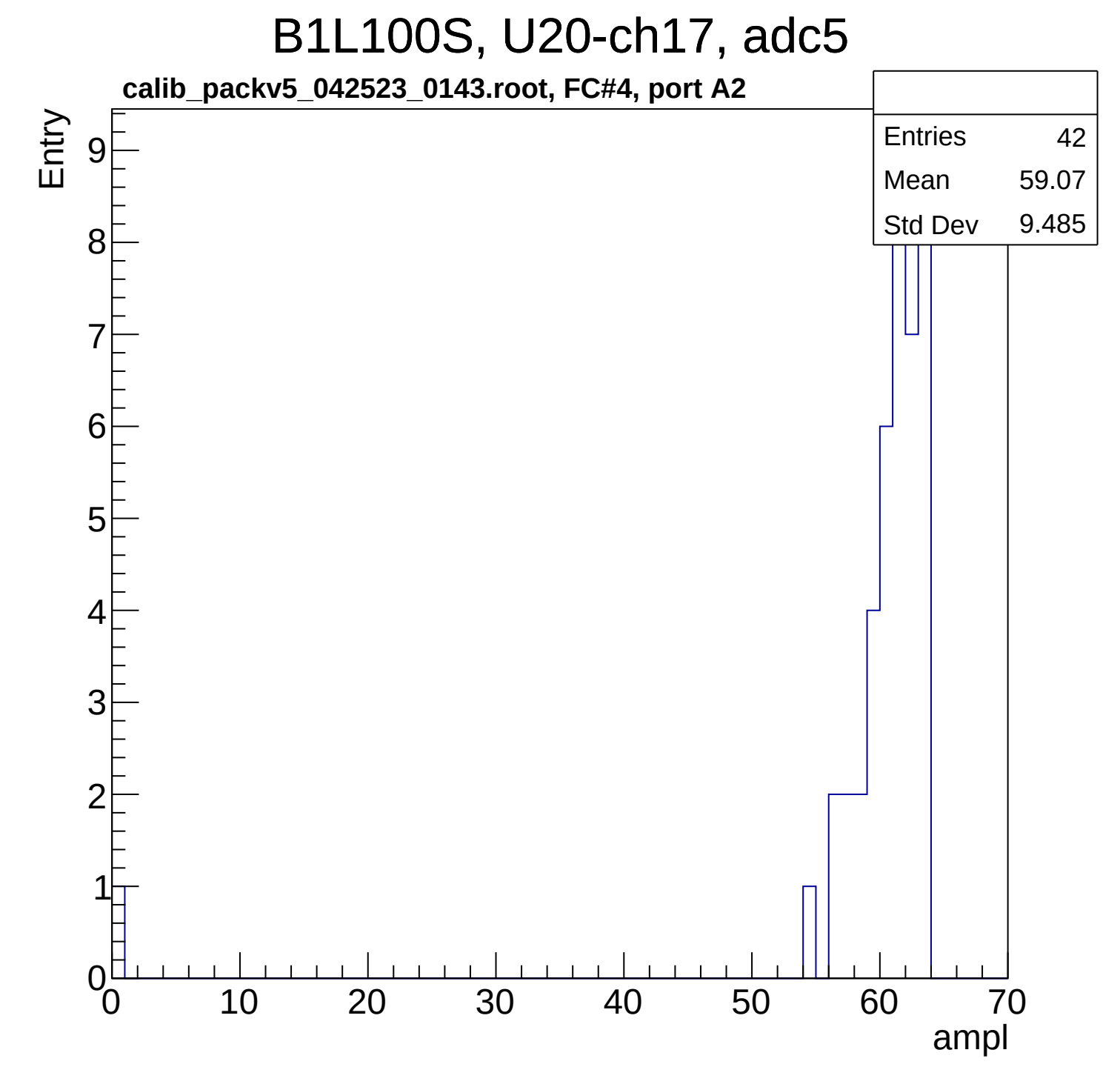
B1L100S, U20-ch17, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.07
Std Dev 9.485
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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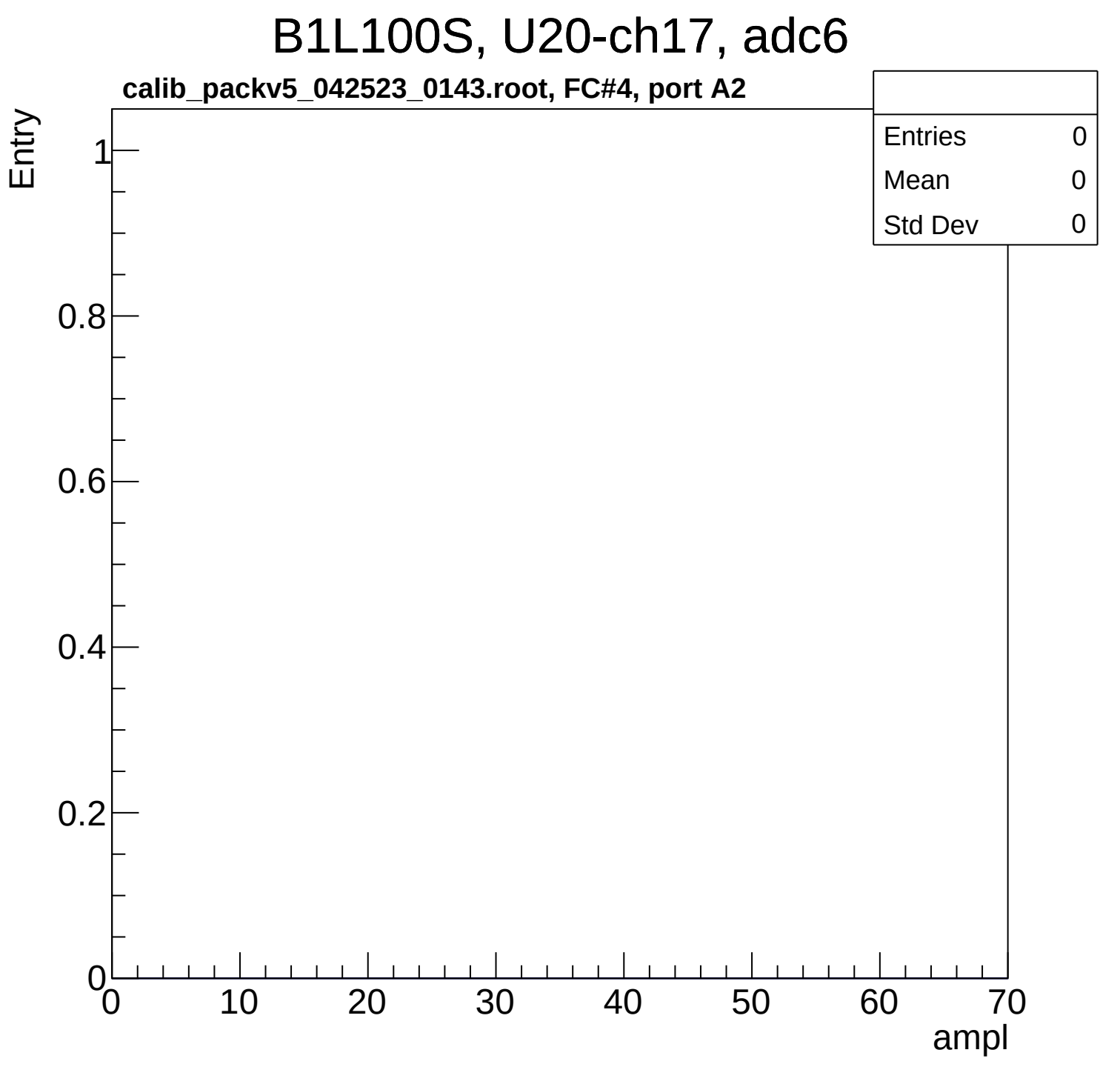
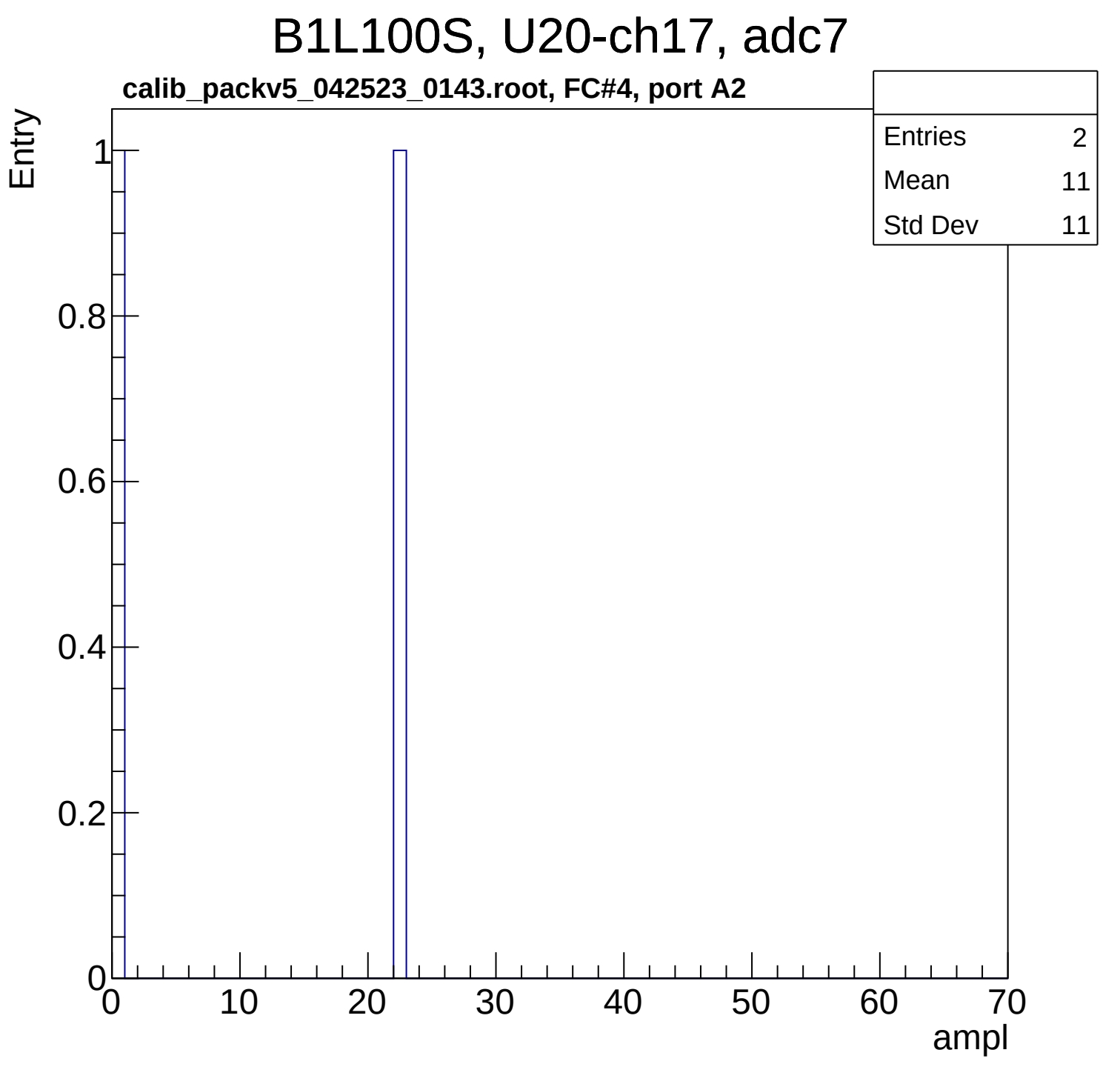
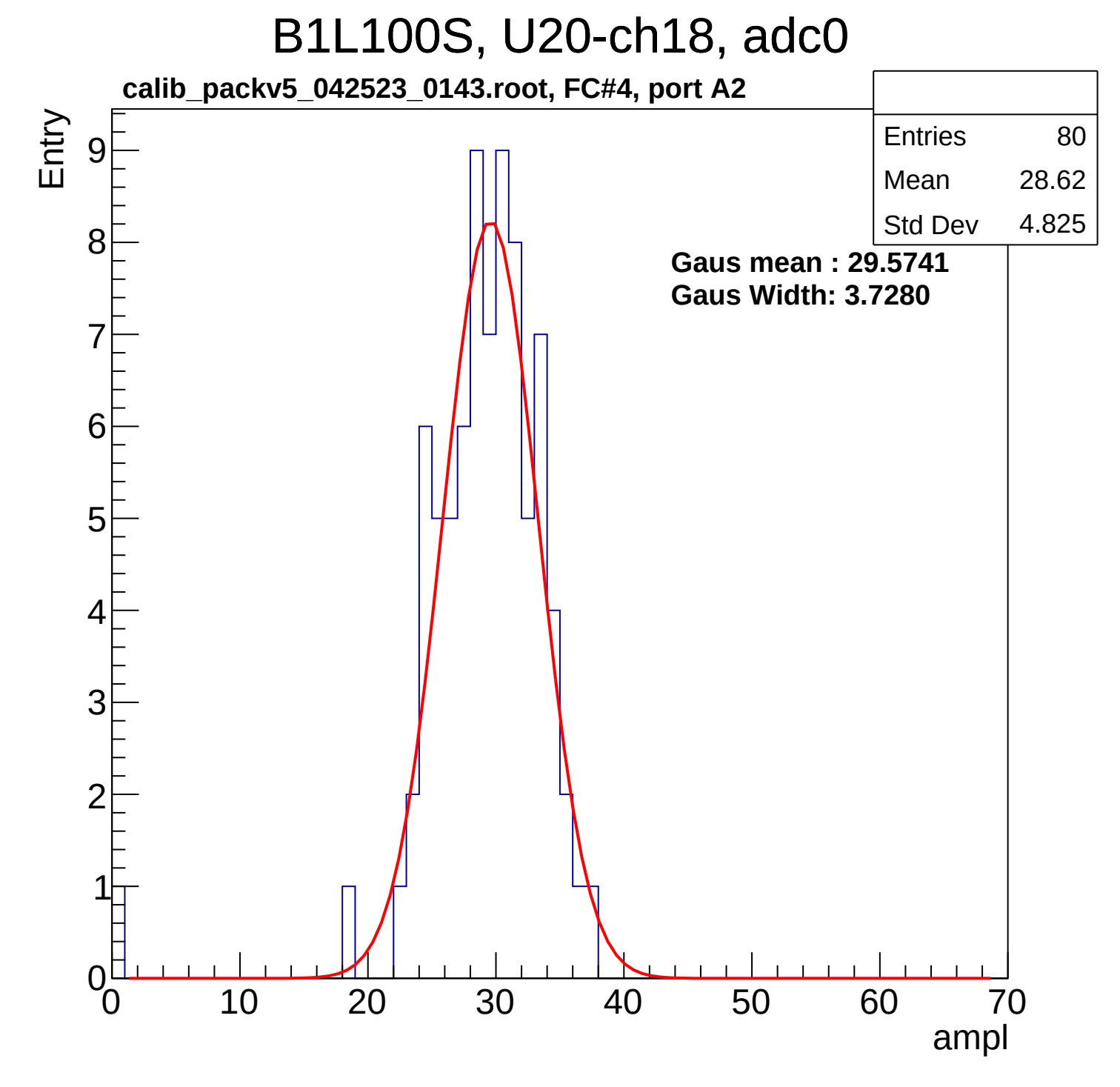

B1L100S, U20-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.62
Std Dev 4.825
Gaus mean : 29.5741
Gaus Width: 3.7280
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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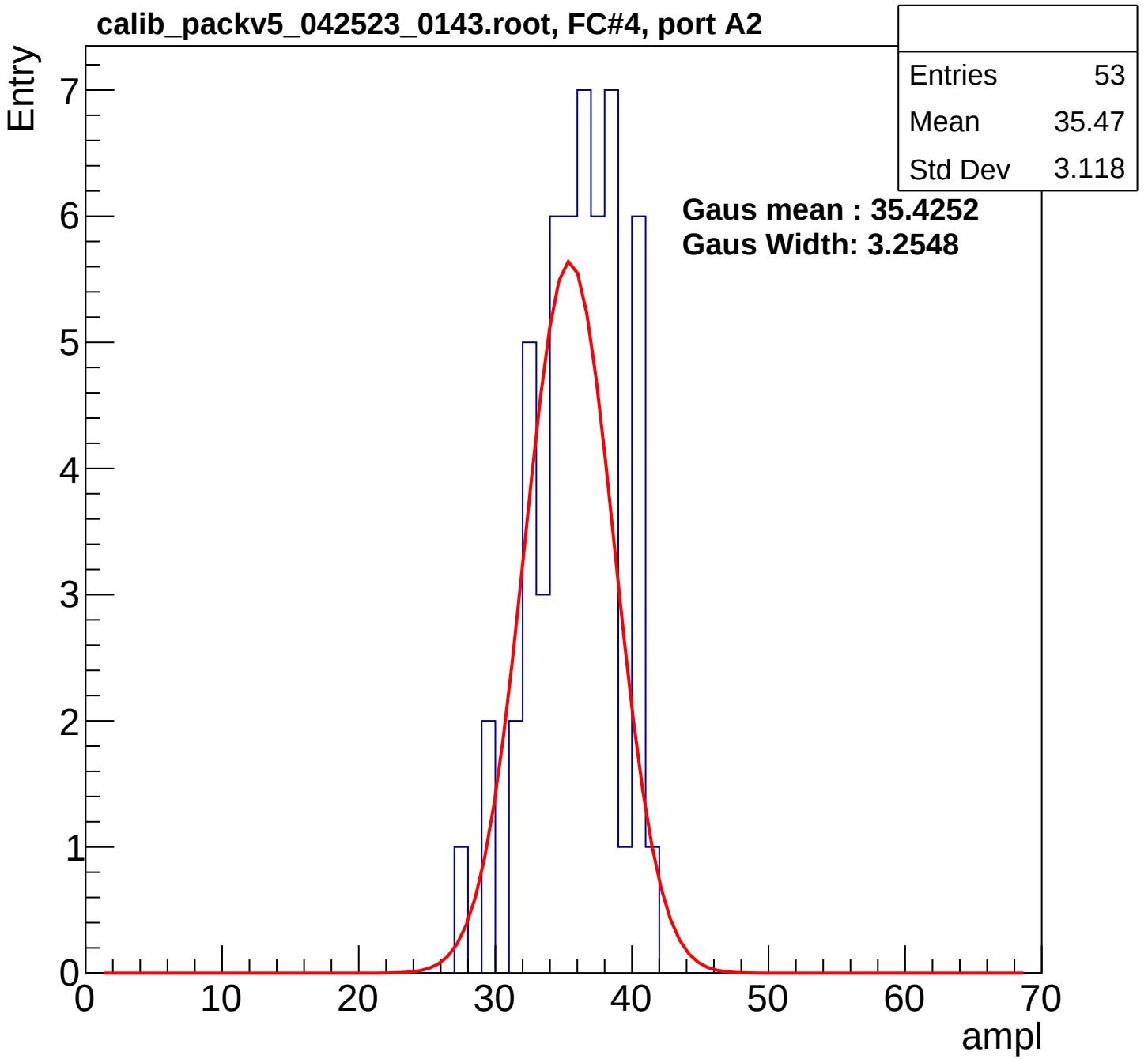
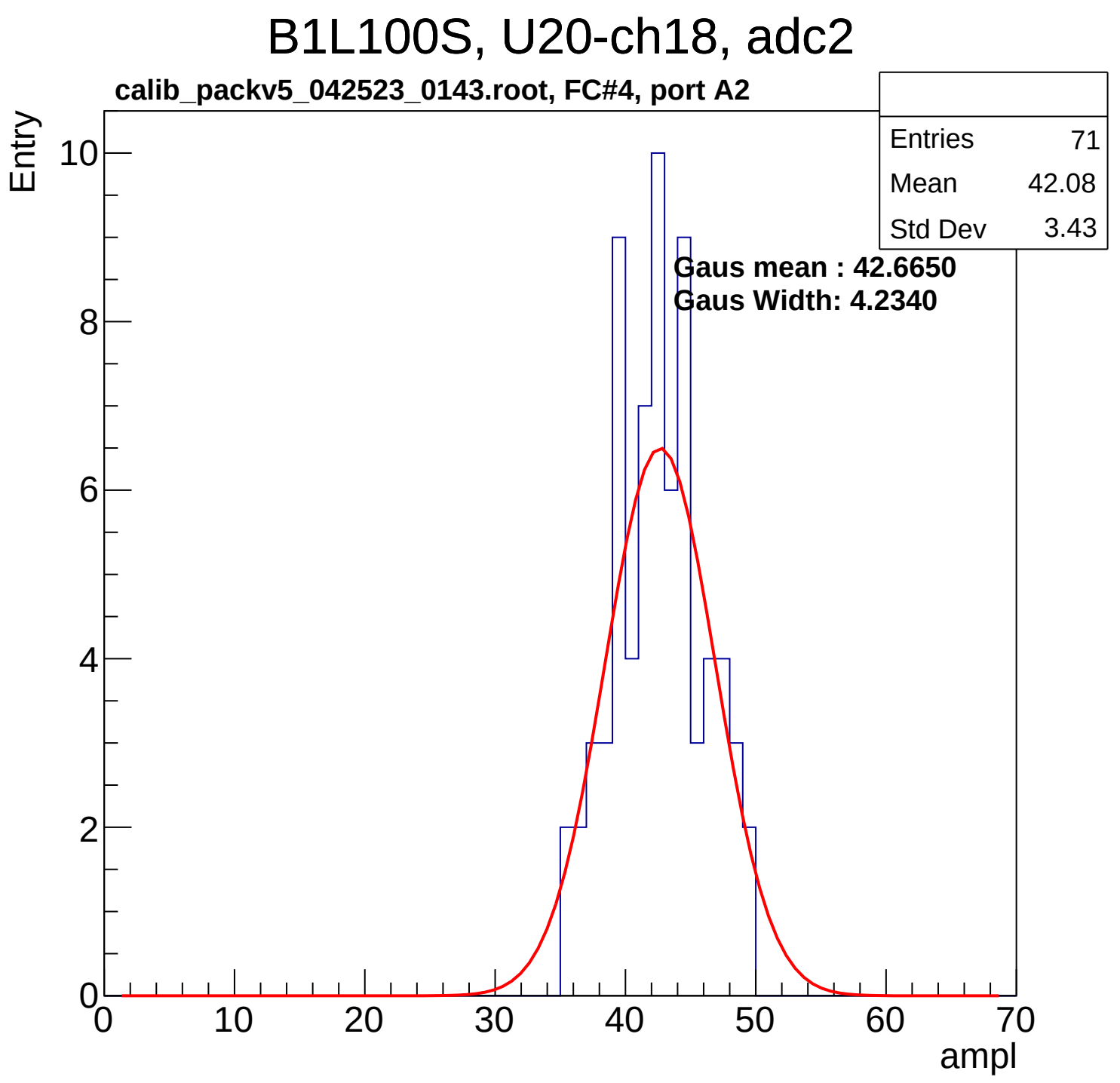
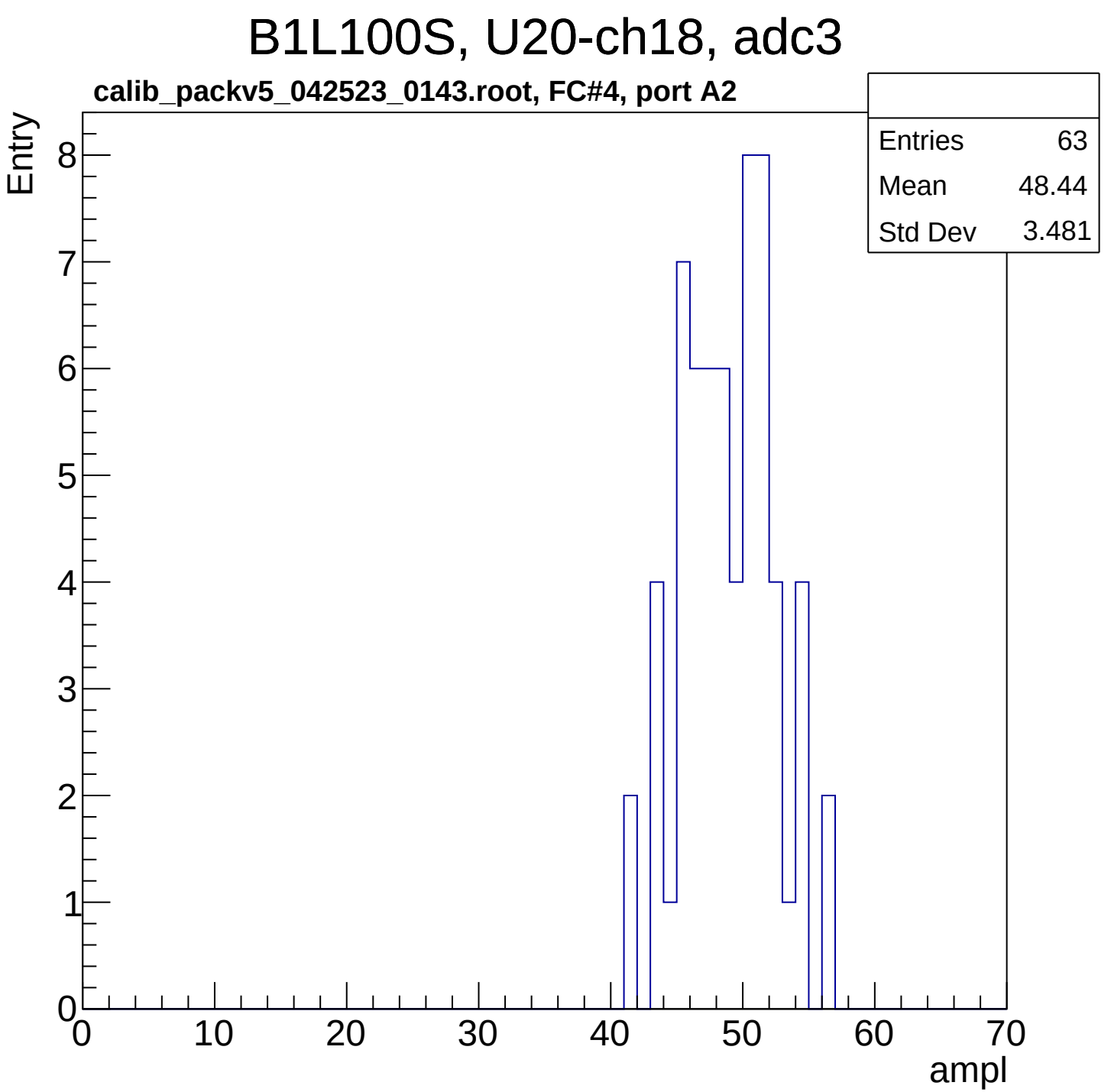
20
30
40
50
60
70


B1L100S, U20-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 35.47
Std Dev 3.118
Gaus mean : 35.4252
Gaus Width: 3.2548
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.08
Std Dev 3.43
Gaus mean : 42.6650
Gaus Width: 4.2340
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.44
Std Dev 3.481
Entry
ampl
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5
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7
8
0
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20
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40
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60
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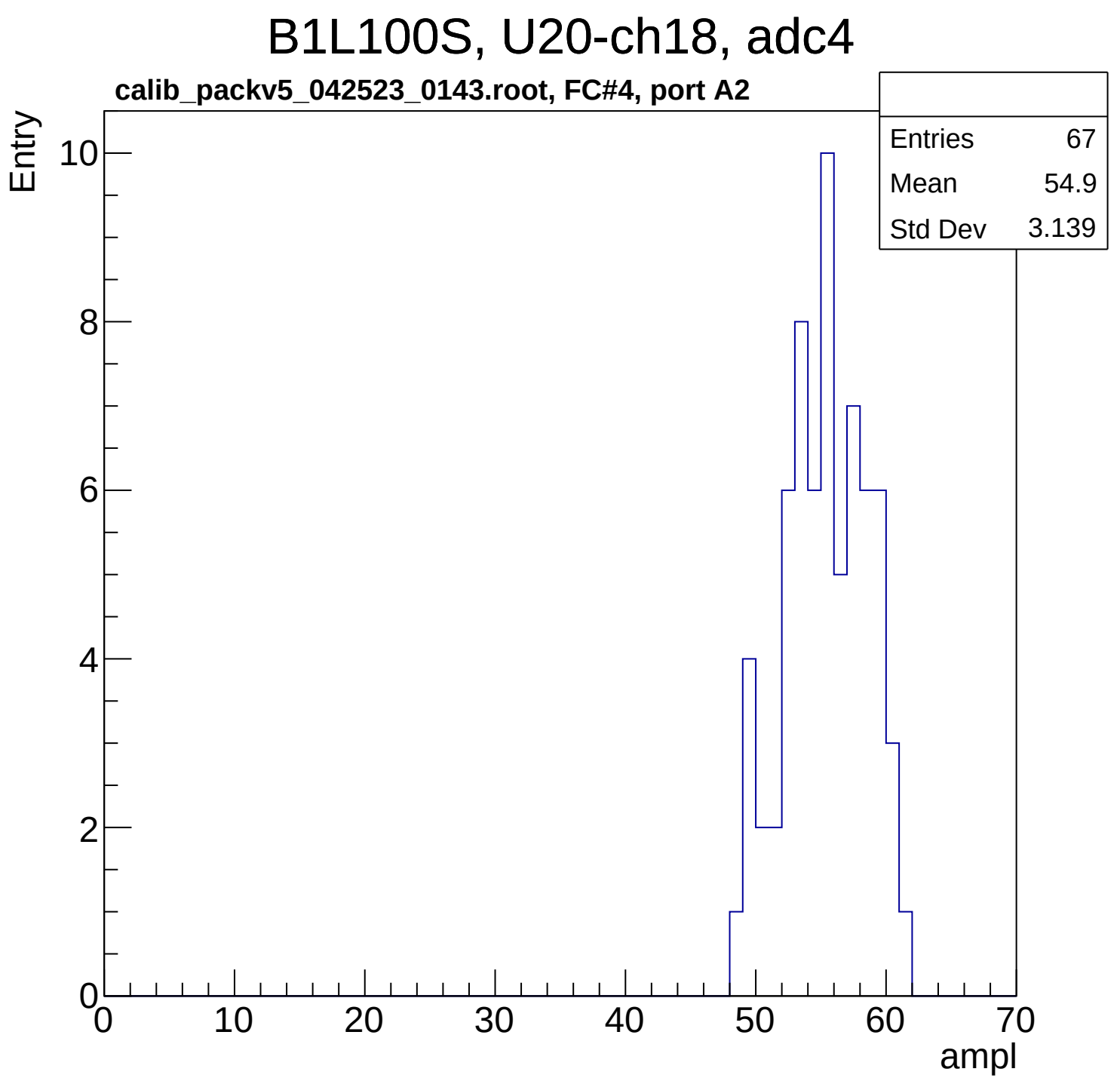
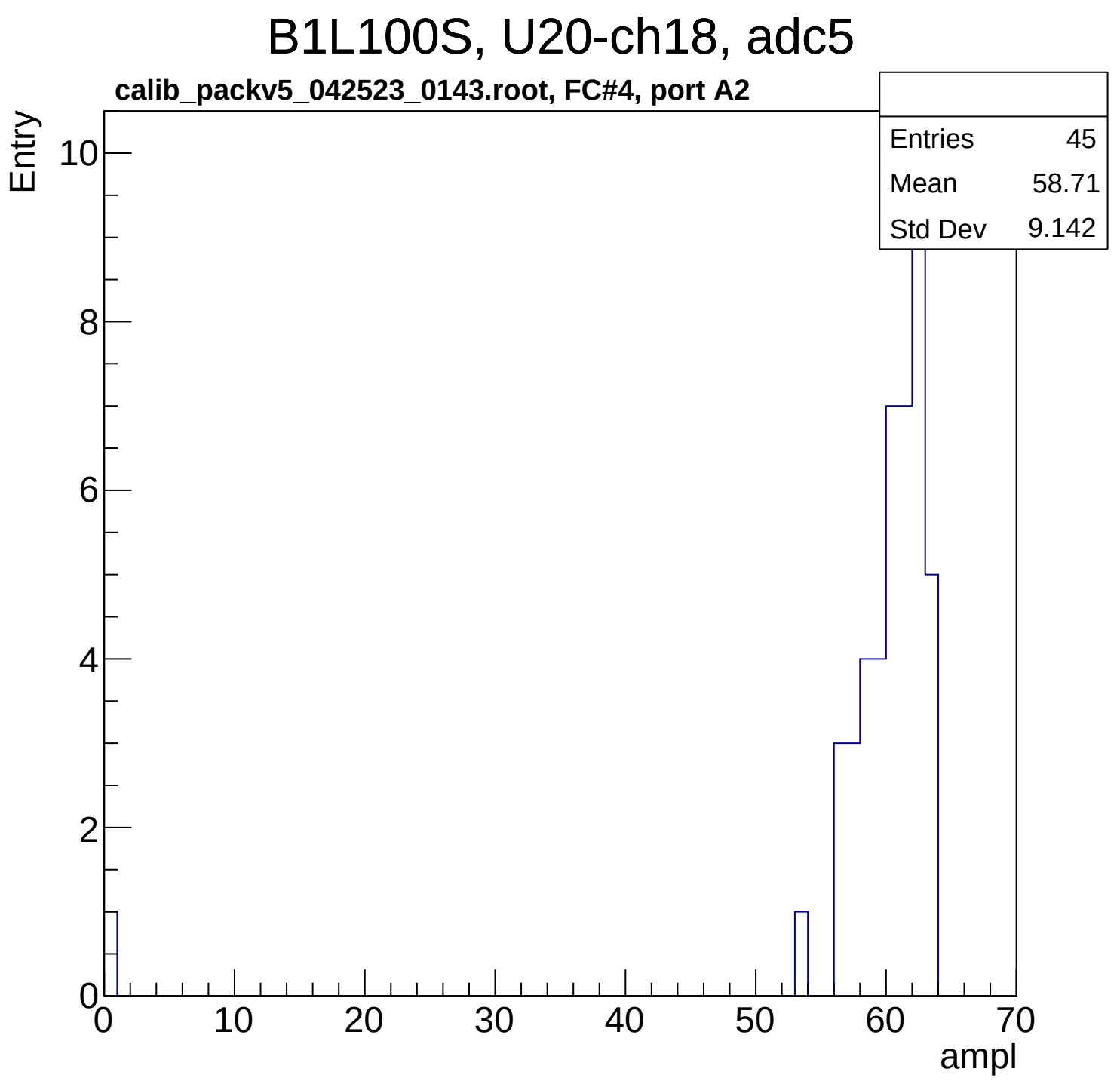
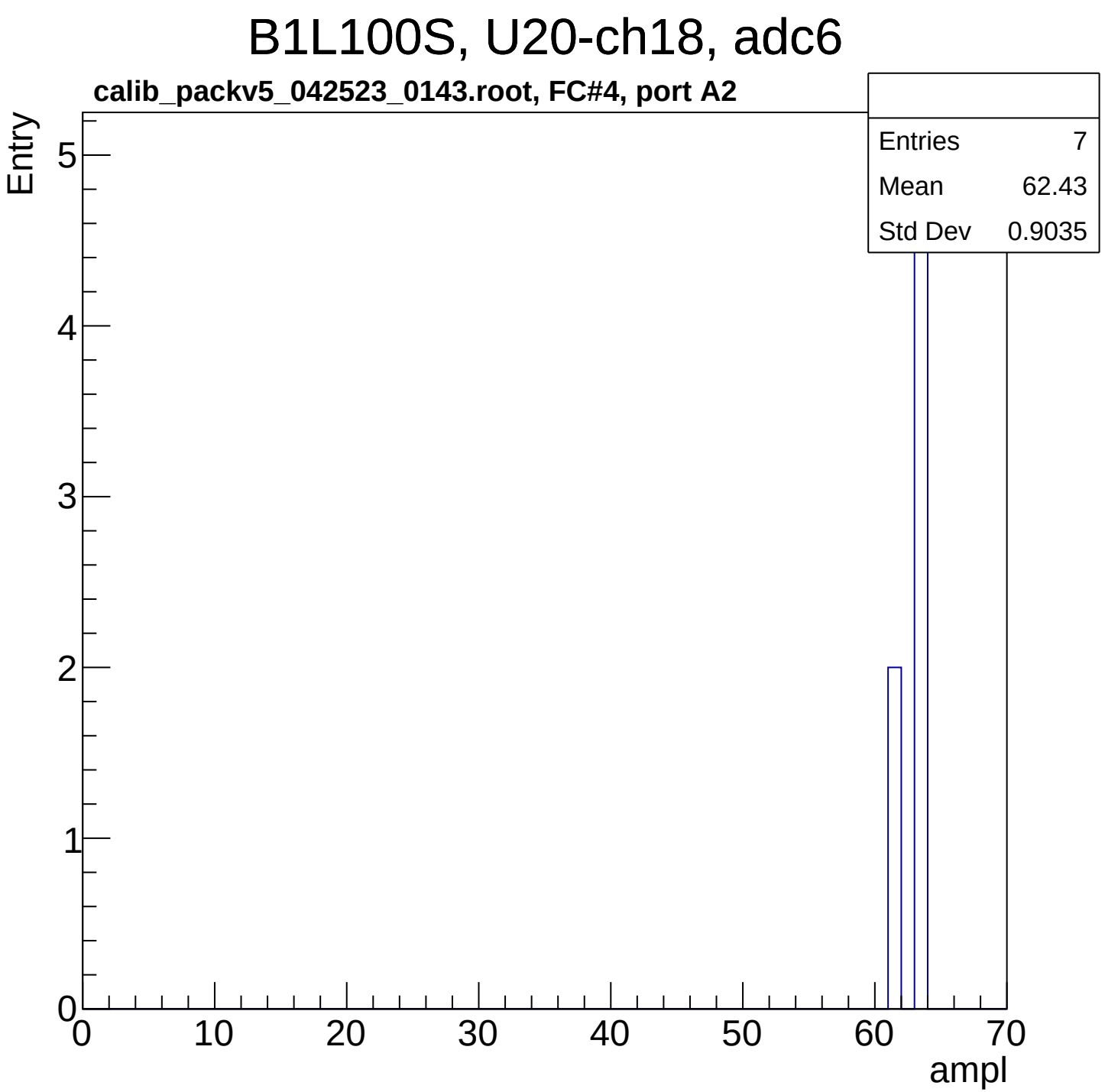
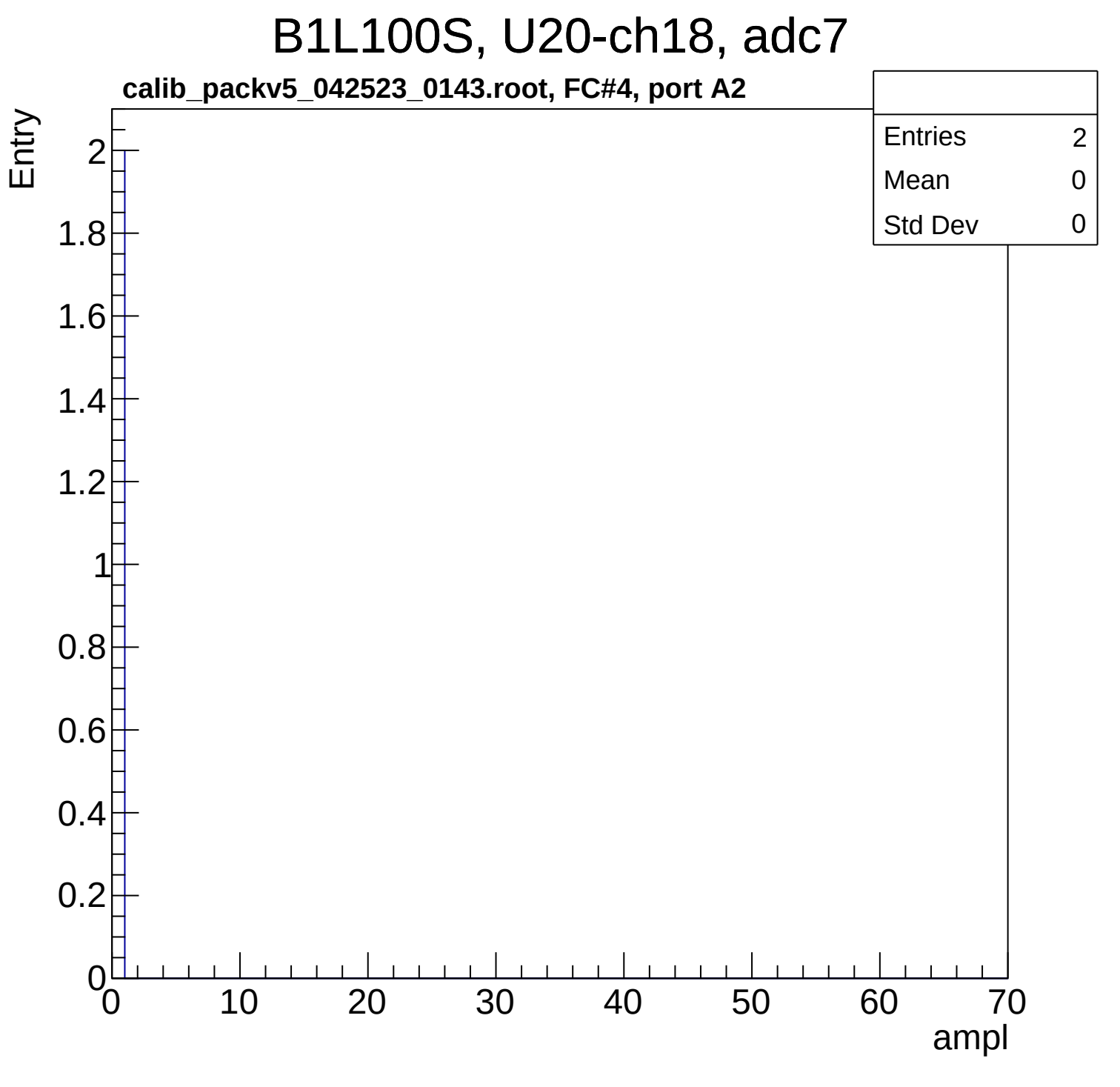

B1L100S, U20-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 54.9
Std Dev 3.139
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.71
Std Dev 9.142
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.43
Std Dev 0.9035
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U20-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
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0.2
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10
20
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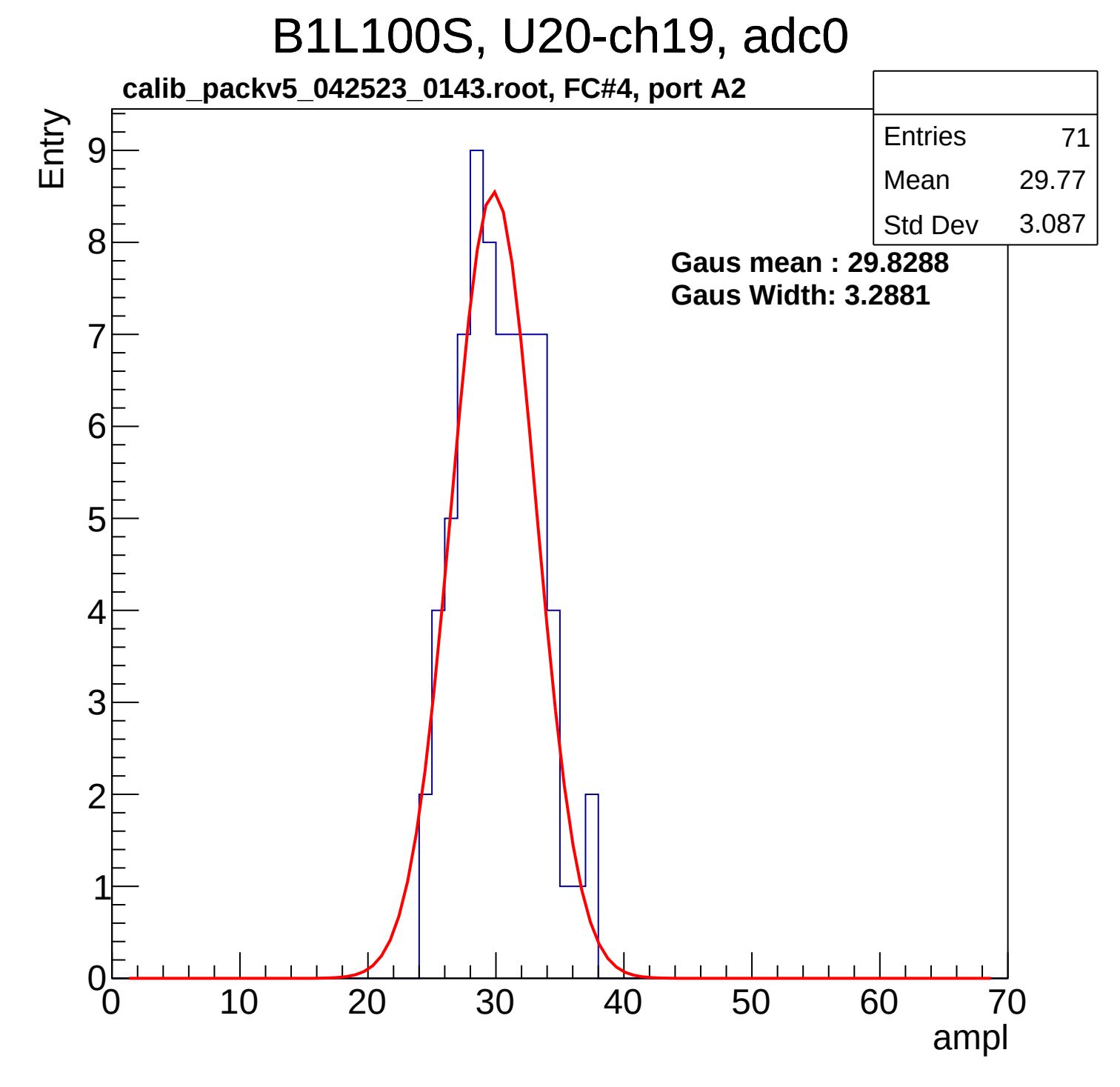
50
60
70
ampl


B1L100S, U20-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.77
Std Dev 3.087
Gaus mean : 29.8288
Gaus Width: 3.2881
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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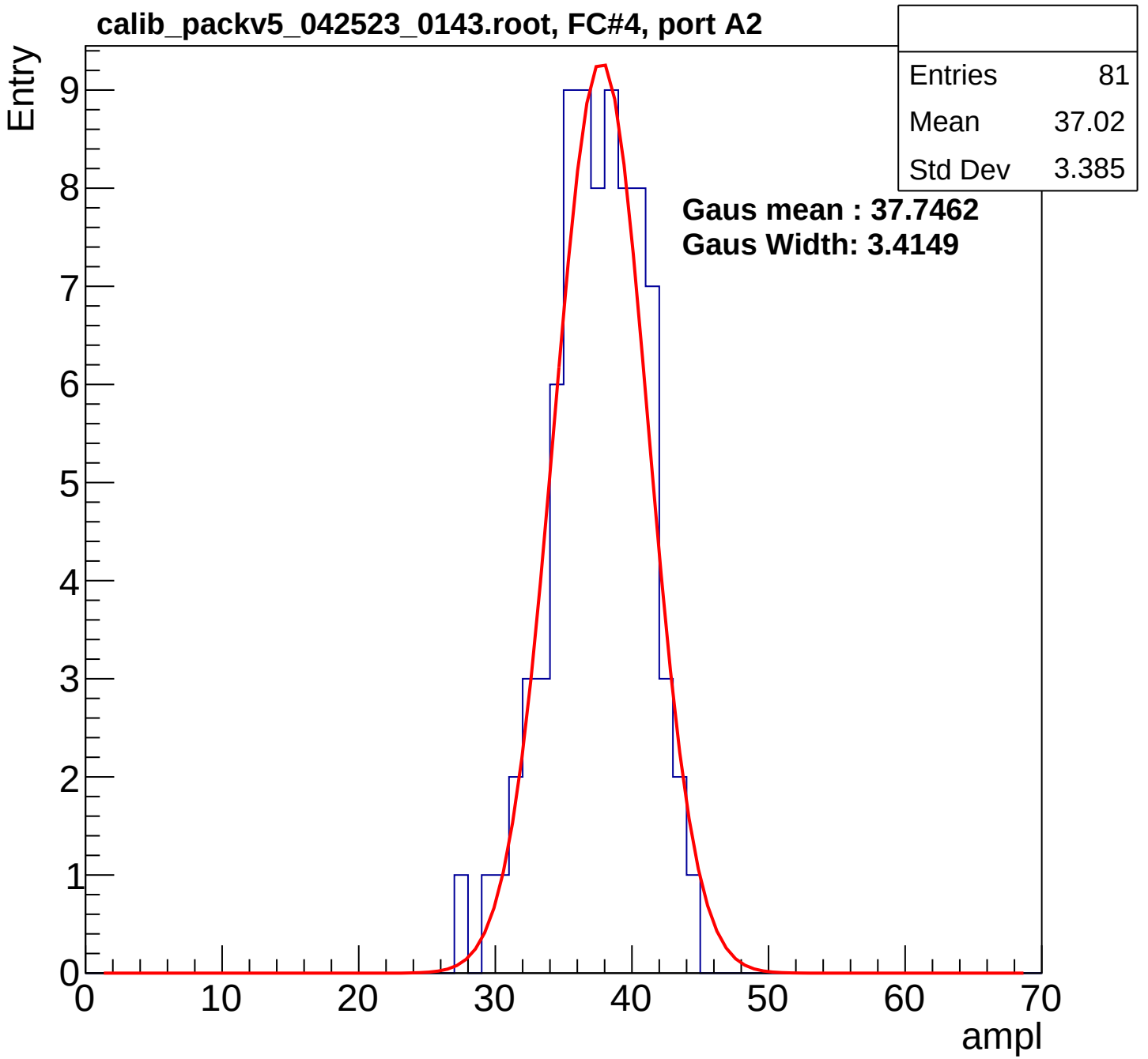
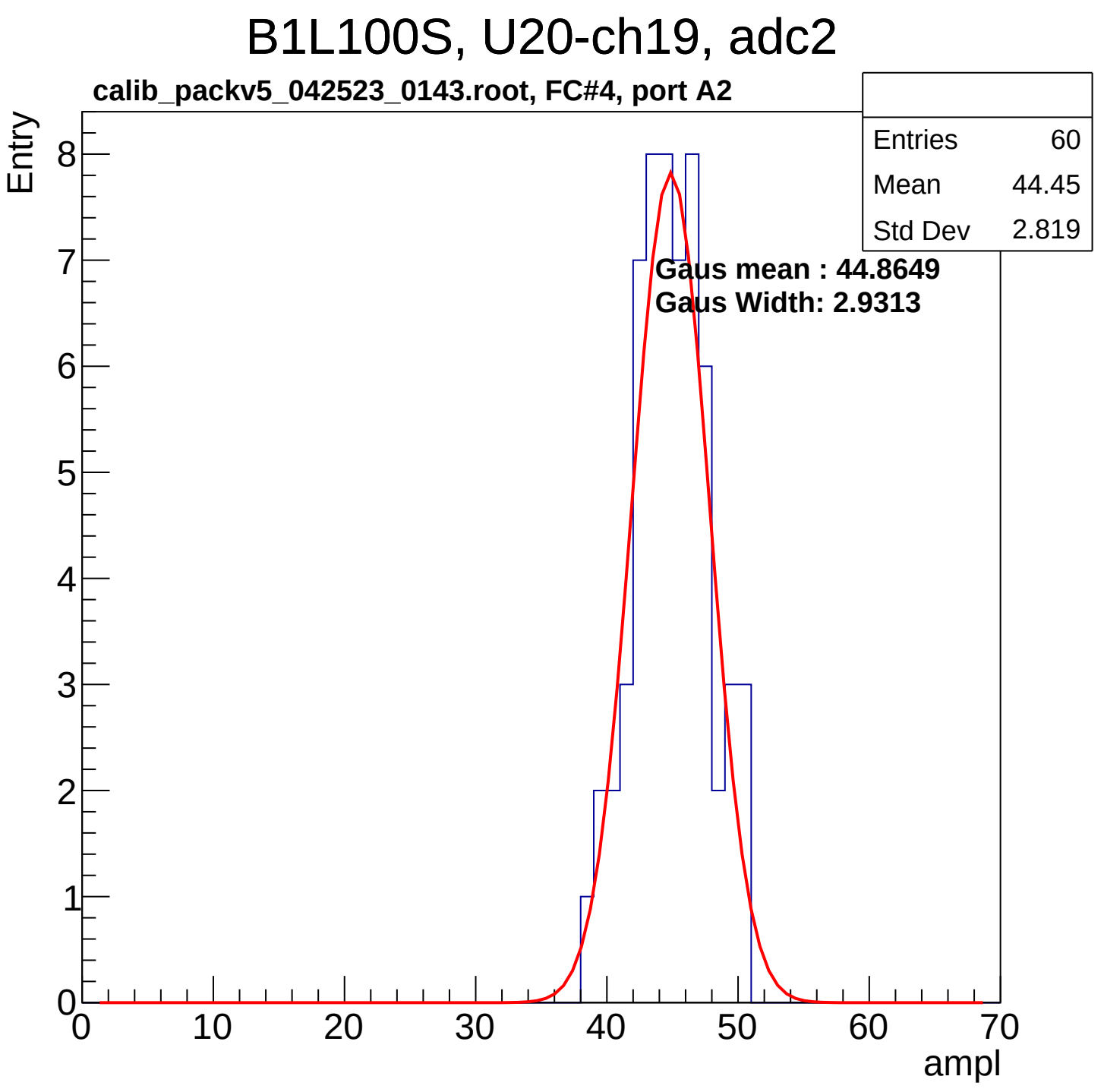
60
70


B1L100S, U20-ch19, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 37.02
Std Dev 3.385
Gaus mean : 37.7462
Gaus Width: 3.4149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.45
Std Dev 2.819
Gaus mean : 44.8649
Gaus Width: 2.9313
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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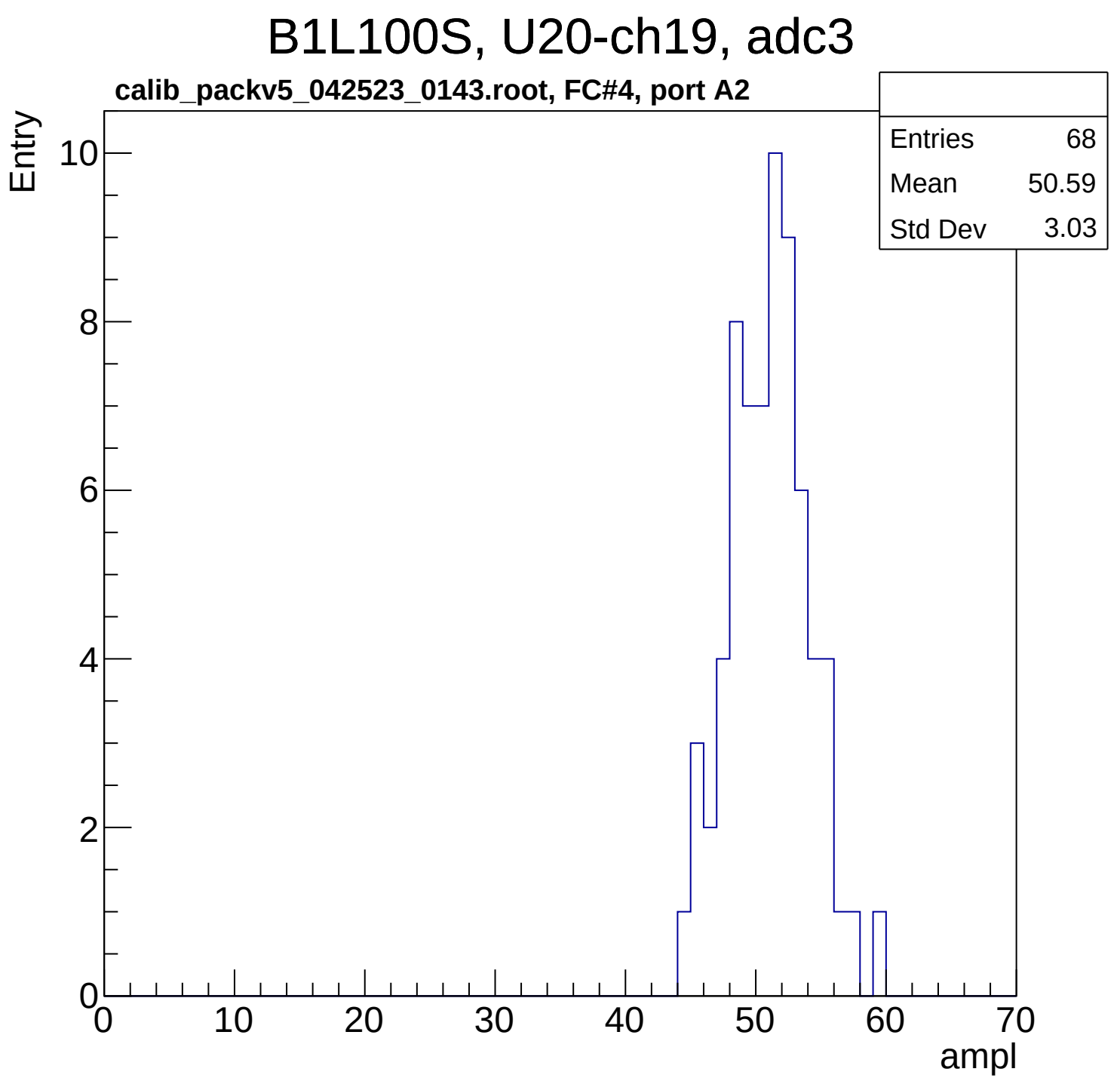
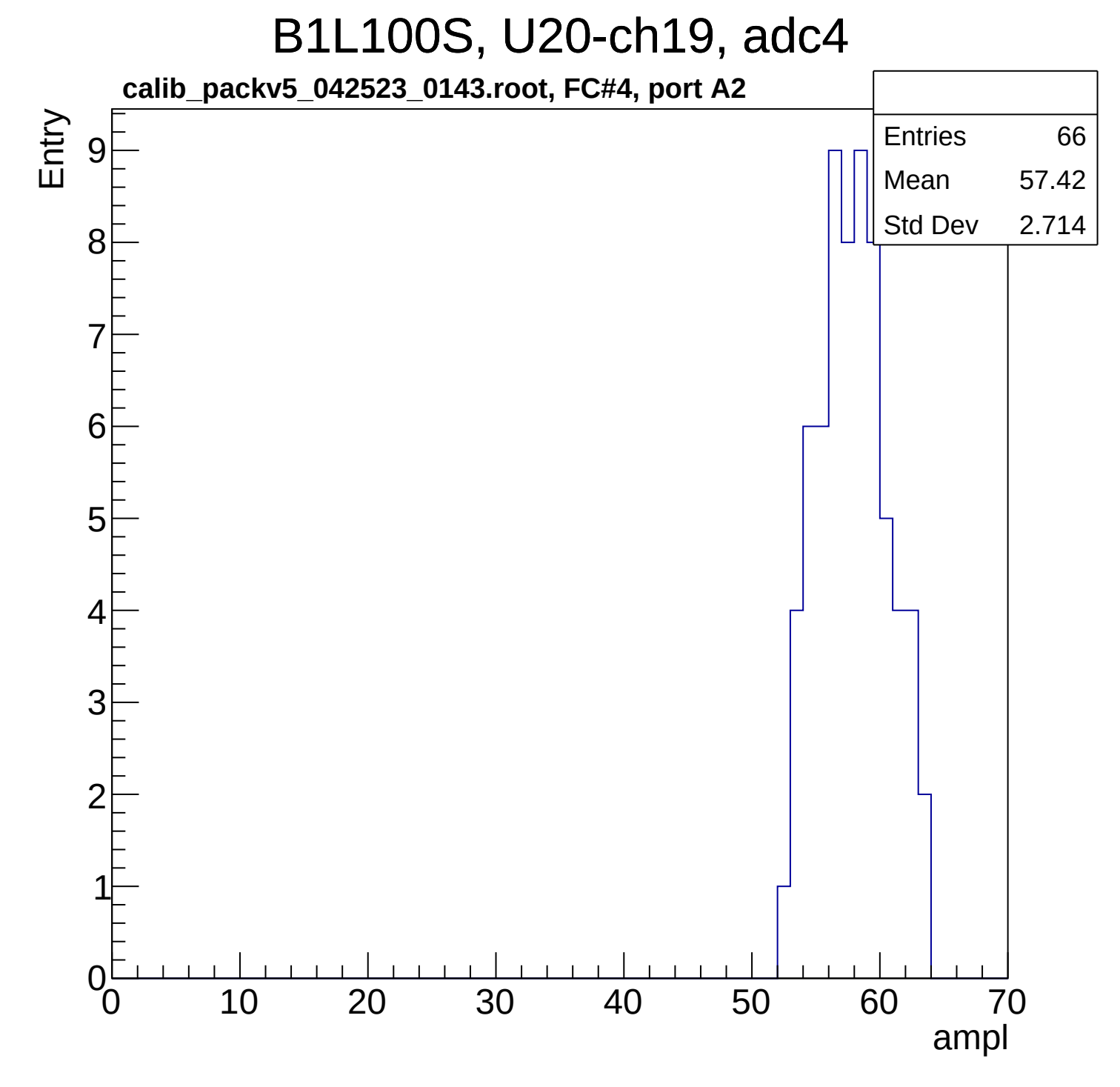

B1L100S, U20-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.59
Std Dev 3.03
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 57.42
Std Dev 2.714
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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20
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60
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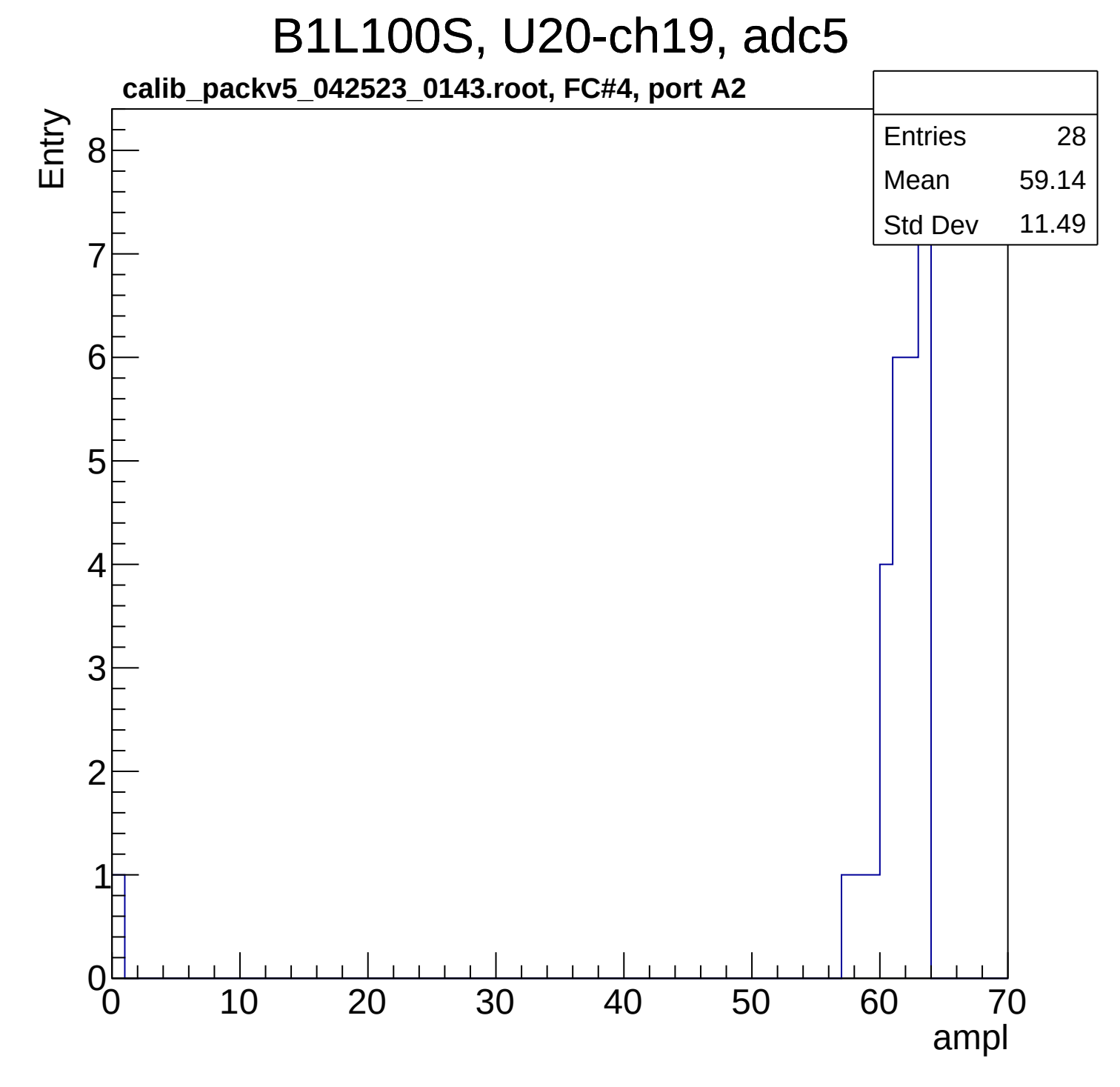
B1L100S, U20-ch19, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 59.14
Std Dev 11.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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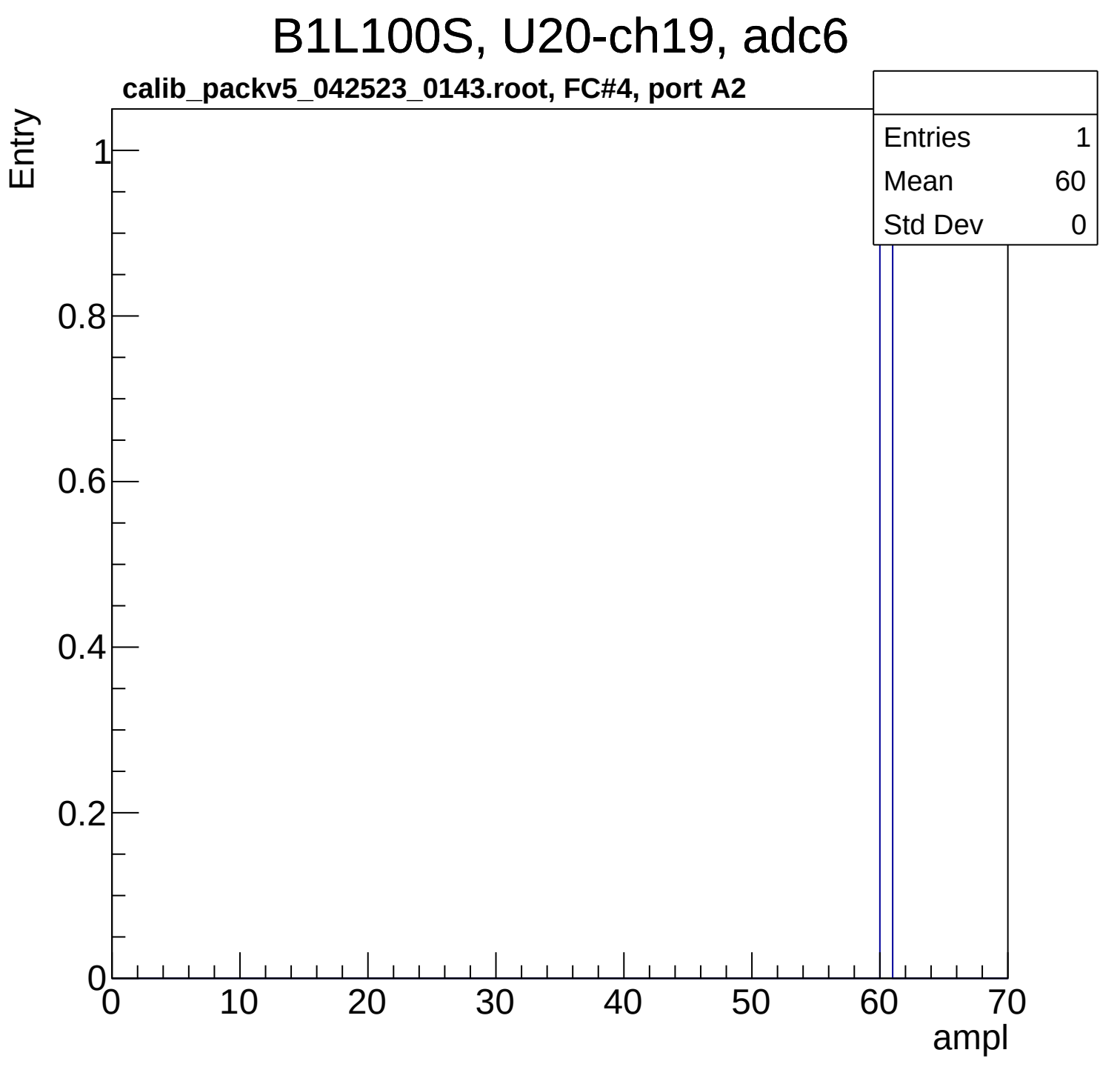
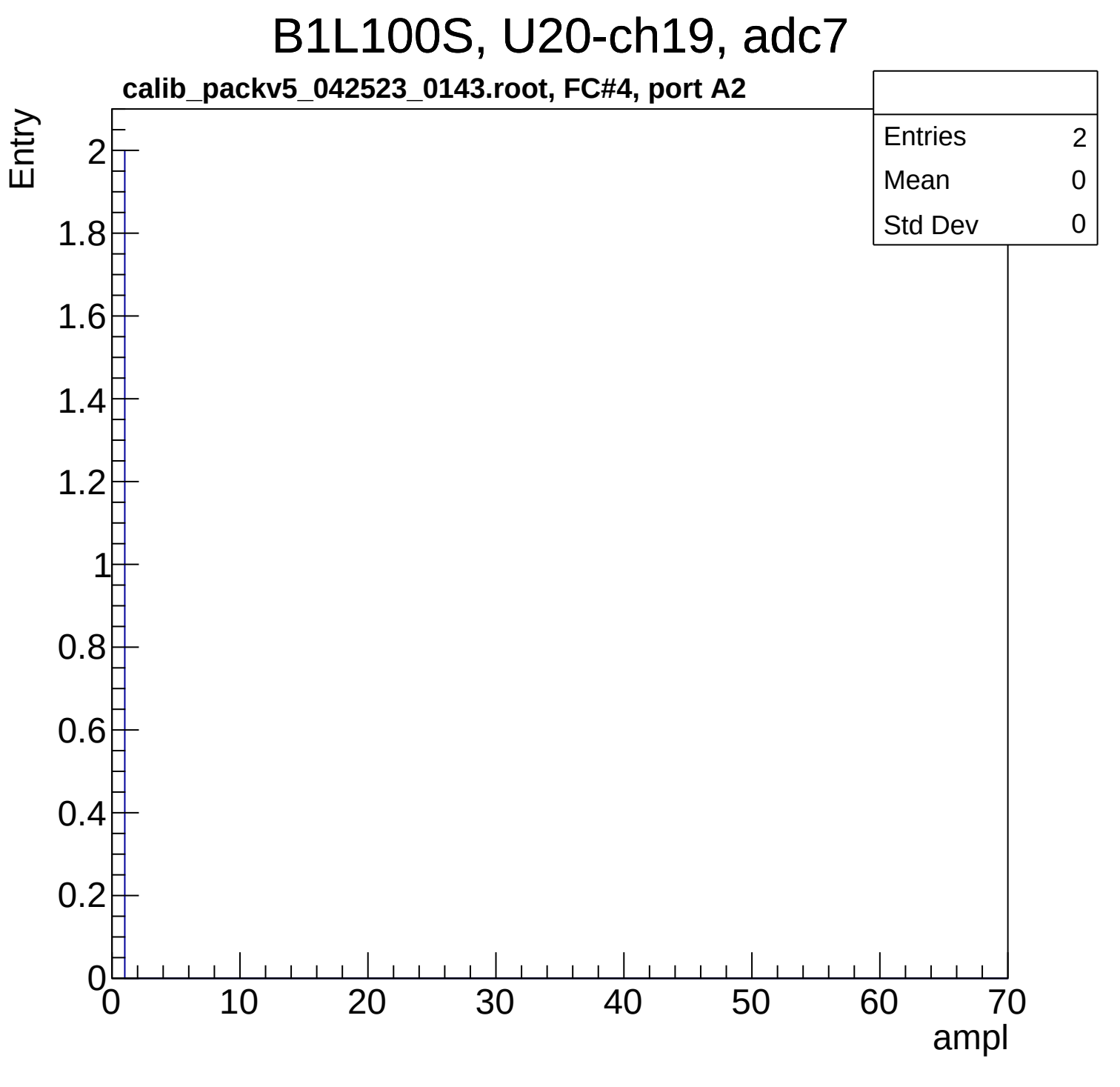
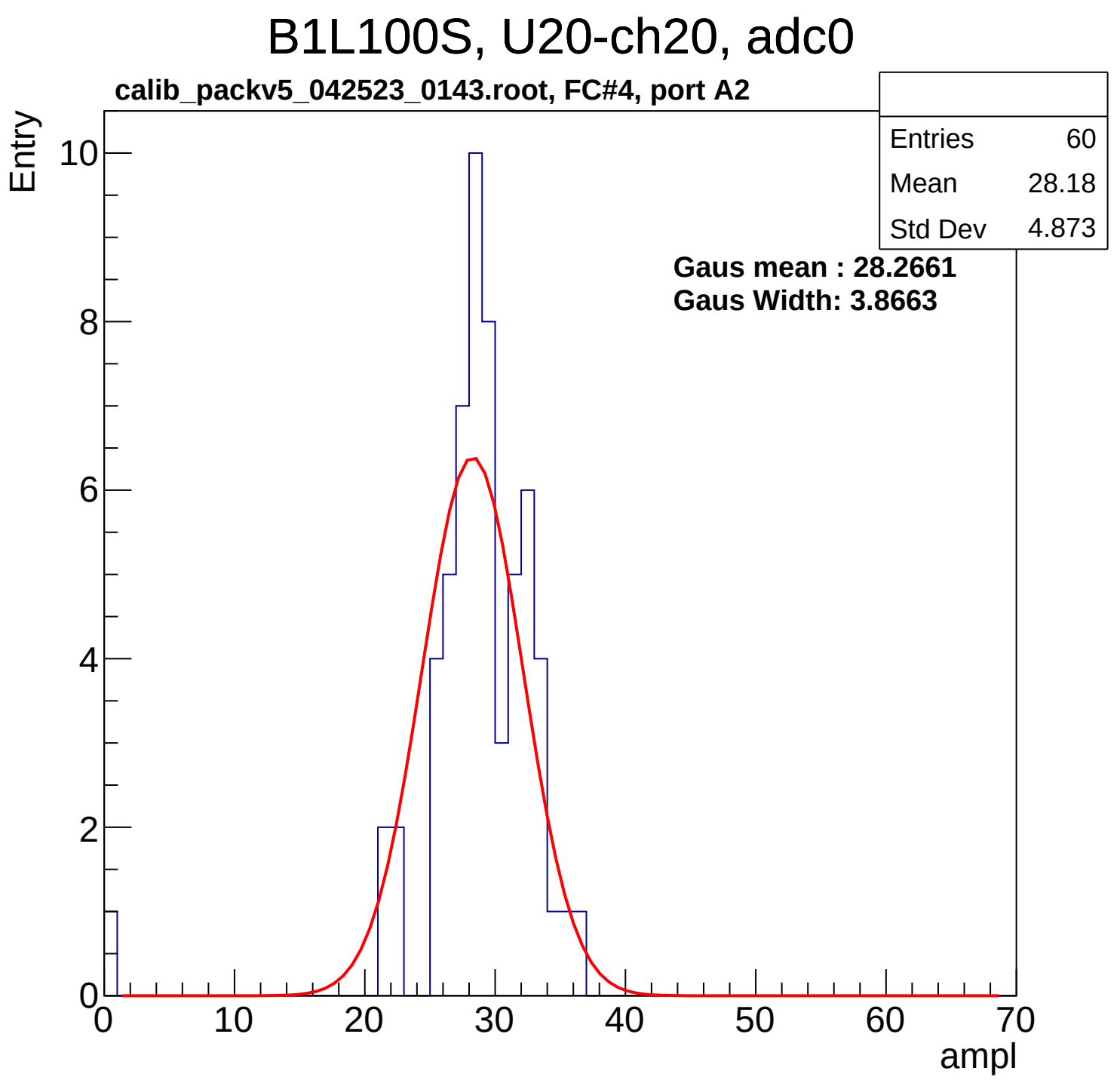
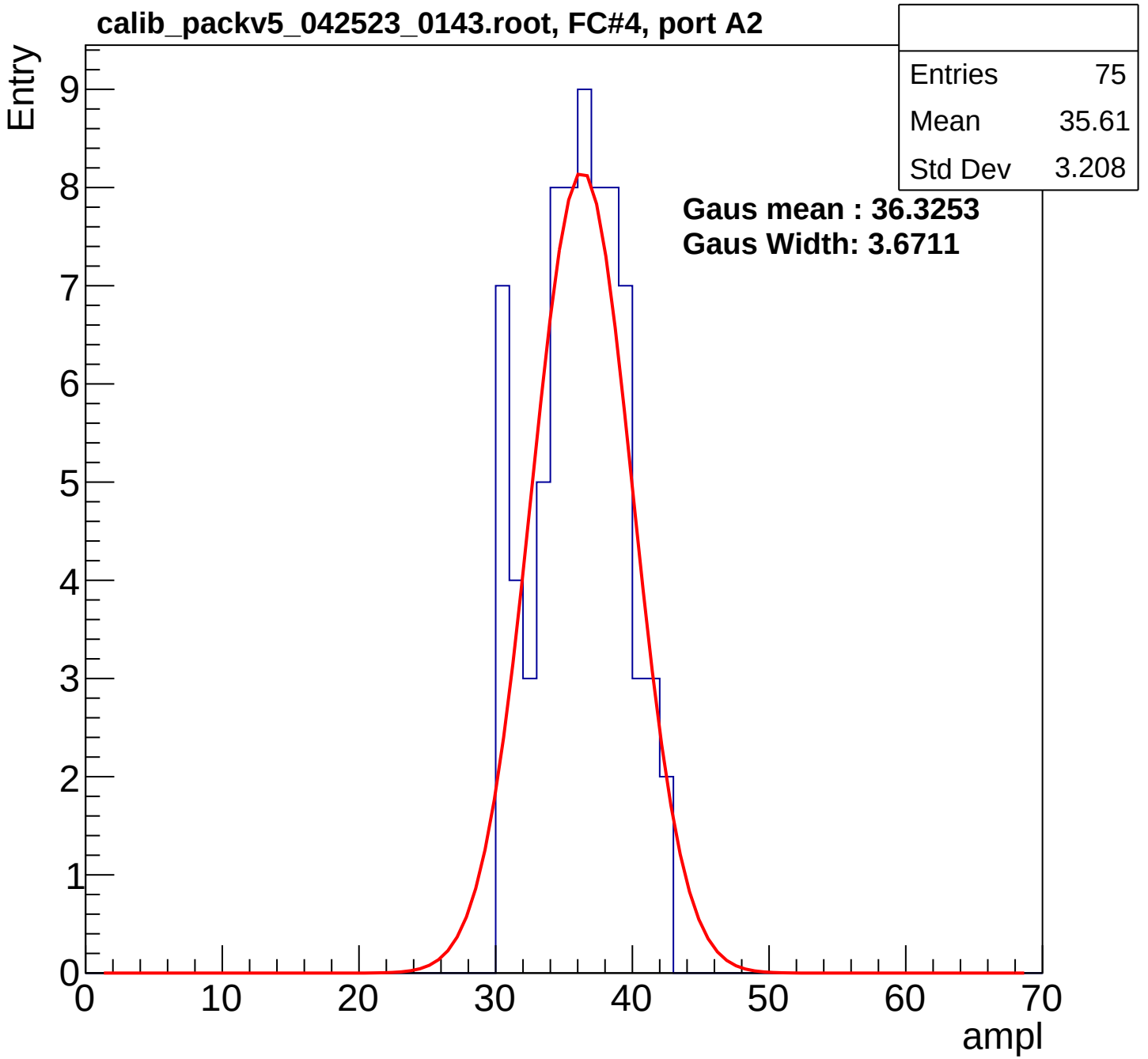

B1L100S, U20-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 60
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 28.18
Std Dev 4.873
Gaus mean : 28.2661
Gaus Width: 3.8663
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.61
Std Dev 3.208
Gaus mean : 36.3253
Gaus Width: 3.6711
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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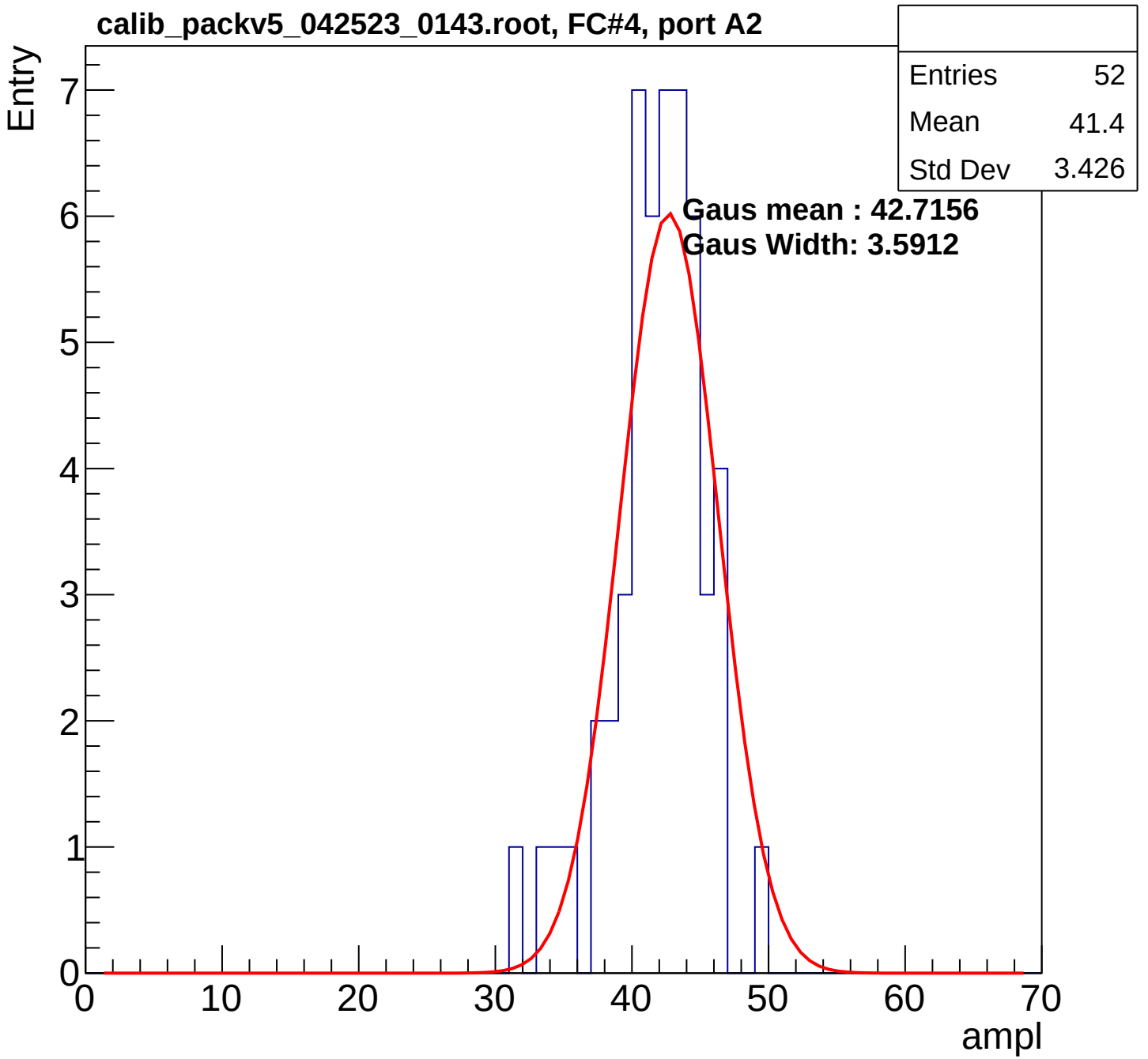
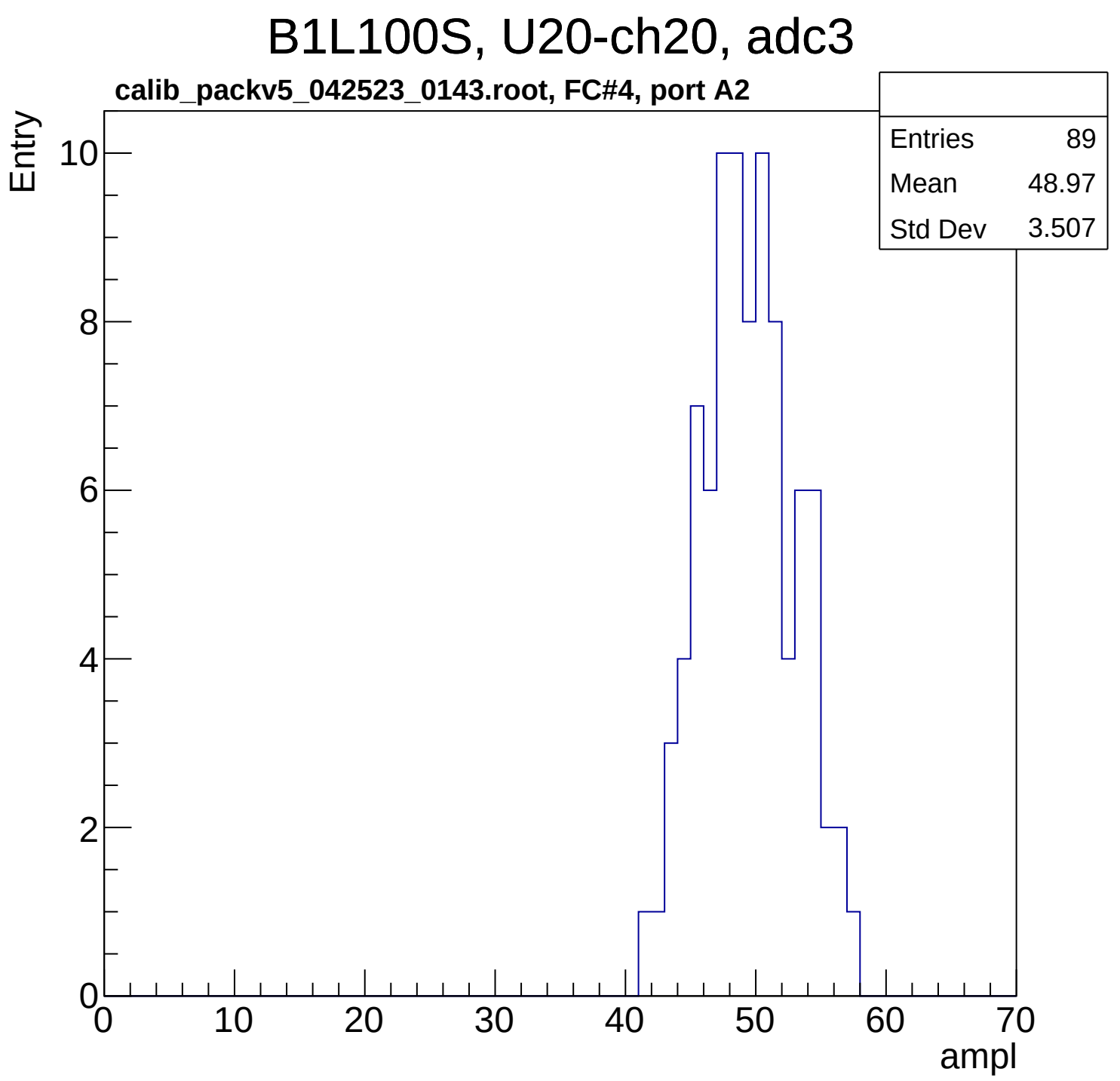
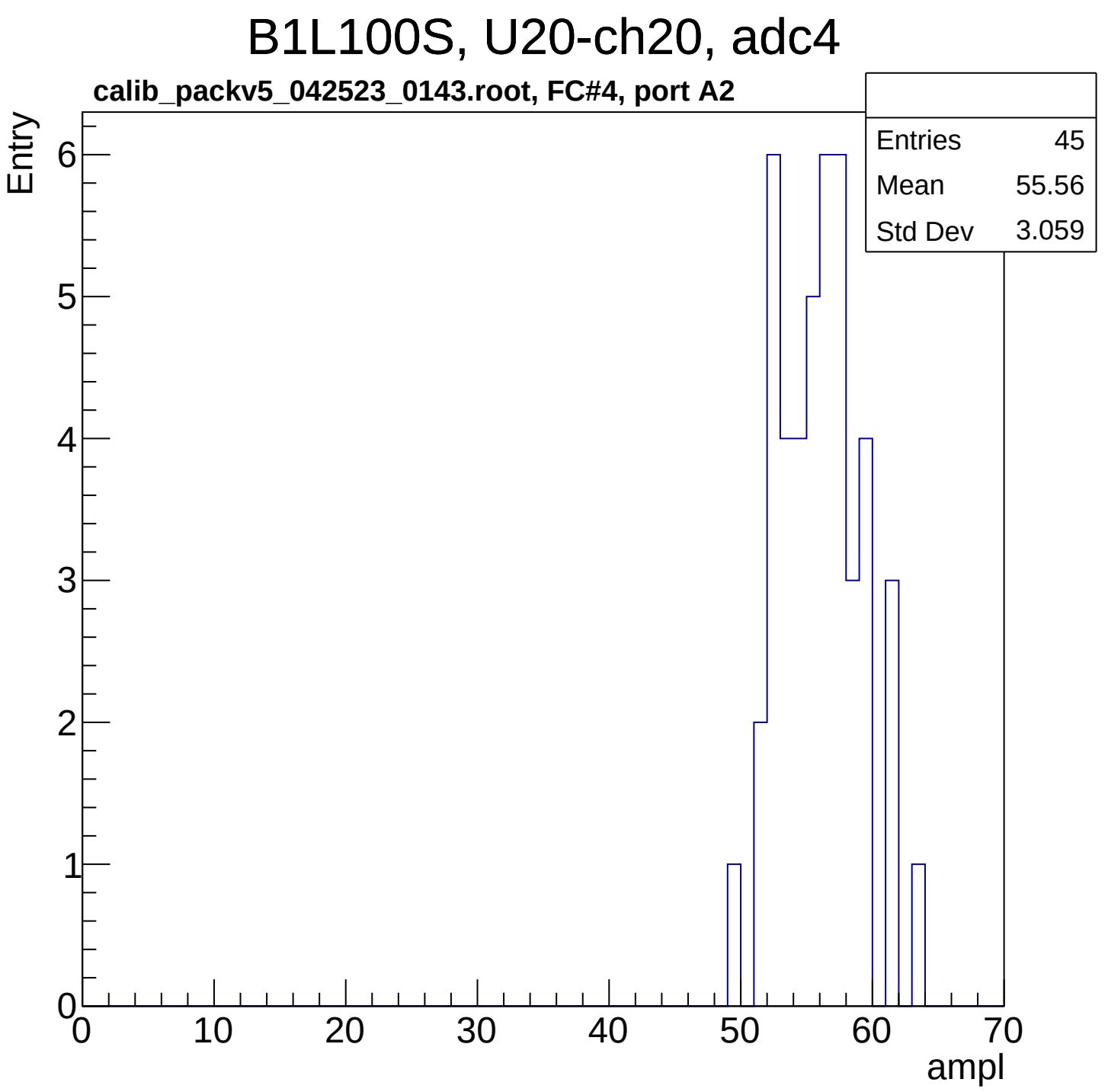
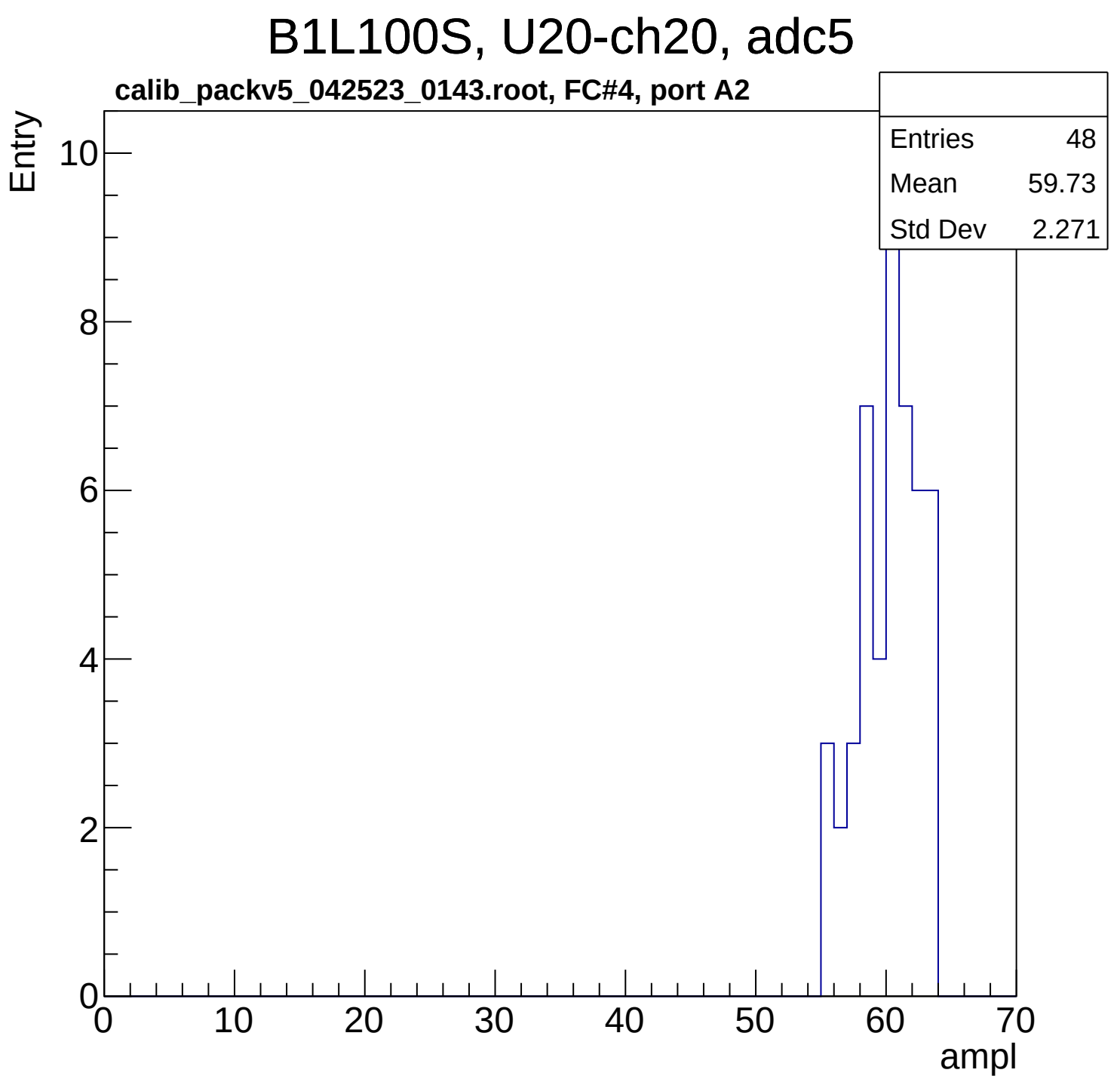
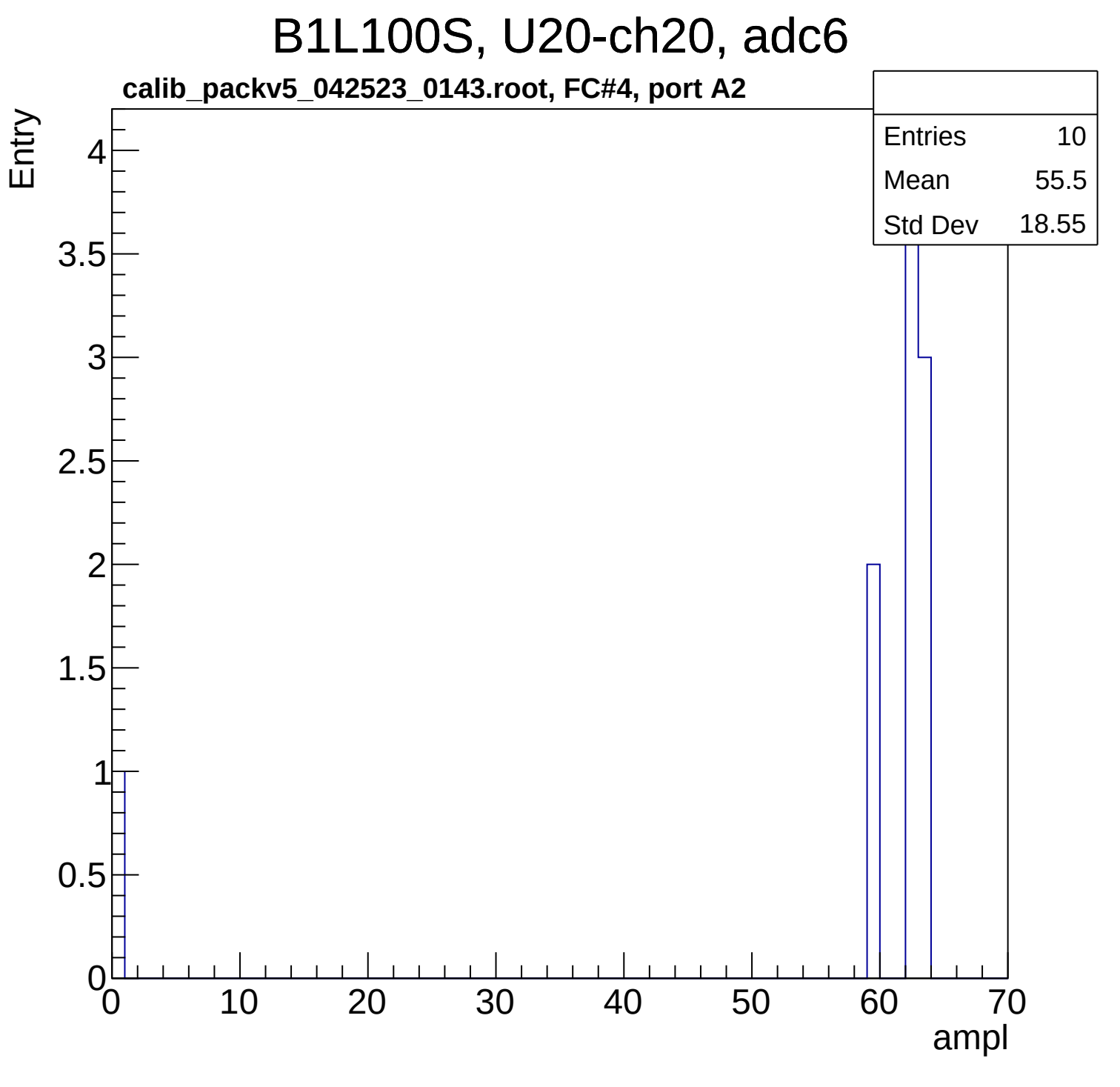
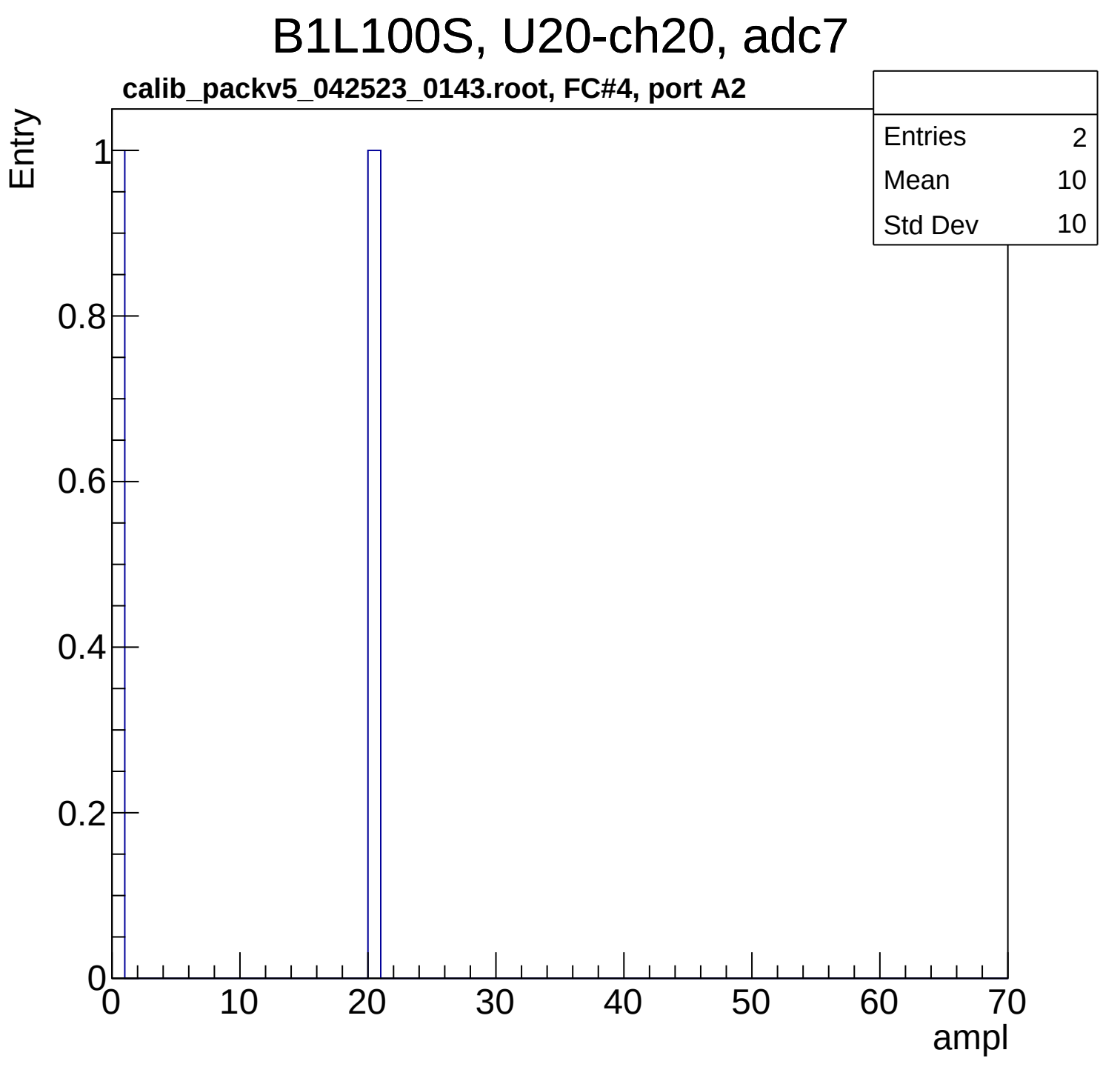

B1L100S, U20-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 41.4
Std Dev 3.426
Gaus mean : 42.7156
Gaus Width: 3.5912
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

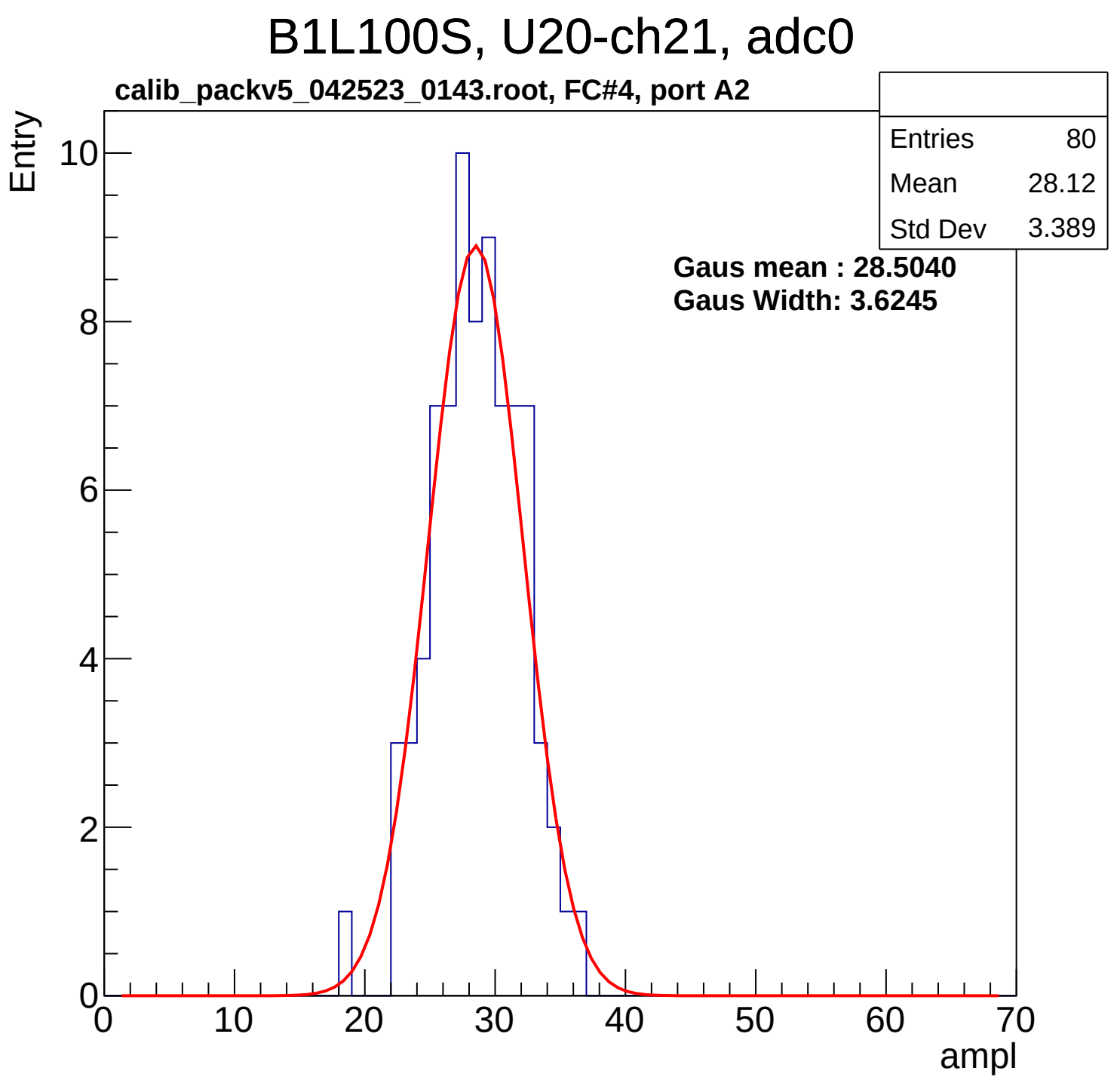

B1L100S, U20-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 48.97
Std Dev 3.507
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

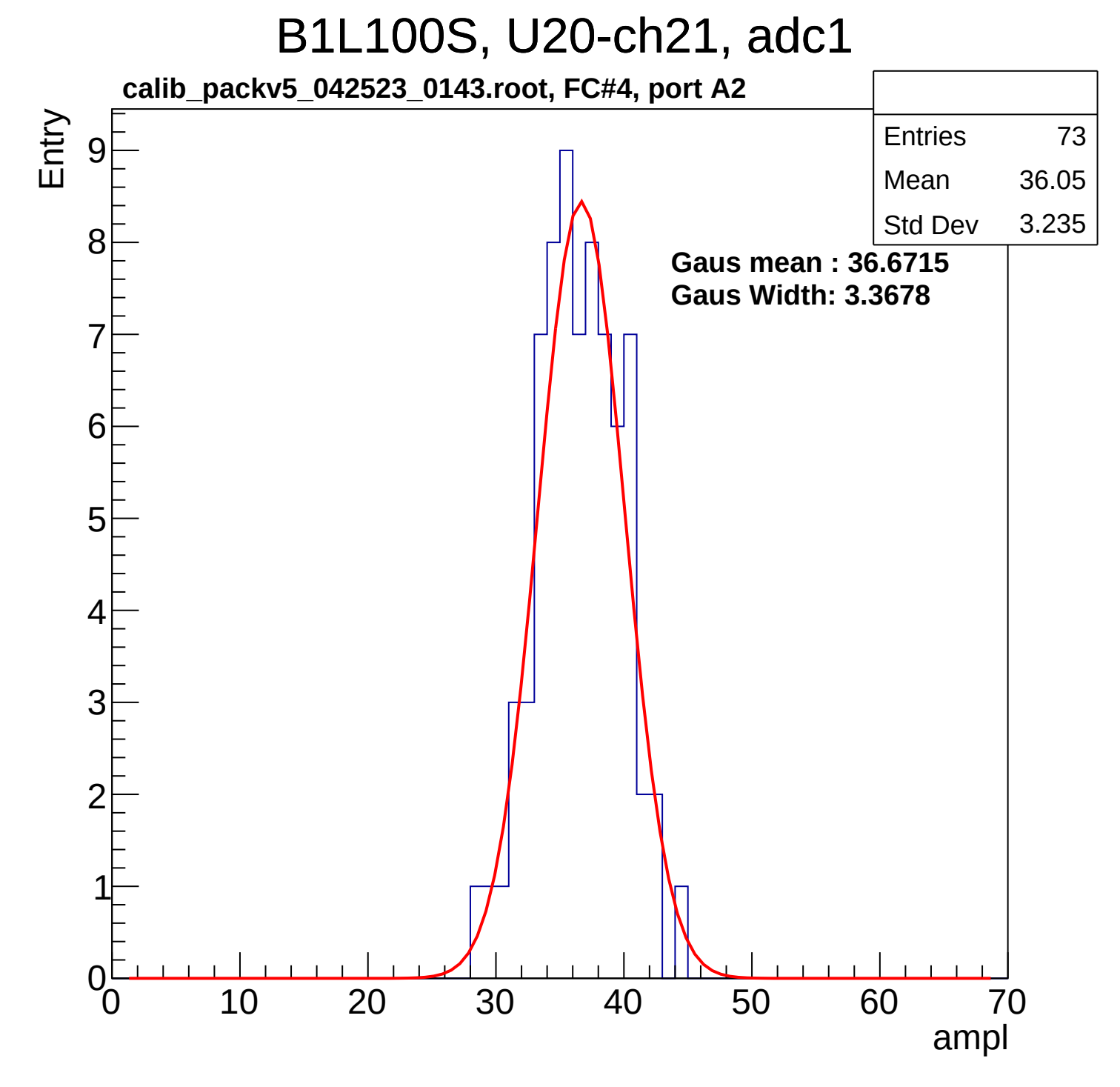

B1L100S, U20-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 55.56
Std Dev 3.059
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

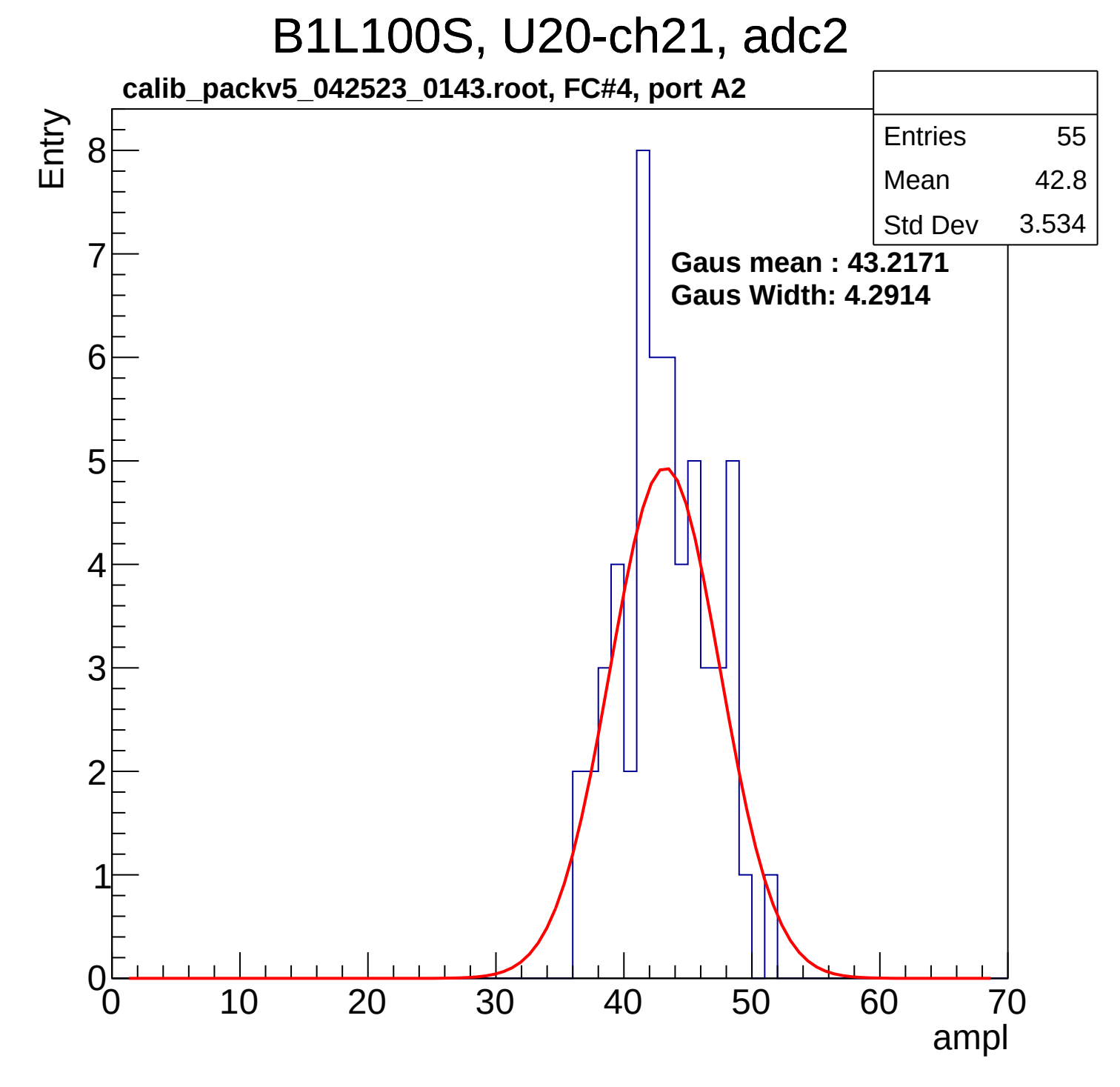

B1L100S, U20-ch20, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.73
Std Dev 2.271
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 55.5
Std Dev 18.55
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch20, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.12
Std Dev 3.389
Gaus mean : 28.5040
Gaus Width: 3.6245
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.05
Std Dev 3.235
Gaus mean : 36.6715
Gaus Width: 3.3678
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 42.8
Std Dev 3.534
Gaus mean : 43.2171
Gaus Width: 4.2914
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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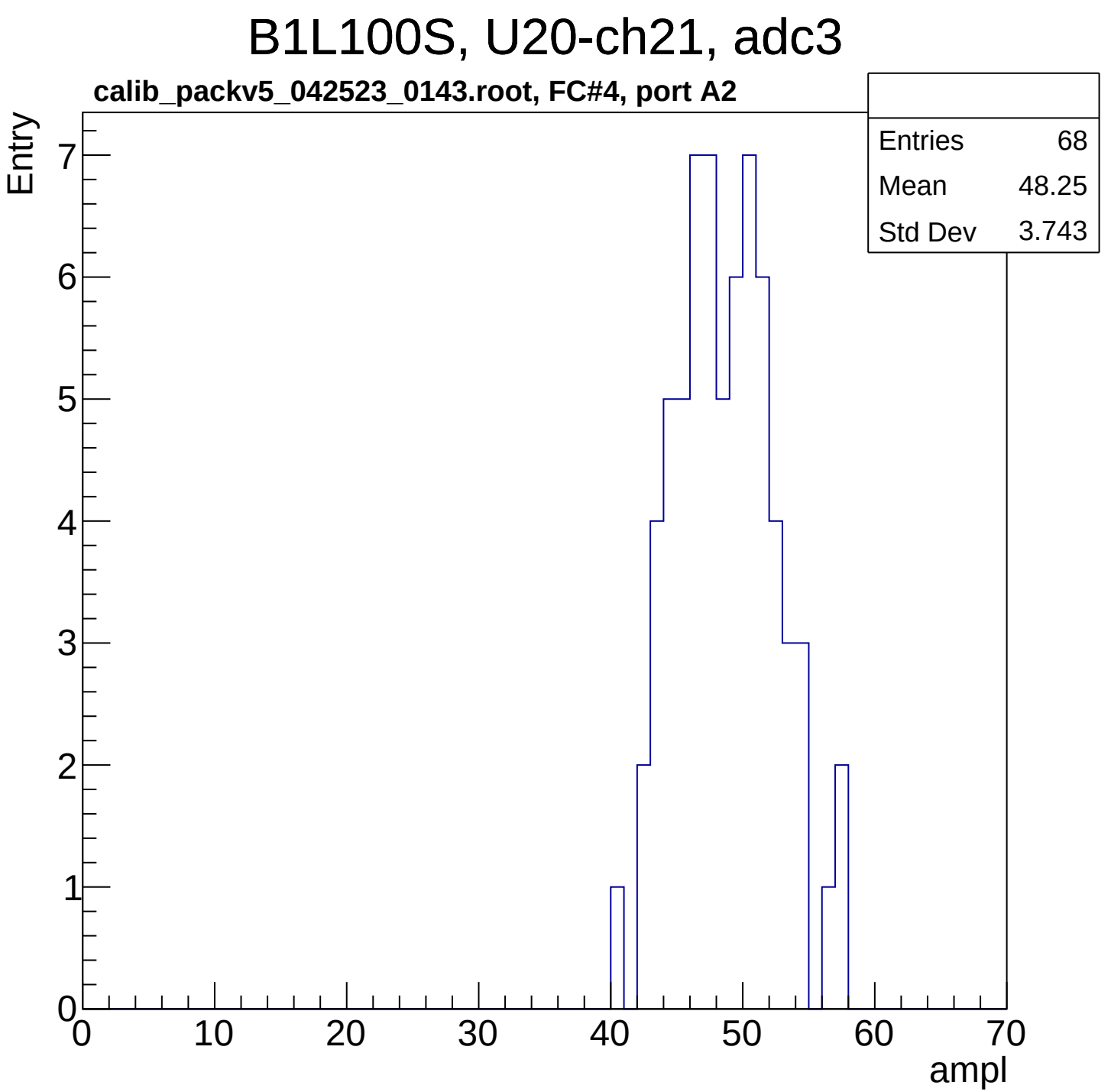
B1L100S, U20-ch21, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.25
Std Dev 3.743
Entry
ampl
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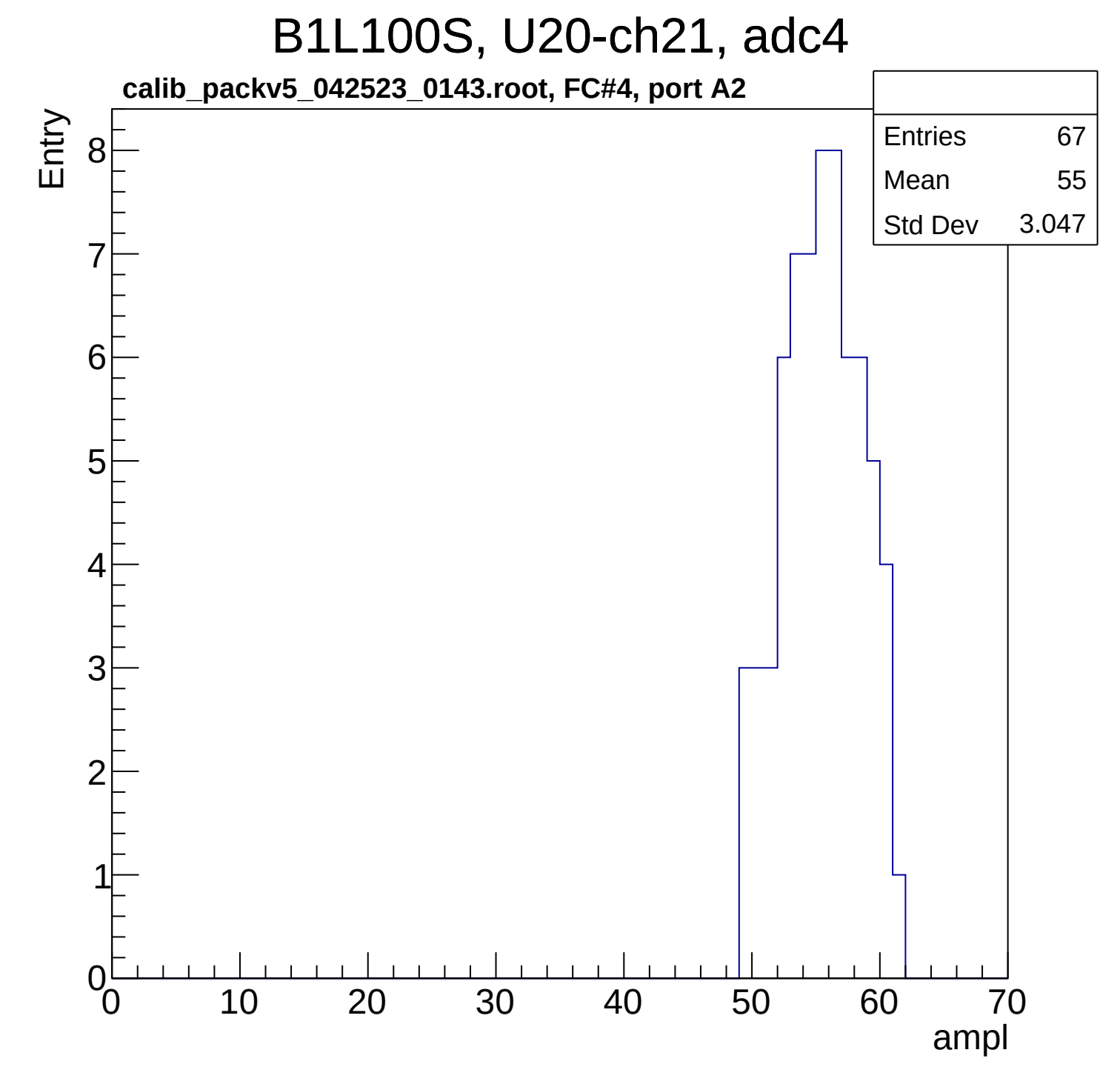
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch21, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55
Std Dev 3.047
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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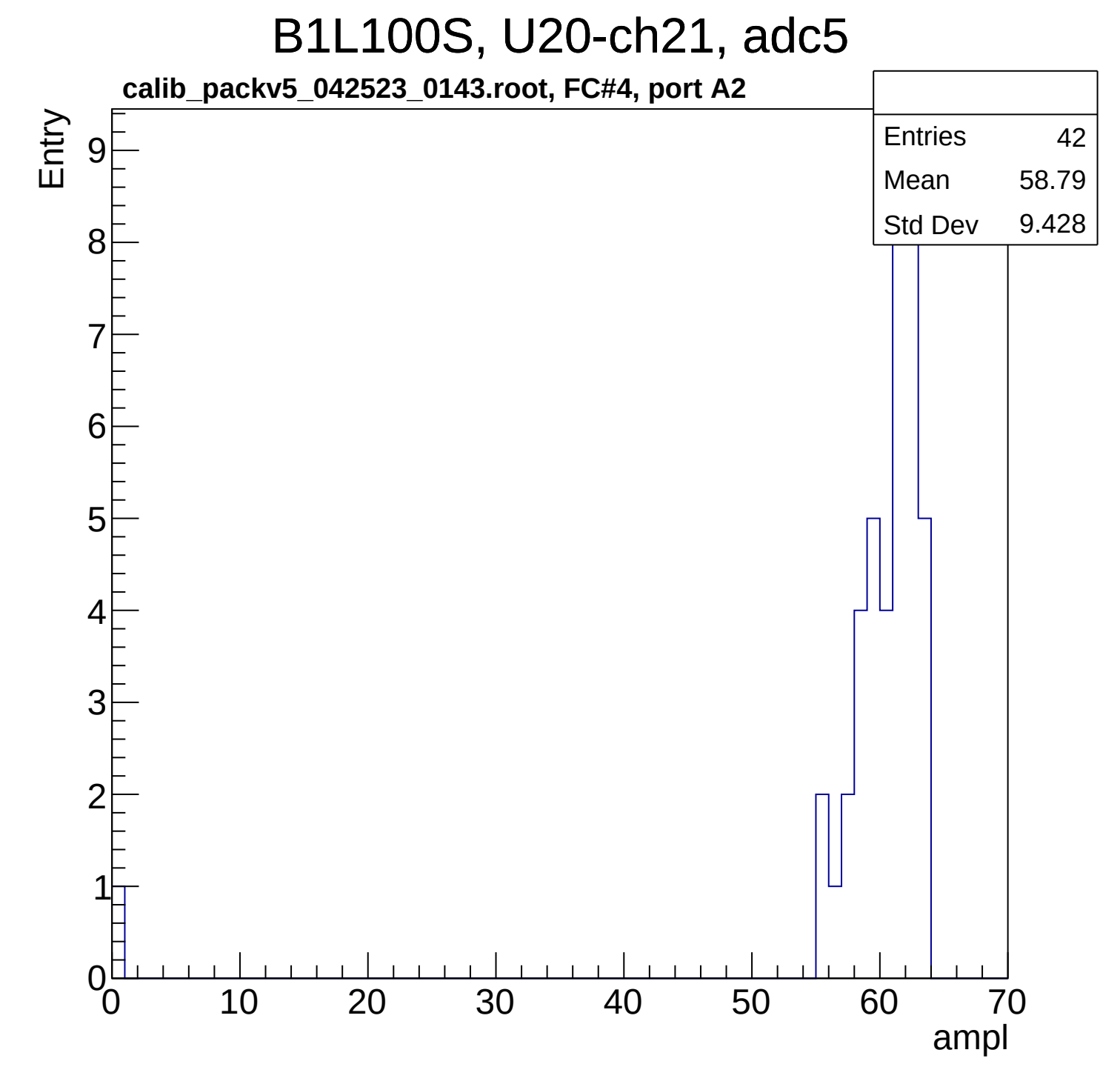
B1L100S, U20-ch21, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.79
Std Dev 9.428
Entry
ampl
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3
4
5
6
7
8
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0
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60
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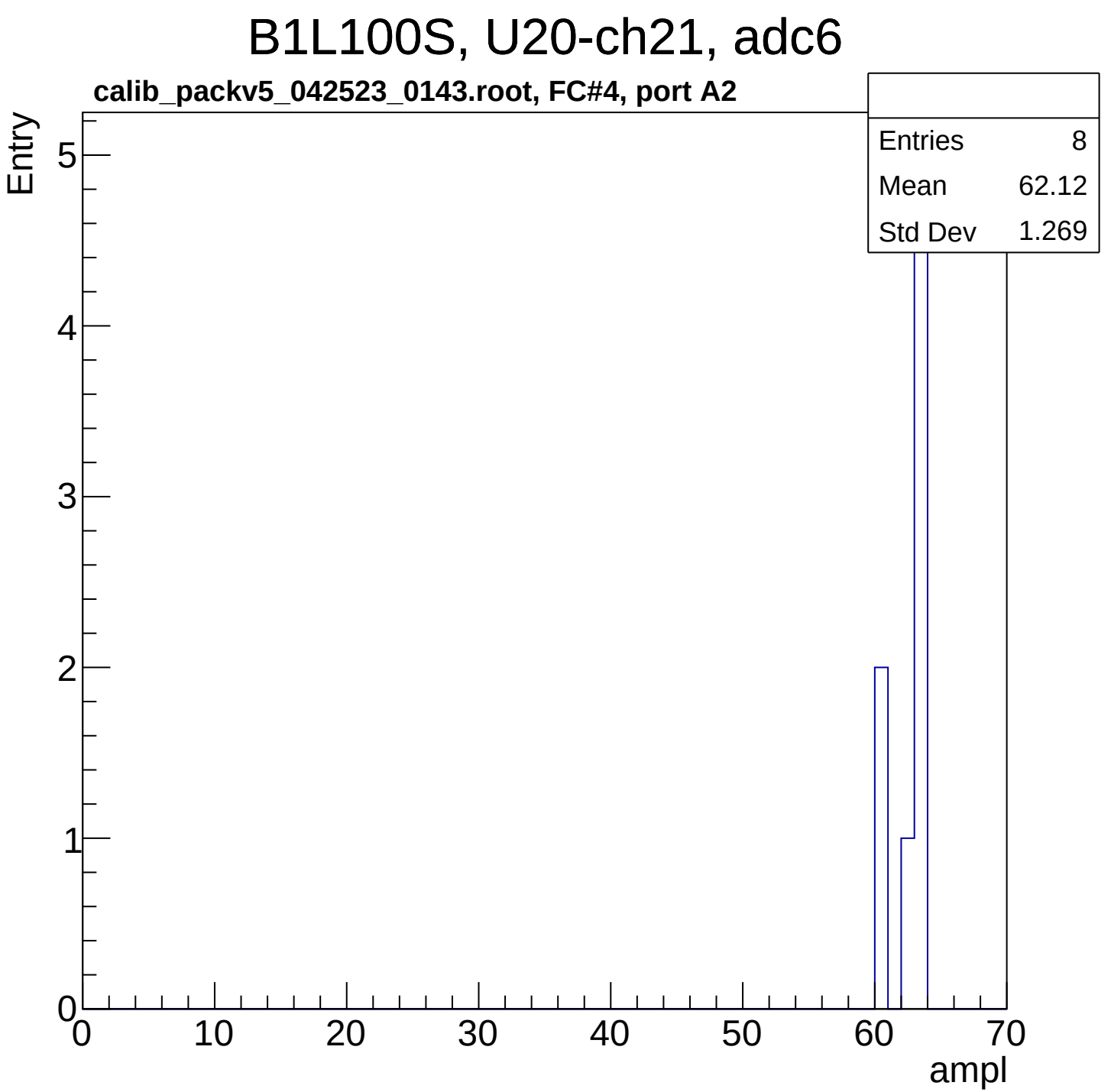
B1L100S, U20-ch21, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.12
Std Dev 1.269
Entry
ampl
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4
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0
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20
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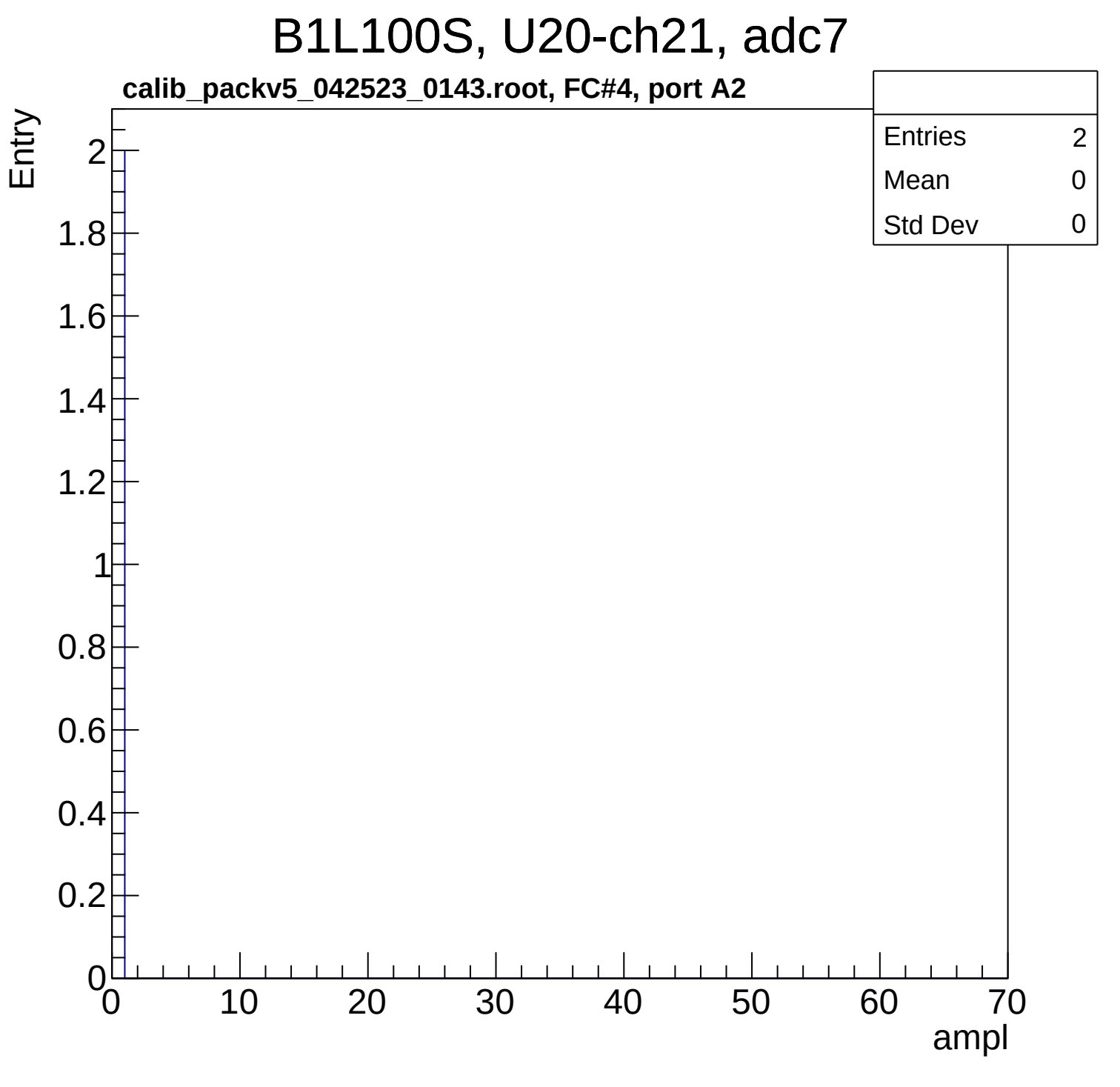
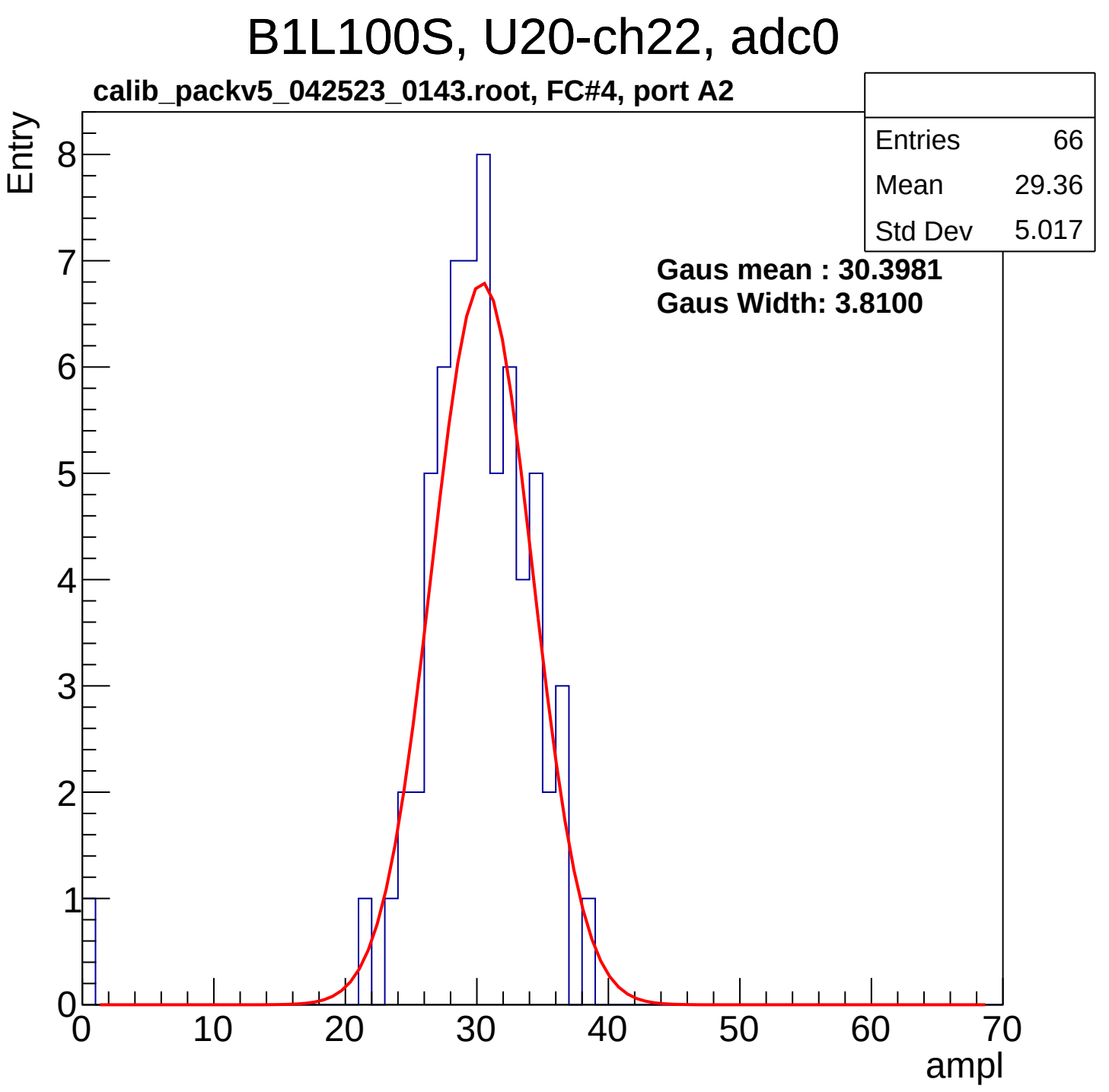
40
50
60
70


B1L100S, U20-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch22, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.36
Std Dev 5.017
Gaus mean : 30.3981
Gaus Width: 3.8100
Entry
ampl
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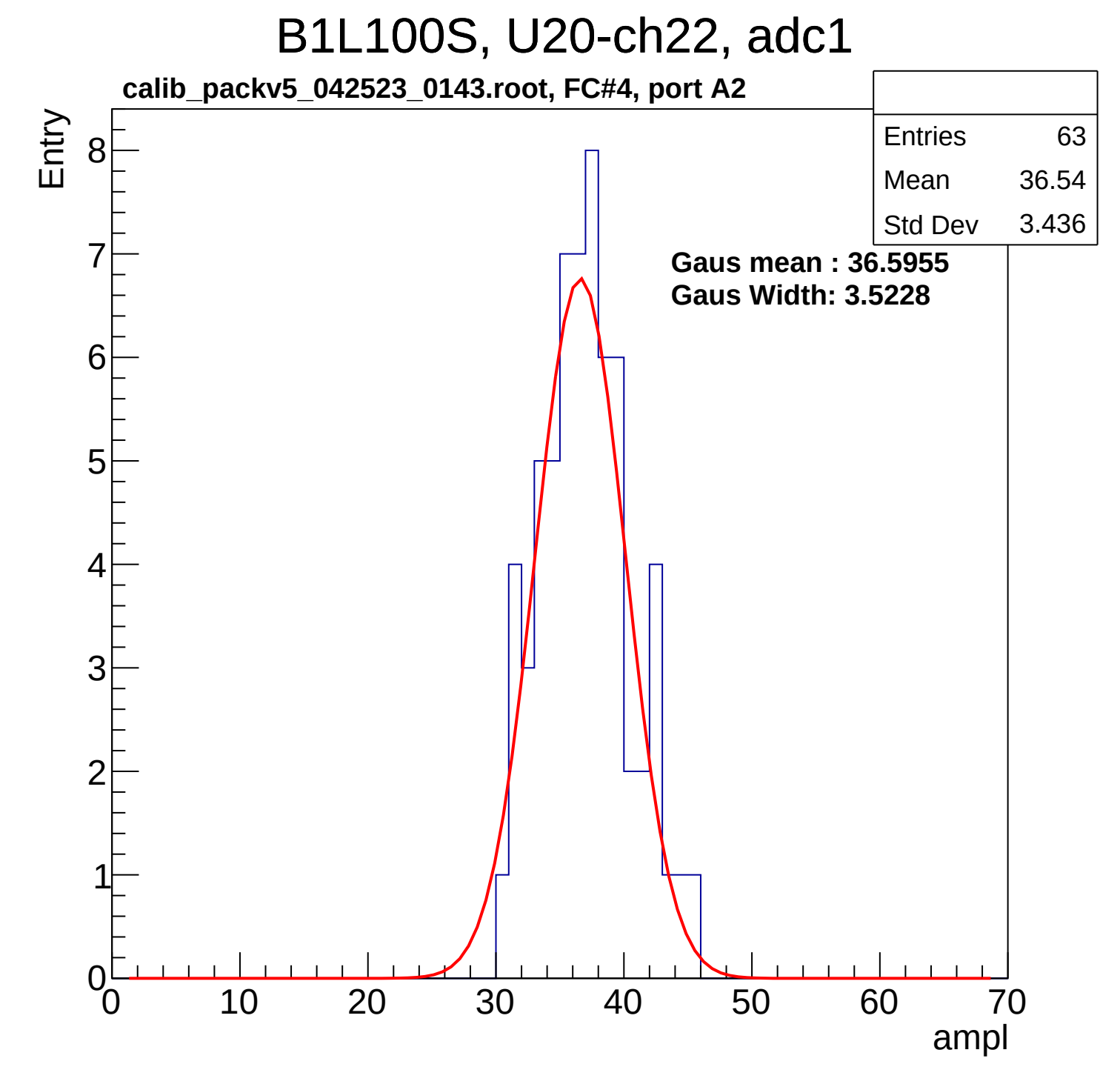
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 36.54
Std Dev 3.436
Gaus mean : 36.5955
Gaus Width: 3.5228
Entry
ampl
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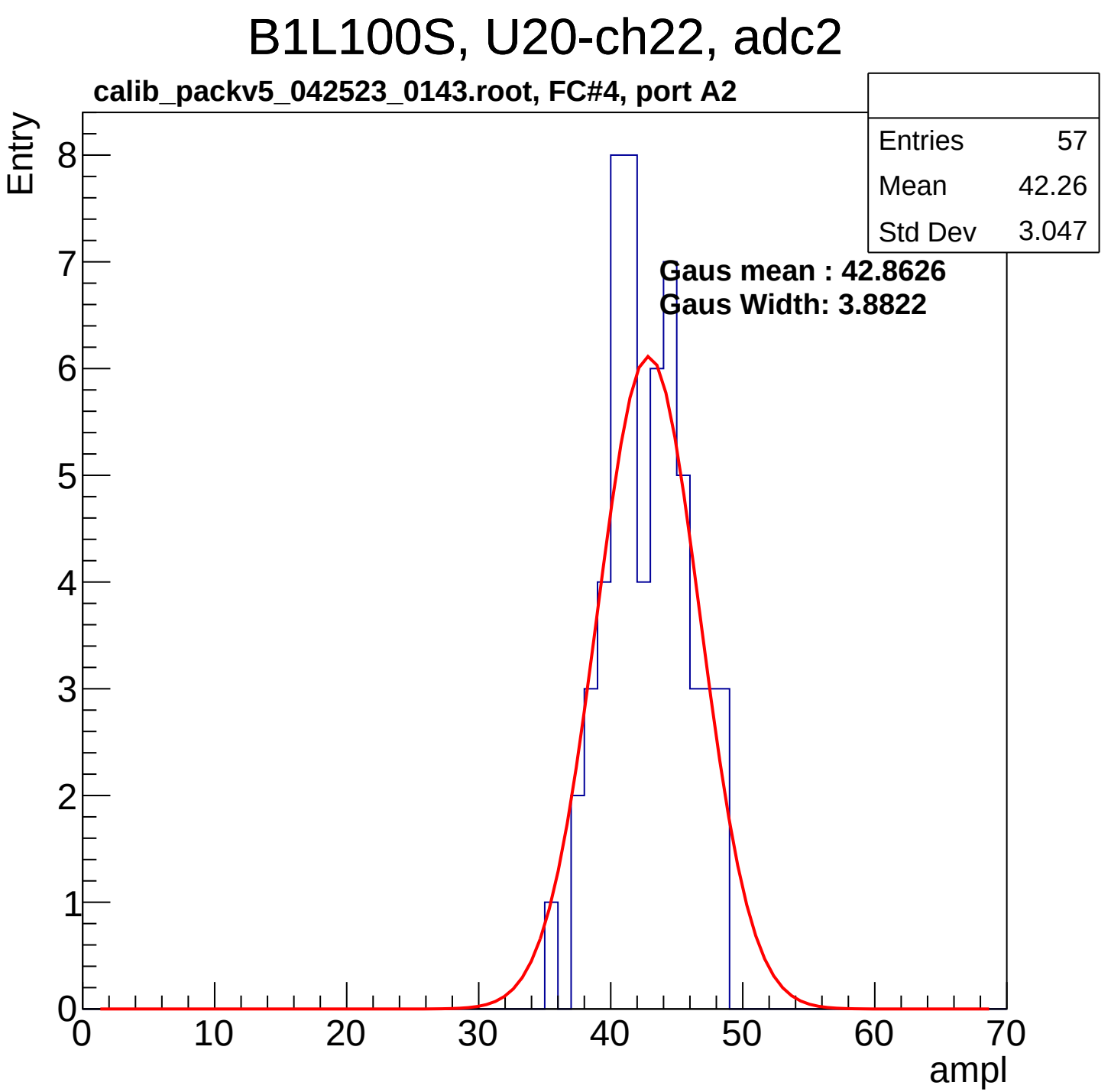
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.26
Std Dev 3.047
Gaus mean : 42.8626
Gaus Width: 3.8822
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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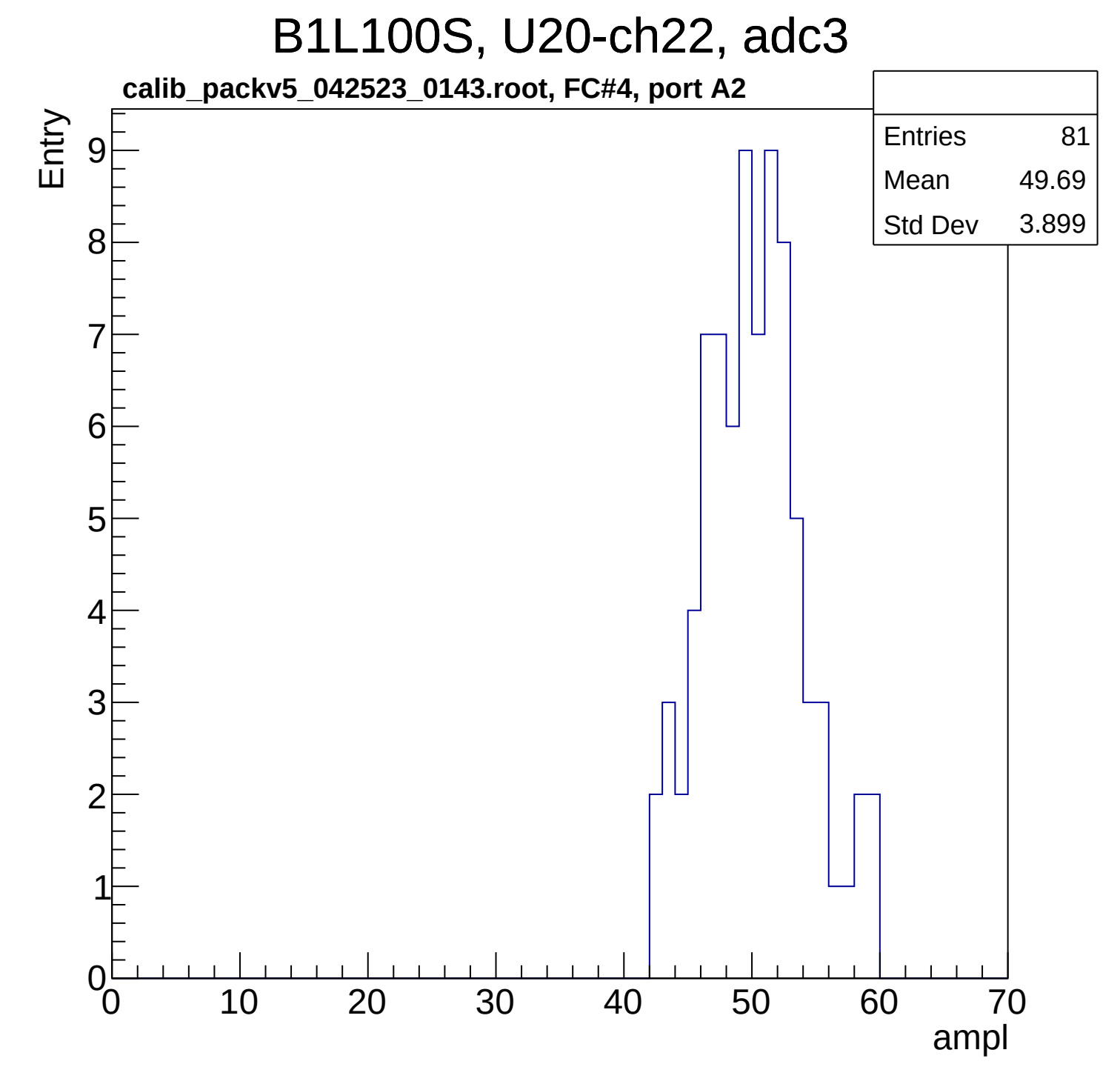
B1L100S, U20-ch22, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 49.69
Std Dev 3.899
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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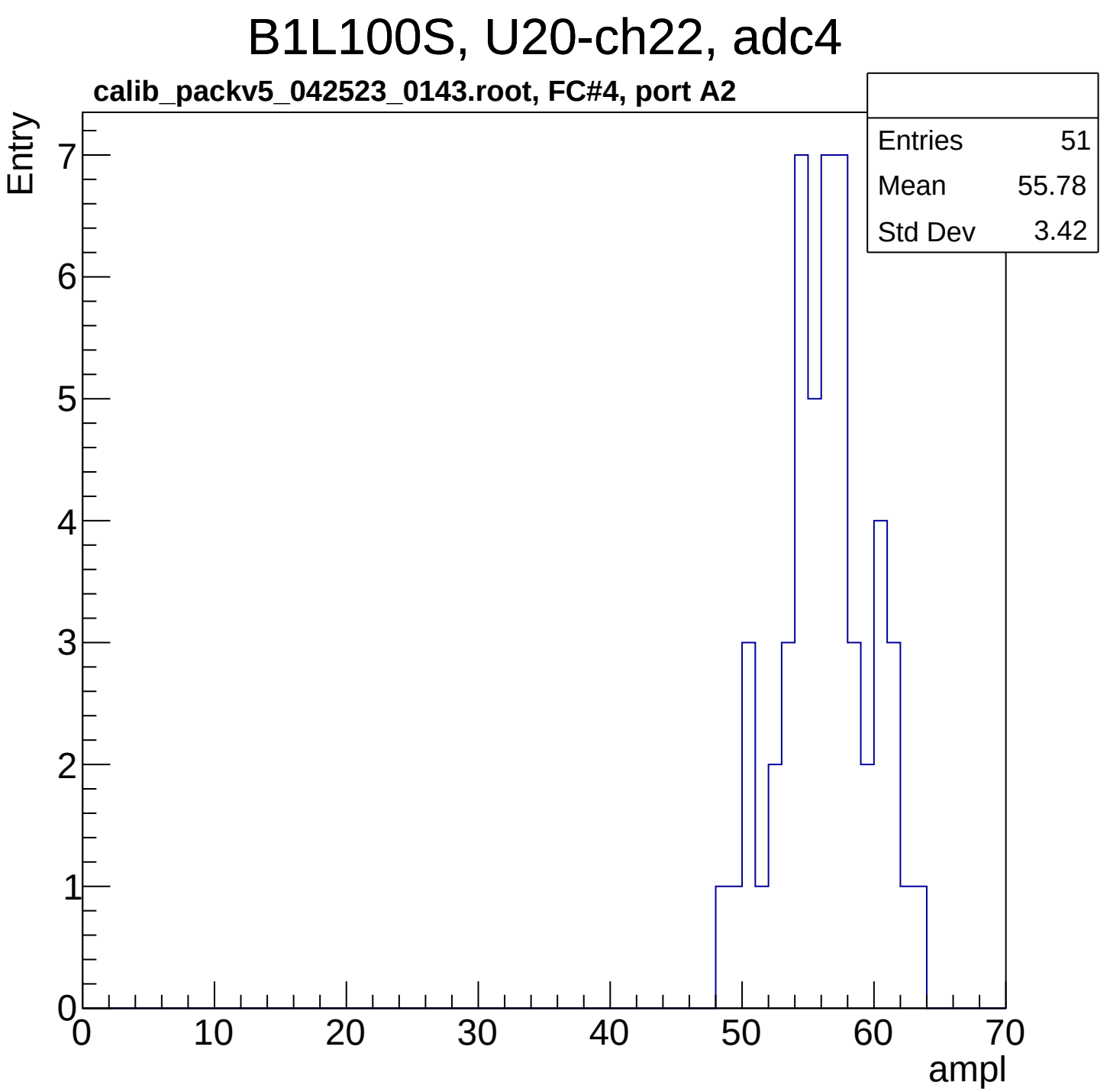
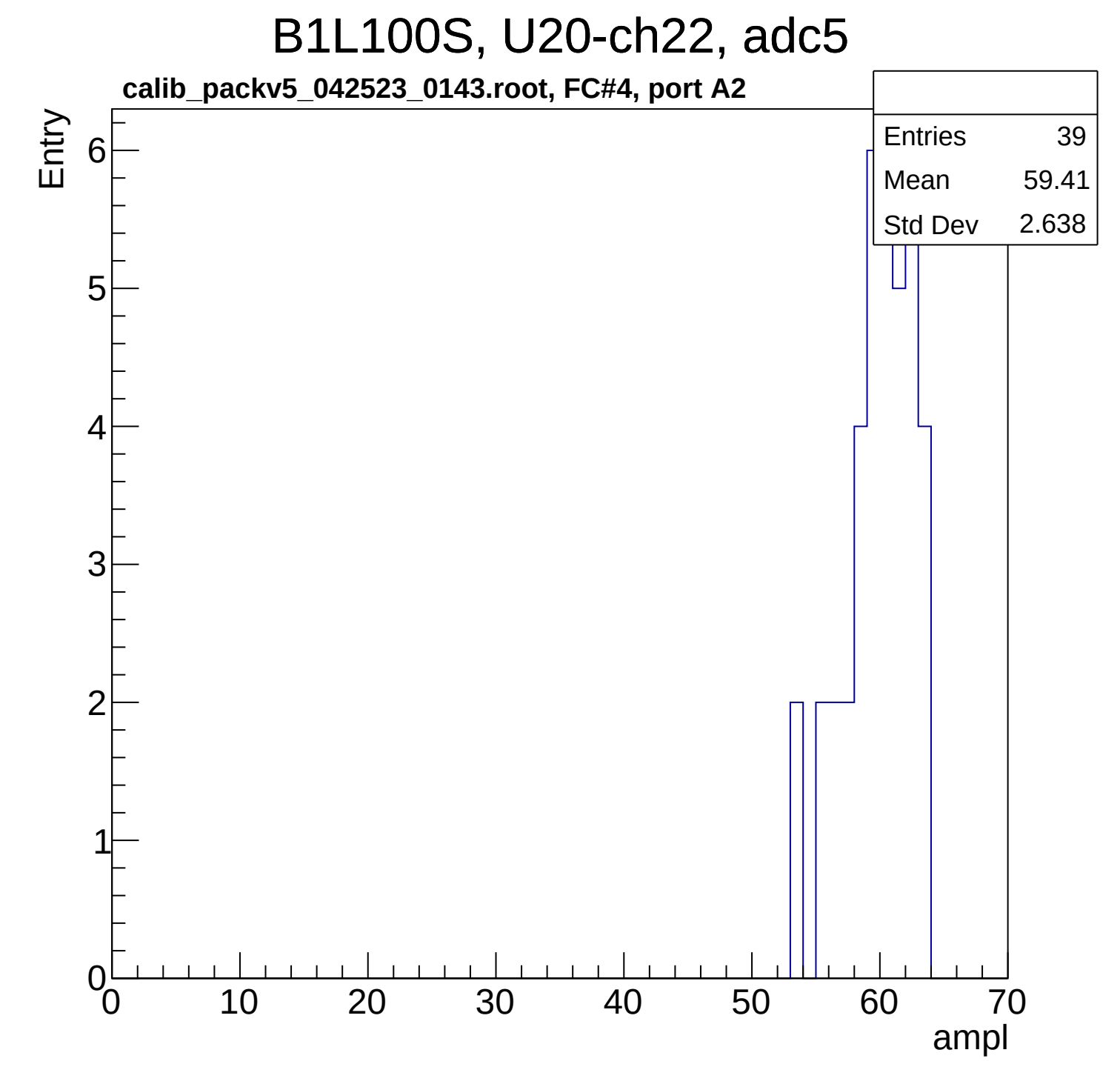

B1L100S, U20-ch22, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.78
Std Dev 3.42
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch22, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.41
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
0
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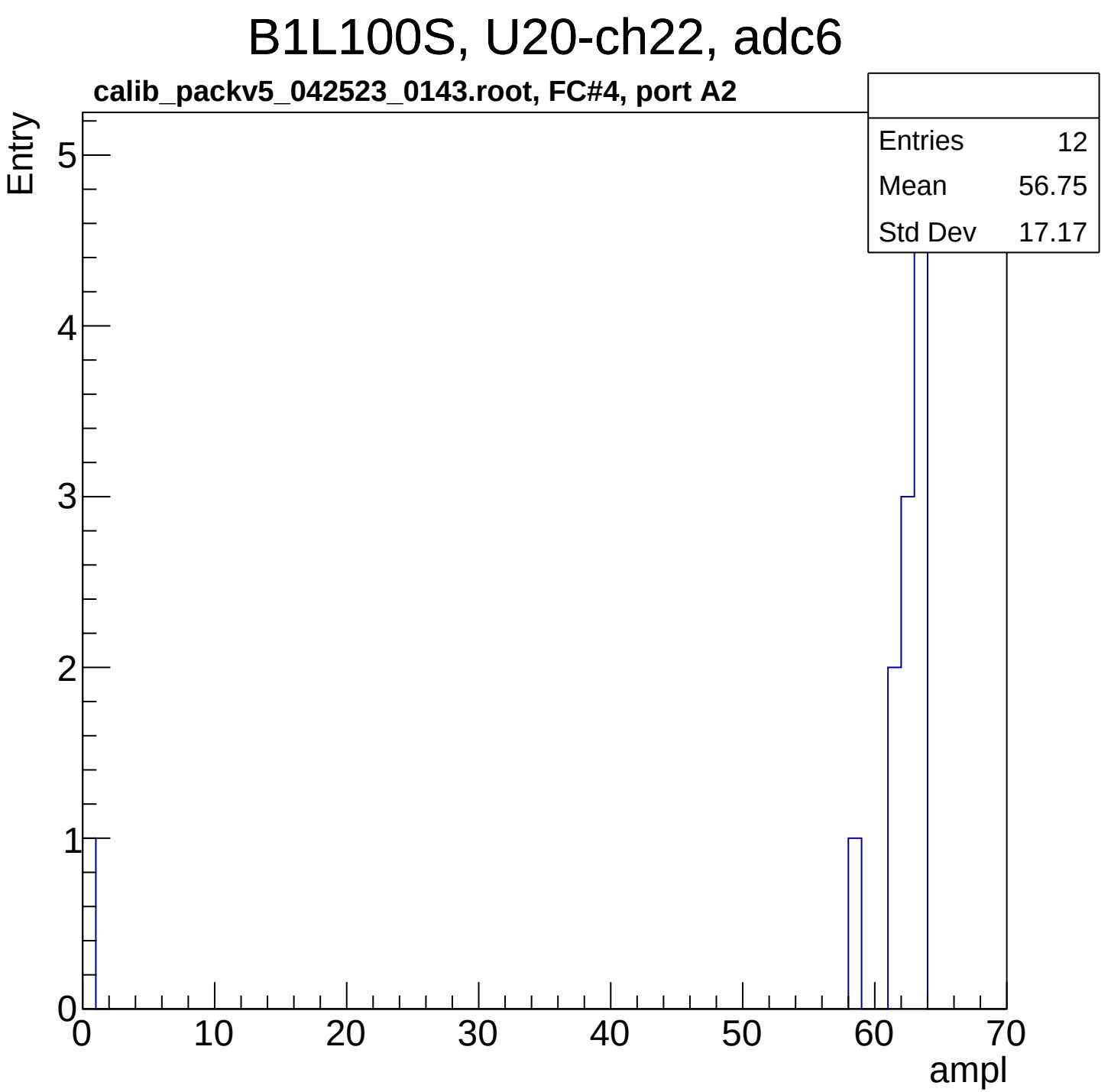
20
30
40
50
60
70


B1L100S, U20-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.75
Std Dev 17.17
Entry
ampl
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2
3
4
5
0
10
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40
50
60
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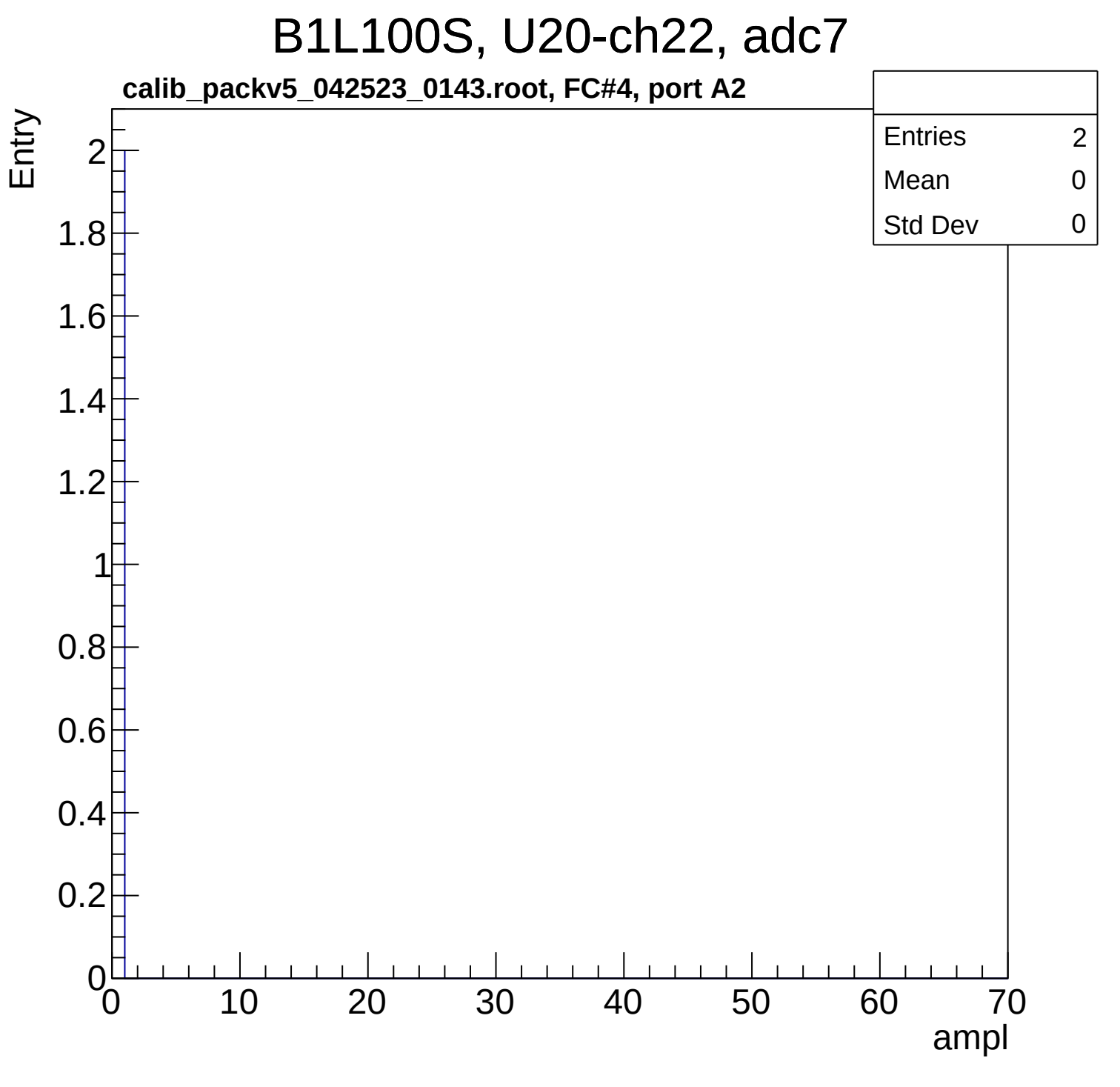
B1L100S, U20-ch22, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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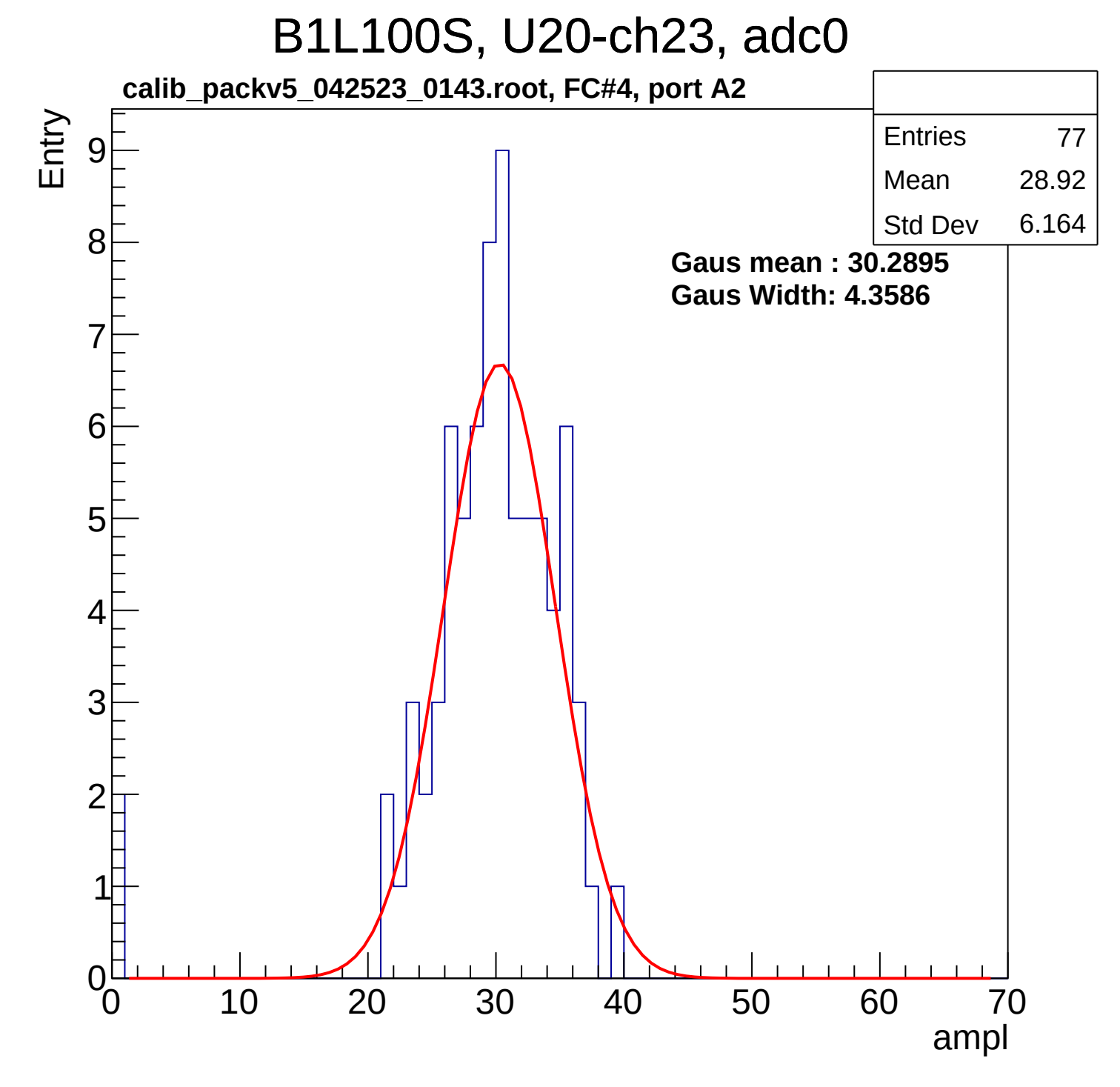
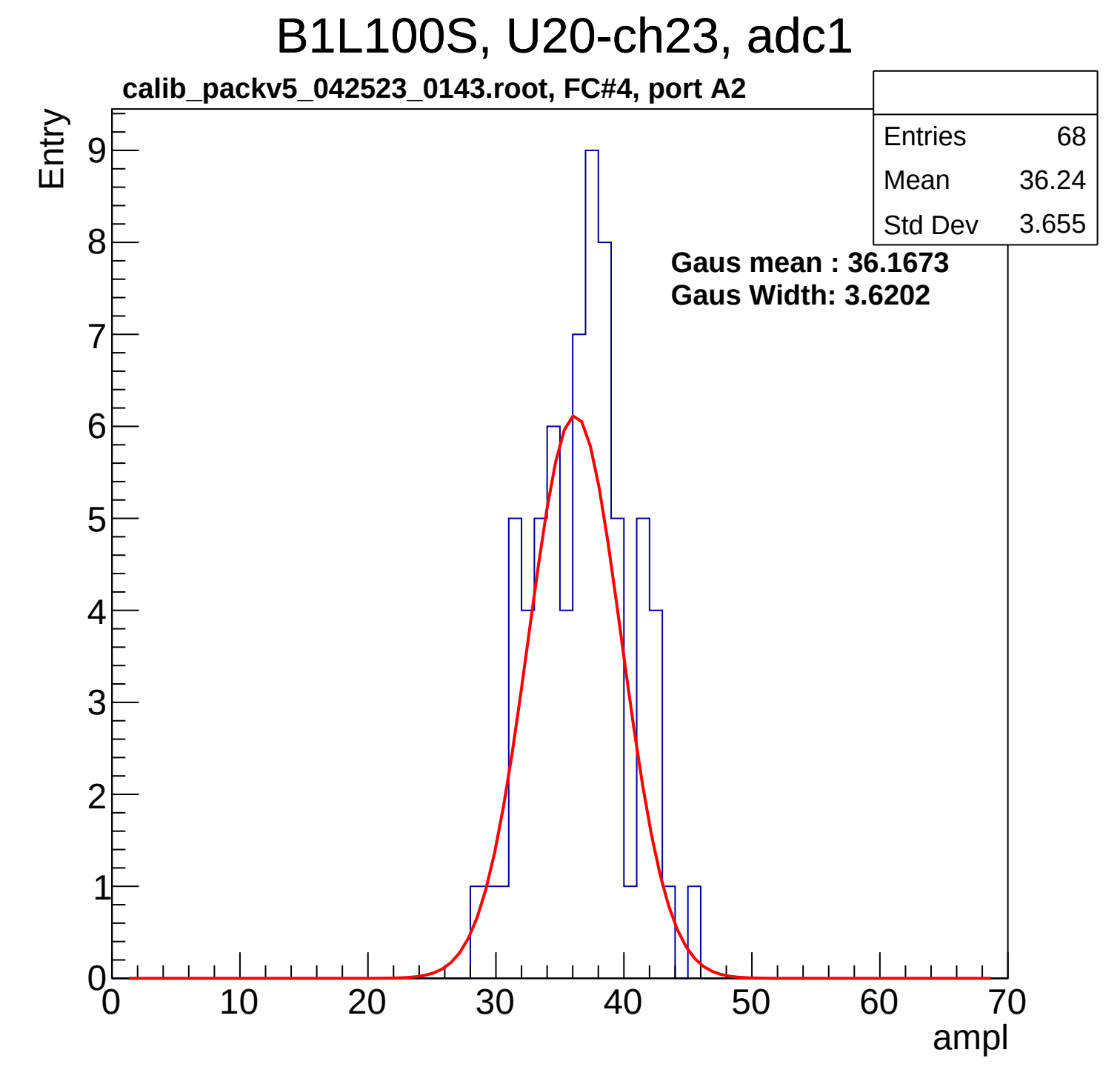
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.92
Std Dev 6.164
Gaus mean : 30.2895
Gaus Width: 4.3586
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.24
Std Dev 3.655
Gaus mean : 36.1673
Gaus Width: 3.6202
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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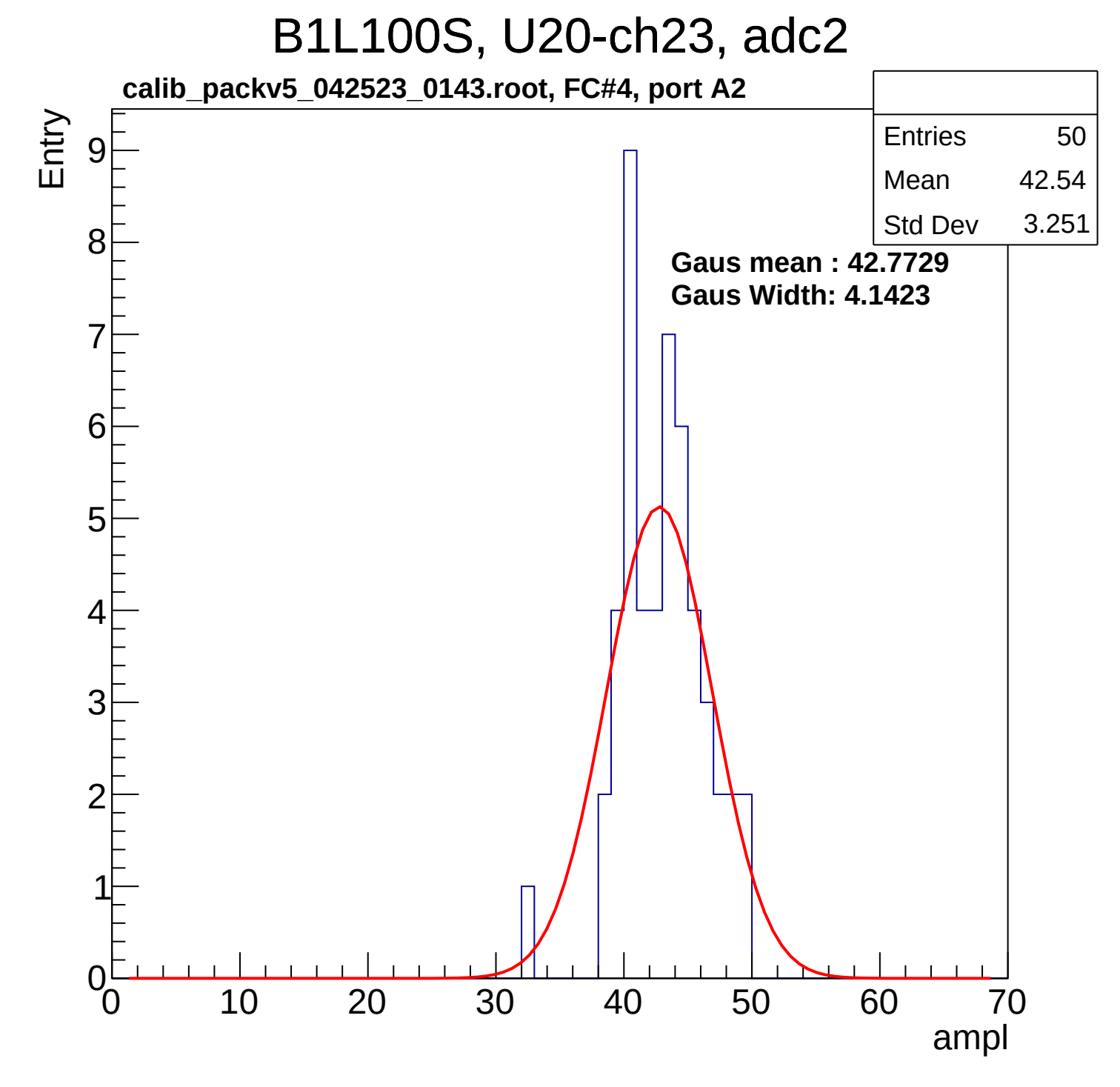
B1L100S, U20-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 42.54
Std Dev 3.251
Gaus mean : 42.7729
Gaus Width: 4.1423
Entry
ampl
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2
3
4
5
6
7
8
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0
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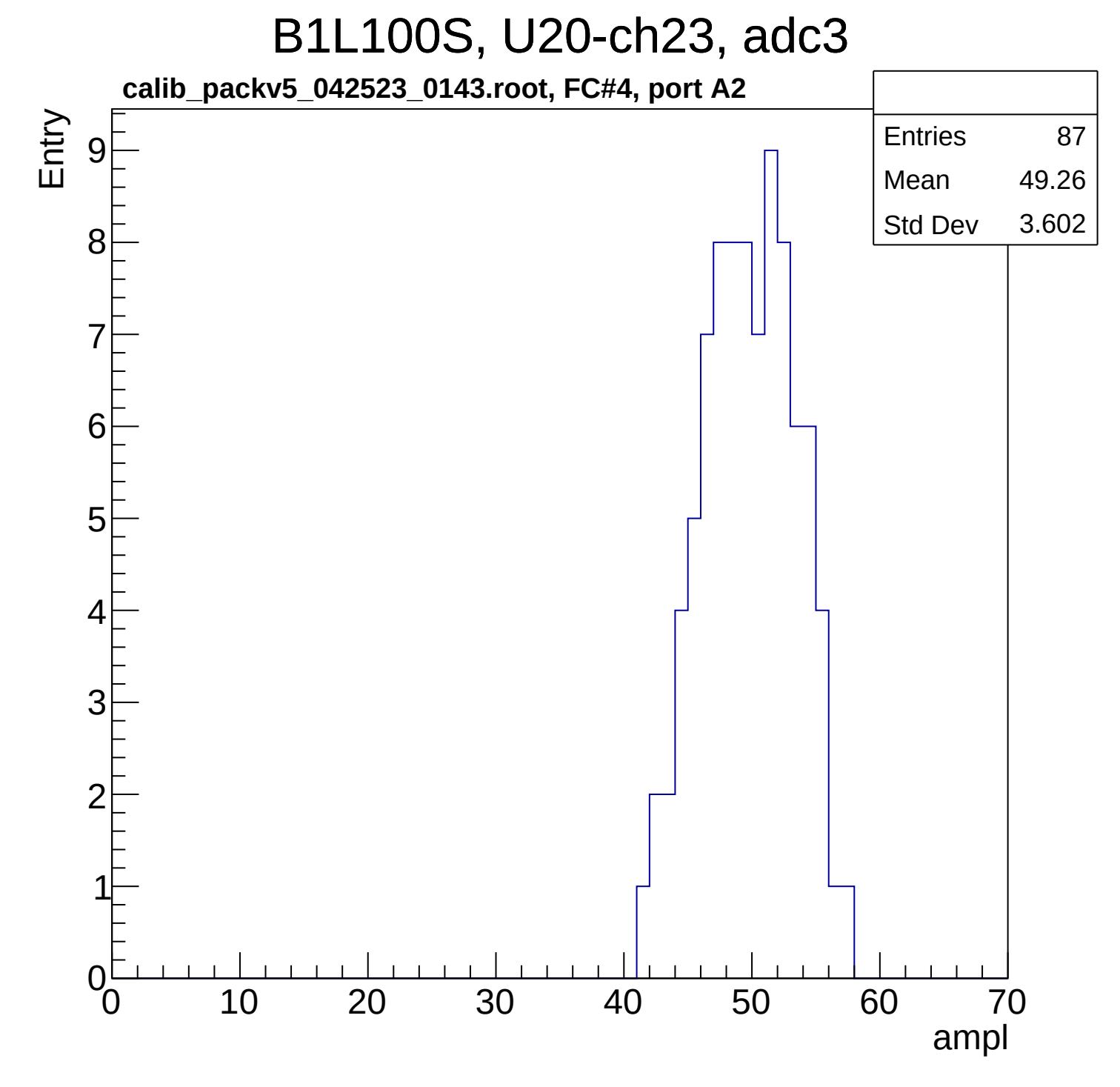
B1L100S, U20-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 49.26
Std Dev 3.602
Entry
ampl
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3
4
5
6
7
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0
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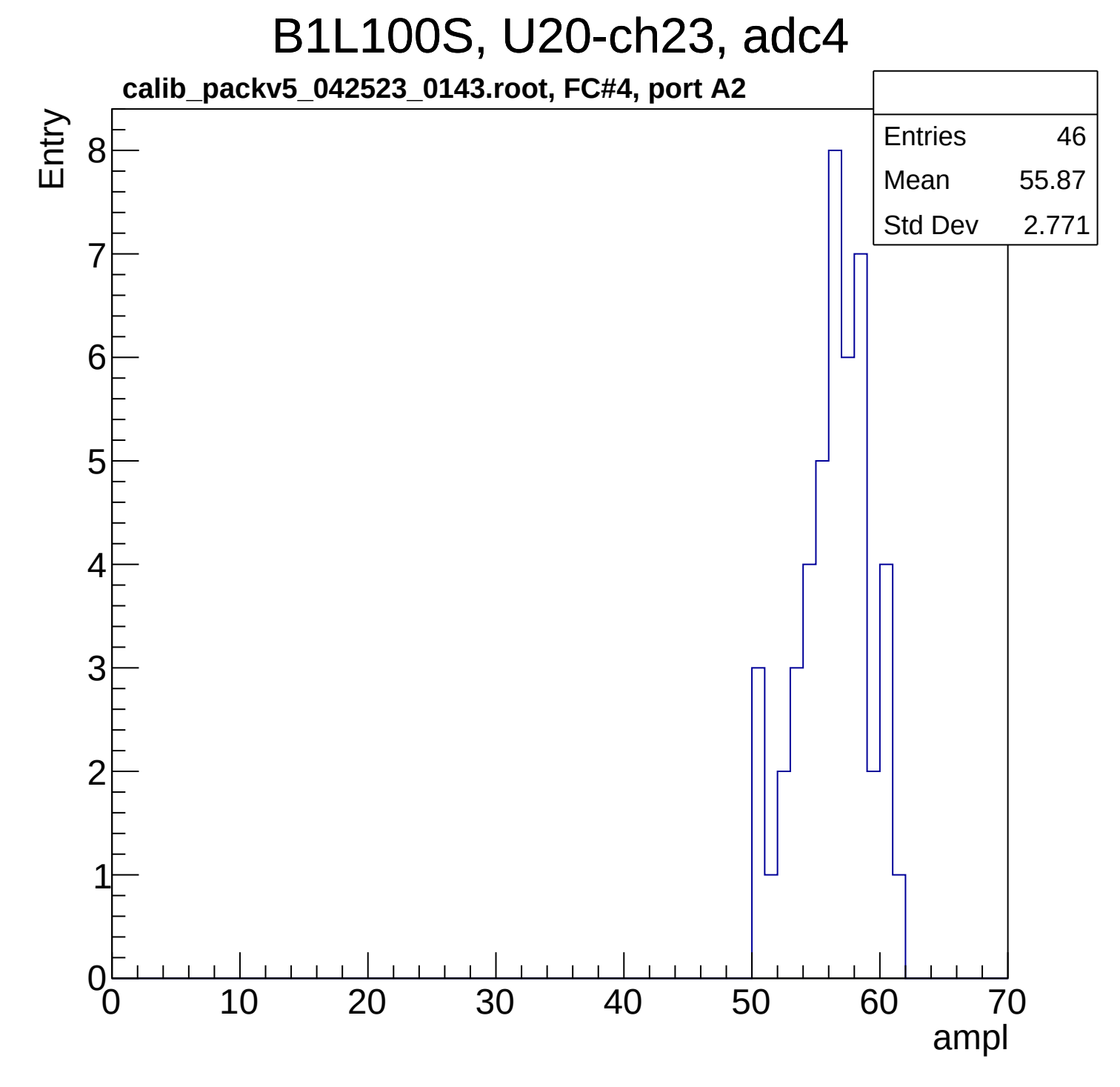
B1L100S, U20-ch23, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 55.87
Std Dev 2.771
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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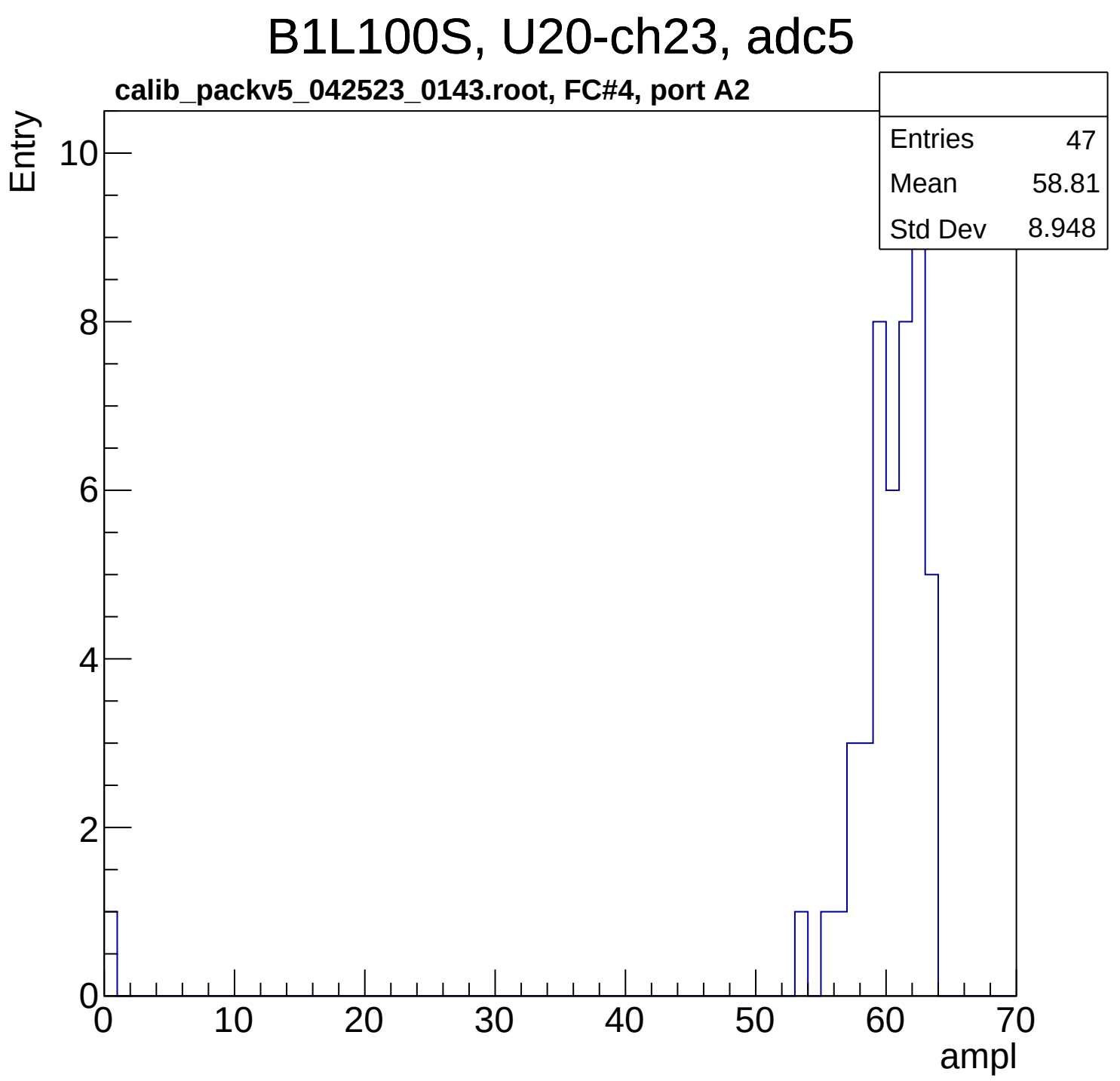
B1L100S, U20-ch23, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.81
Std Dev 8.948
Entry
ampl
0
2
4
6
8
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0
10
20
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40
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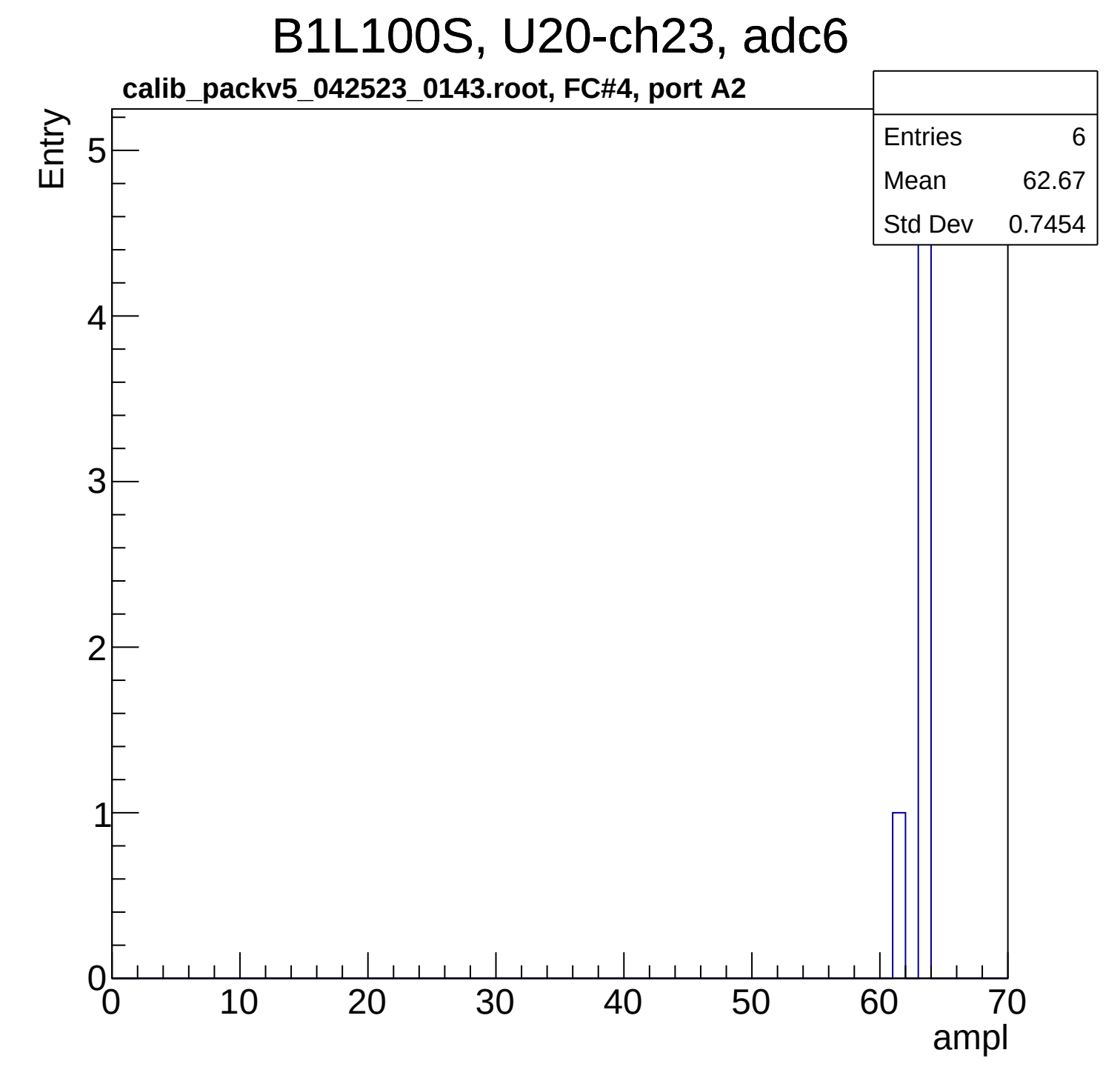
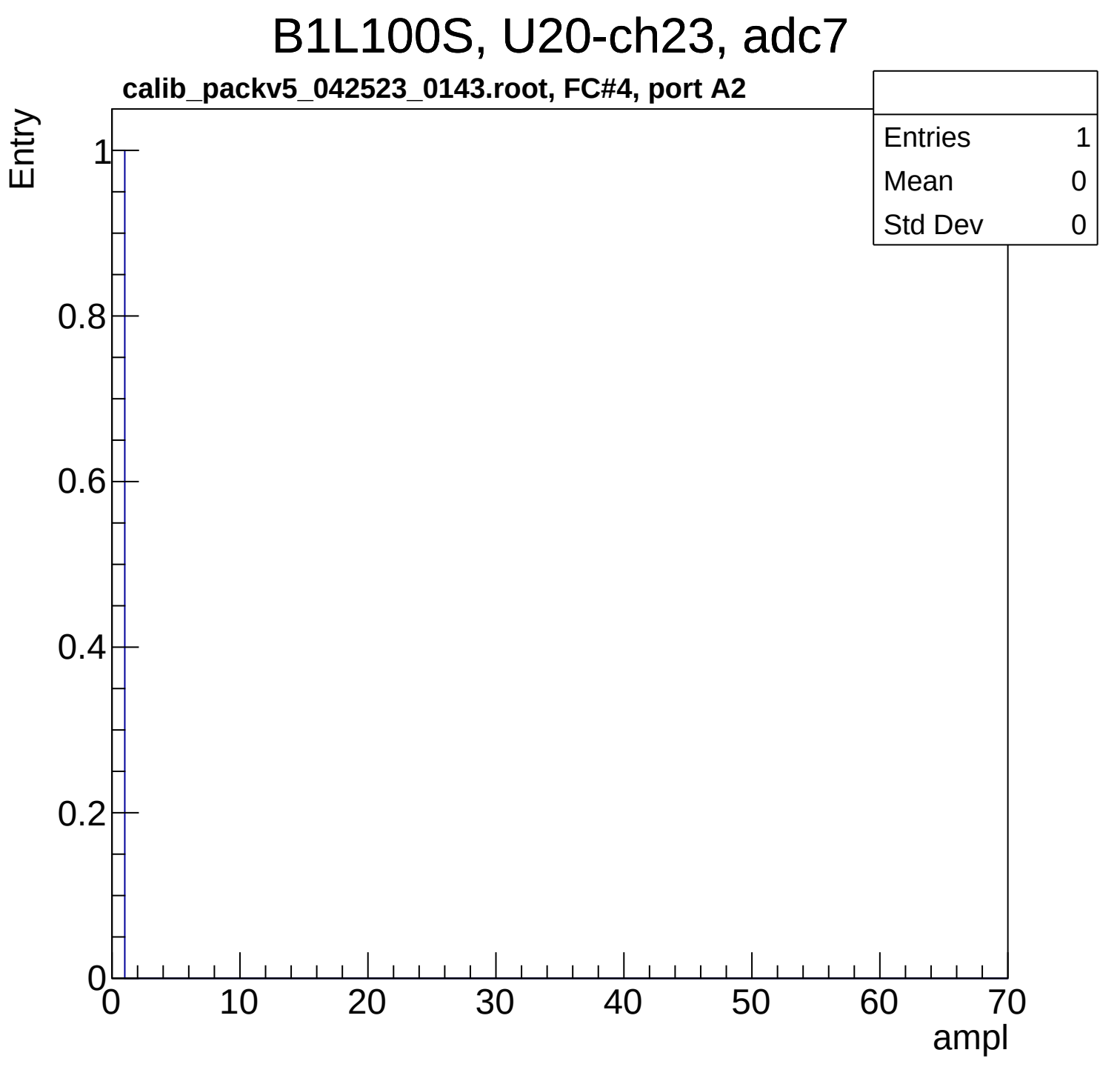
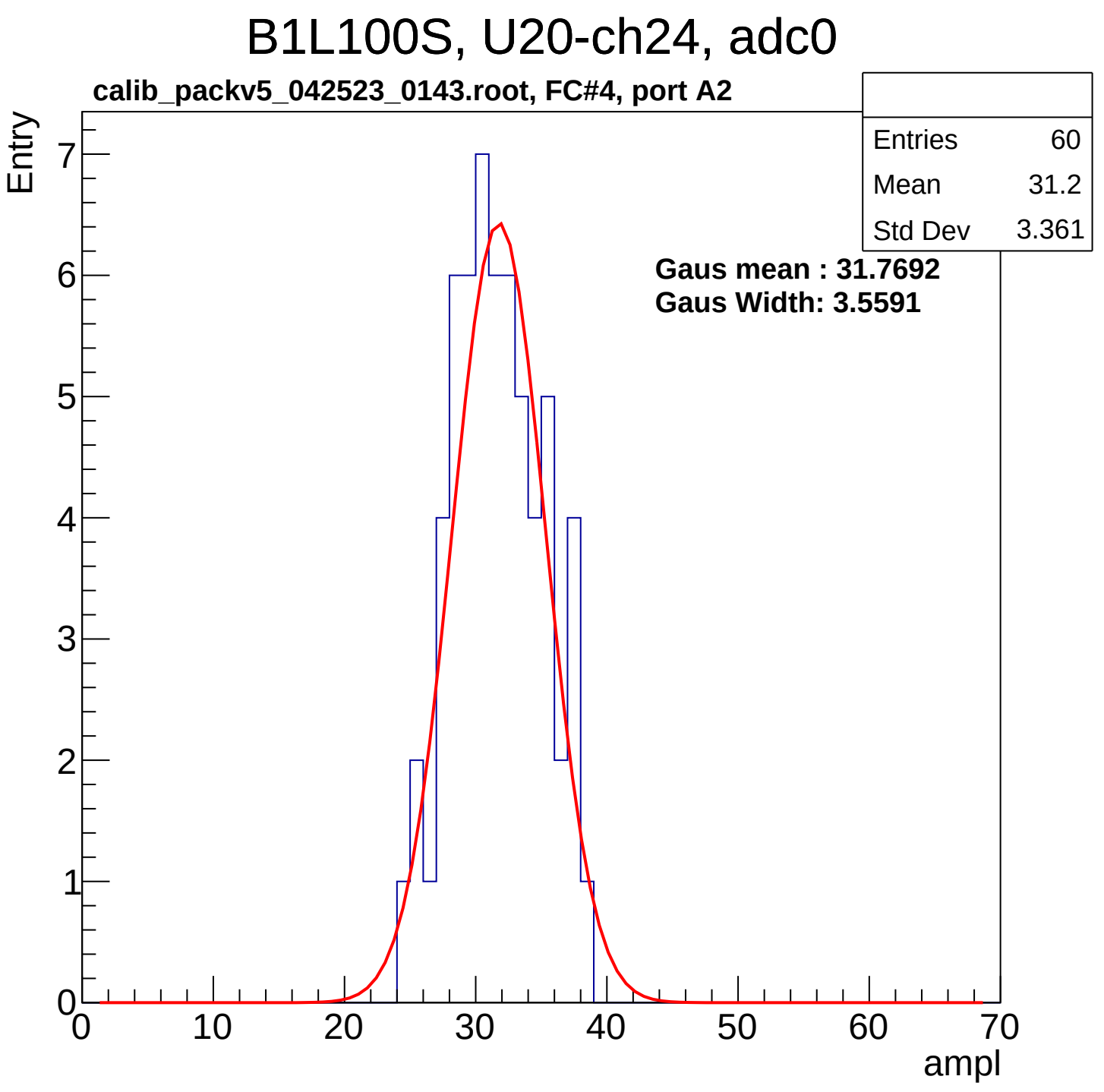
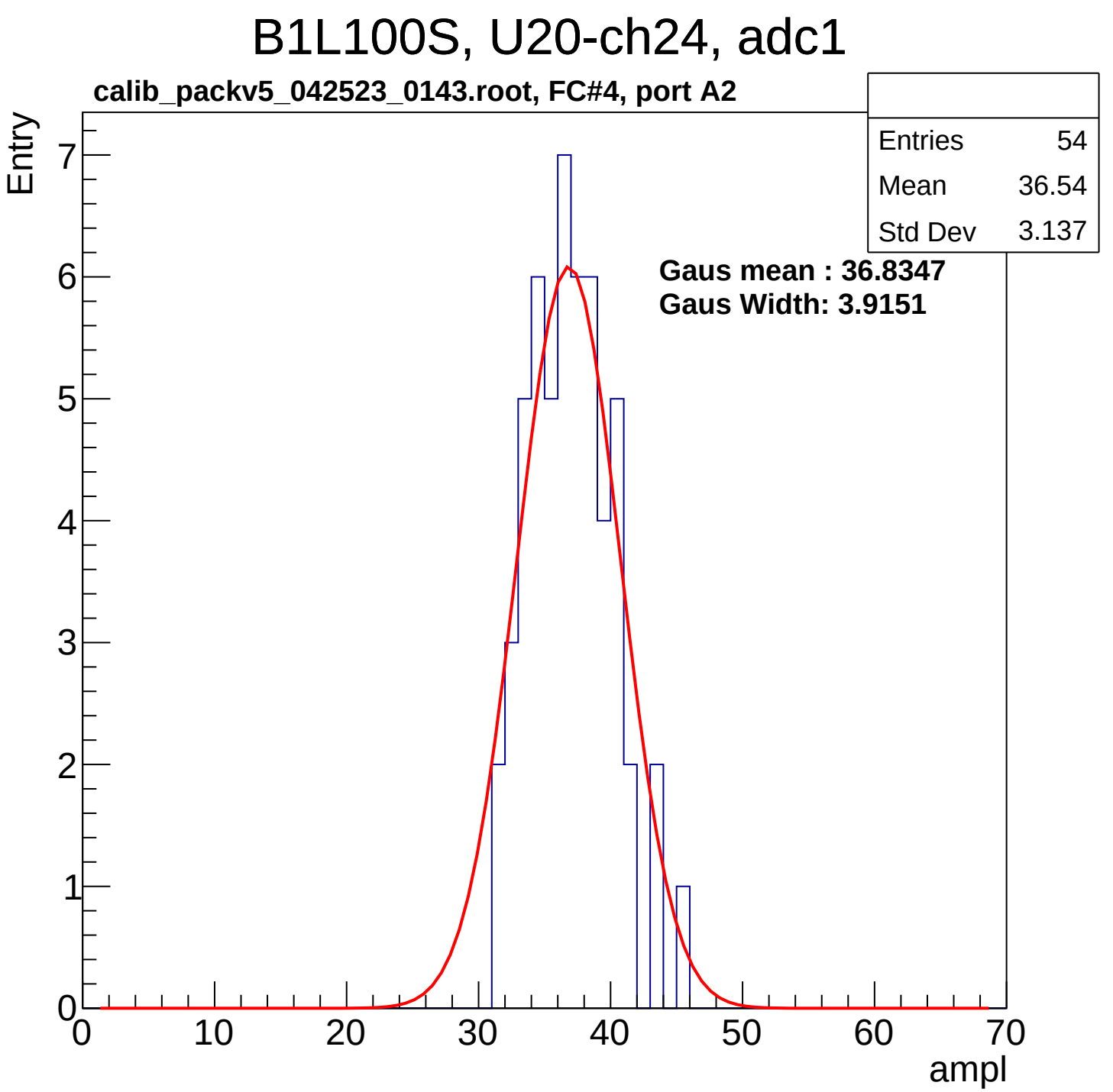
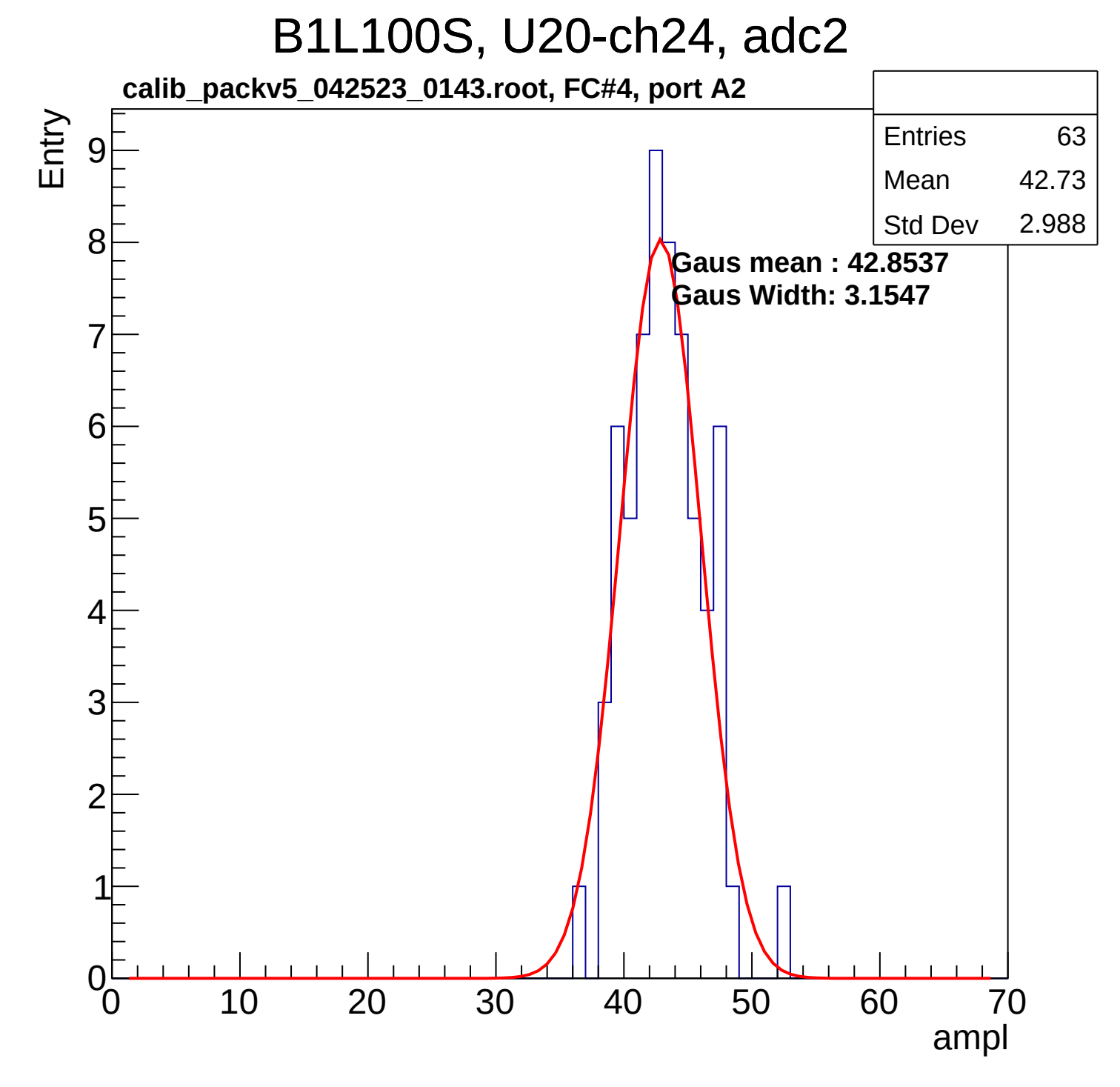

B1L100S, U20-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.67
Std Dev 0.7454
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U20-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 31.2
Std Dev 3.361
Gaus mean : 31.7692
Gaus Width: 3.5591
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 36.54
Std Dev 3.137
Gaus mean : 36.8347
Gaus Width: 3.9151
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.73
Std Dev 2.988
Gaus mean : 42.8537
Gaus Width: 3.1547
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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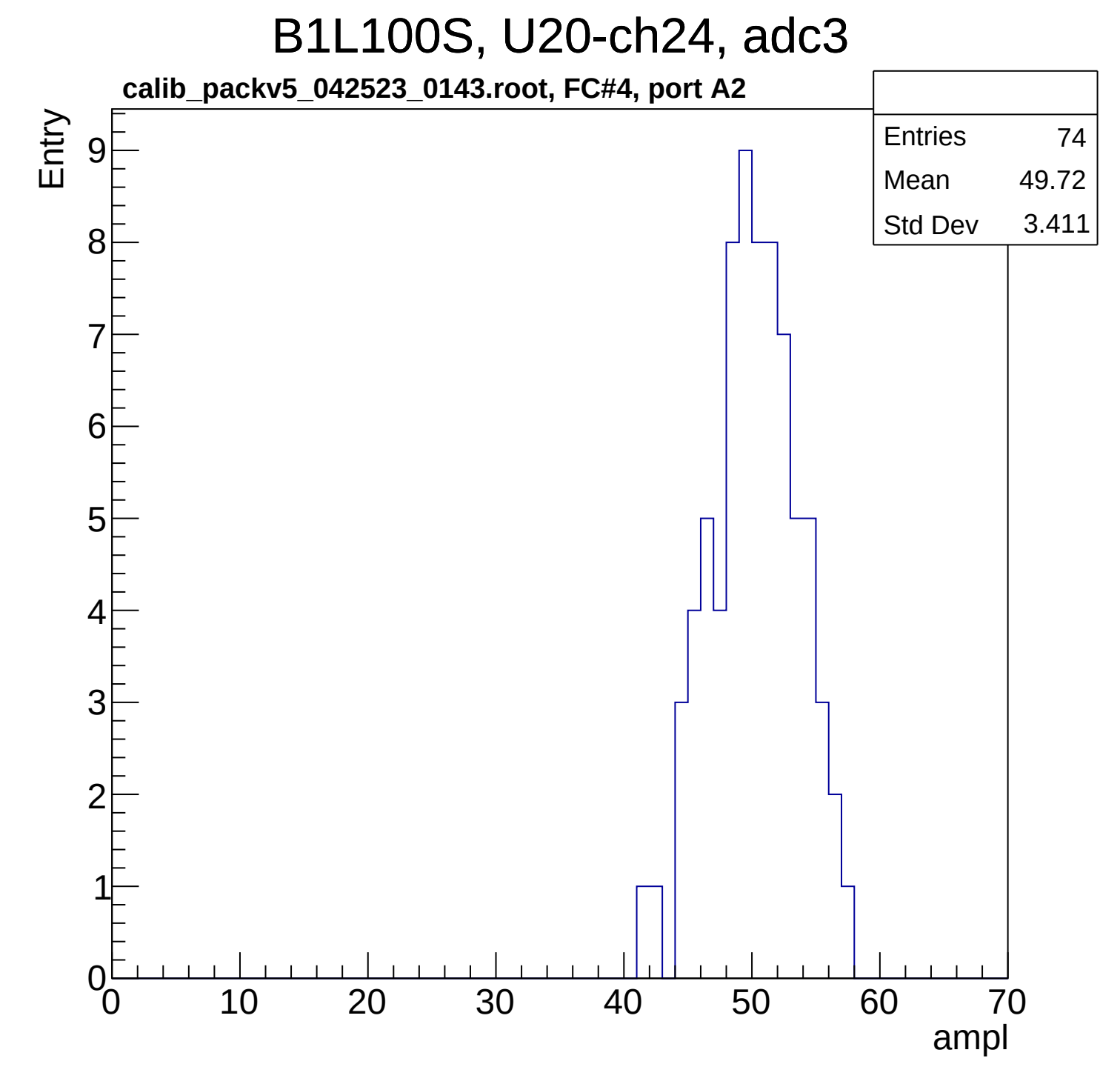
B1L100S, U20-ch24, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.72
Std Dev 3.411
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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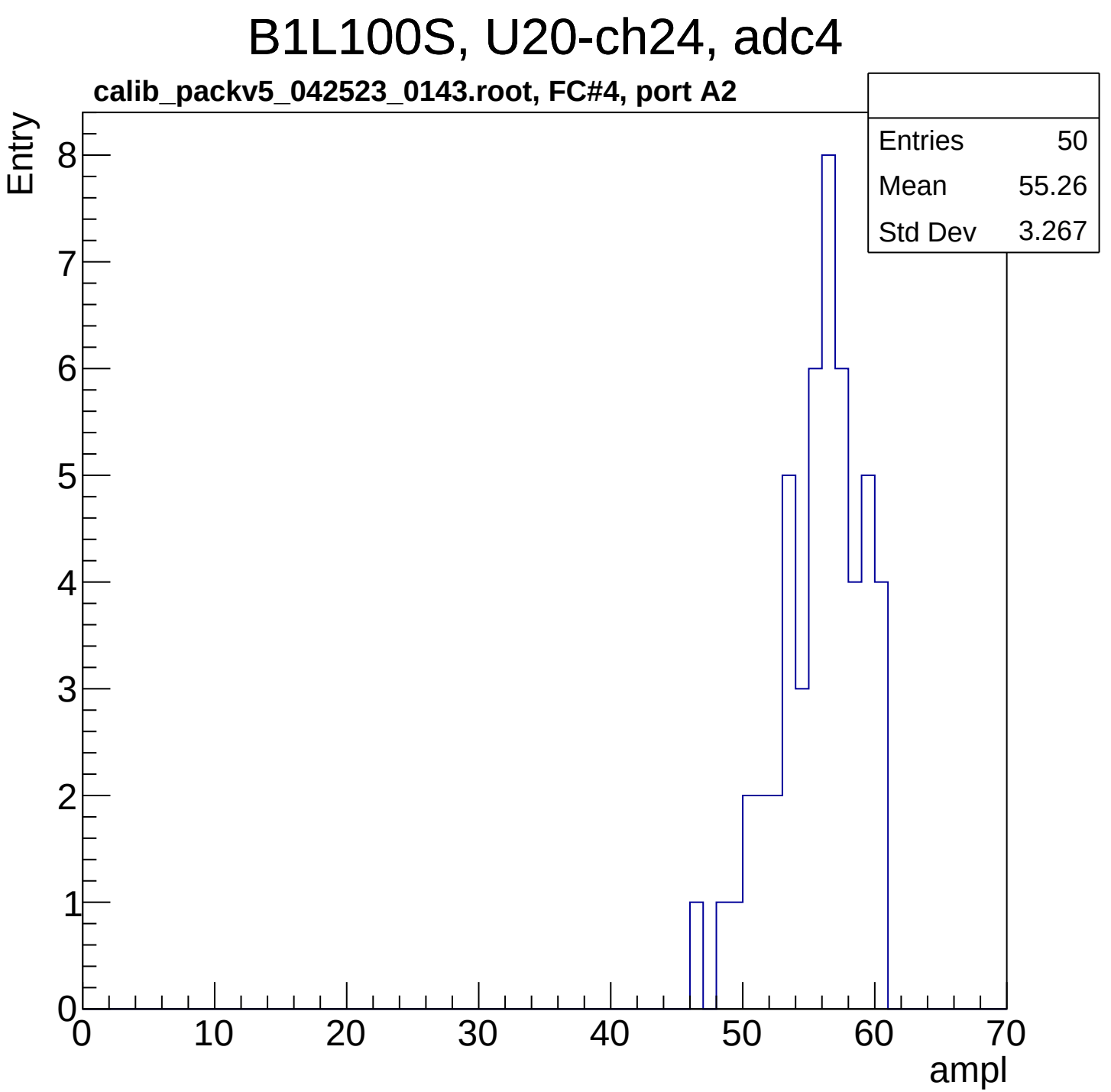
B1L100S, U20-ch24, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.26
Std Dev 3.267
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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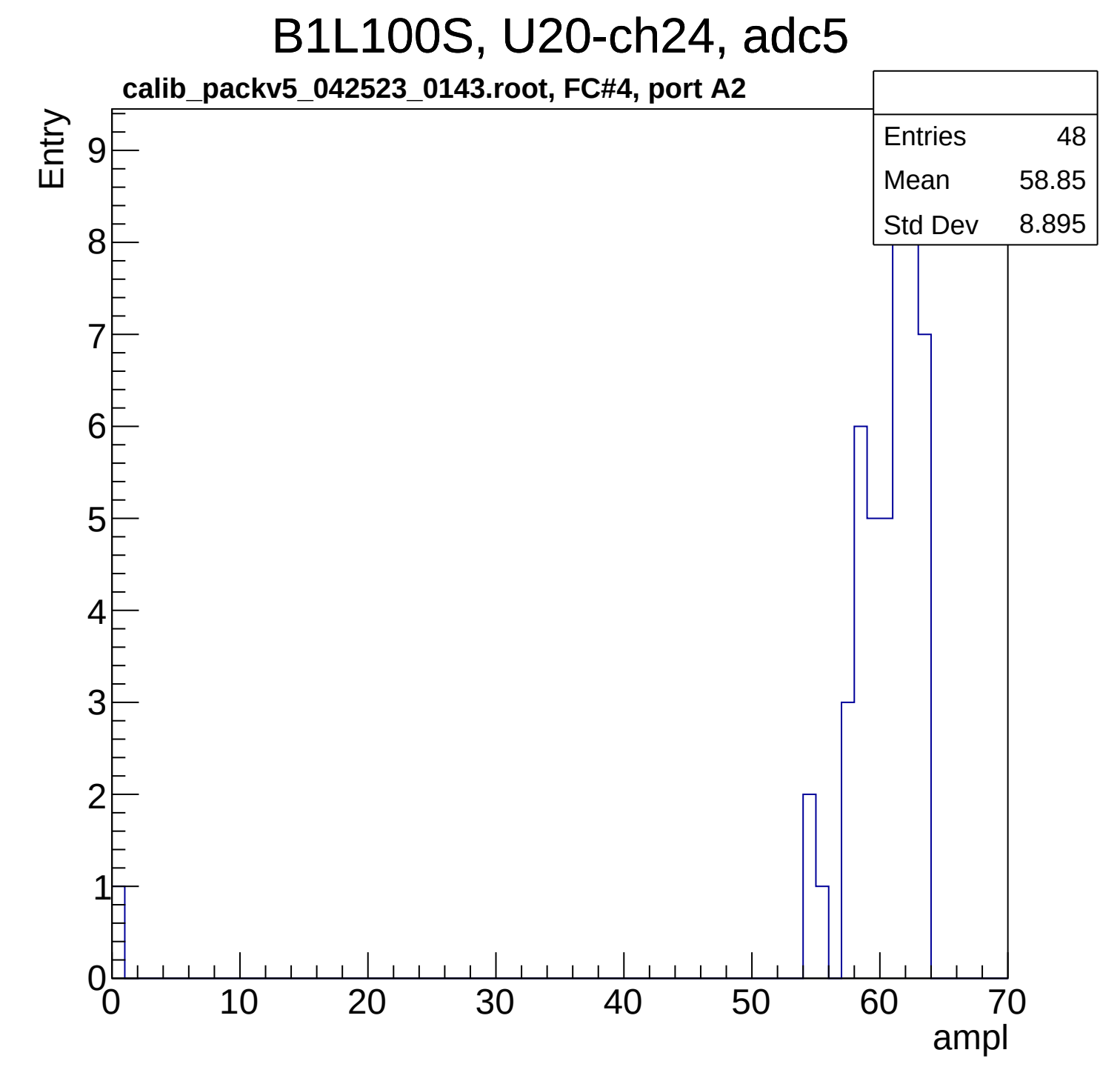
B1L100S, U20-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.85
Std Dev 8.895
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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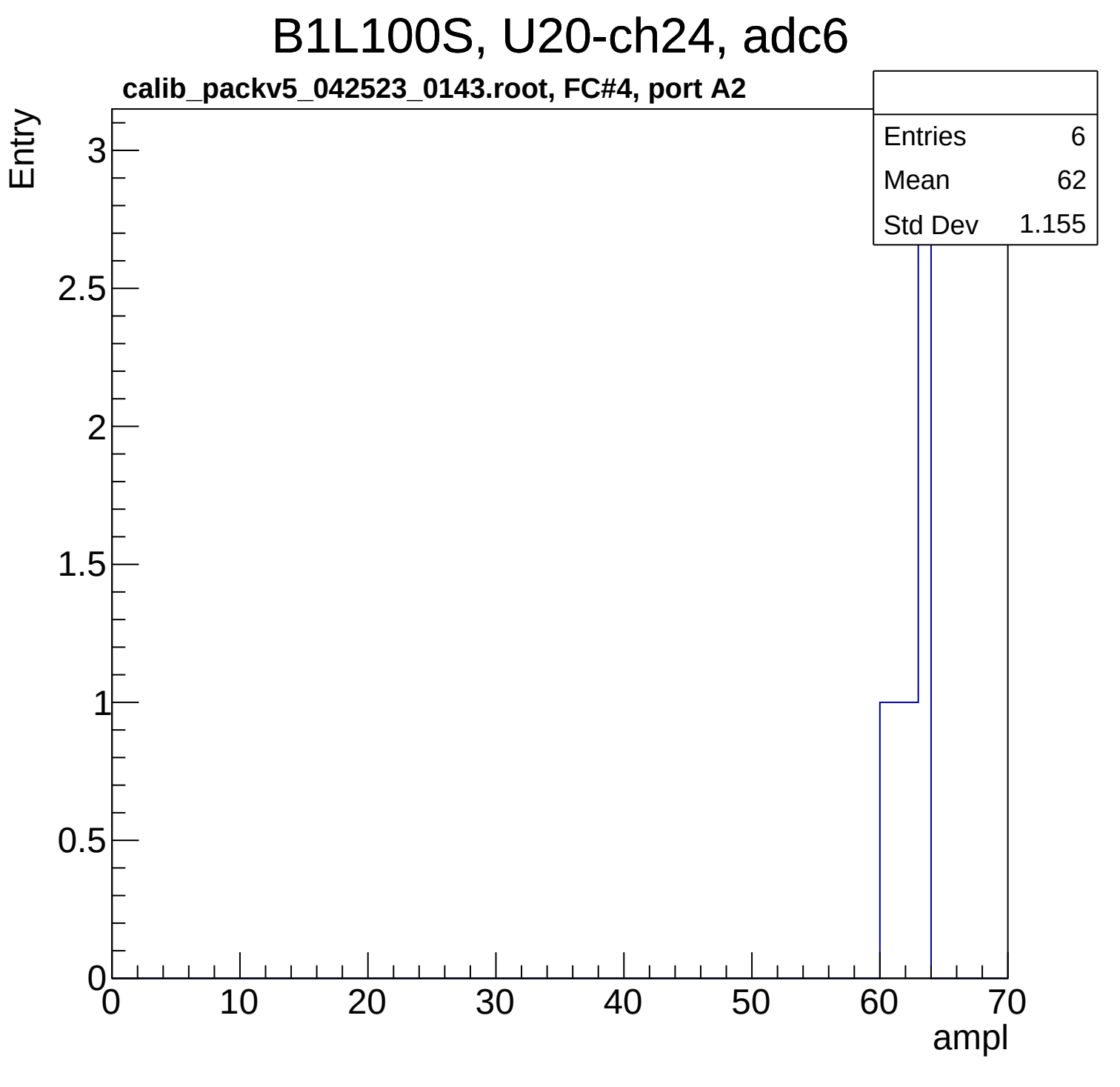
B1L100S, U20-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
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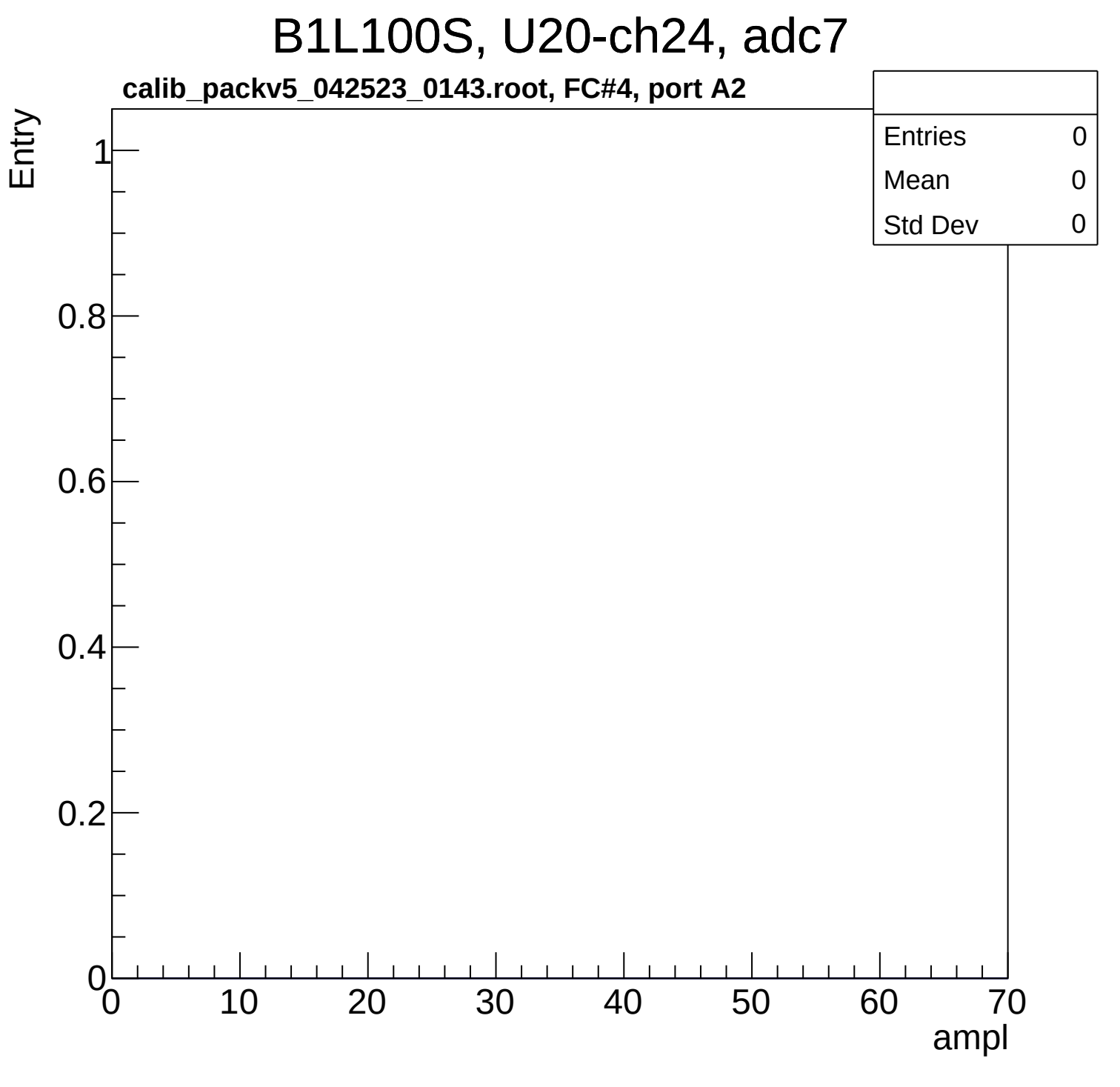
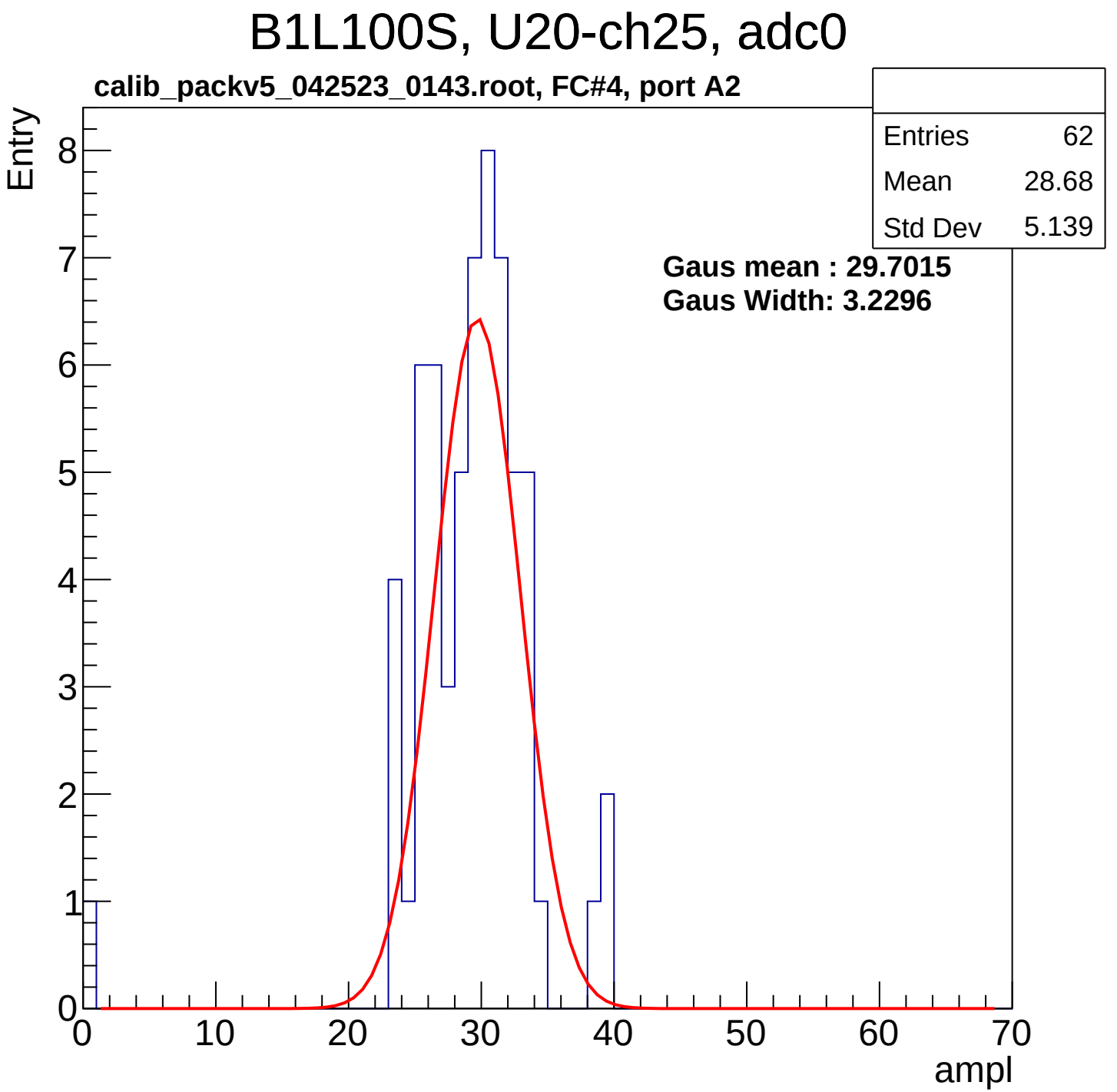
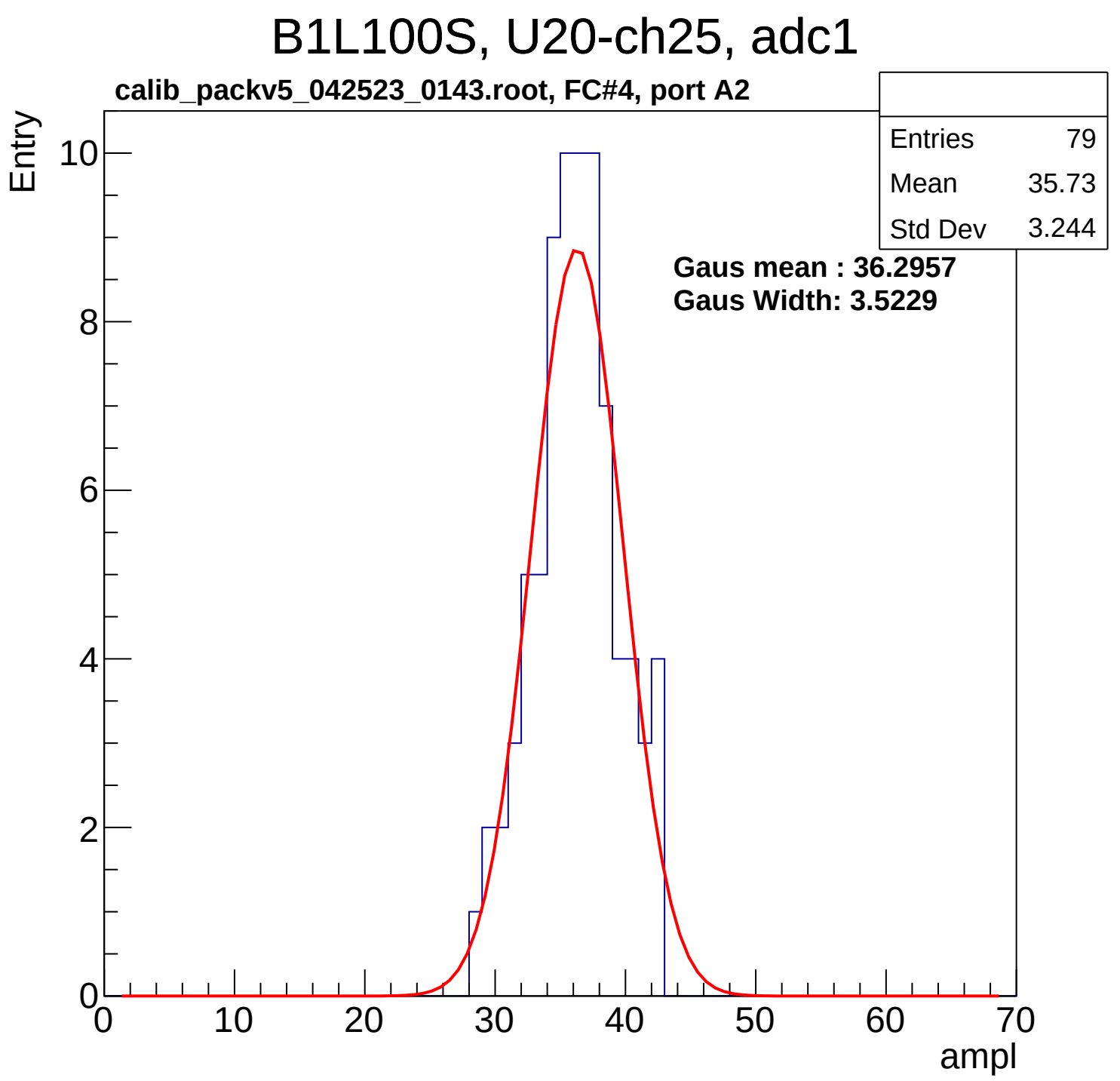
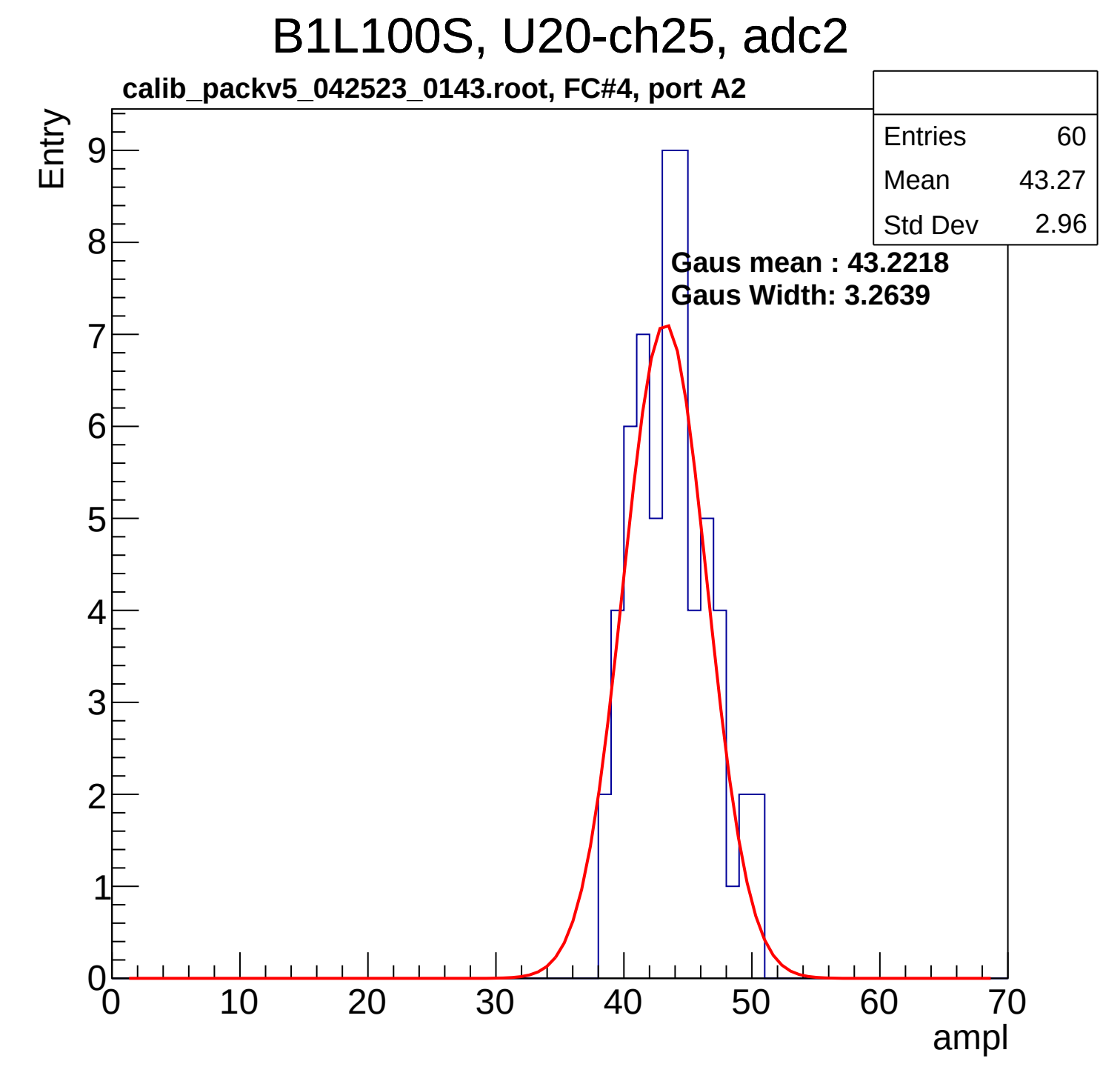
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch24, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.68
Std Dev 5.139
Gaus mean : 29.7015
Gaus Width: 3.2296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.73
Std Dev 3.244
Gaus mean : 36.2957
Gaus Width: 3.5229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.27
Std Dev 2.96
Gaus mean : 43.2218
Gaus Width: 3.2639
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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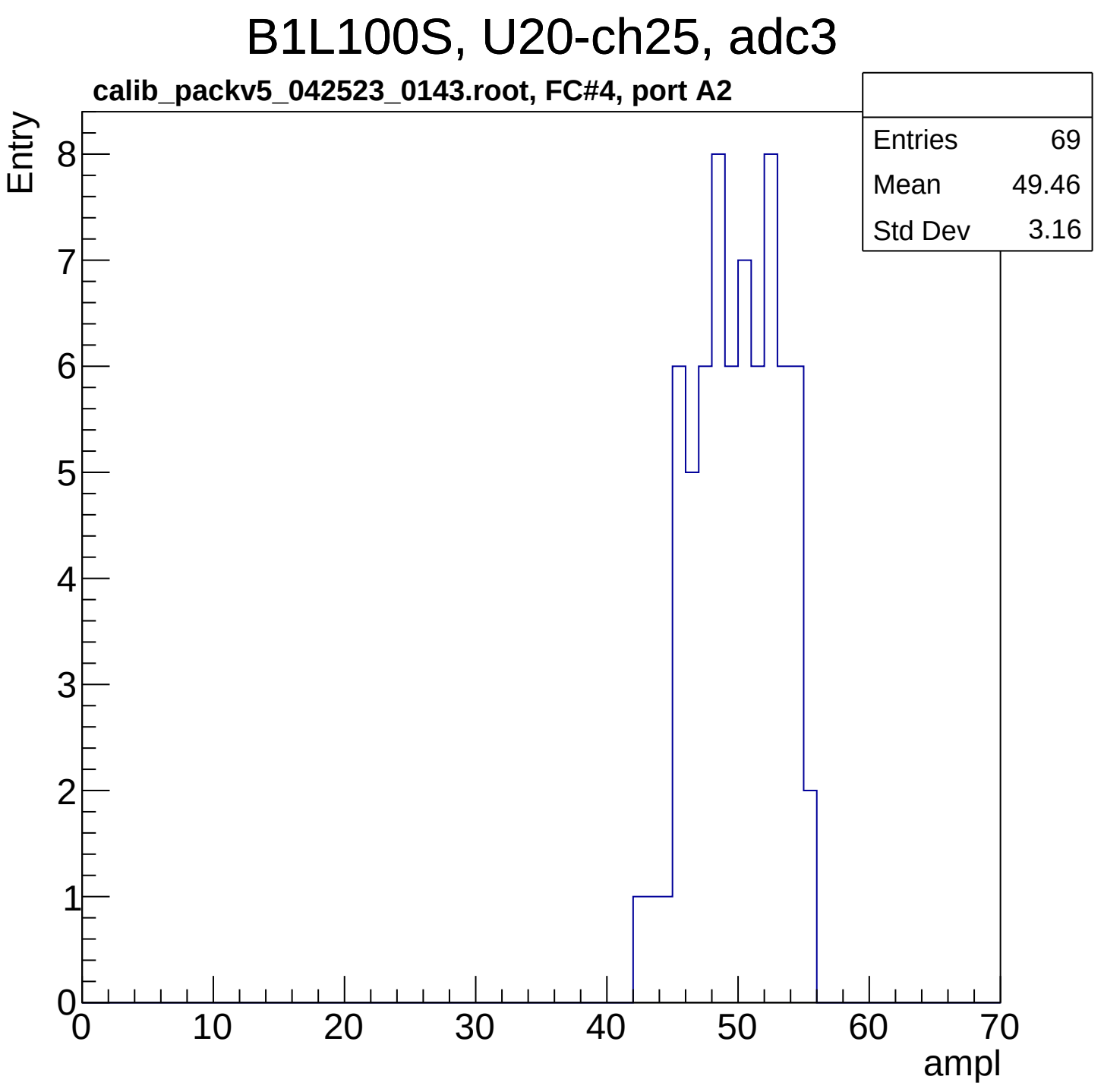
60
70


B1L100S, U20-ch25, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.46
Std Dev 3.16
Entry
ampl
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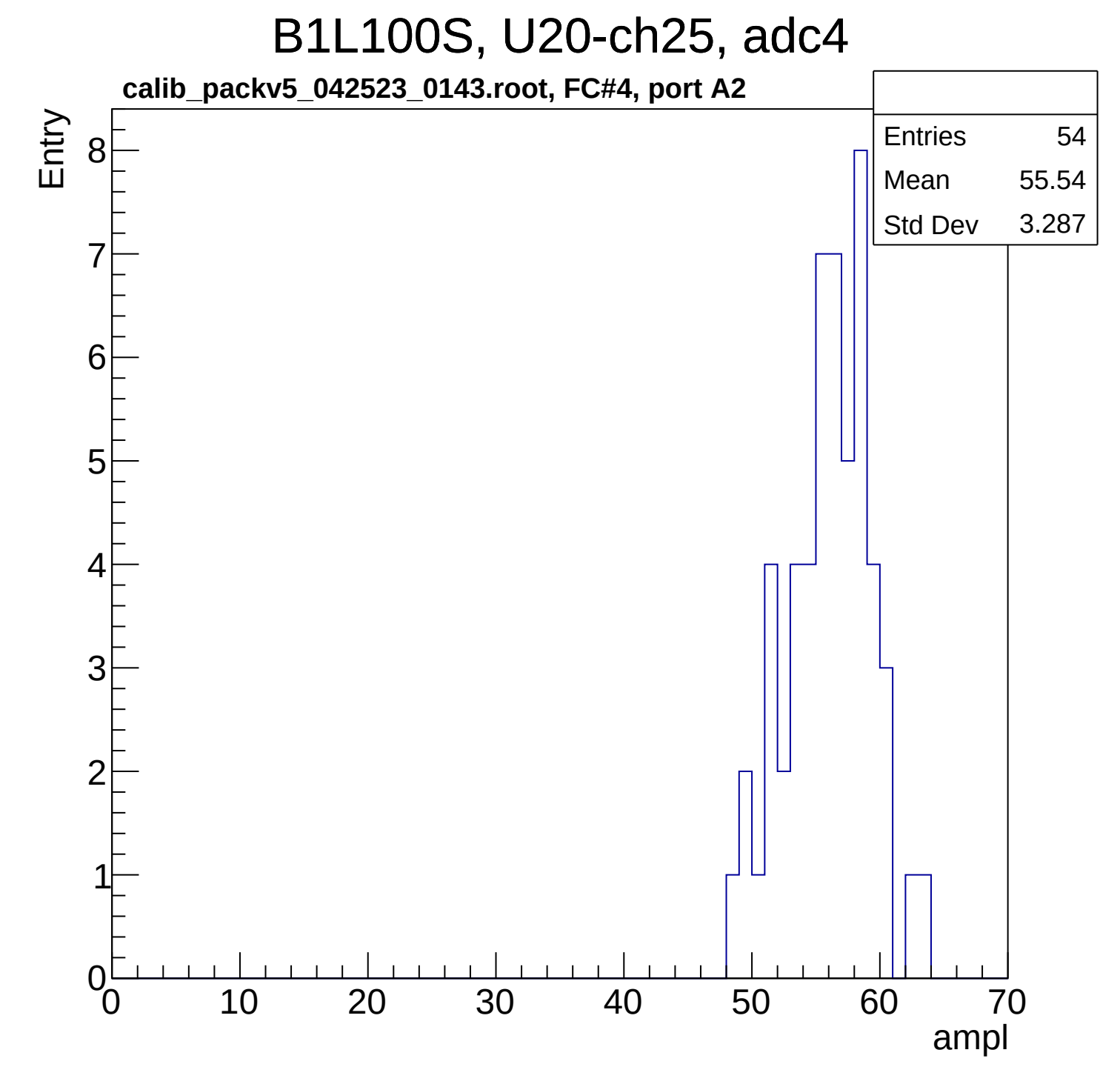
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.54
Std Dev 3.287
Entry
ampl
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3
4
5
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7
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0
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40
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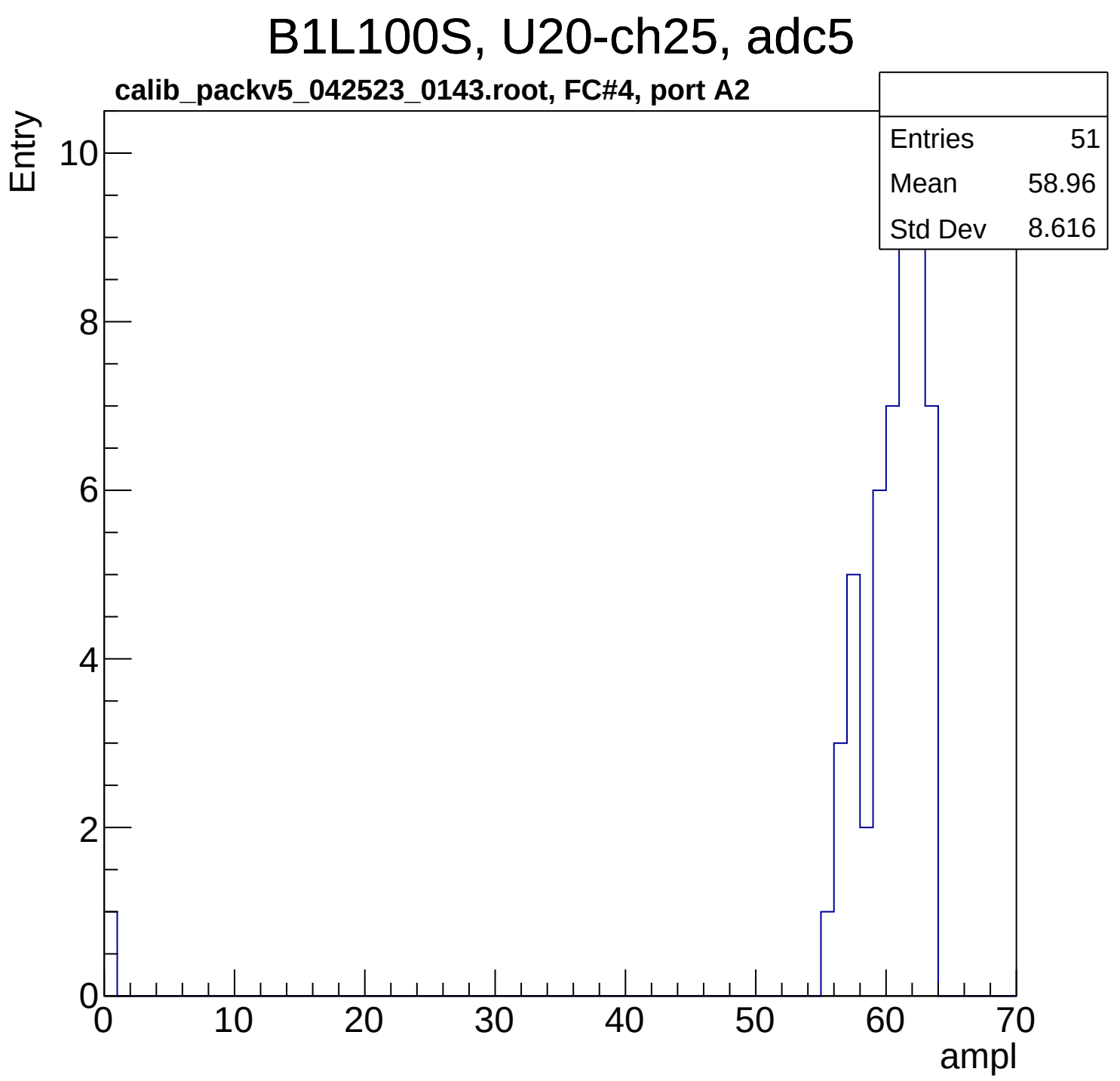
60
70


B1L100S, U20-ch25, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.96
Std Dev 8.616
Entry
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10
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10
20
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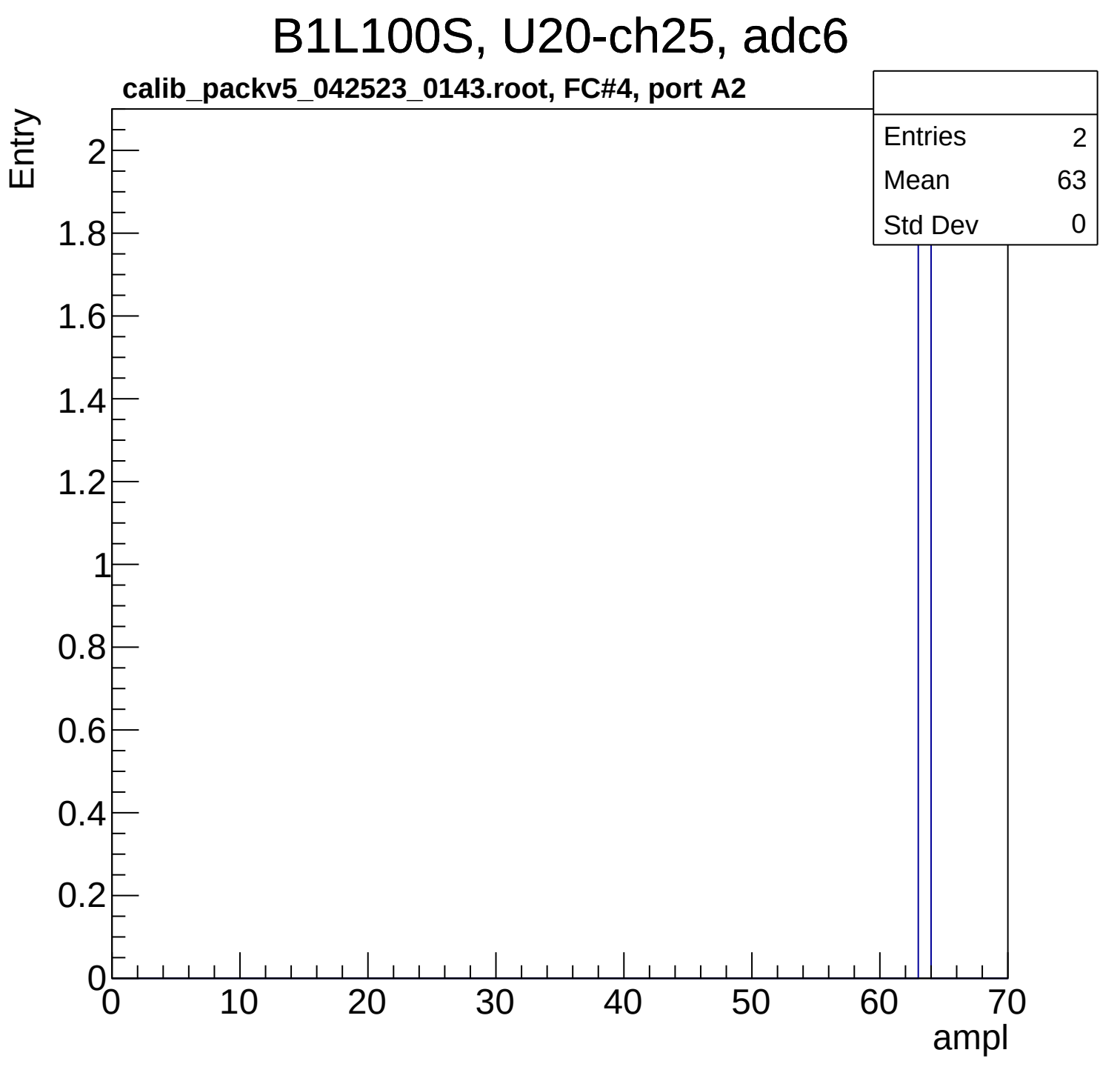
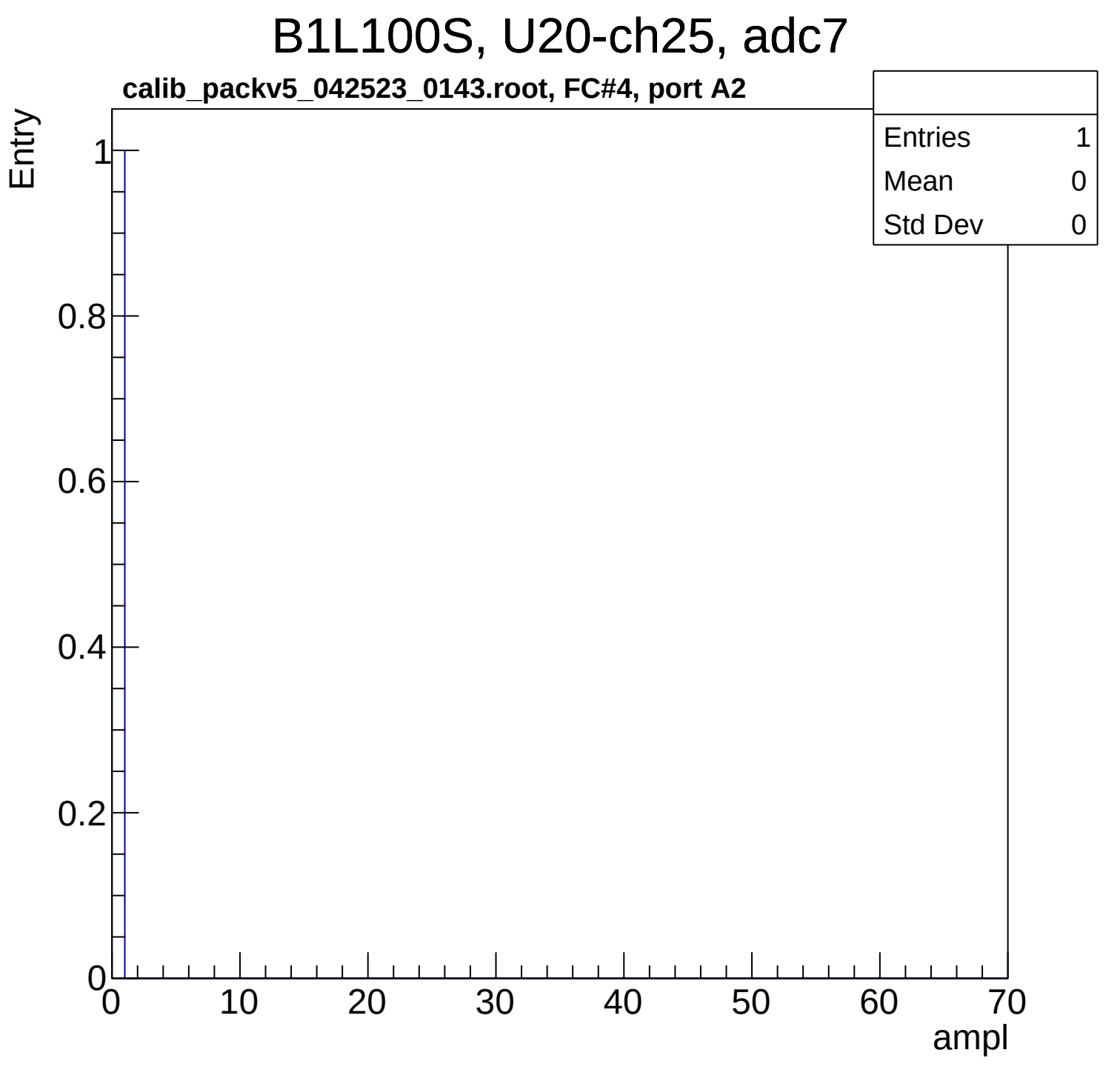
50
60
70
ampl


B1L100S, U20-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch25, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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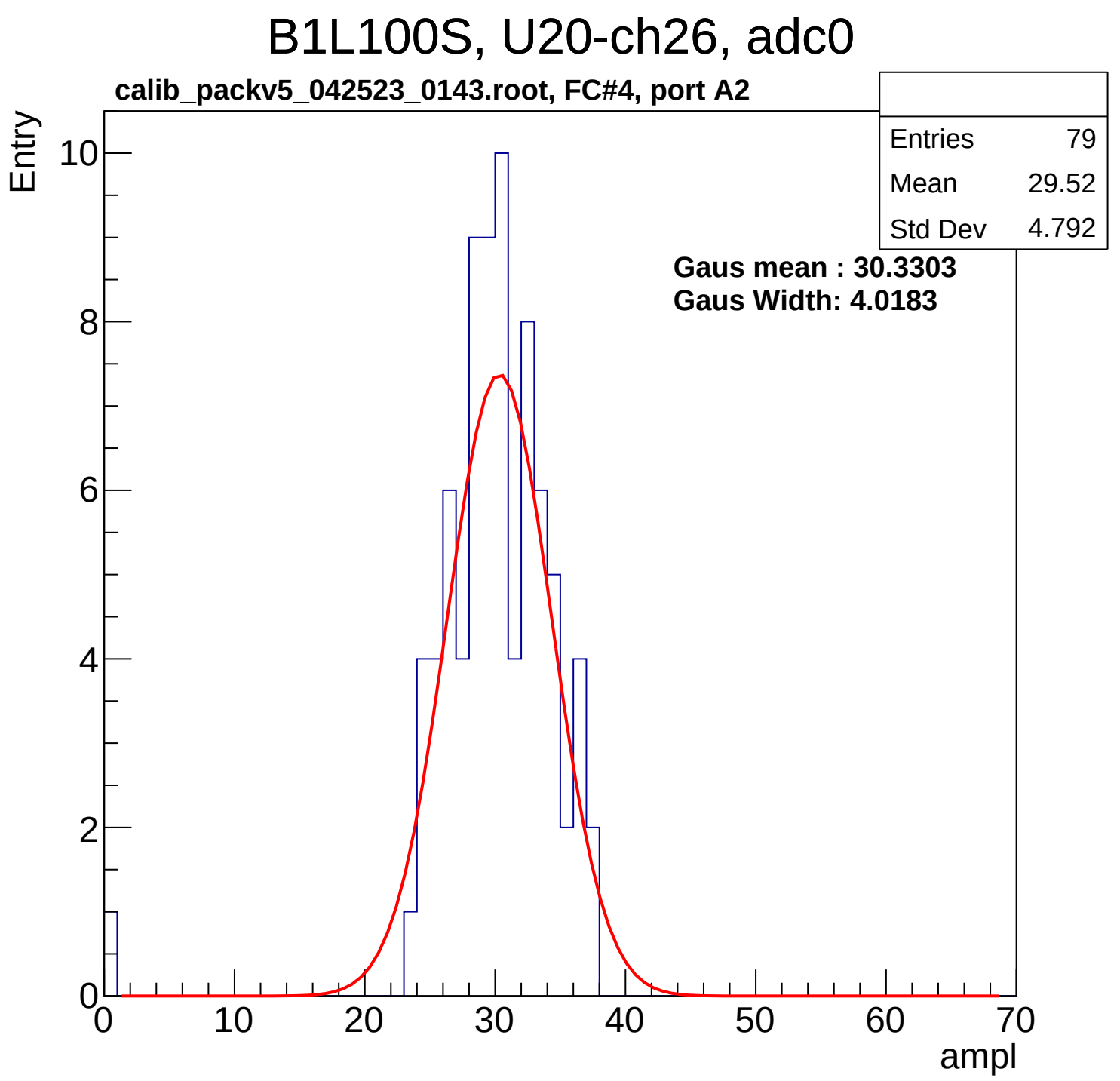
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.52
Std Dev 4.792
Gaus mean : 30.3303
Gaus Width: 4.0183
Entry
ampl
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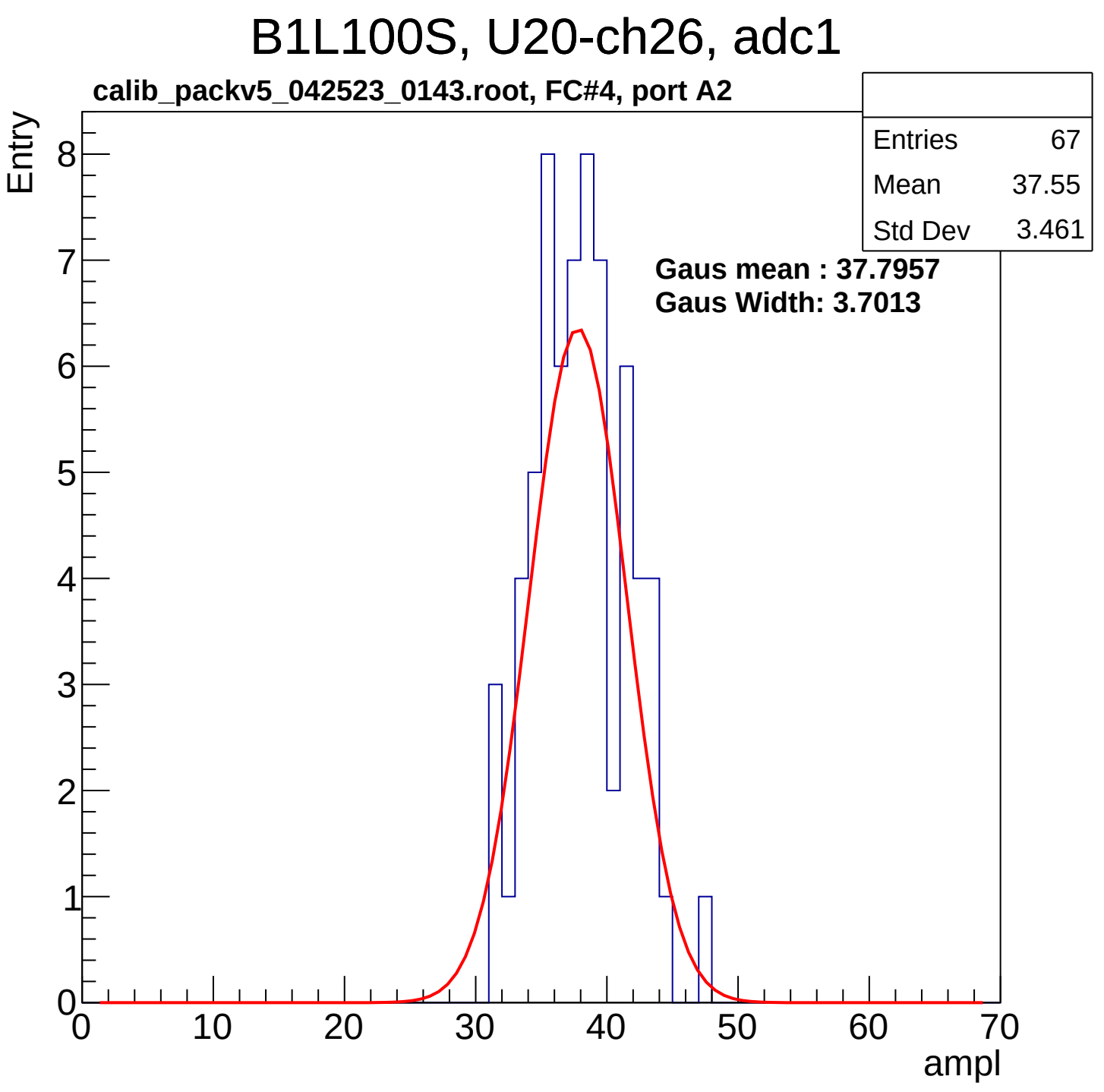
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.55
Std Dev 3.461
Gaus mean : 37.7957
Gaus Width: 3.7013
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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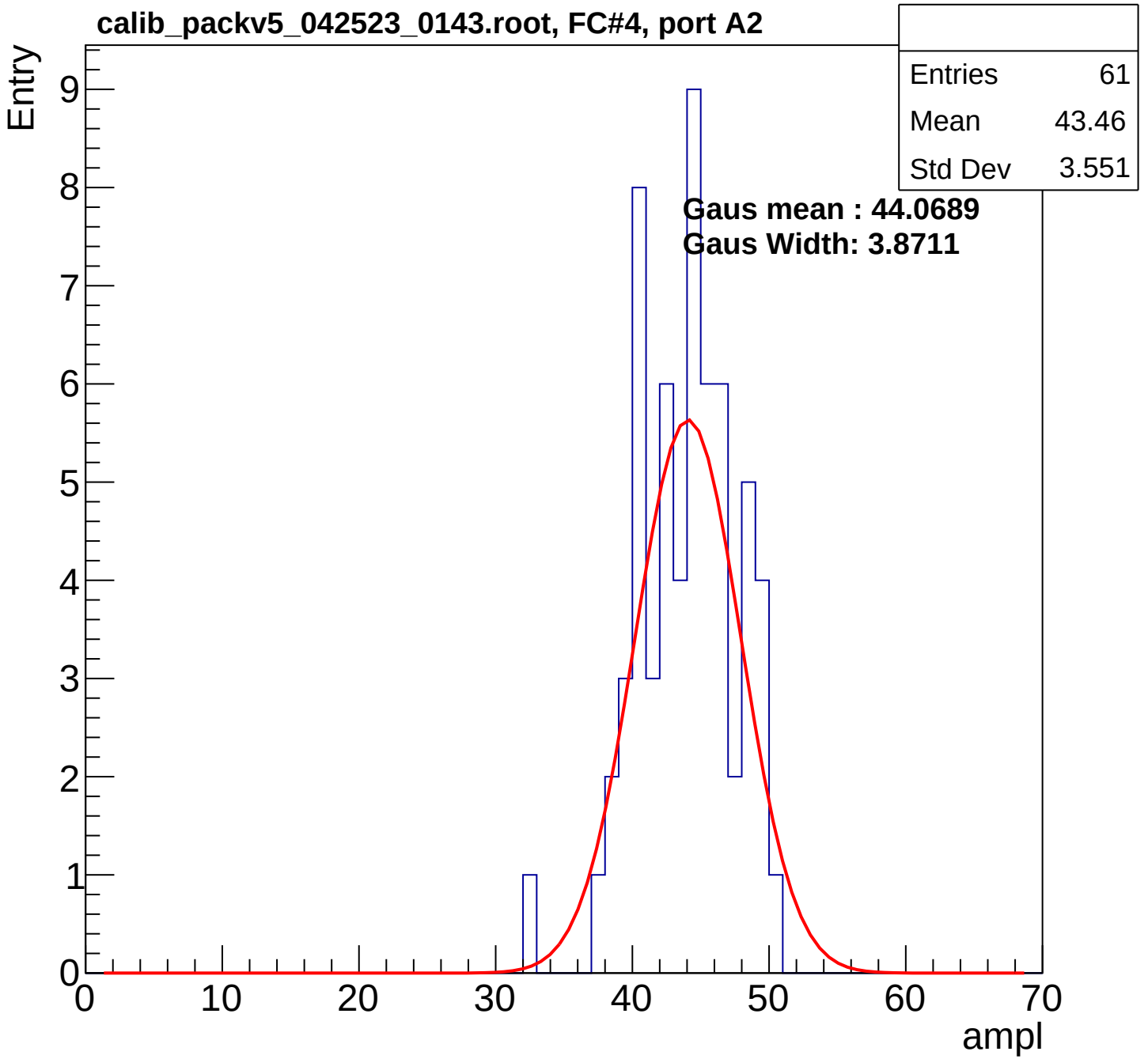
B1L100S, U20-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 43.46
Std Dev 3.551
Gaus mean : 44.0689
Gaus Width: 3.8711
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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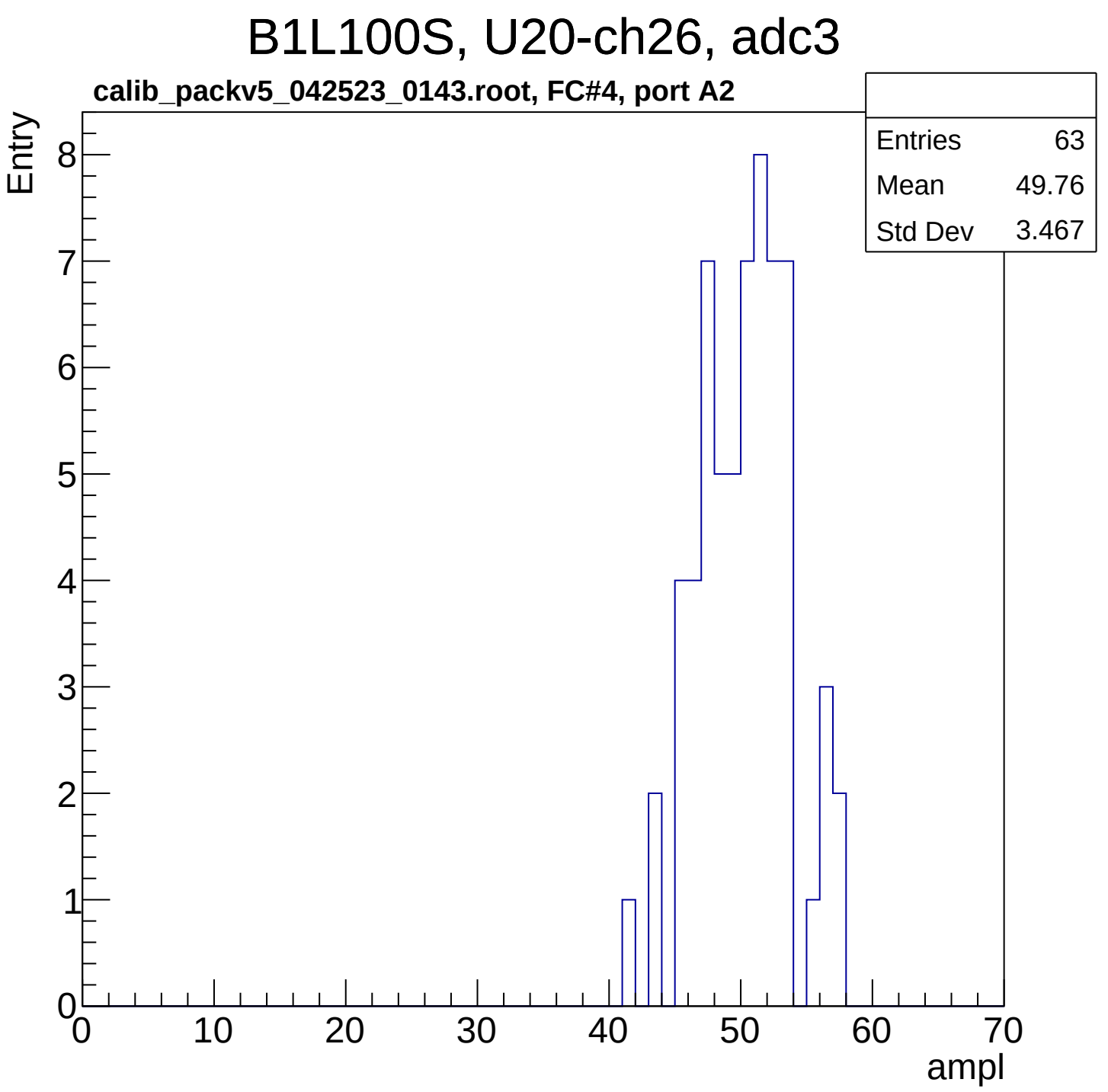
B1L100S, U20-ch26, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.76
Std Dev 3.467
Entry
ampl
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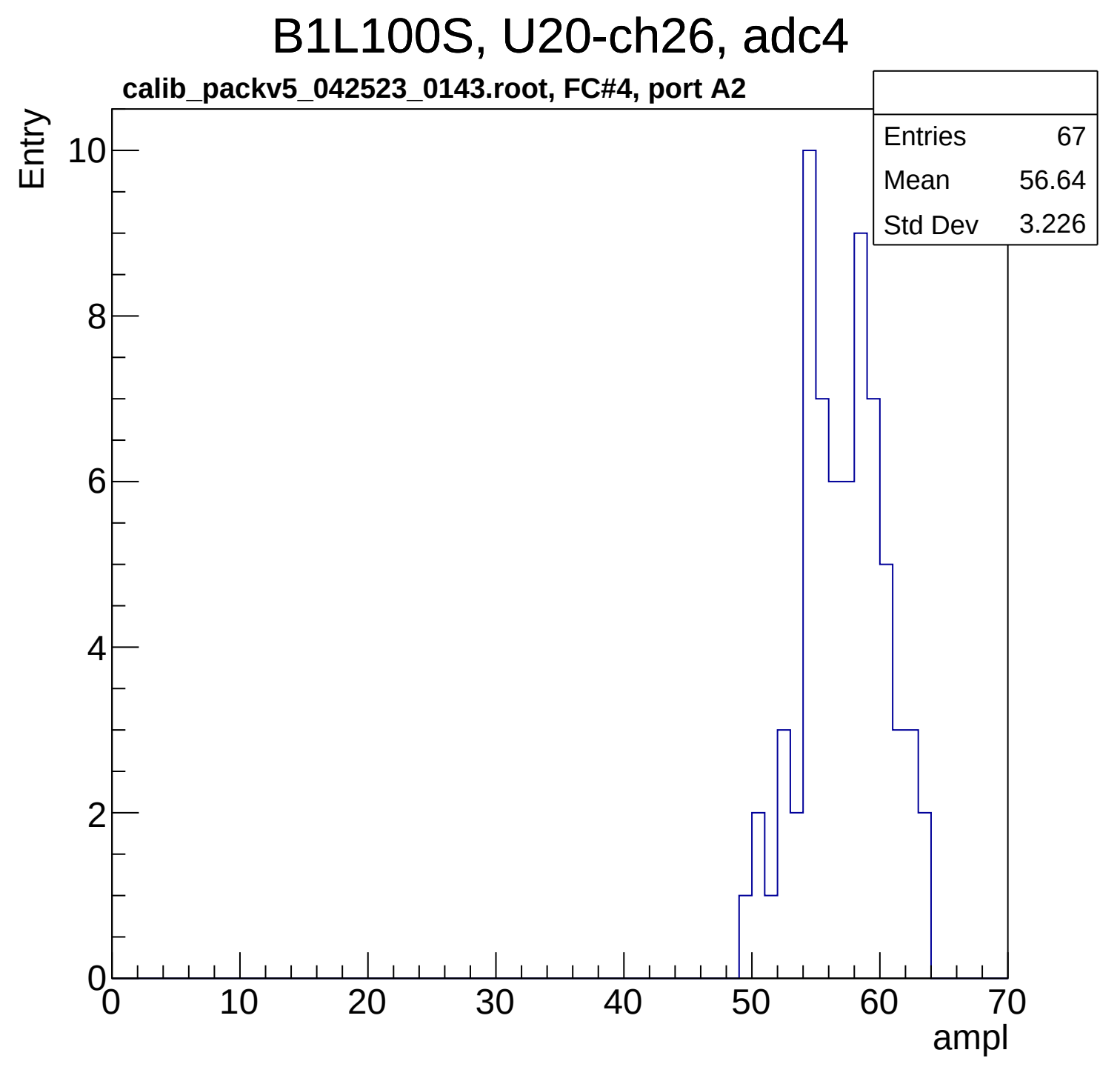
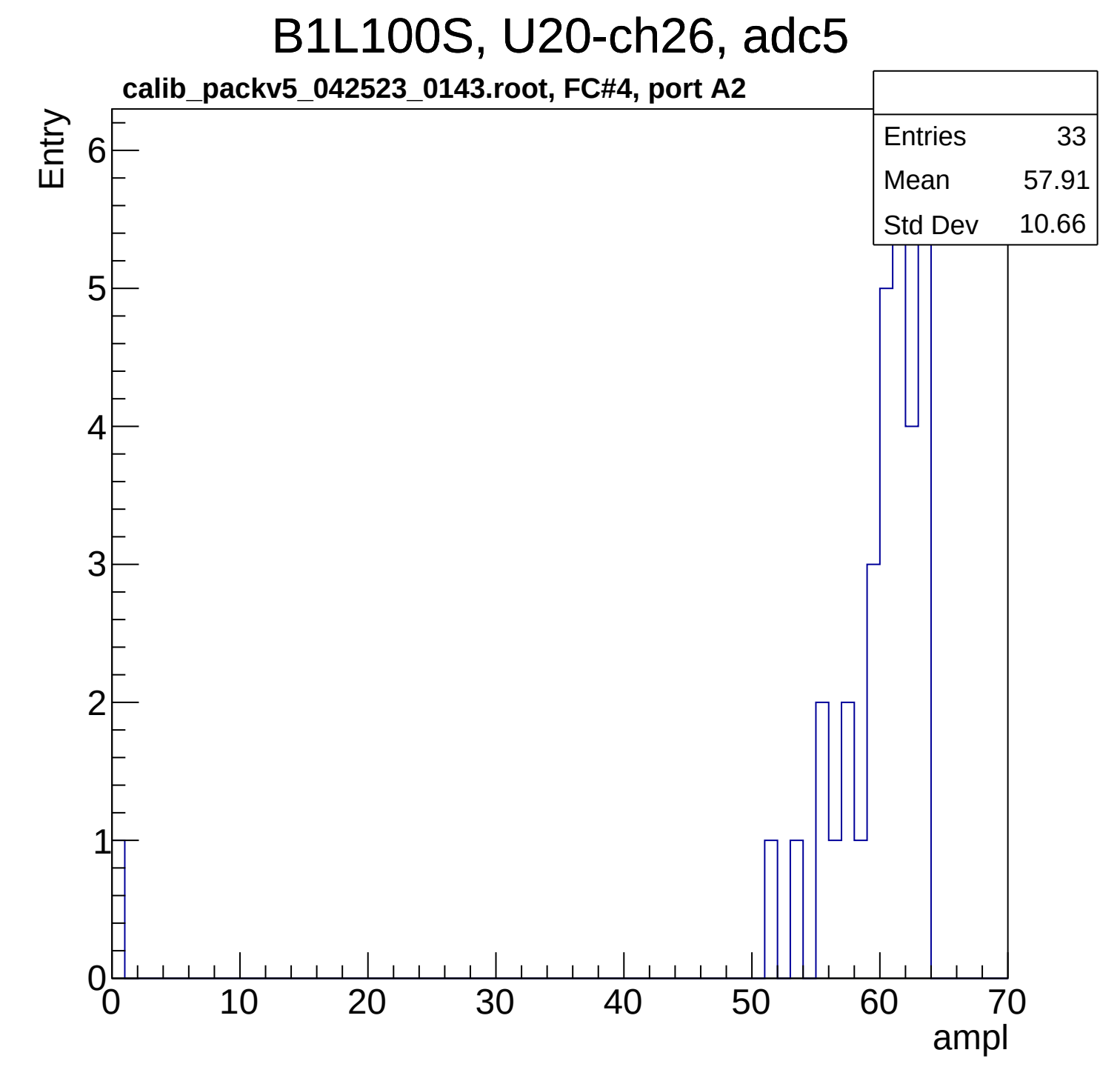
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.64
Std Dev 3.226
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch26, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 57.91
Std Dev 10.66
Entry
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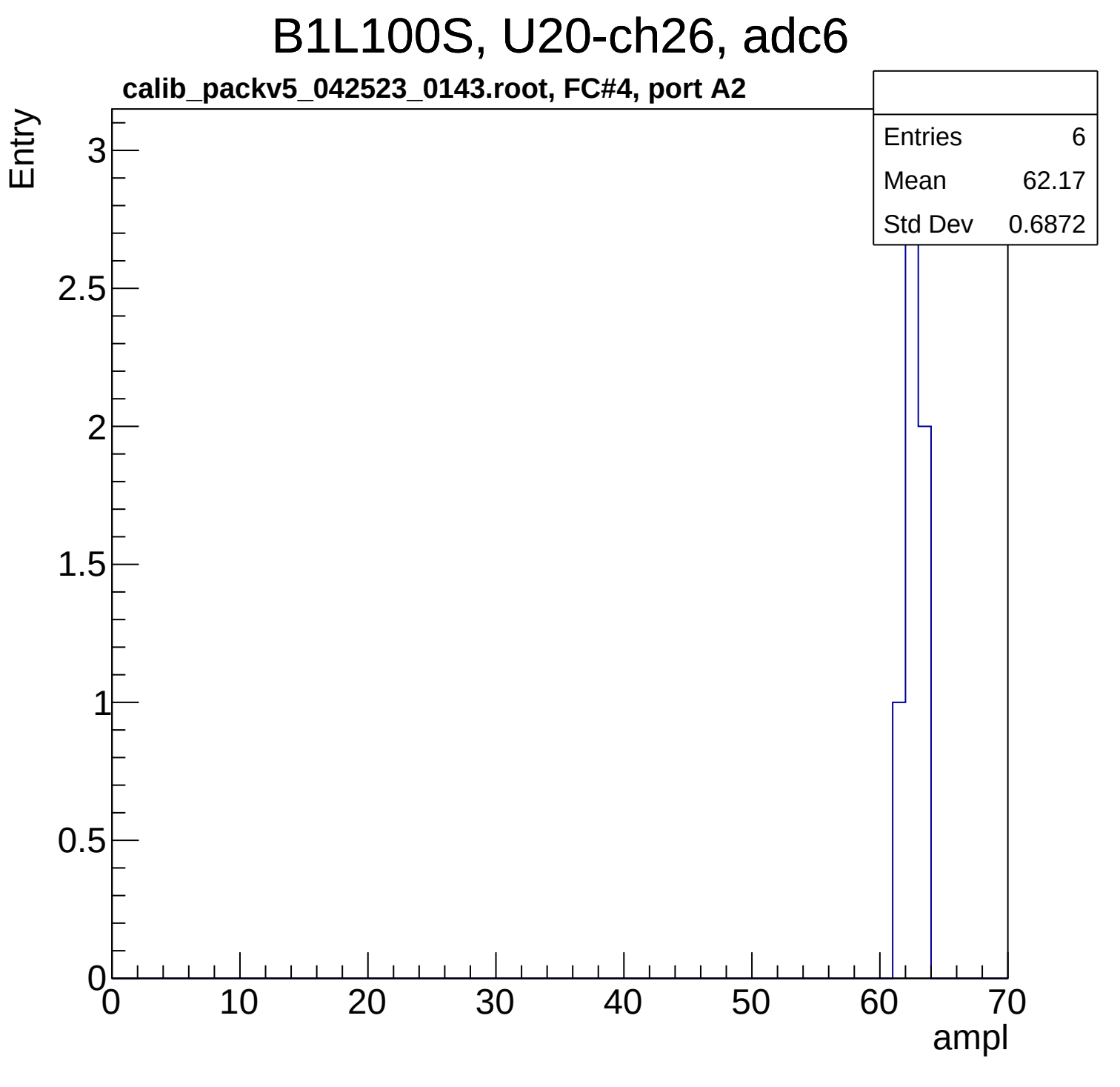
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
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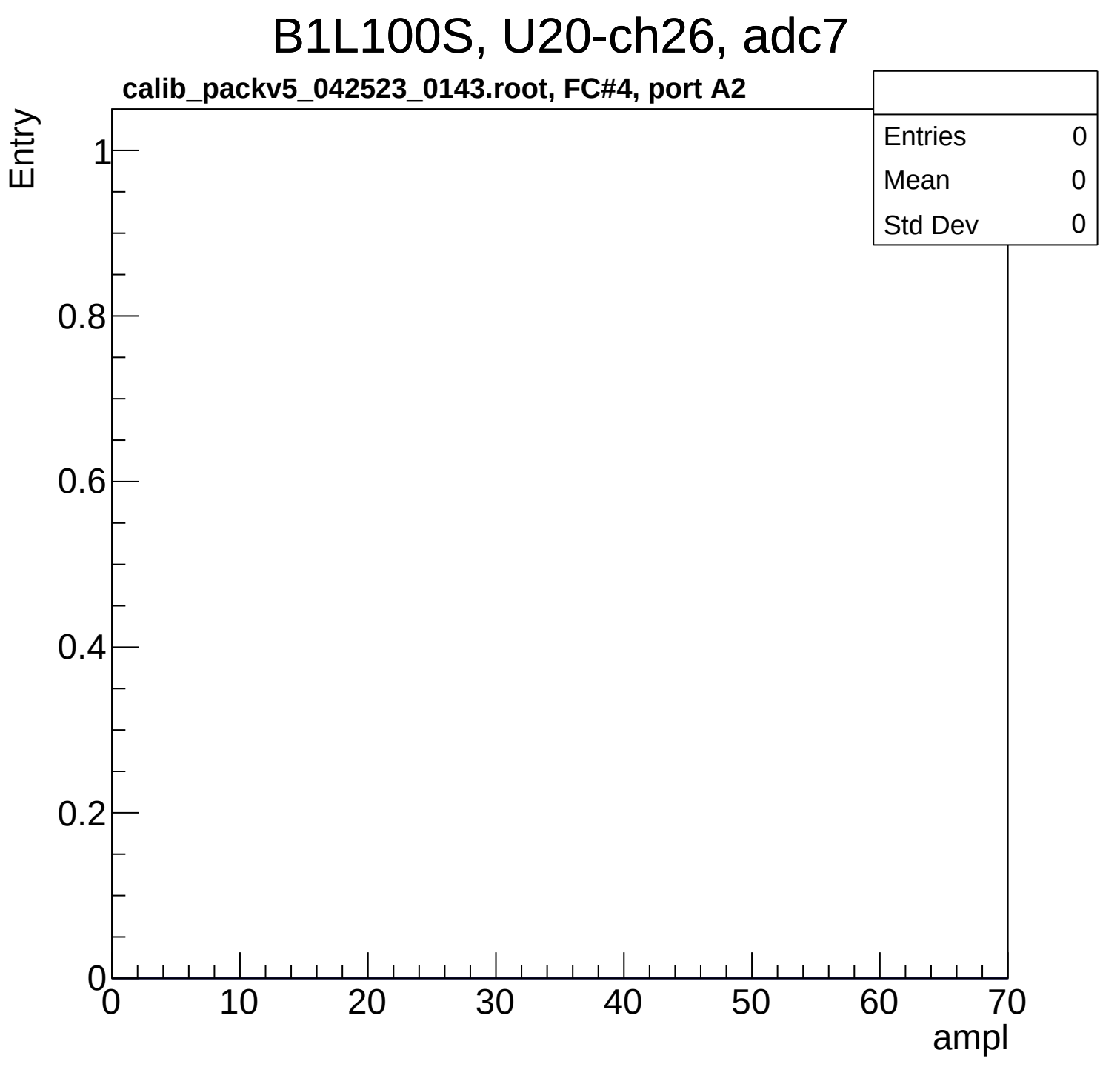
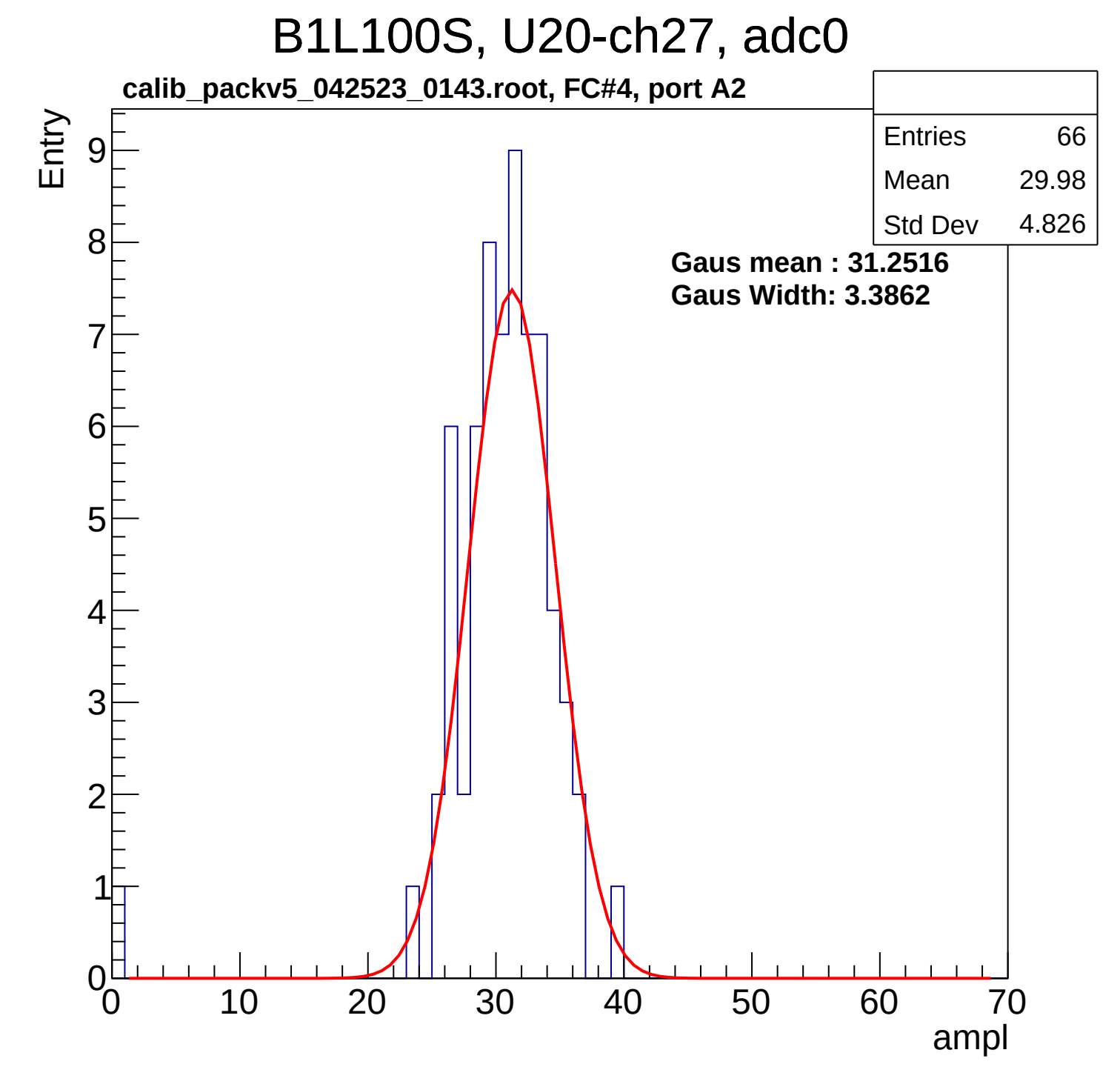
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch26, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.98
Std Dev 4.826
Gaus mean : 31.2516
Gaus Width: 3.3862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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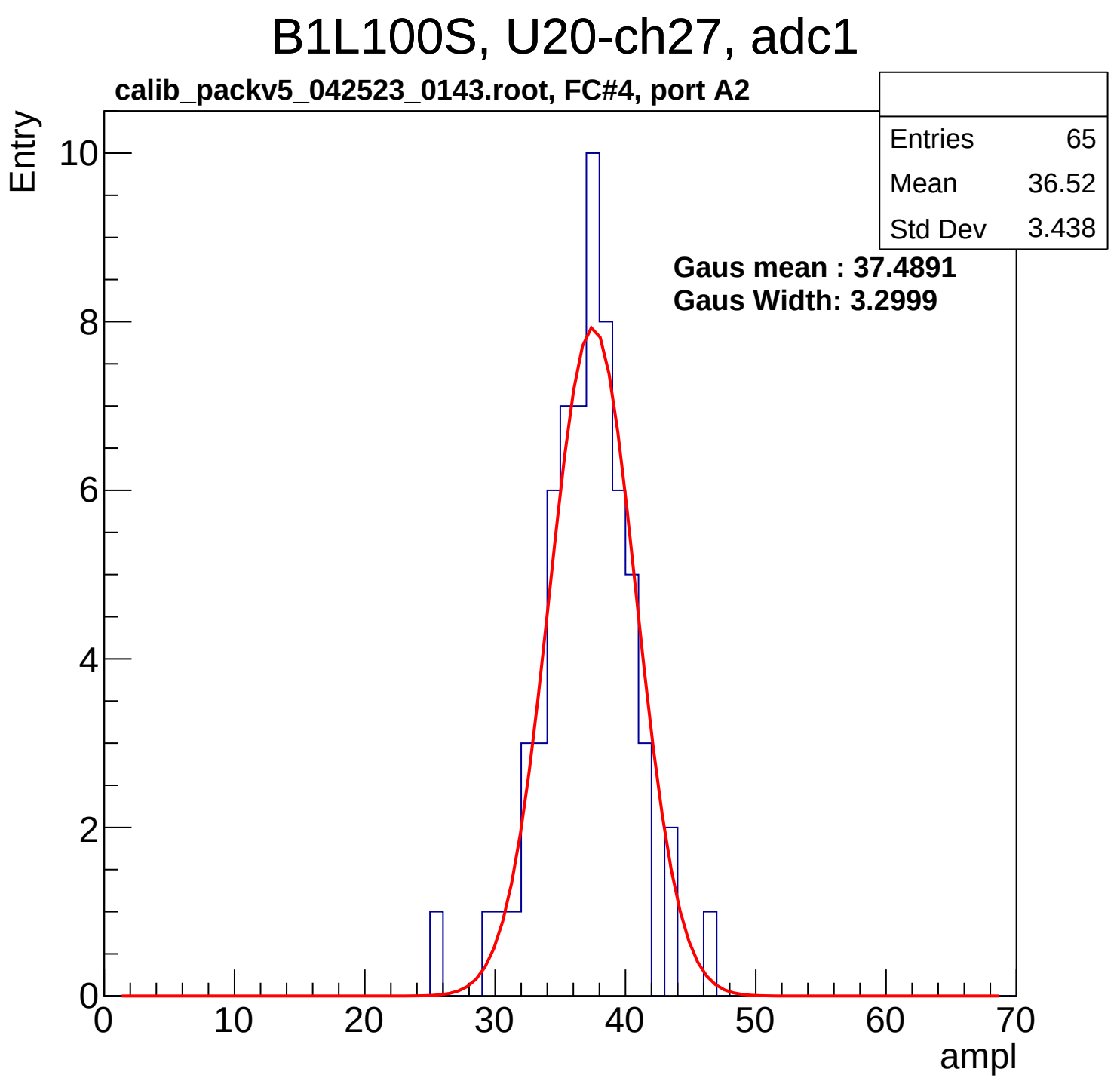
B1L100S, U20-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.52
Std Dev 3.438
Gaus mean : 37.4891
Gaus Width: 3.2999
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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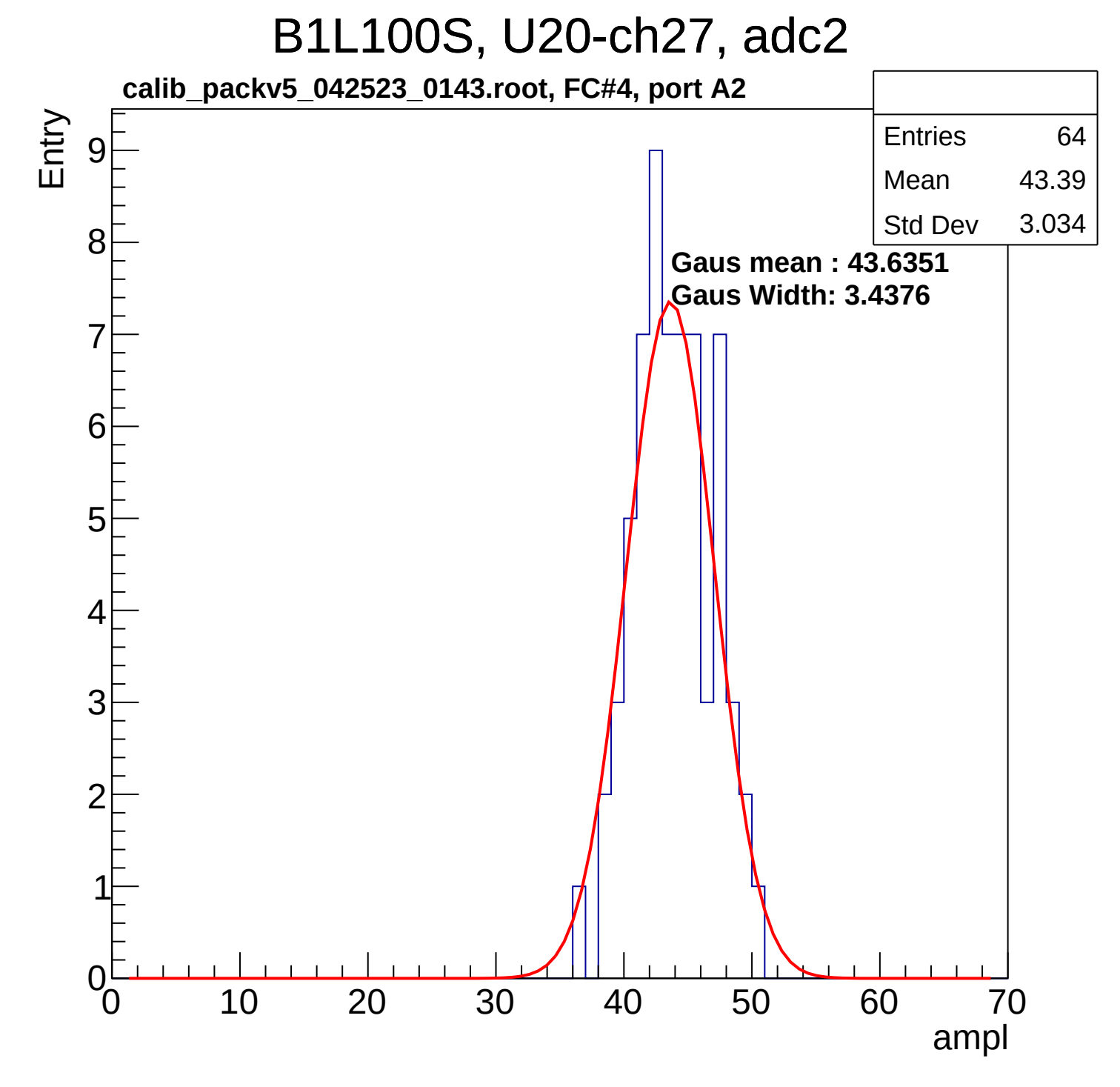
B1L100S, U20-ch27, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.39
Std Dev 3.034
Gaus mean : 43.6351
Gaus Width: 3.4376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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30
40
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60
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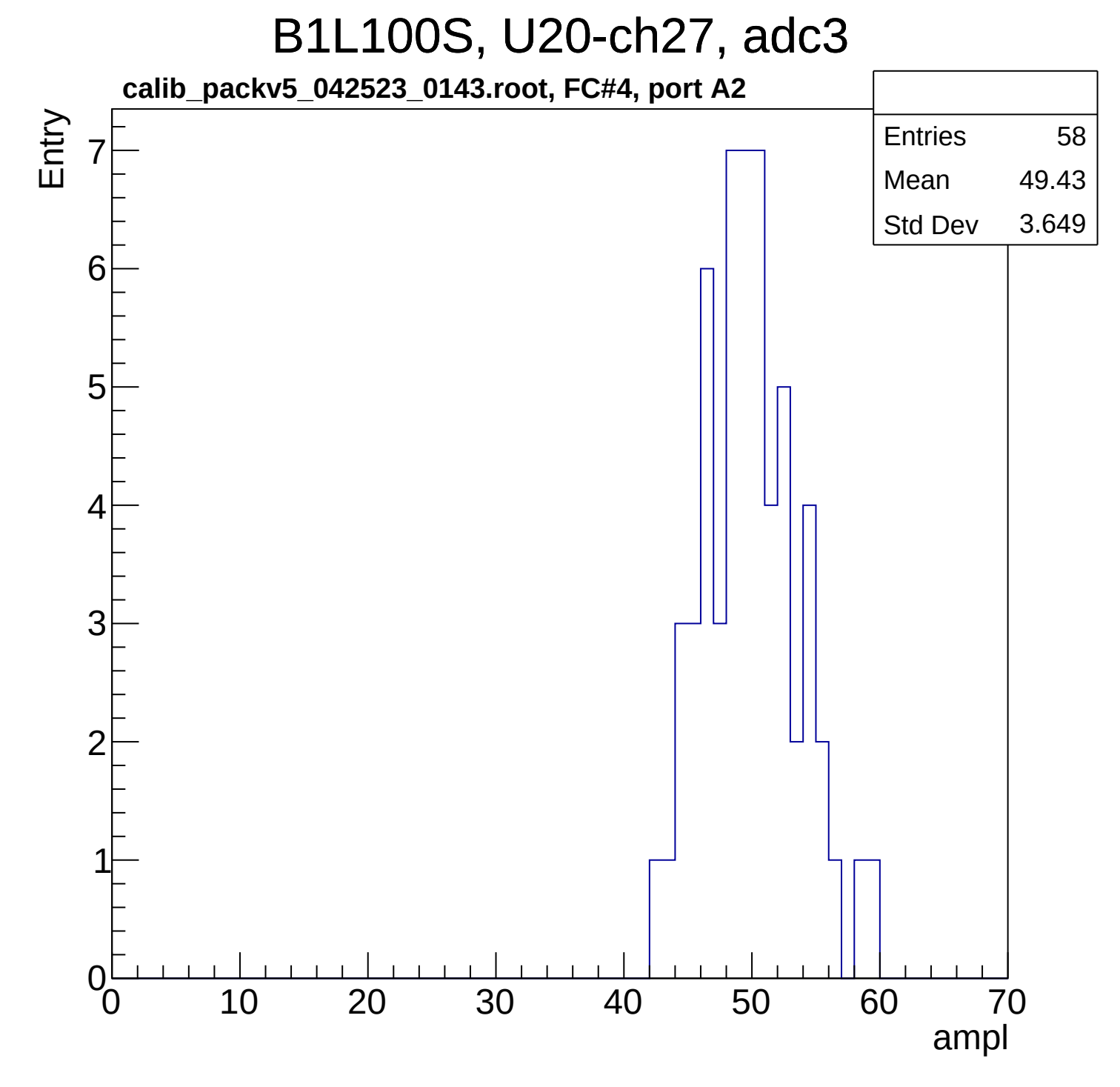
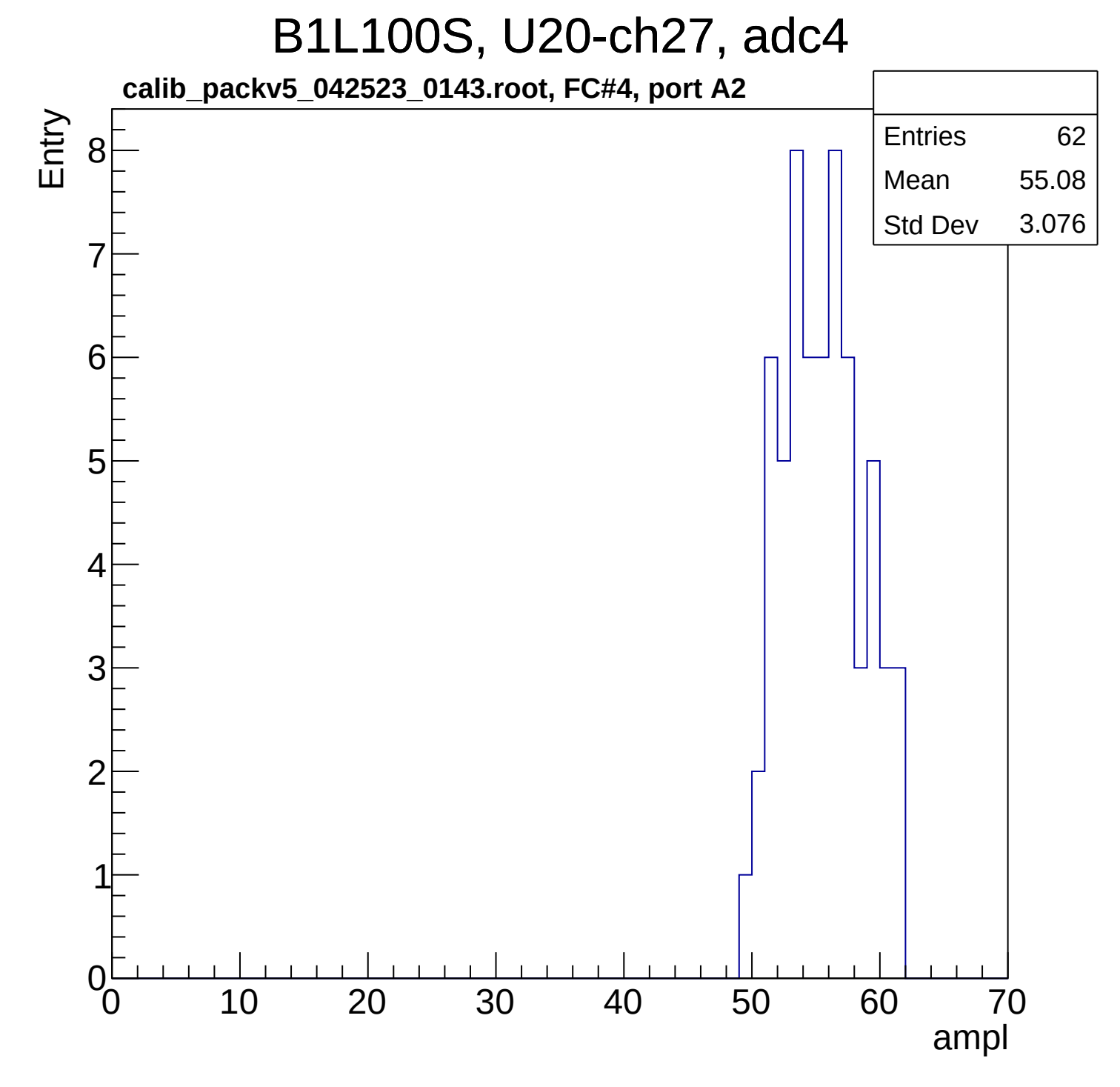

B1L100S, U20-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.43
Std Dev 3.649
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 55.08
Std Dev 3.076
Entry
ampl
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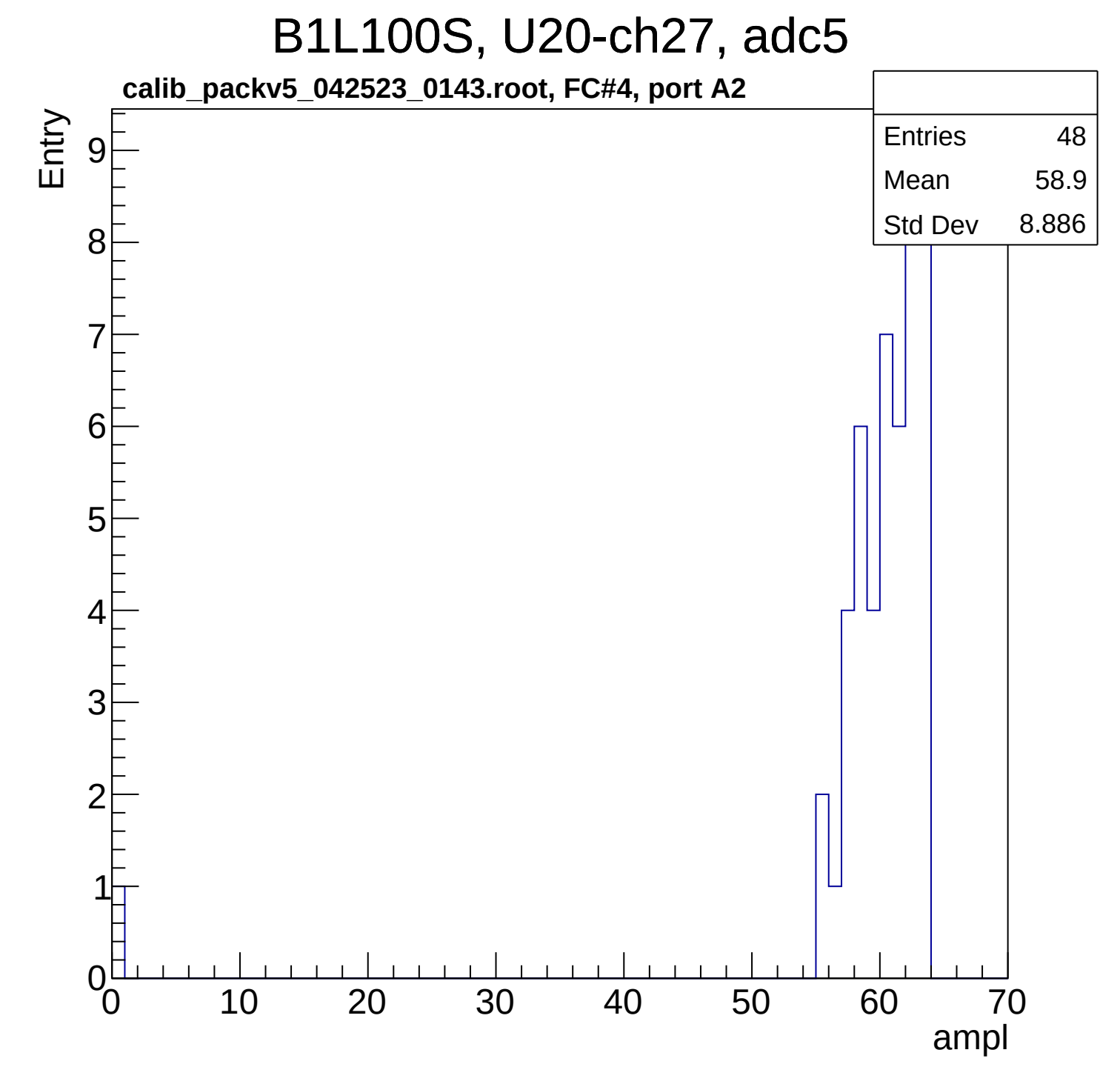
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.9
Std Dev 8.886
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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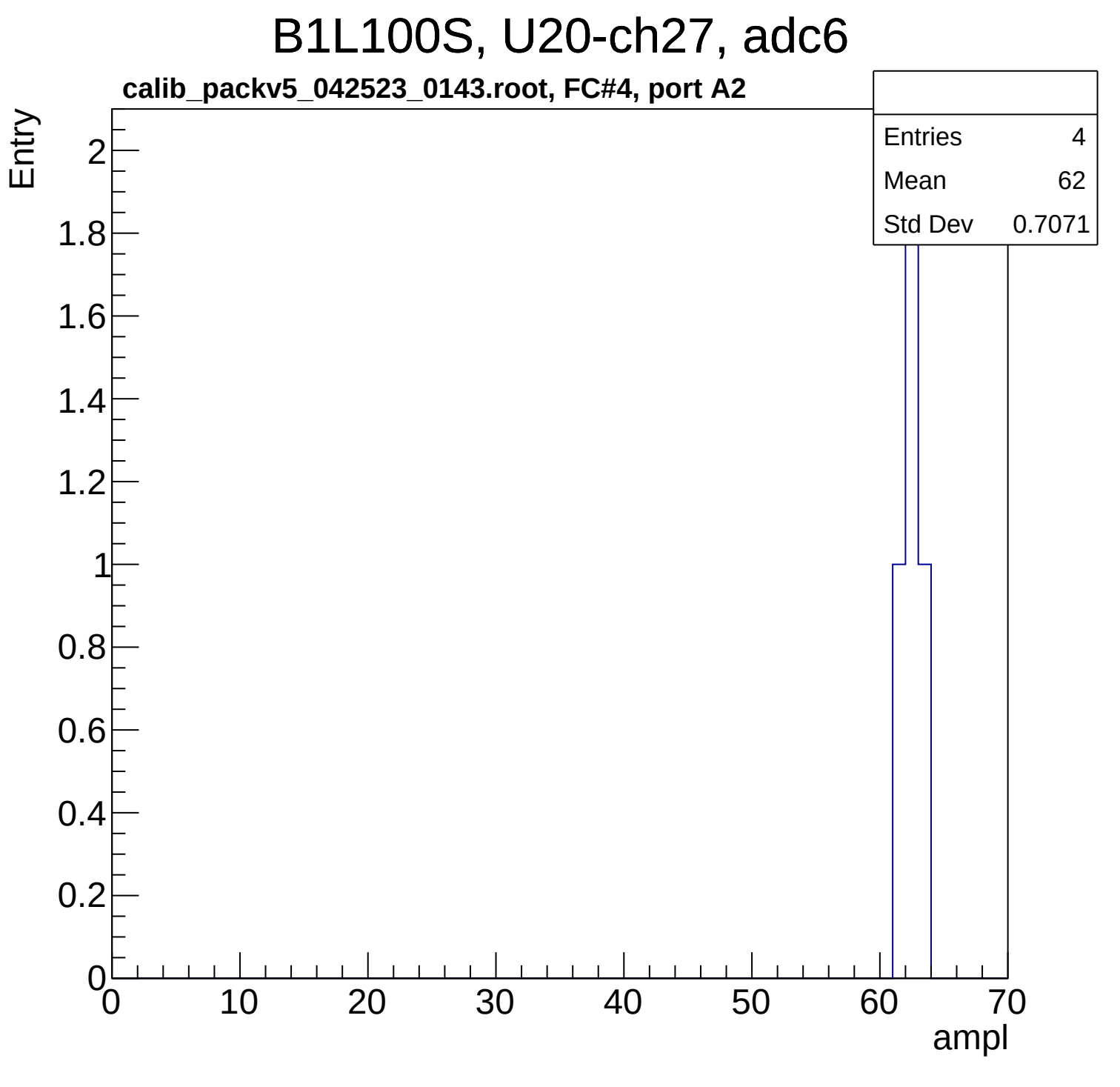
40
50
60
70


B1L100S, U20-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 0.7071
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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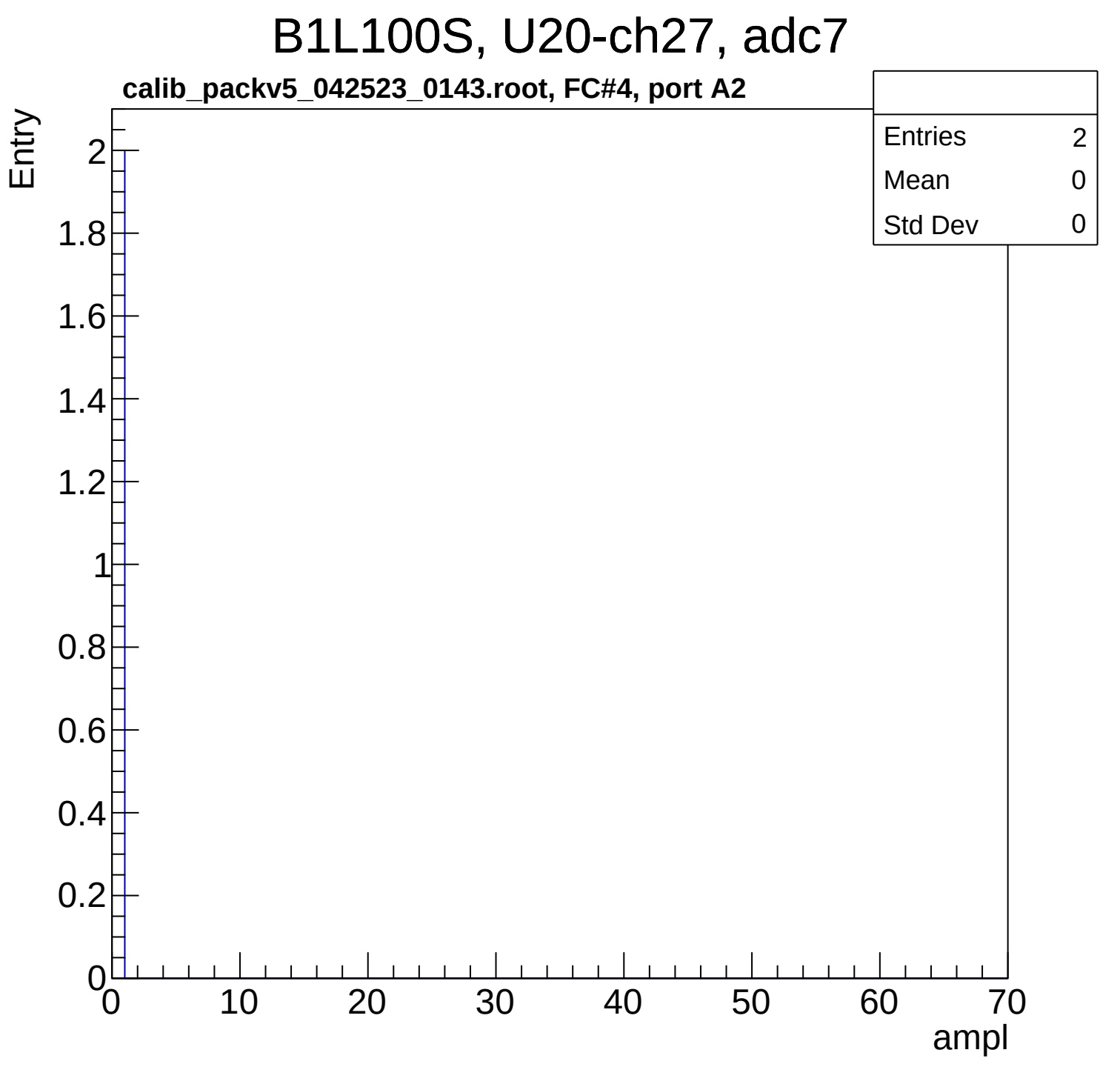
B1L100S, U20-ch27, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
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0.2
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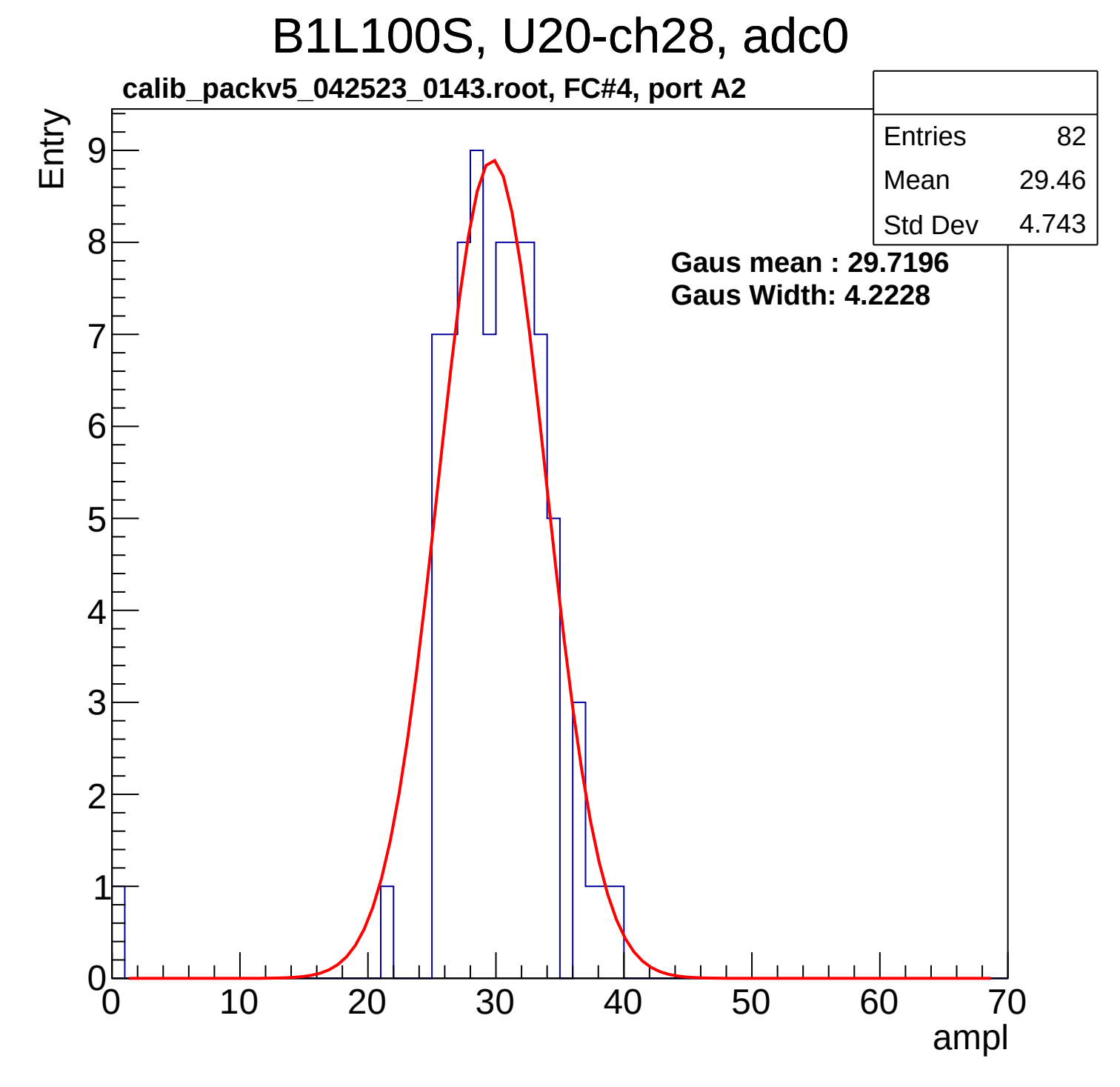
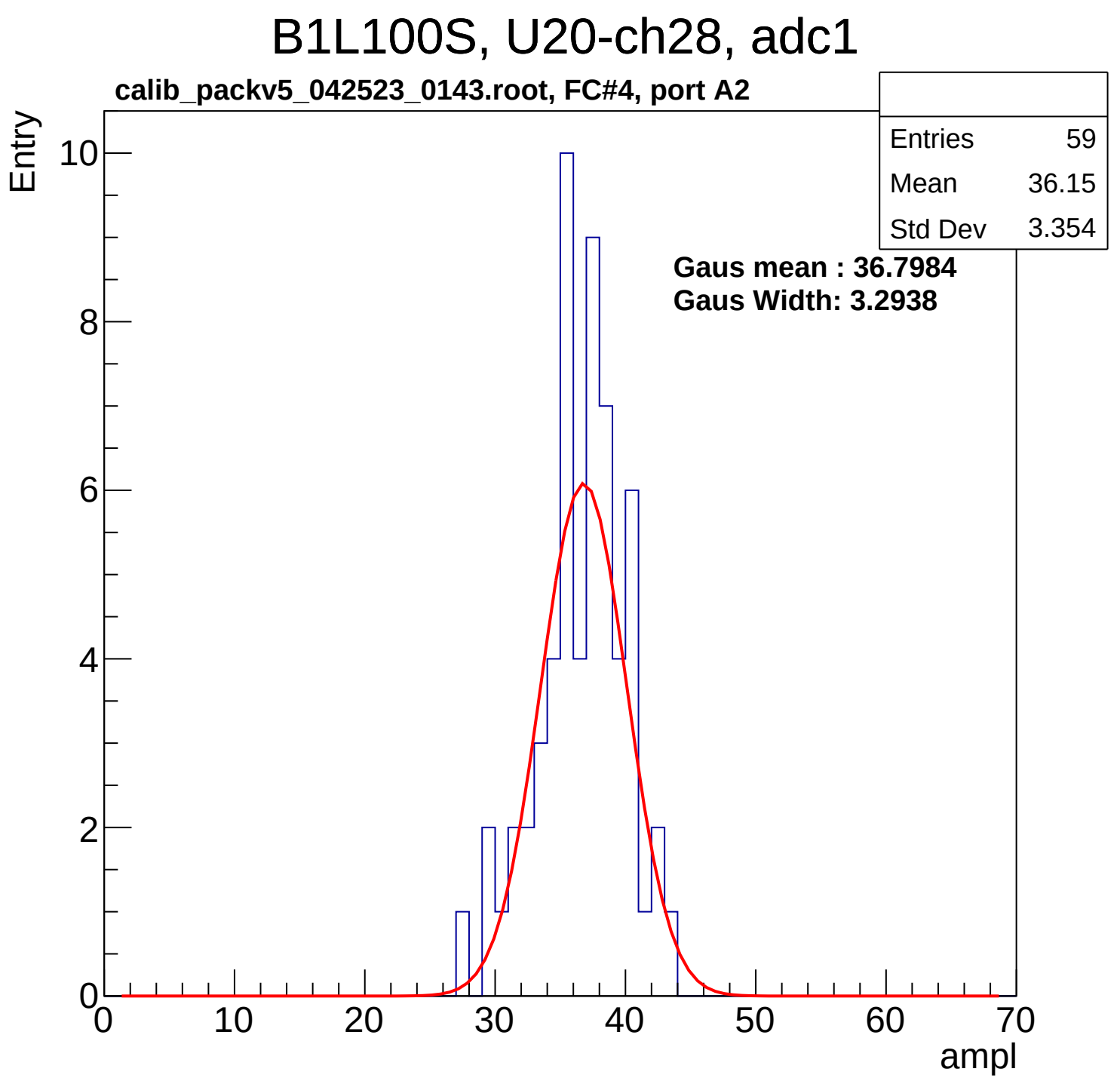
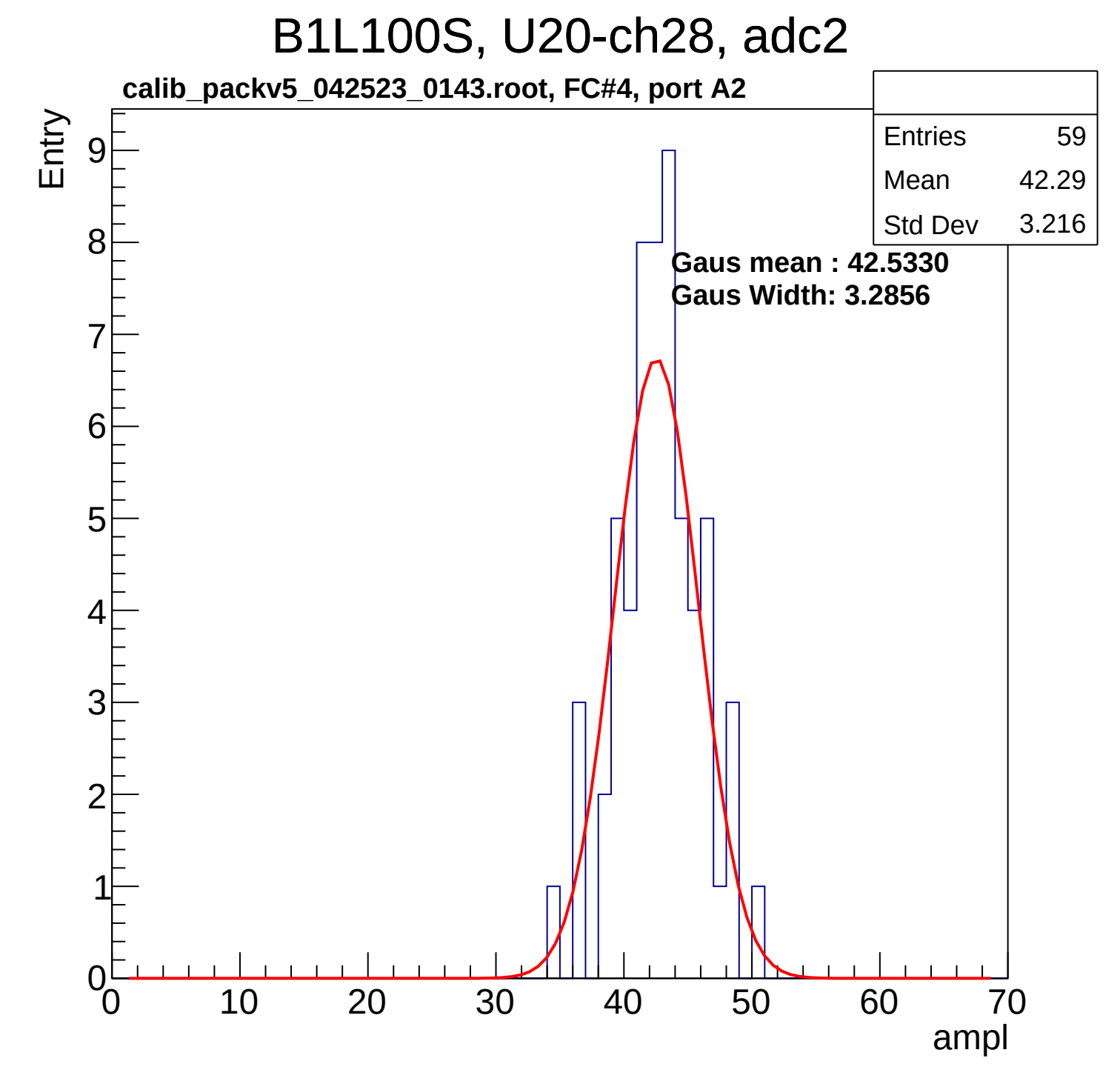
50
60
70
ampl


B1L100S, U20-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 29.46
Std Dev 4.743
Gaus mean : 29.7196
Gaus Width: 4.2228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 36.15
Std Dev 3.354
Gaus mean : 36.7984
Gaus Width: 3.2938
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.29
Std Dev 3.216
Gaus mean : 42.5330
Gaus Width: 3.2856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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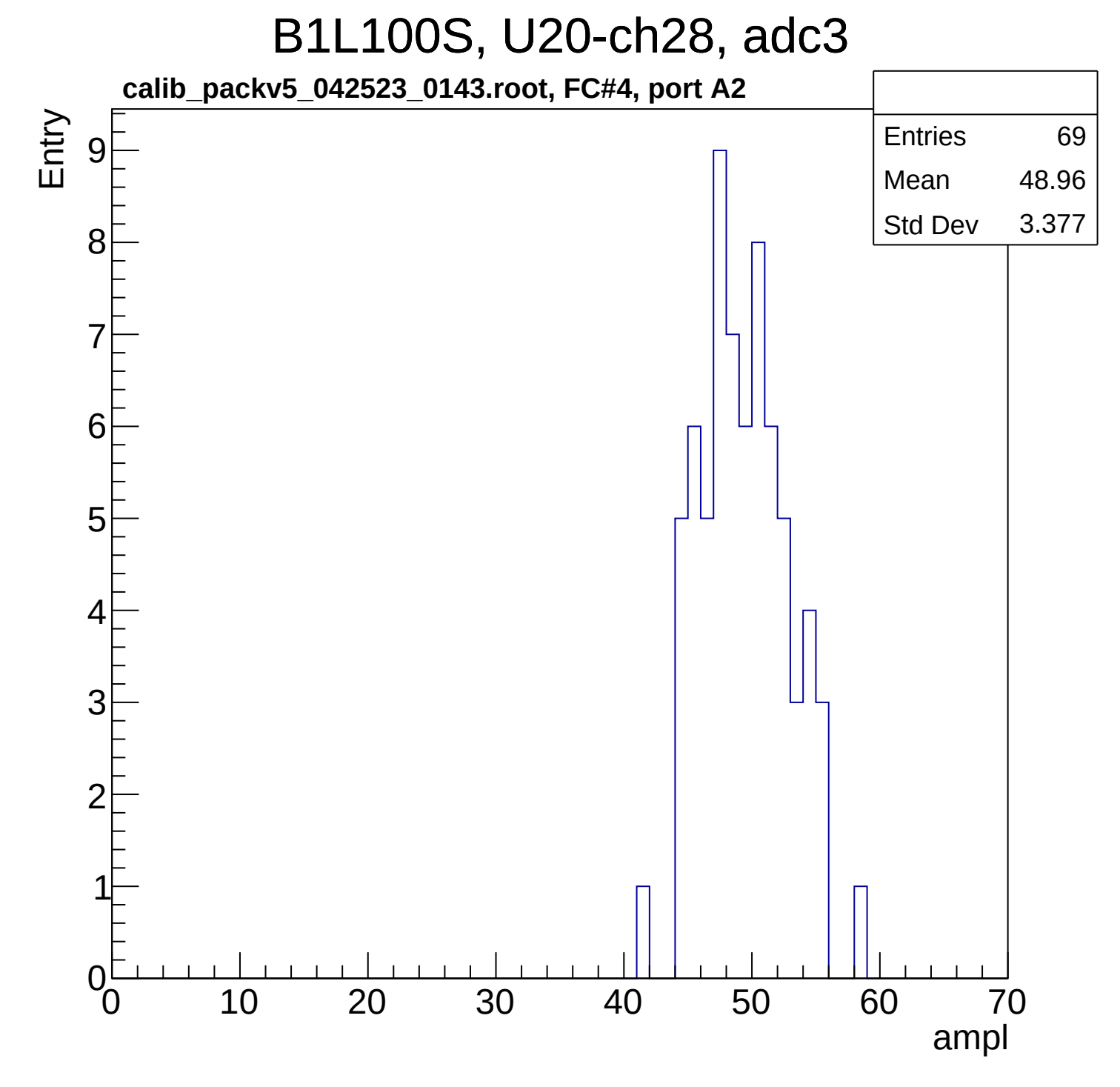
B1L100S, U20-ch28, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.96
Std Dev 3.377
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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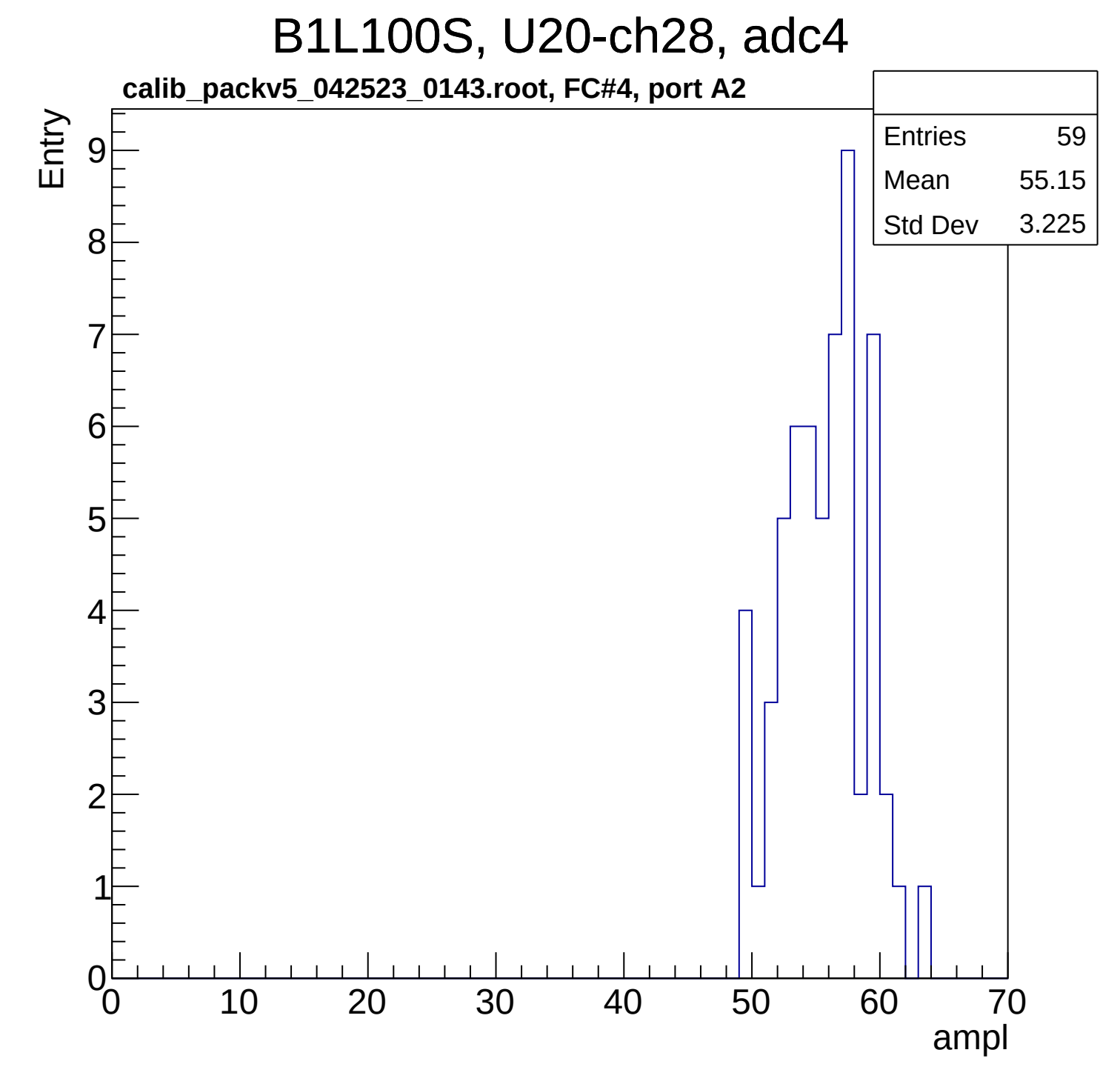
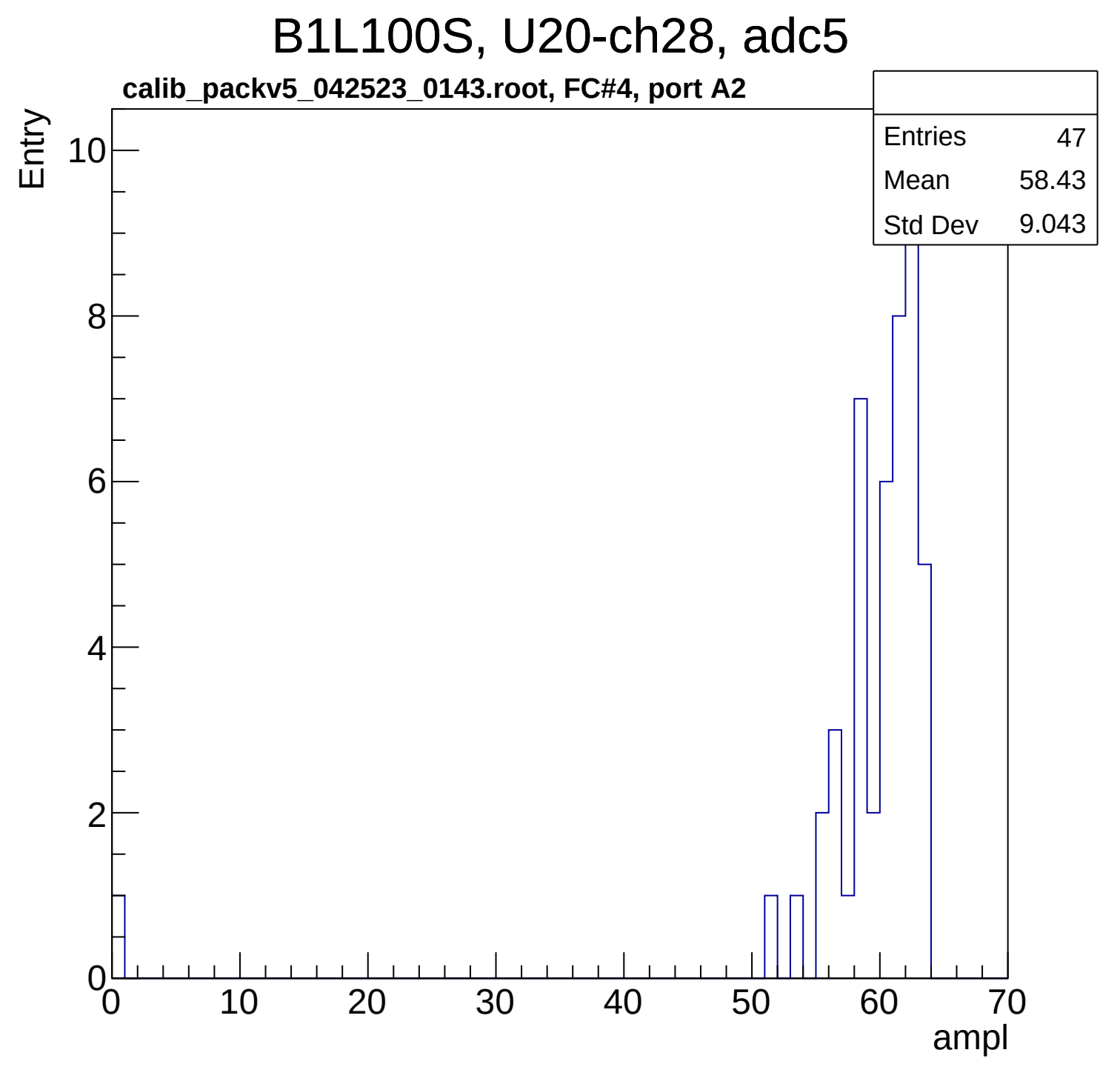
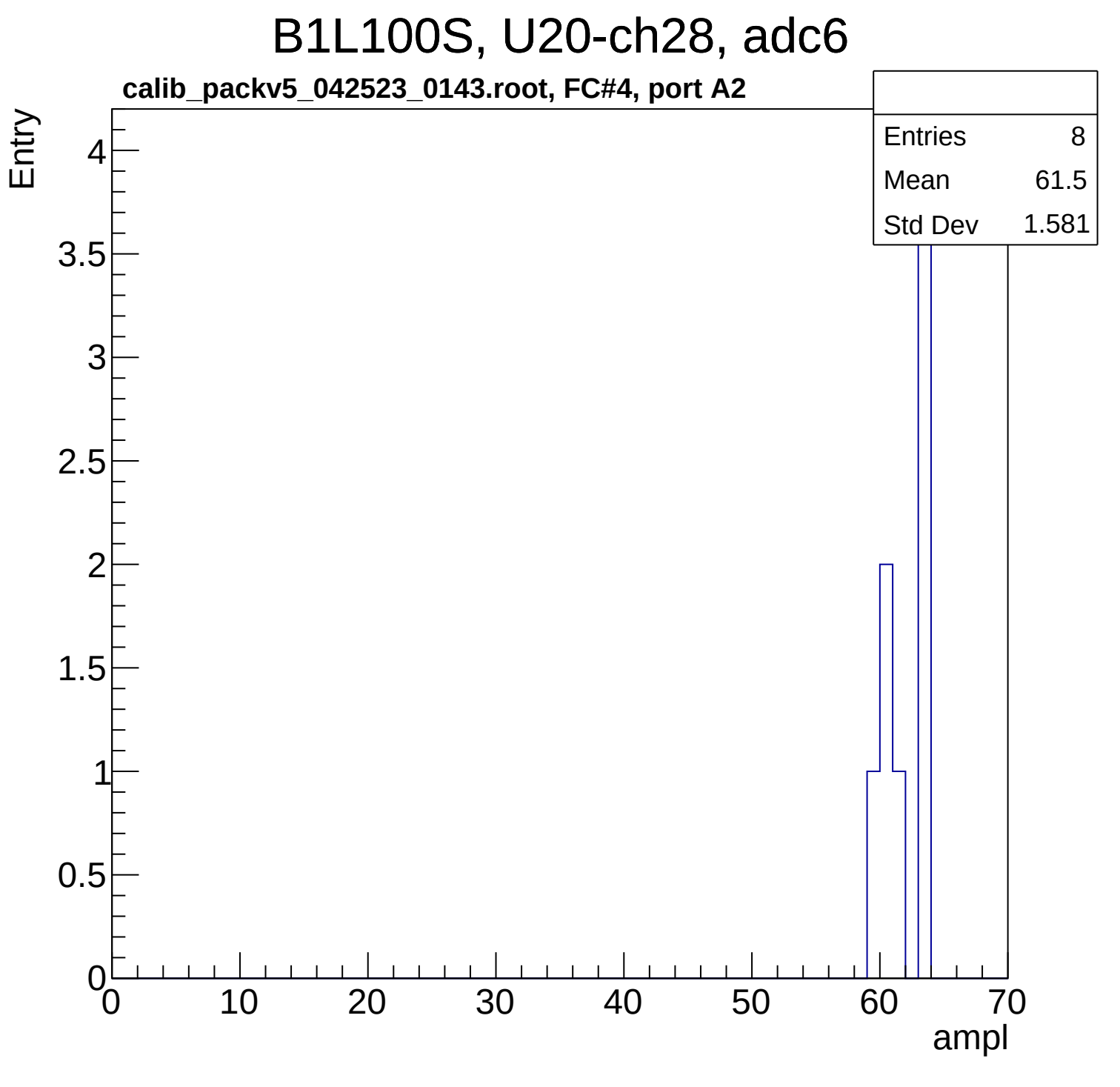
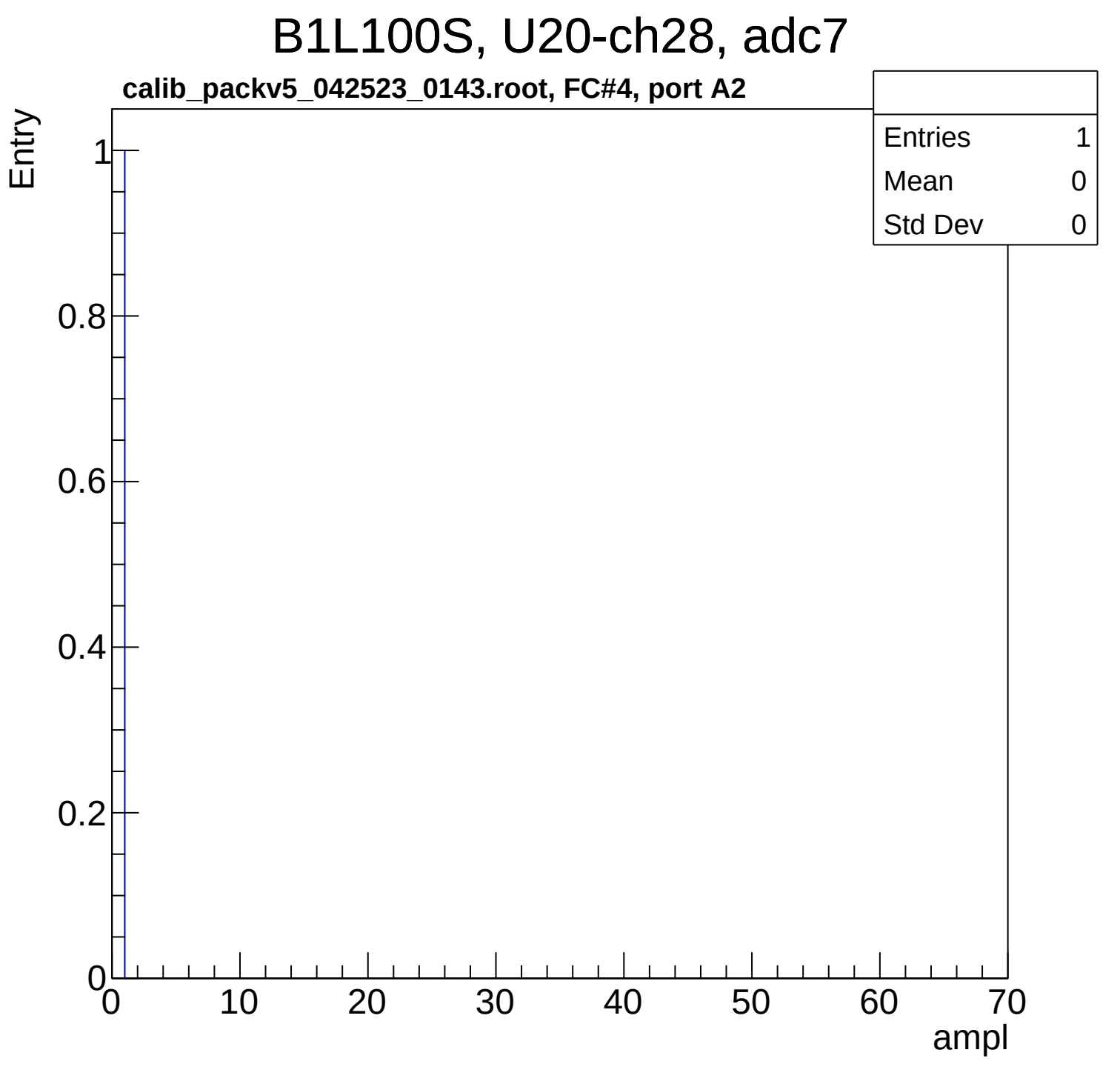
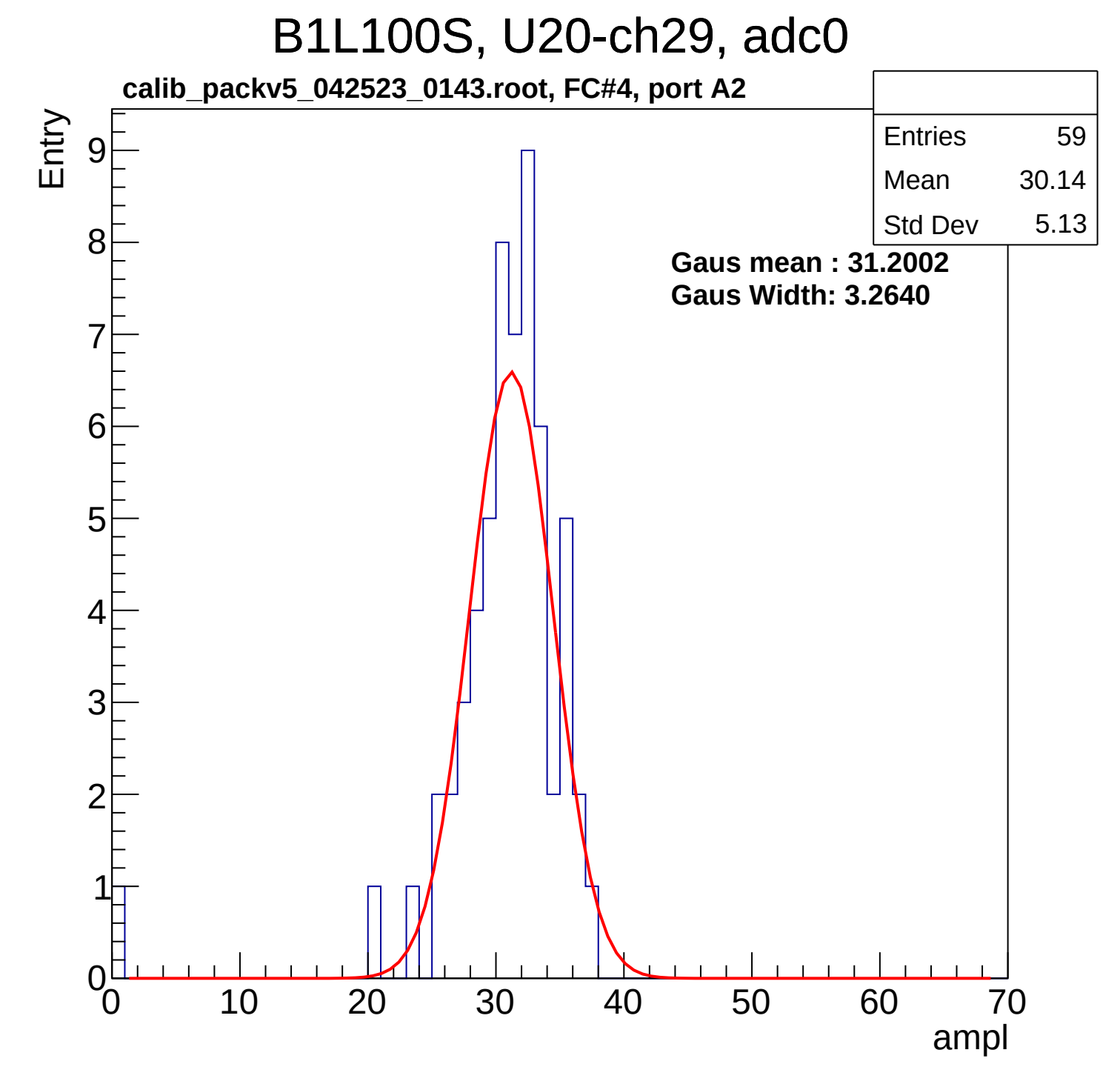

B1L100S, U20-ch28, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.15
Std Dev 3.225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.43
Std Dev 9.043
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.5
Std Dev 1.581
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 30.14
Std Dev 5.13
Gaus mean : 31.2002
Gaus Width: 3.2640
Entry
ampl
0
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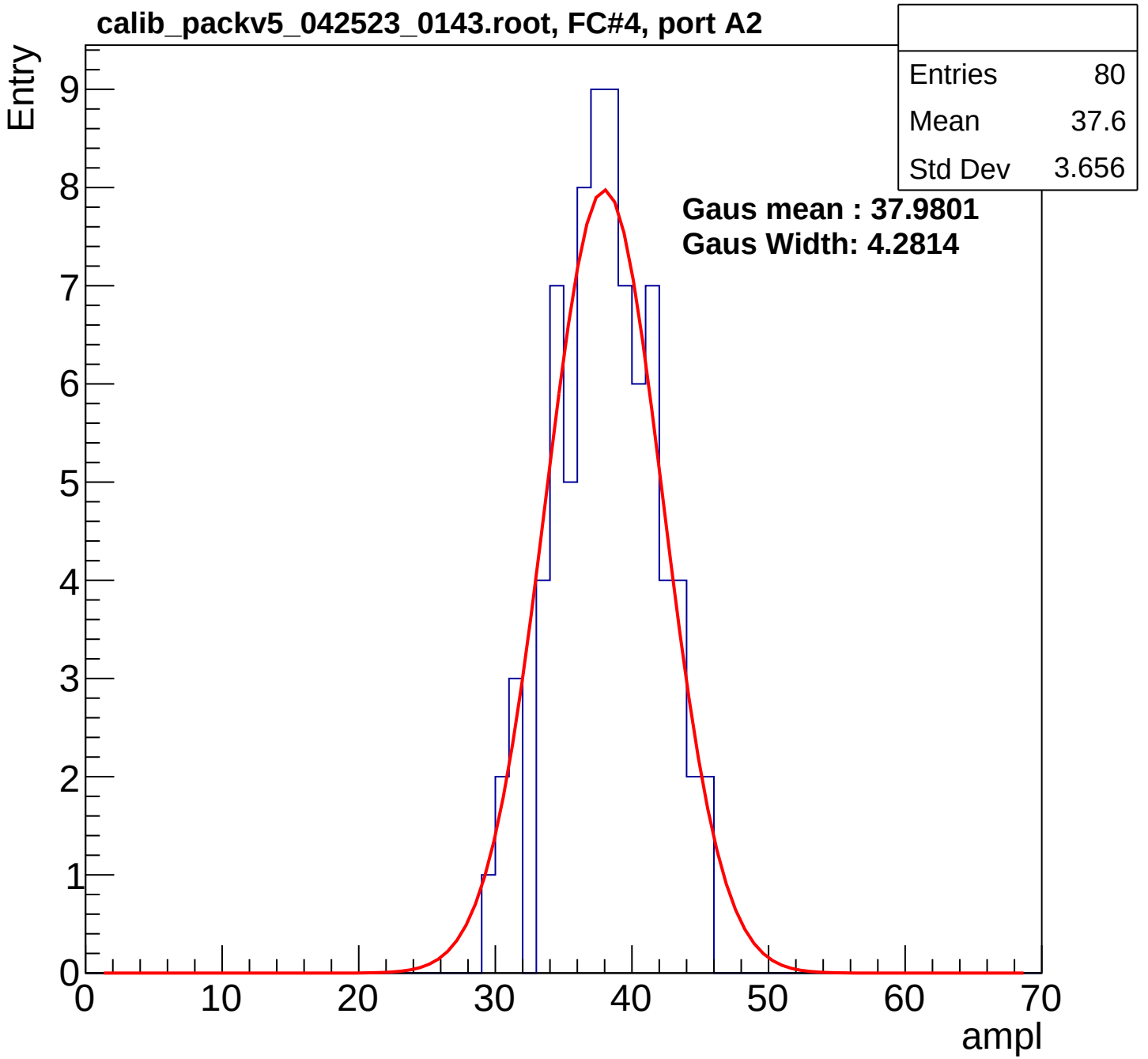
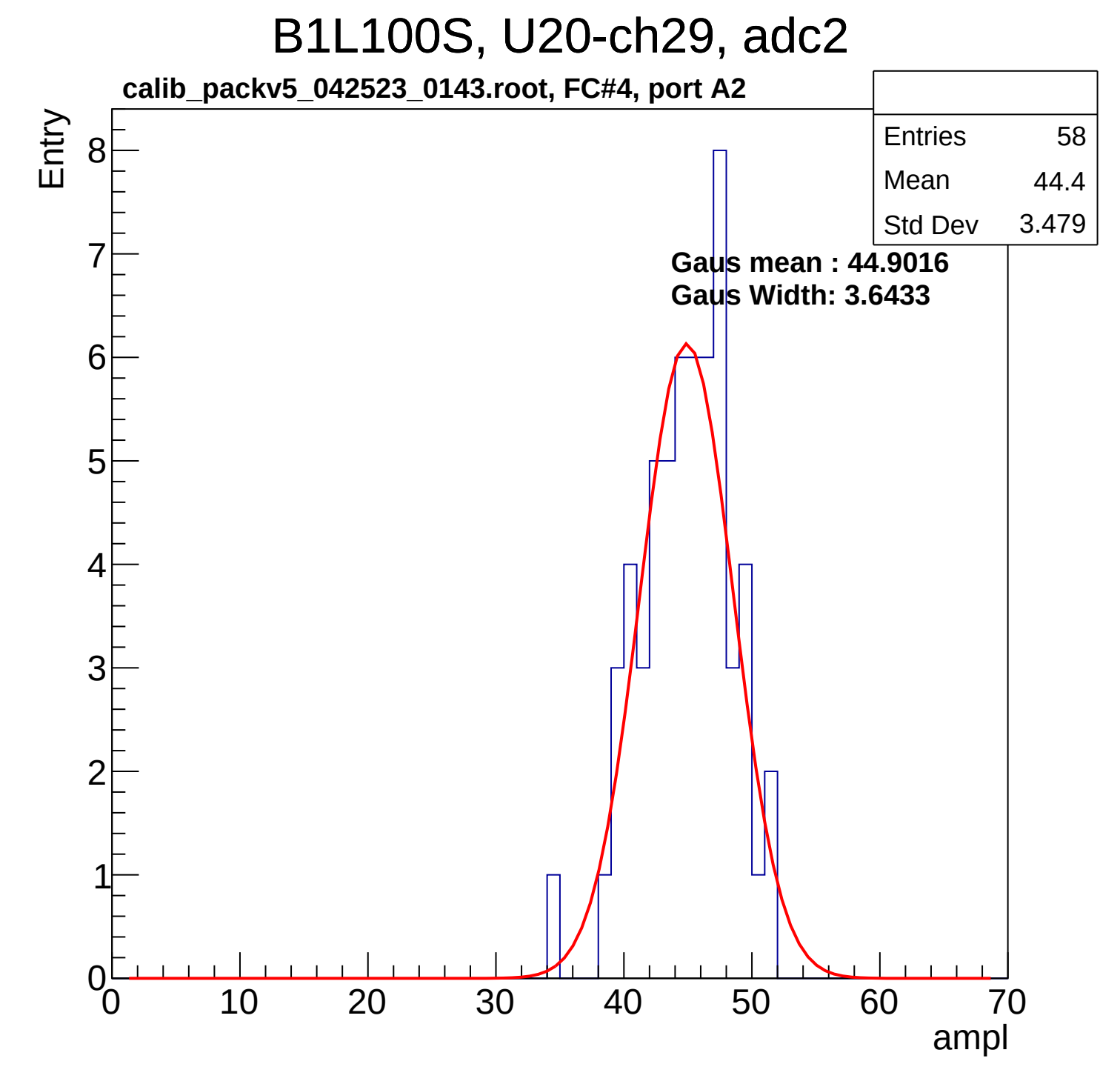
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 37.6
Std Dev 3.656
Gaus mean : 37.9801
Gaus Width: 4.2814
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 44.4
Std Dev 3.479
Gaus mean : 44.9016
Gaus Width: 3.6433
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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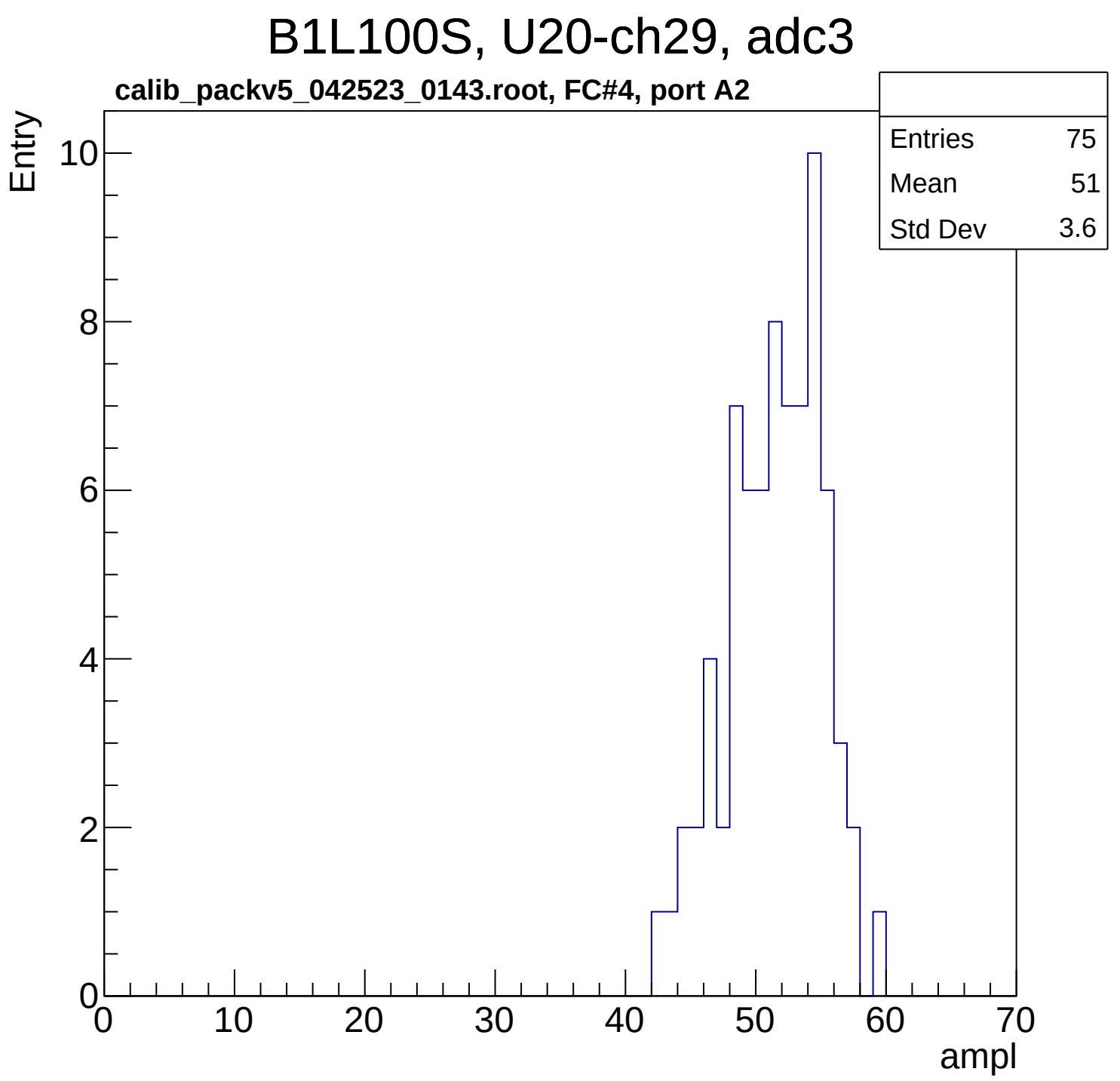
B1L100S, U20-ch29, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 51
Std Dev 3.6
Entry
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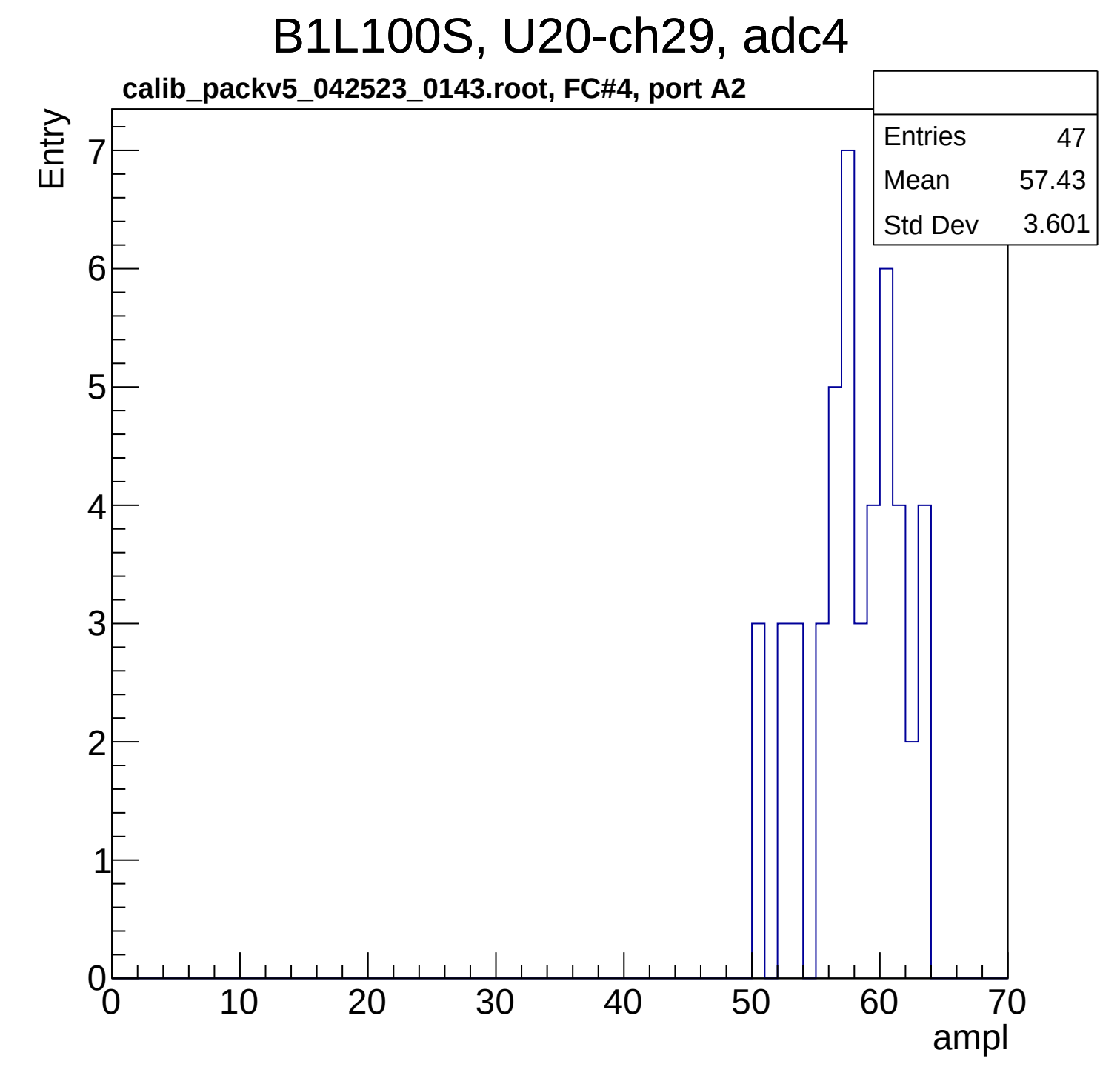
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch29, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 57.43
Std Dev 3.601
Entry
ampl
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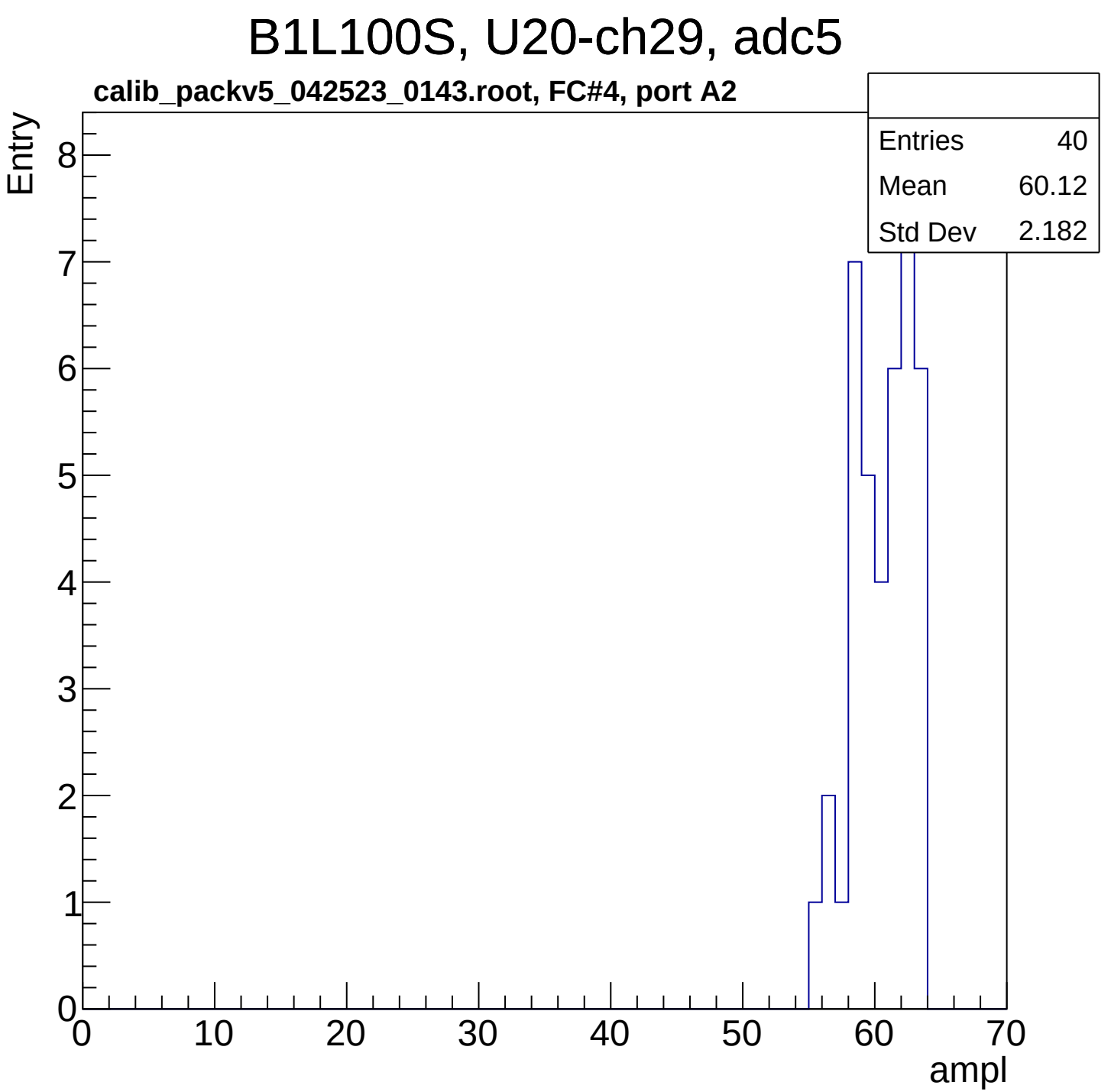
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 60.12
Std Dev 2.182
Entry
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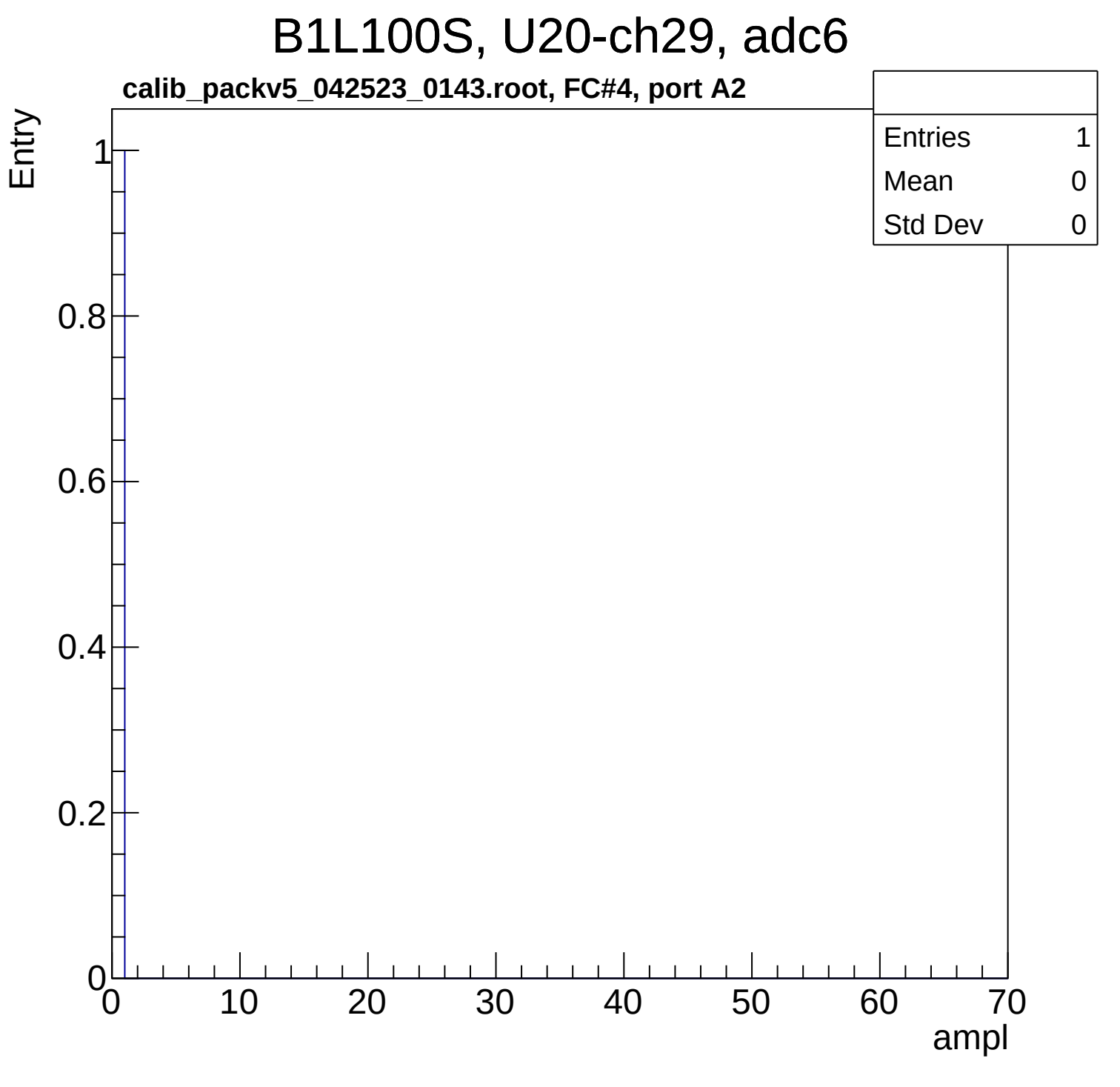
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch29, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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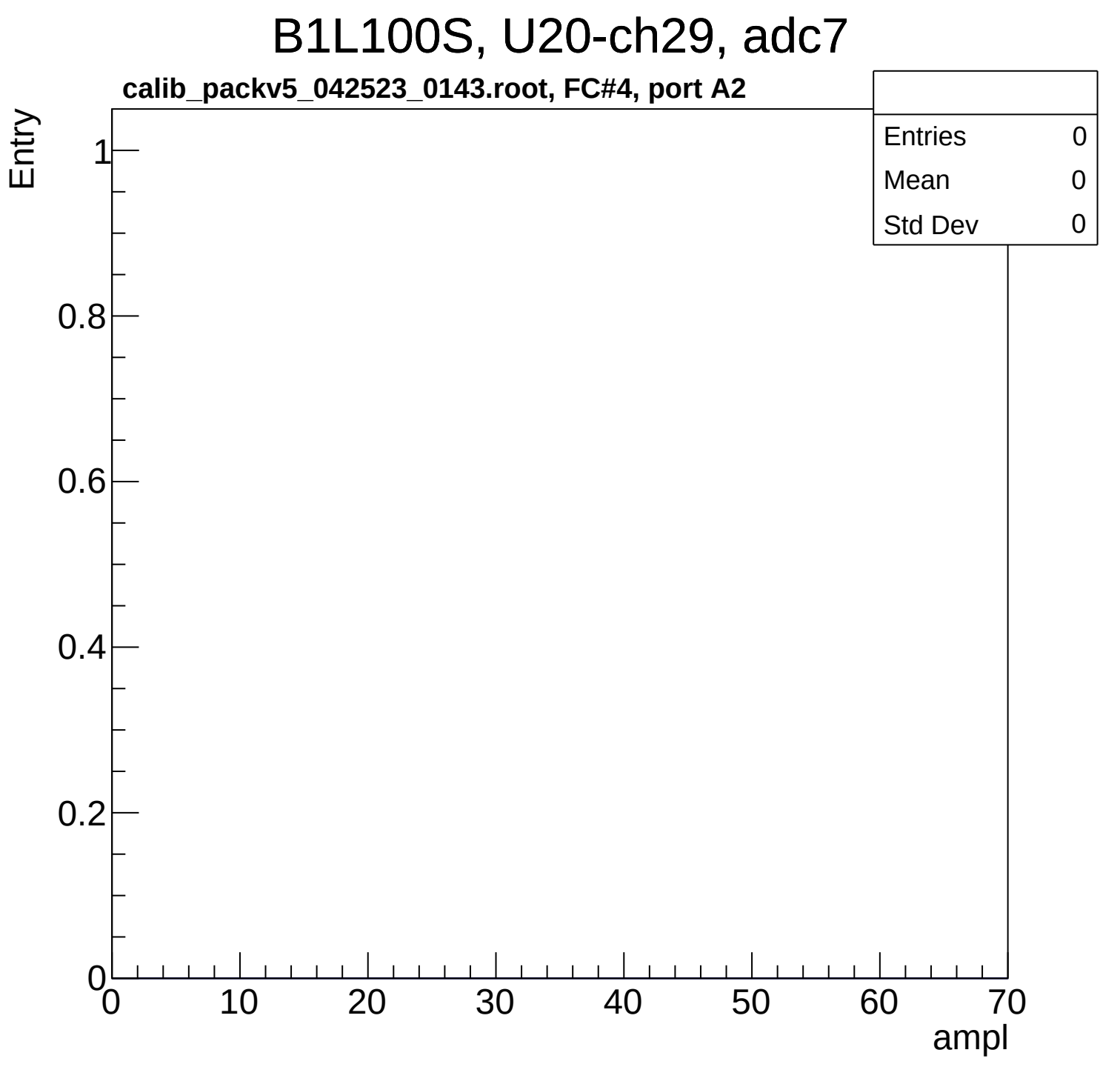
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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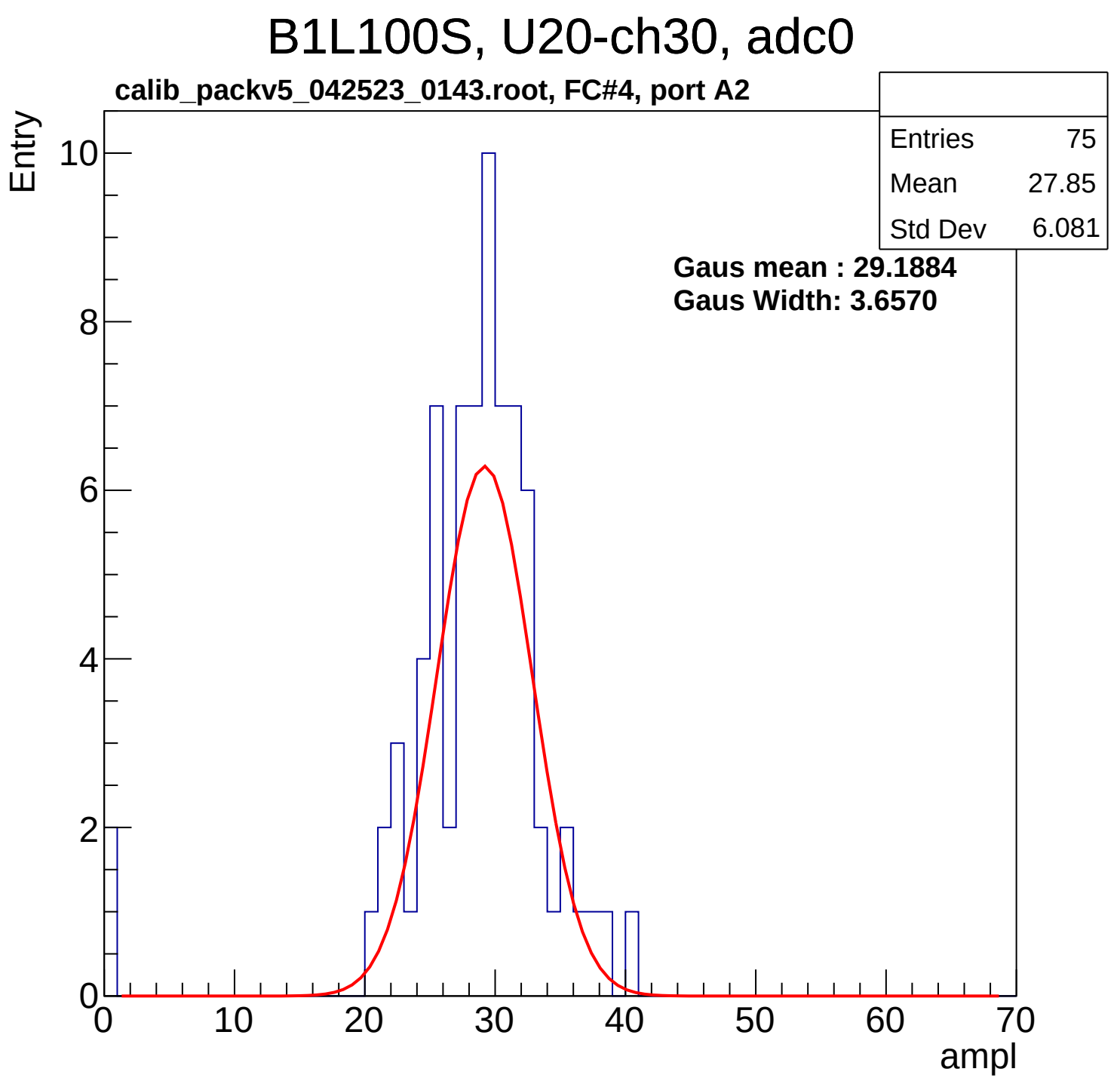
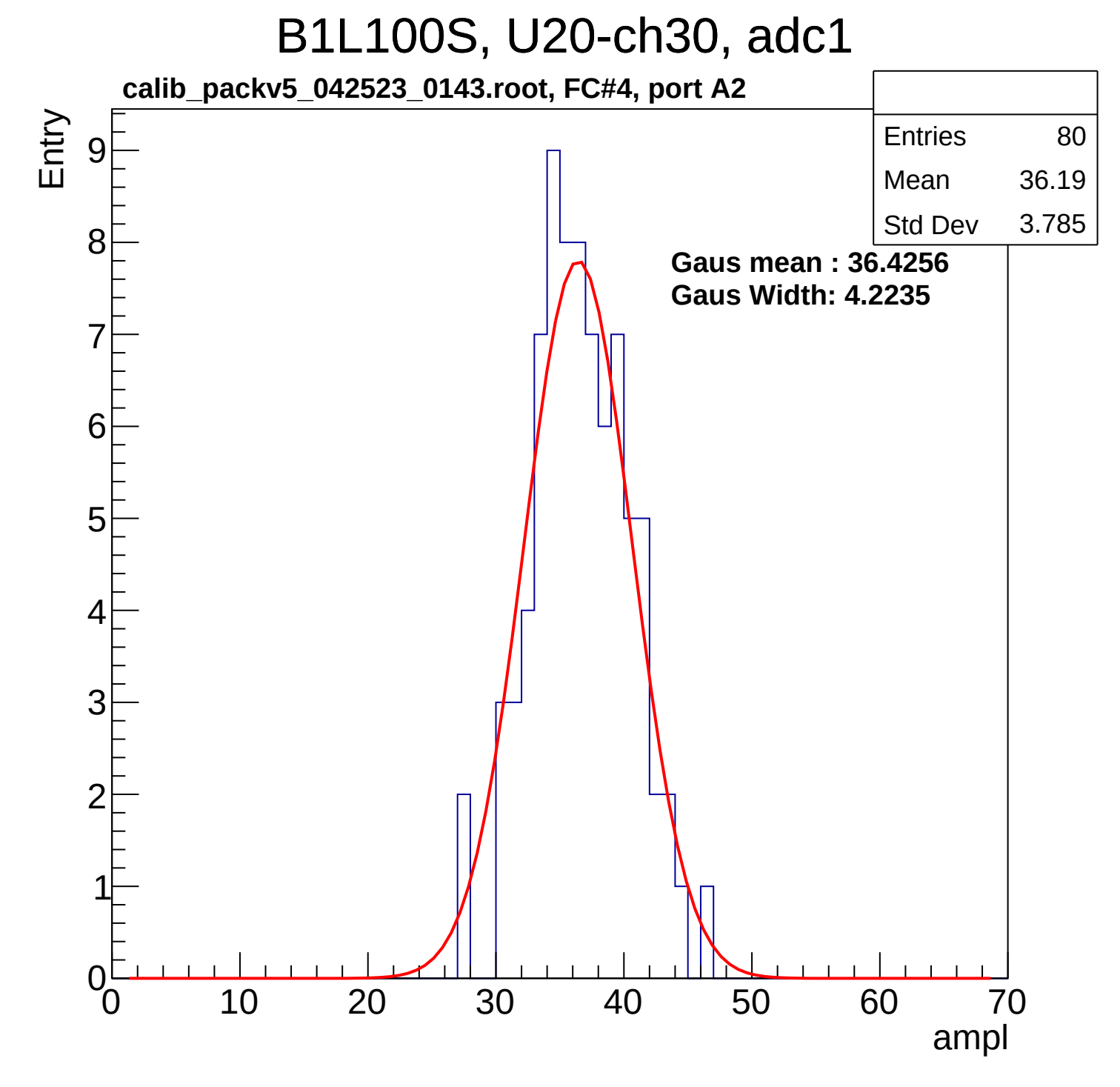
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 27.85
Std Dev 6.081
Gaus mean : 29.1884
Gaus Width: 3.6570
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.19
Std Dev 3.785
Gaus mean : 36.4256
Gaus Width: 4.2235
Entry
ampl
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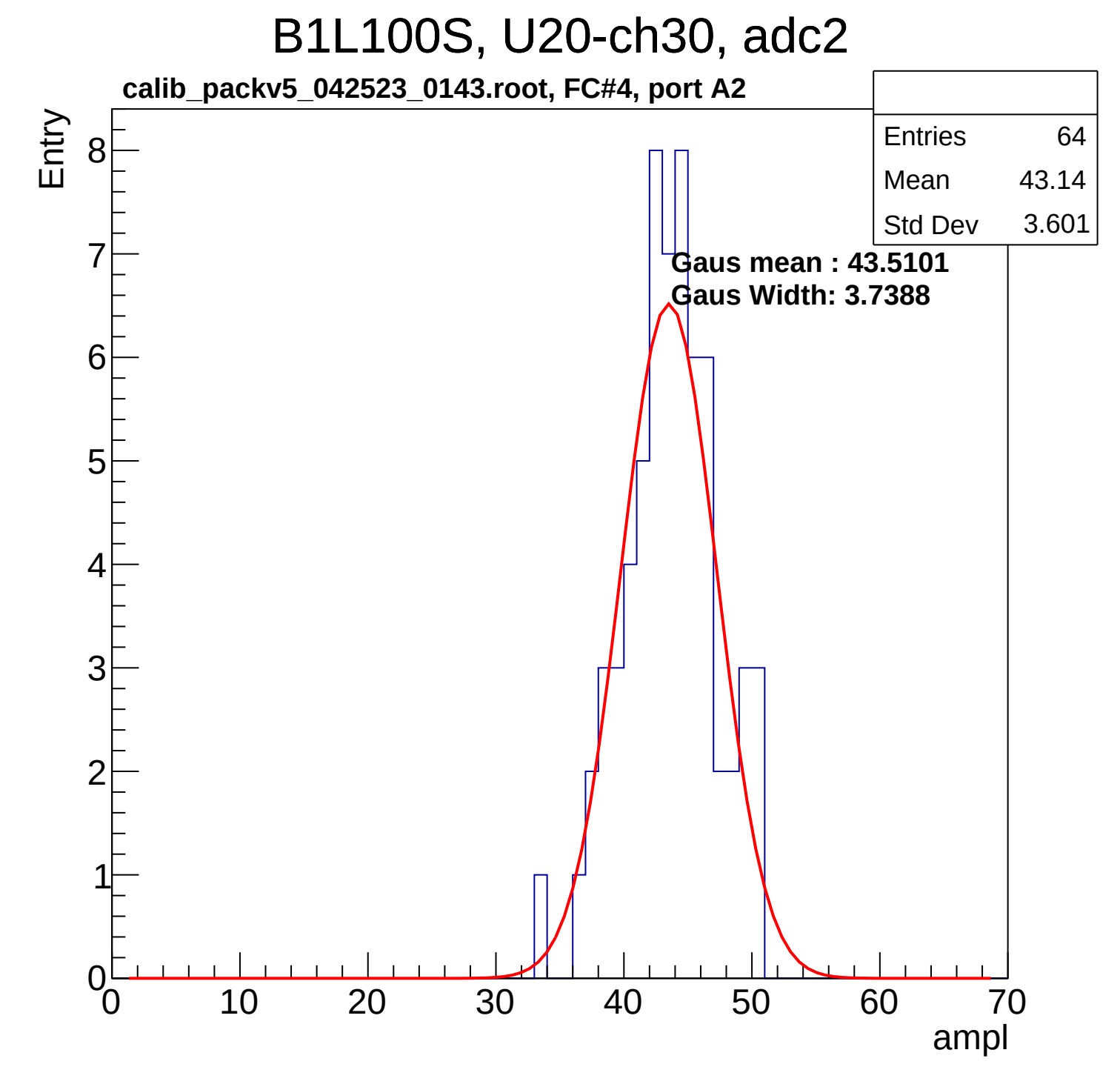
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.14
Std Dev 3.601
Gaus mean : 43.5101
Gaus Width: 3.7388
Entry
ampl
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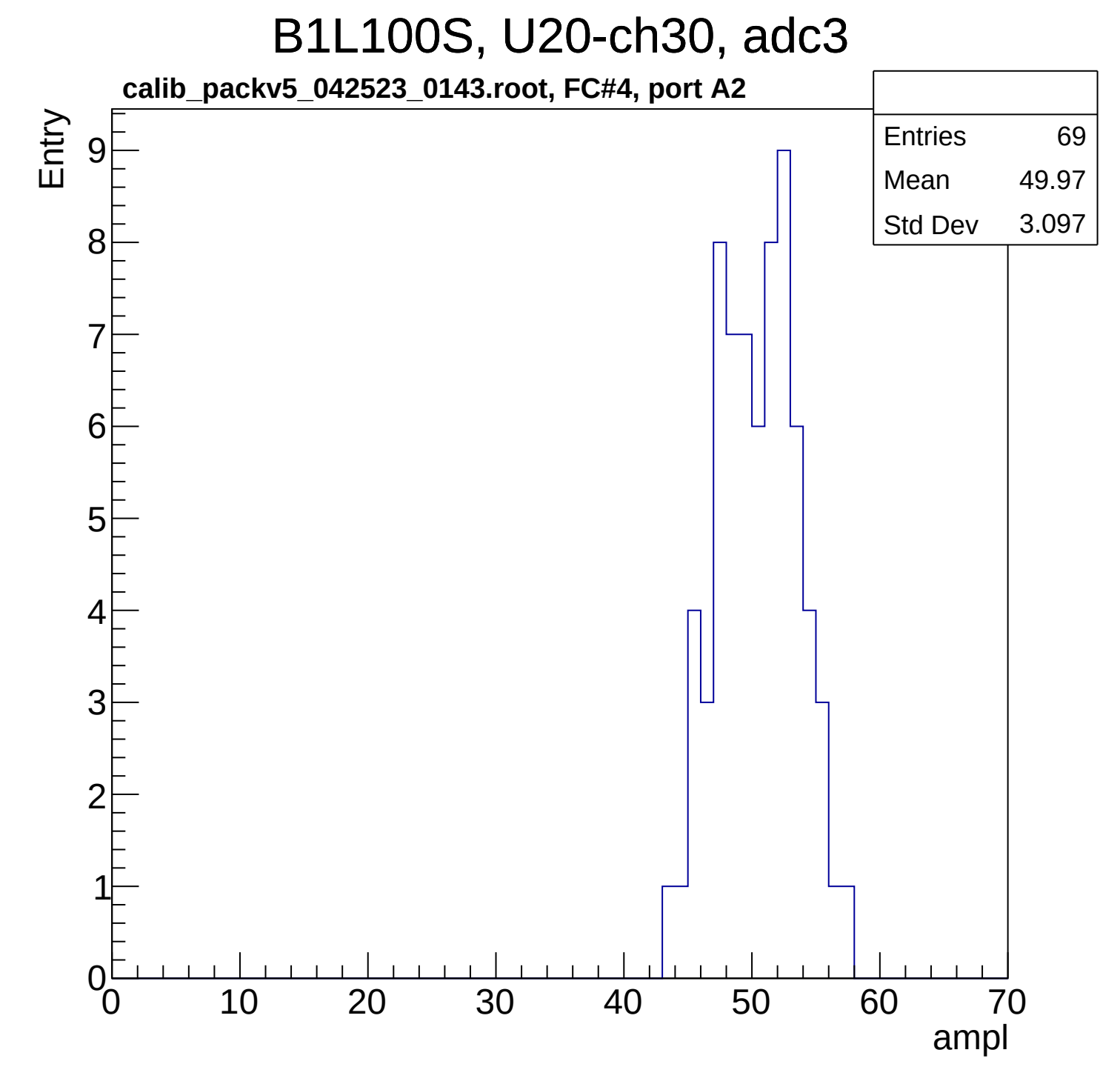
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.97
Std Dev 3.097
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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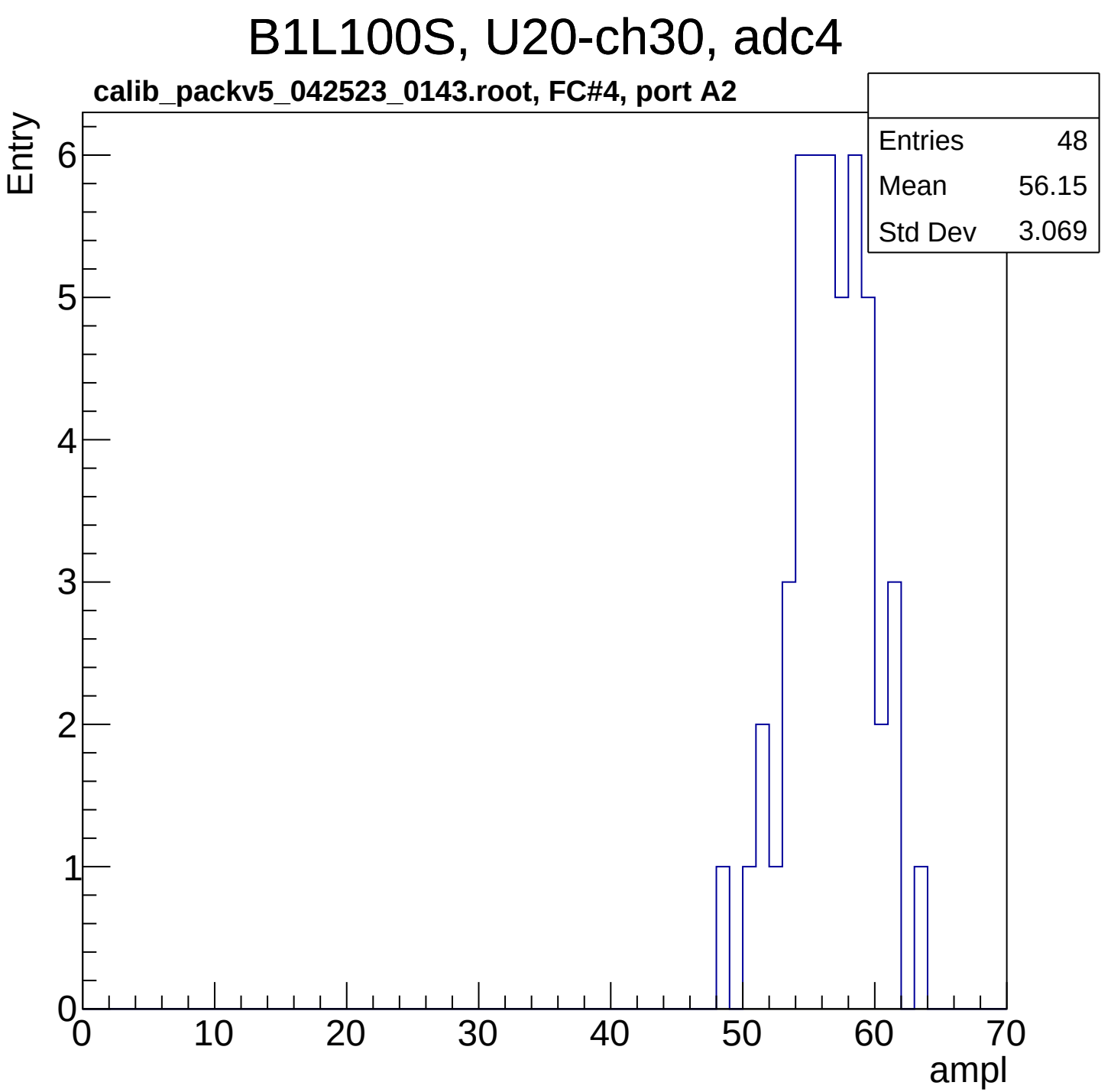
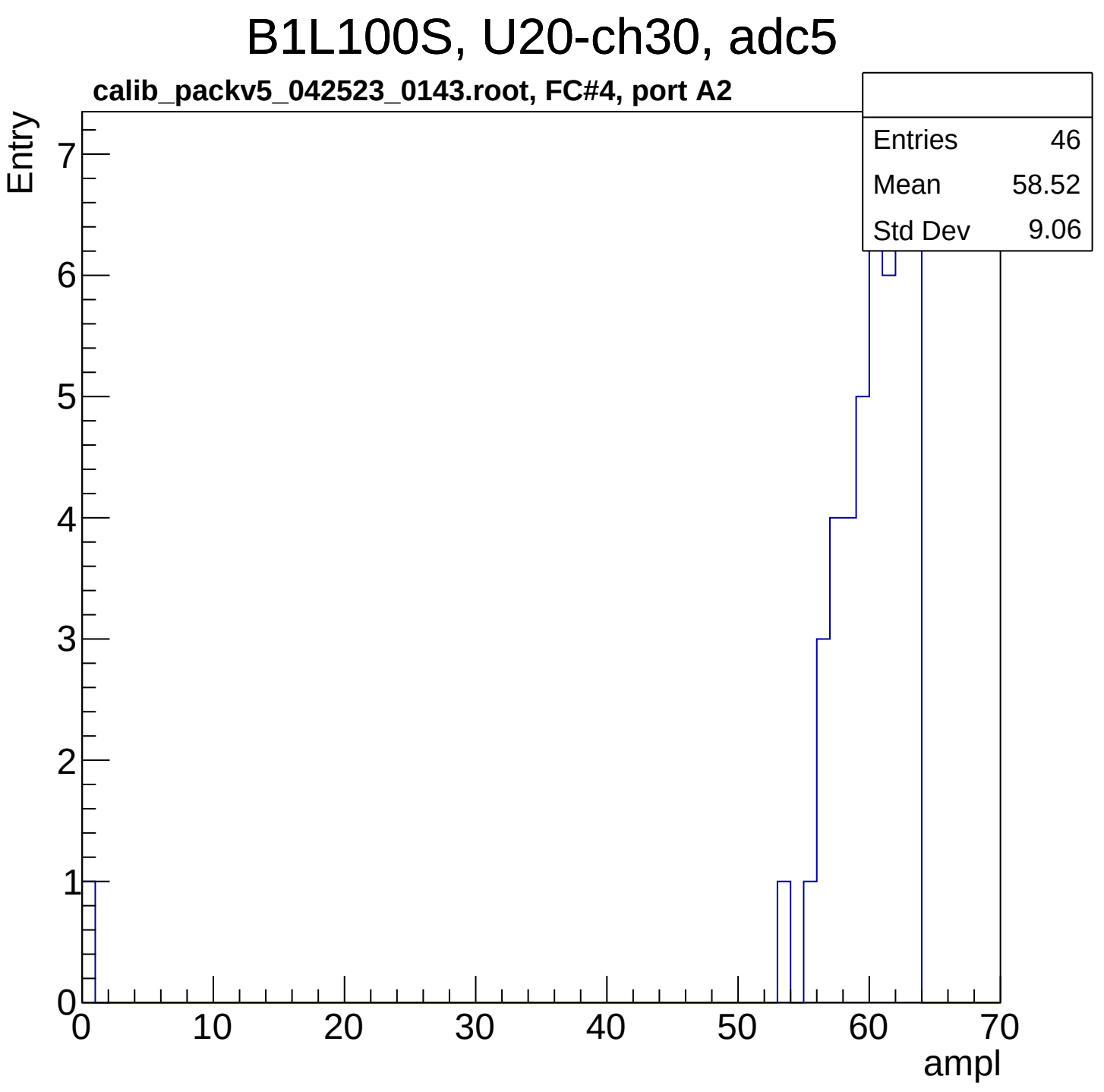

B1L100S, U20-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.15
Std Dev 3.069
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.52
Std Dev 9.06
Entry
ampl
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3
4
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0
10
20
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50
60
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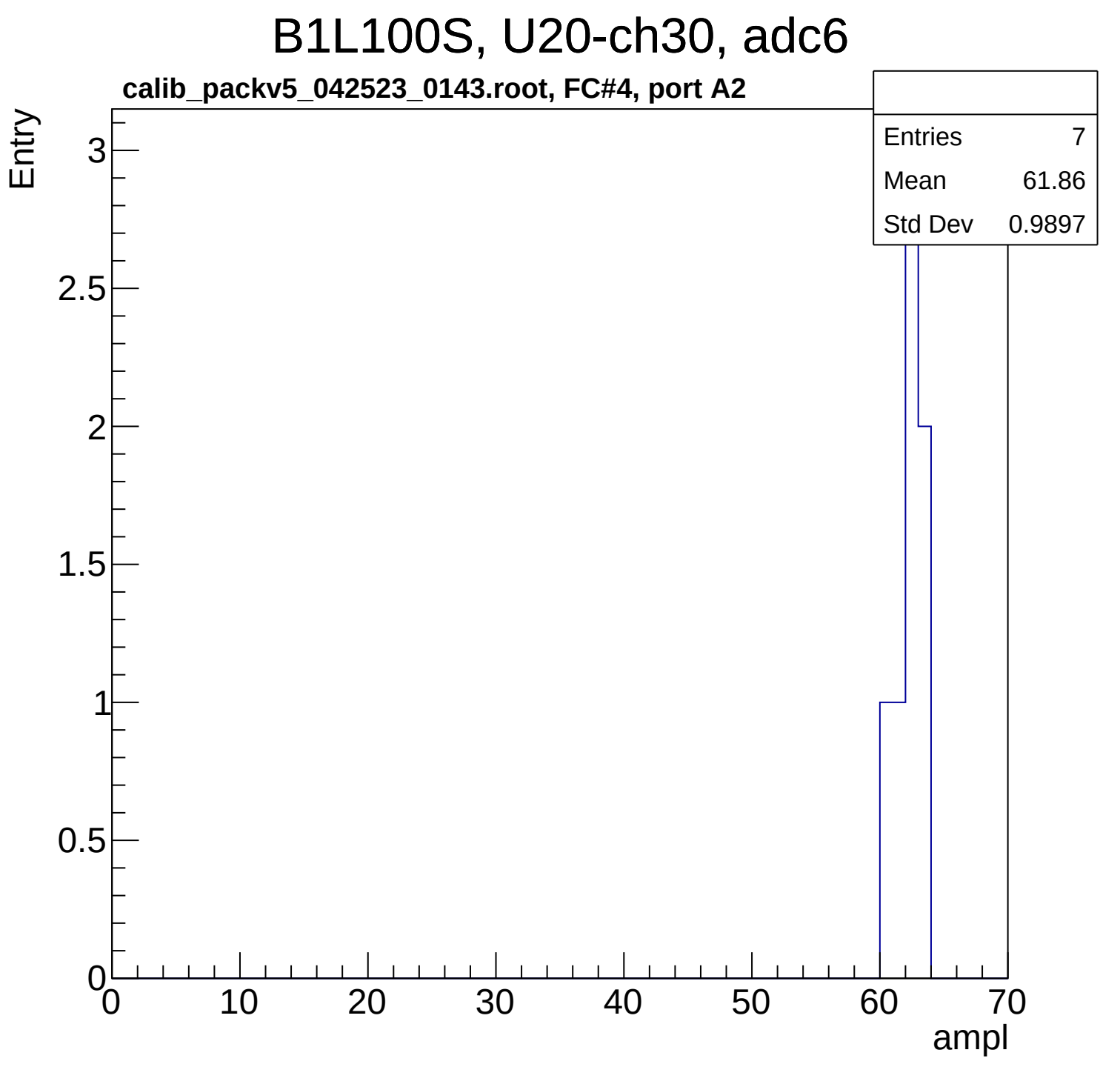
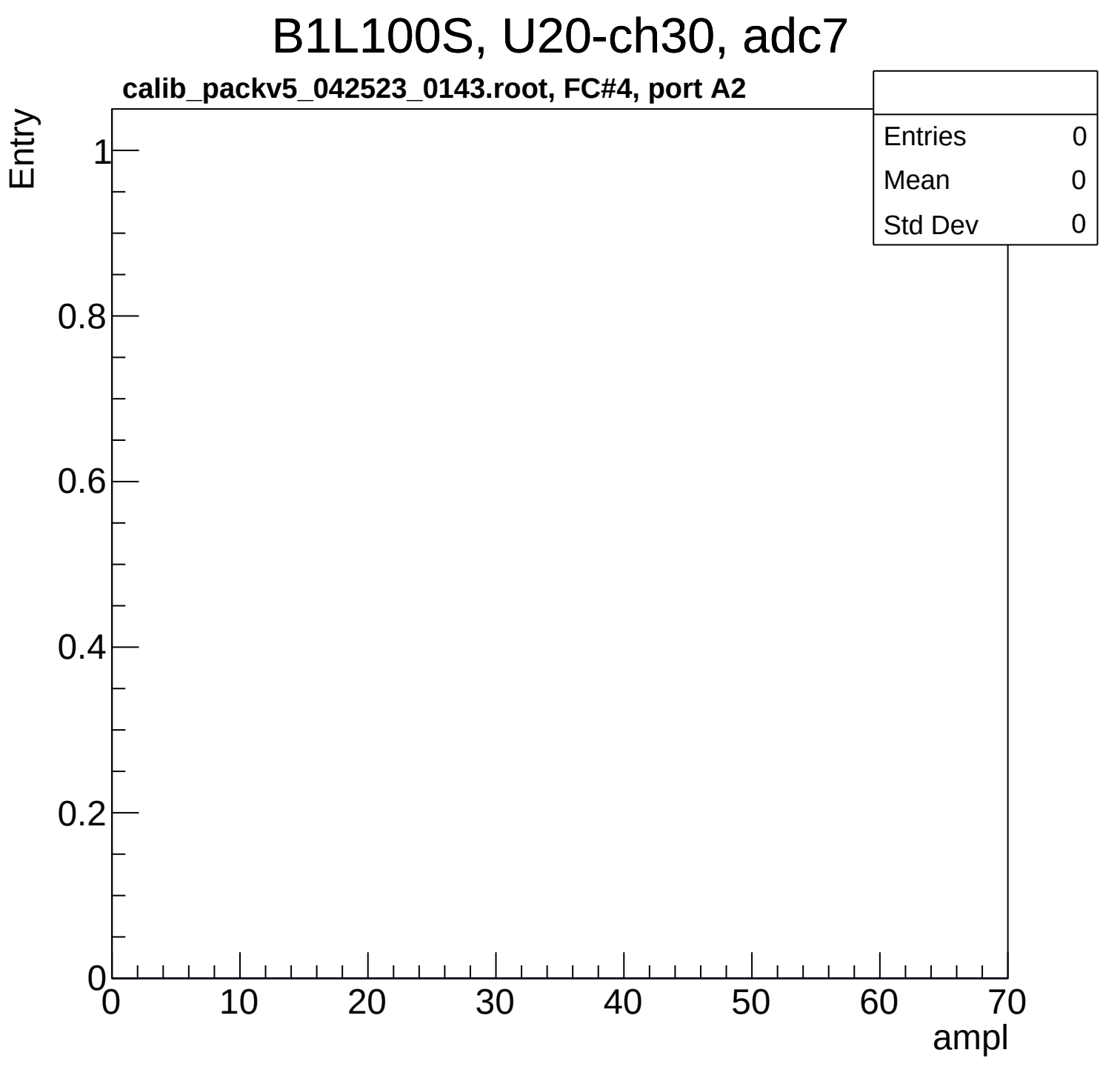
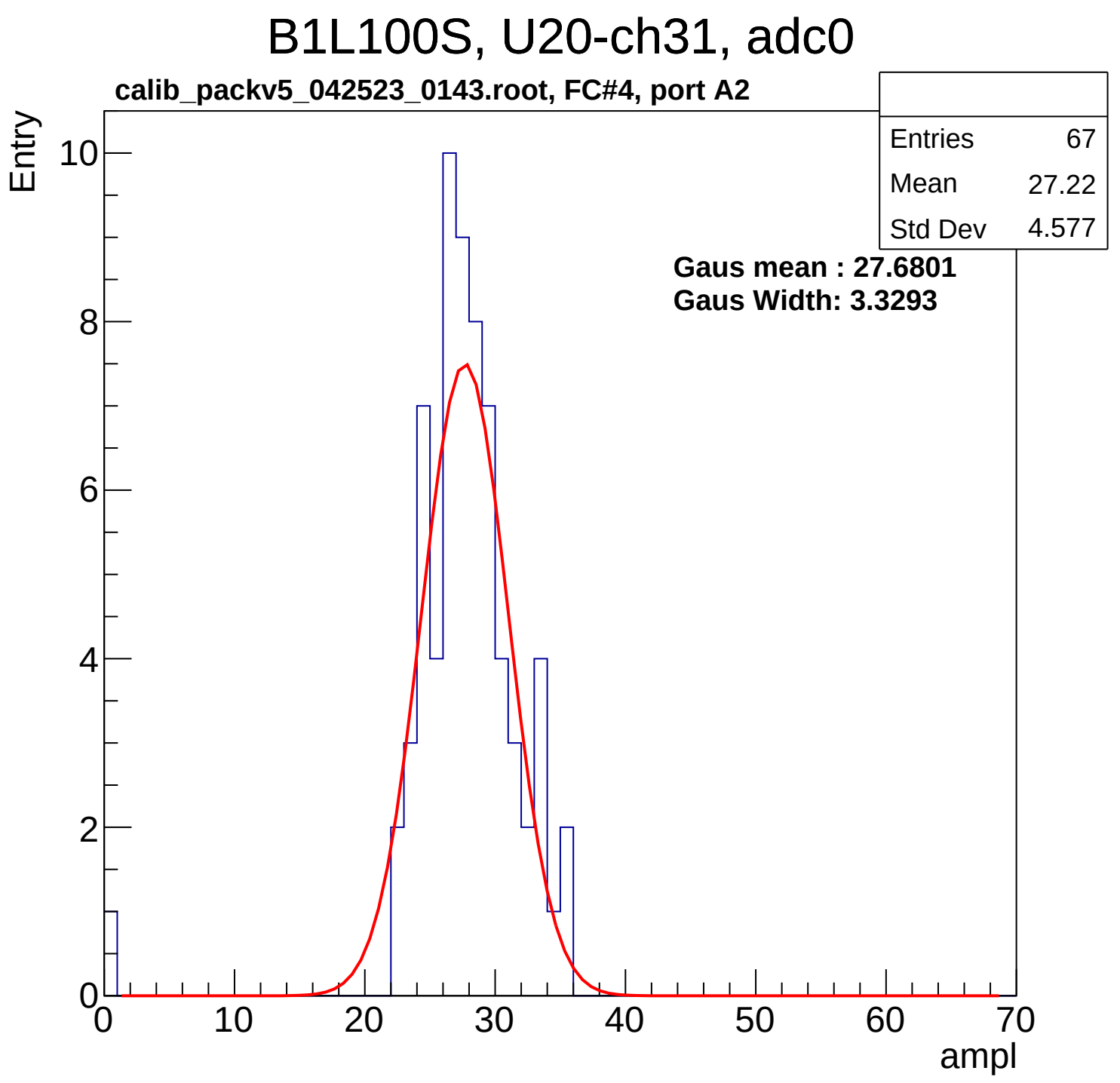

B1L100S, U20-ch30, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.22
Std Dev 4.577
Gaus mean : 27.6801
Gaus Width: 3.3293
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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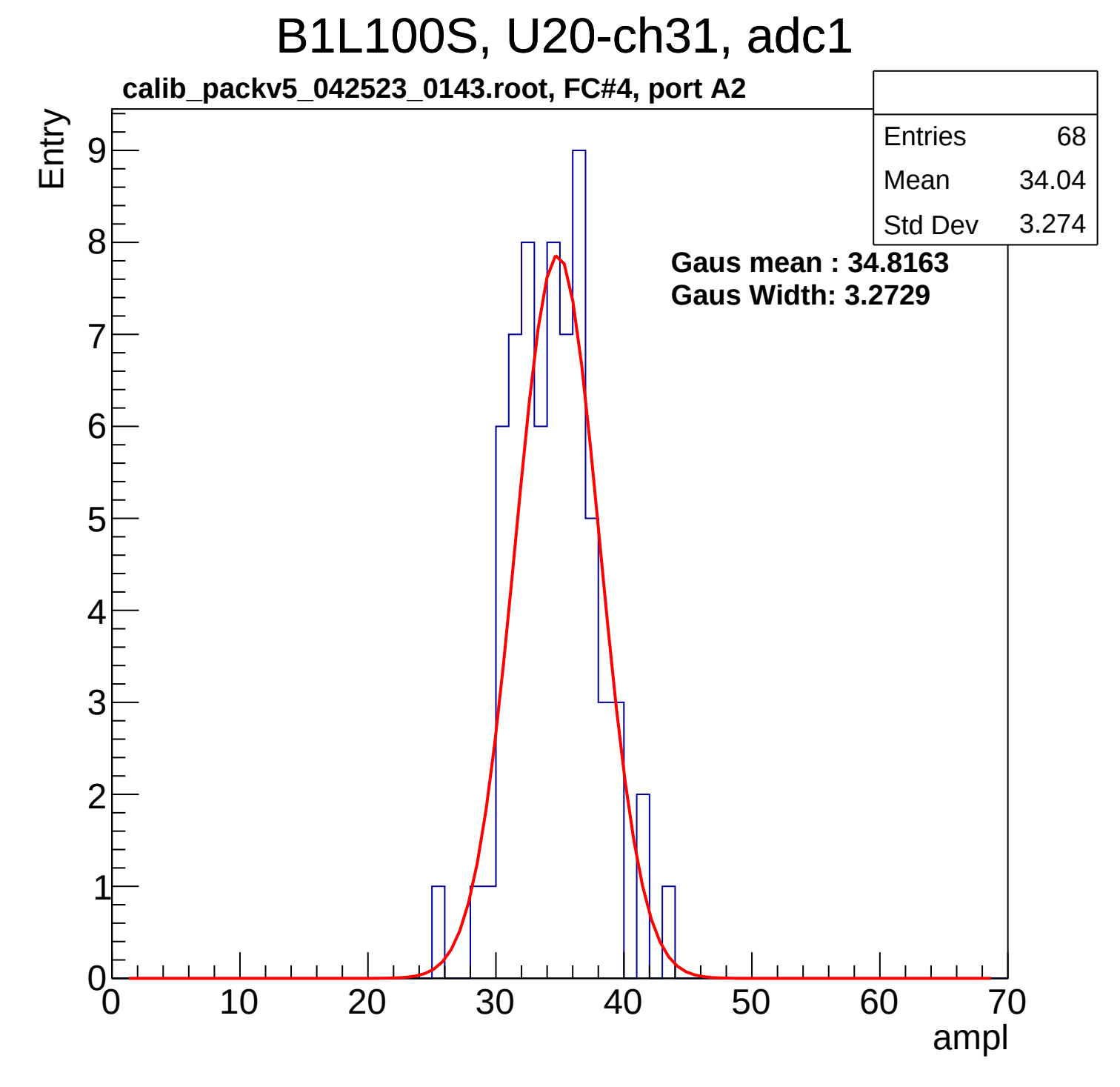
B1L100S, U20-ch31, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 34.04
Std Dev 3.274
Gaus mean : 34.8163
Gaus Width: 3.2729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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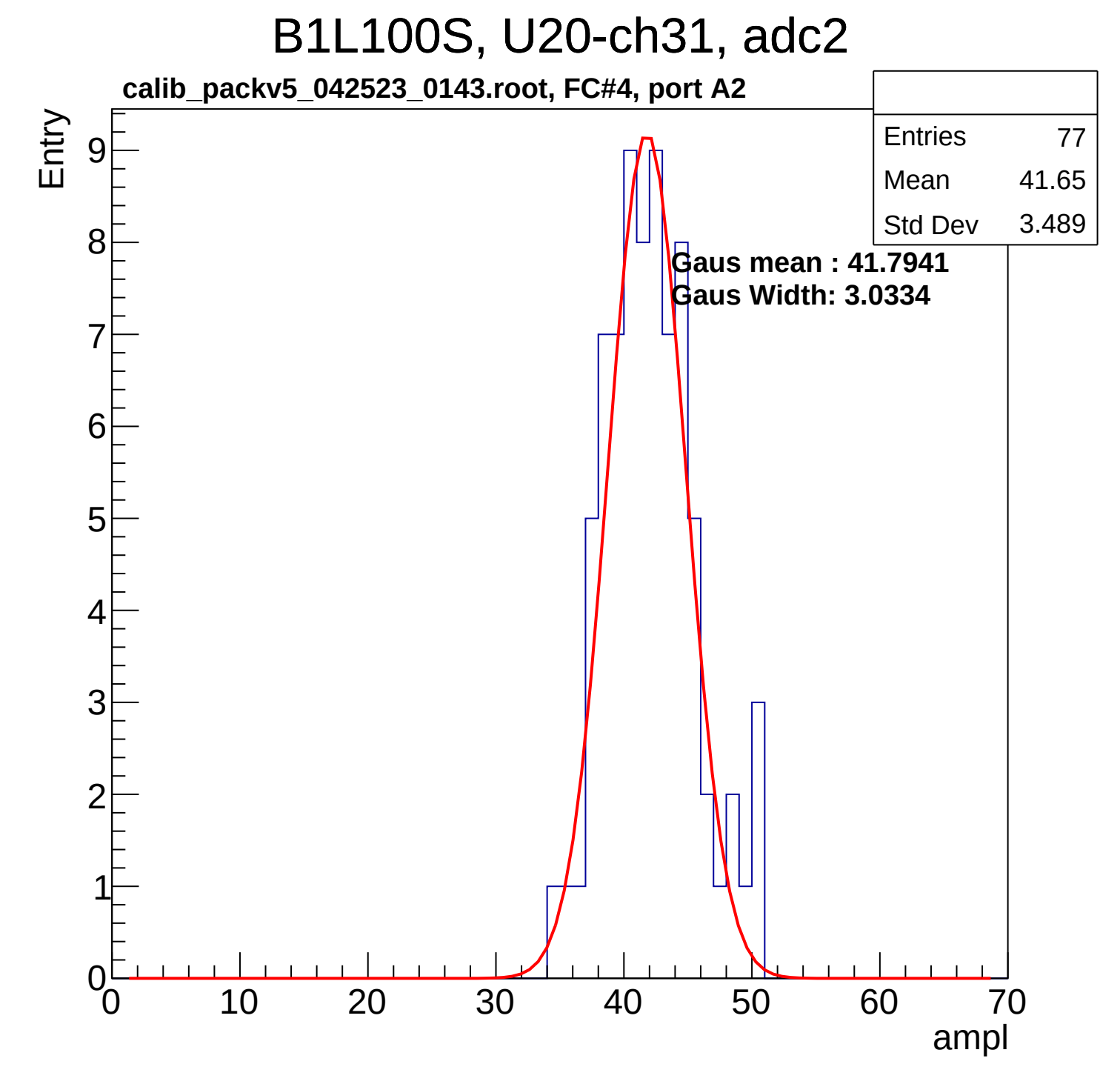
B1L100S, U20-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 41.65
Std Dev 3.489
Gaus mean : 41.7941
Gaus Width: 3.0334
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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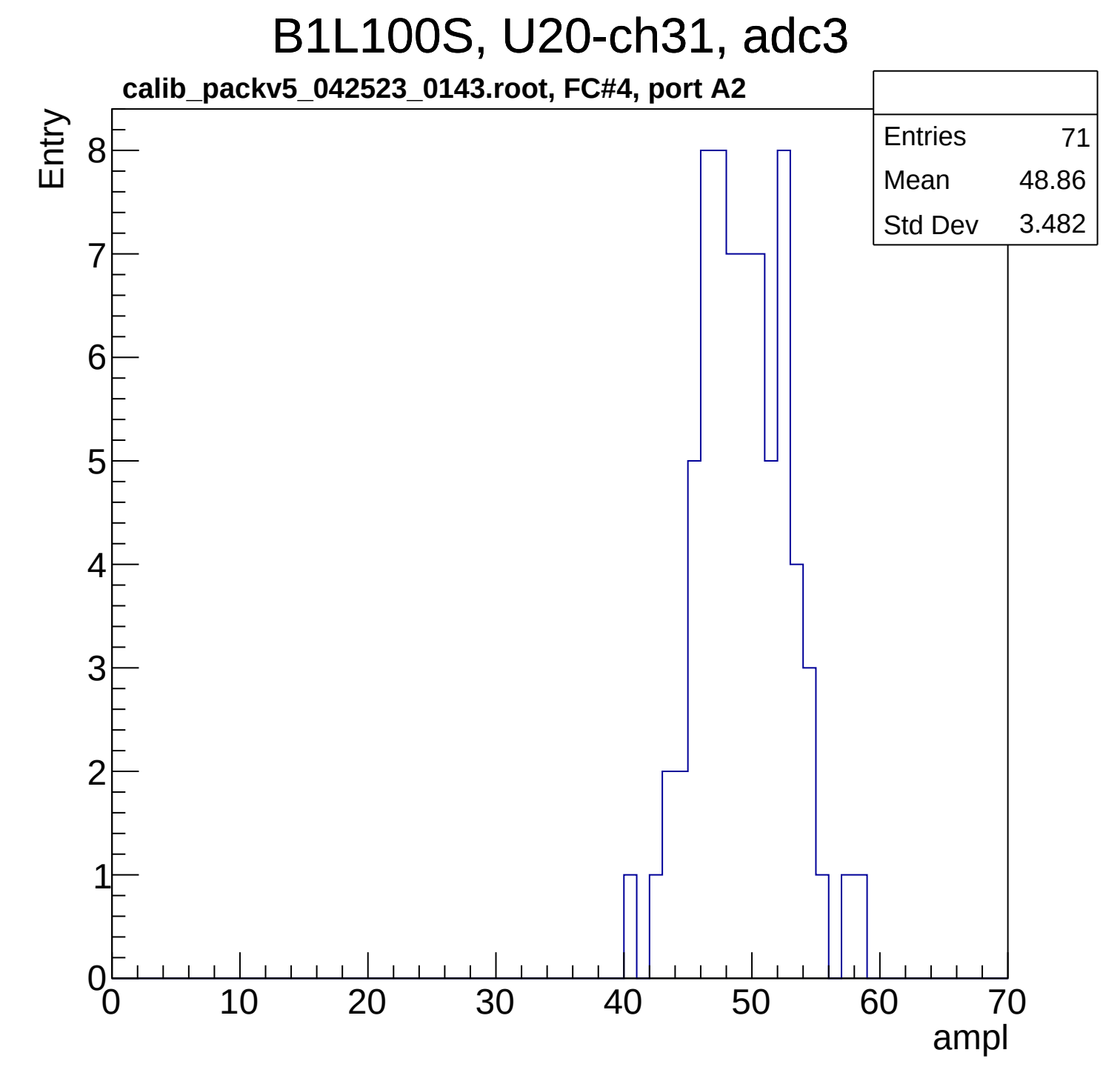
B1L100S, U20-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.86
Std Dev 3.482
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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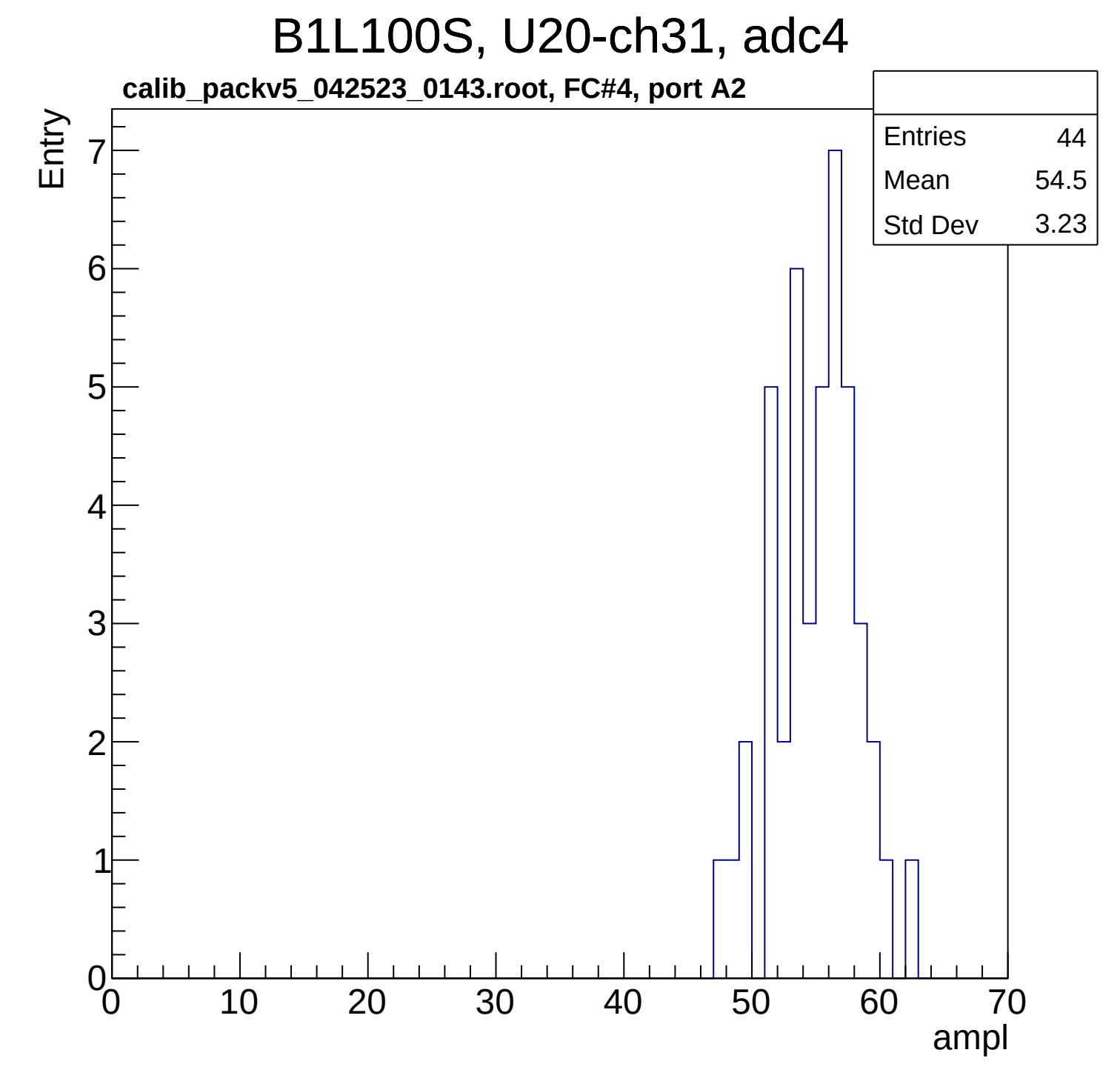
B1L100S, U20-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 54.5
Std Dev 3.23
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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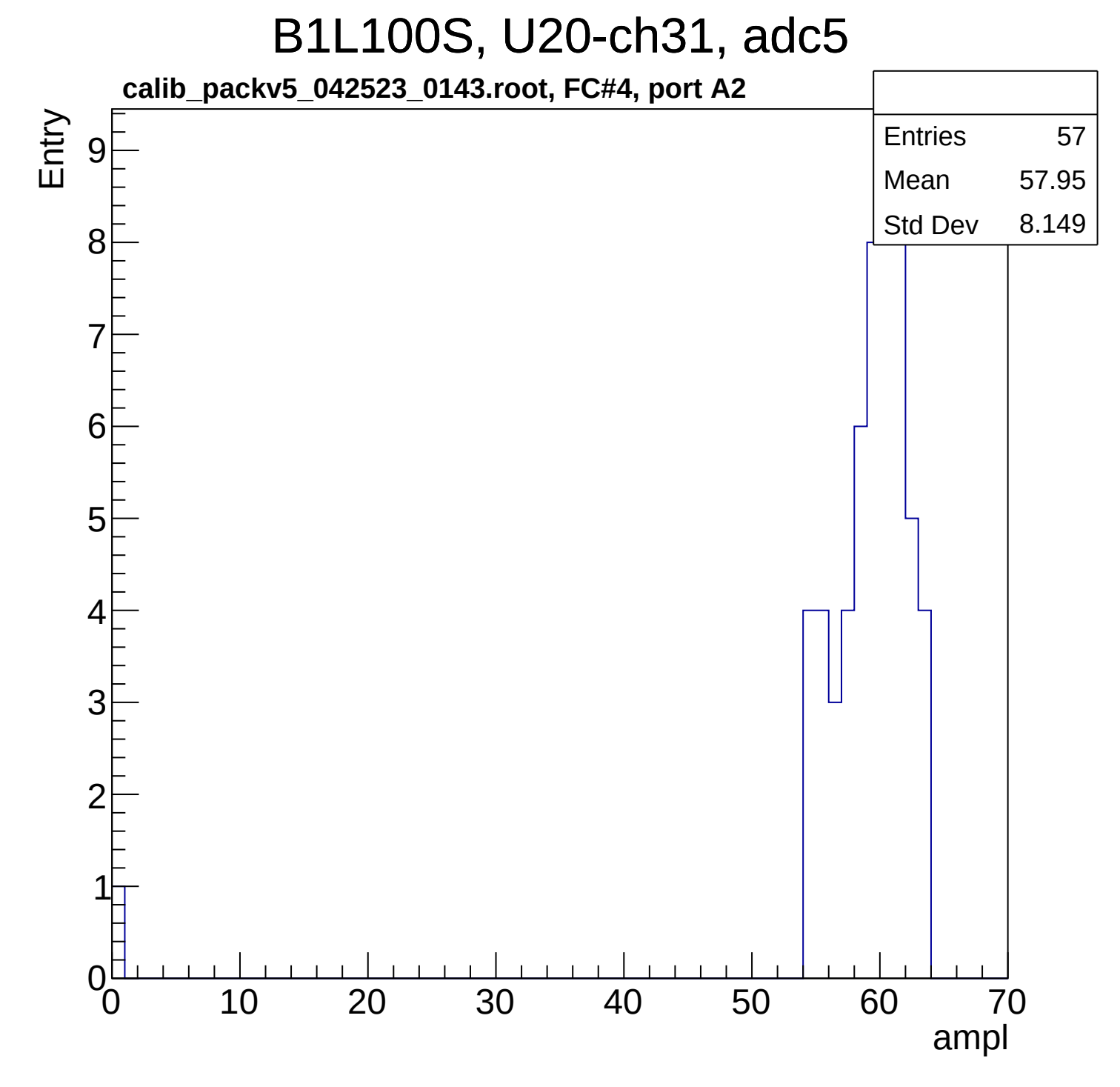
B1L100S, U20-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.95
Std Dev 8.149
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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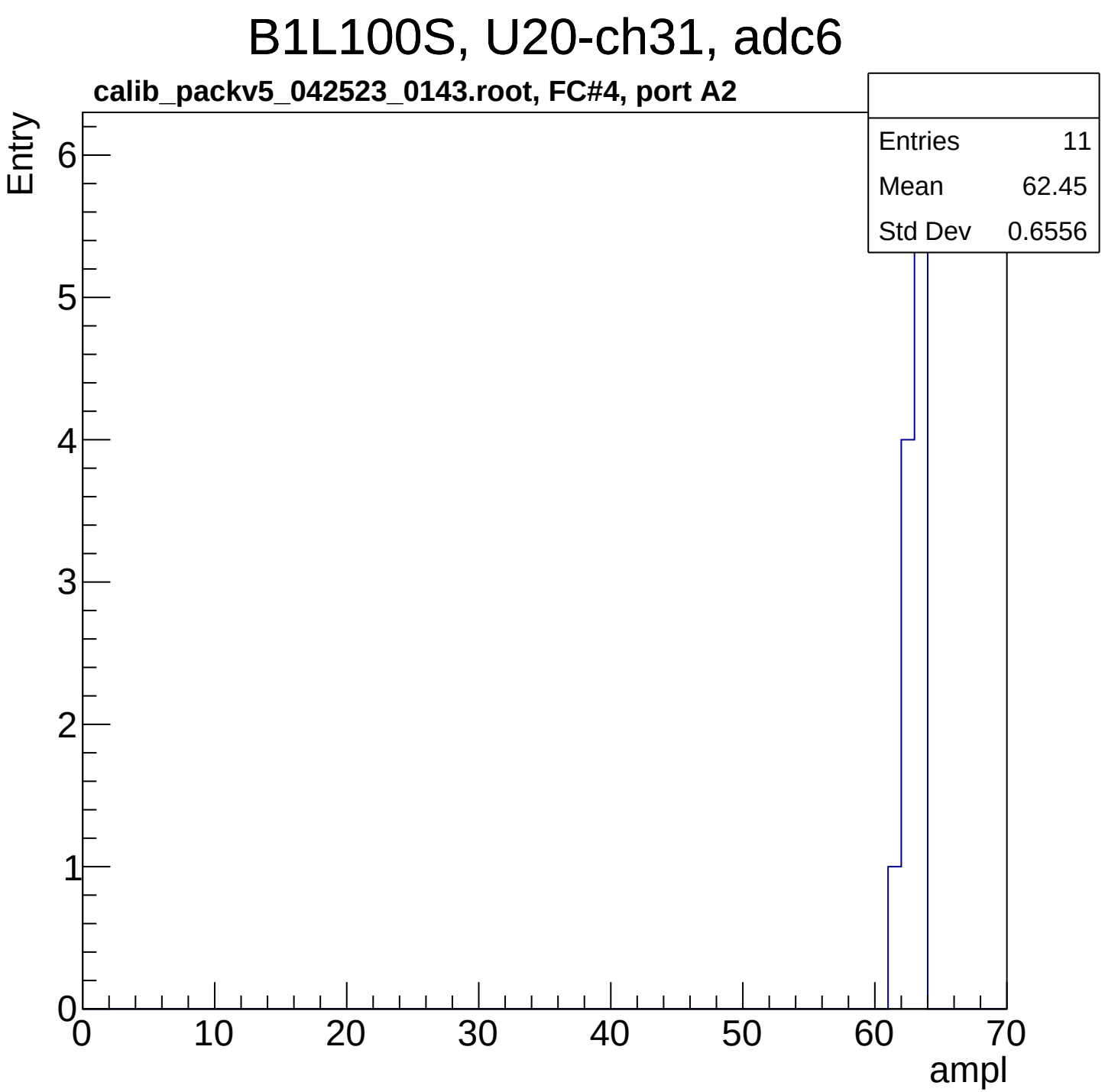
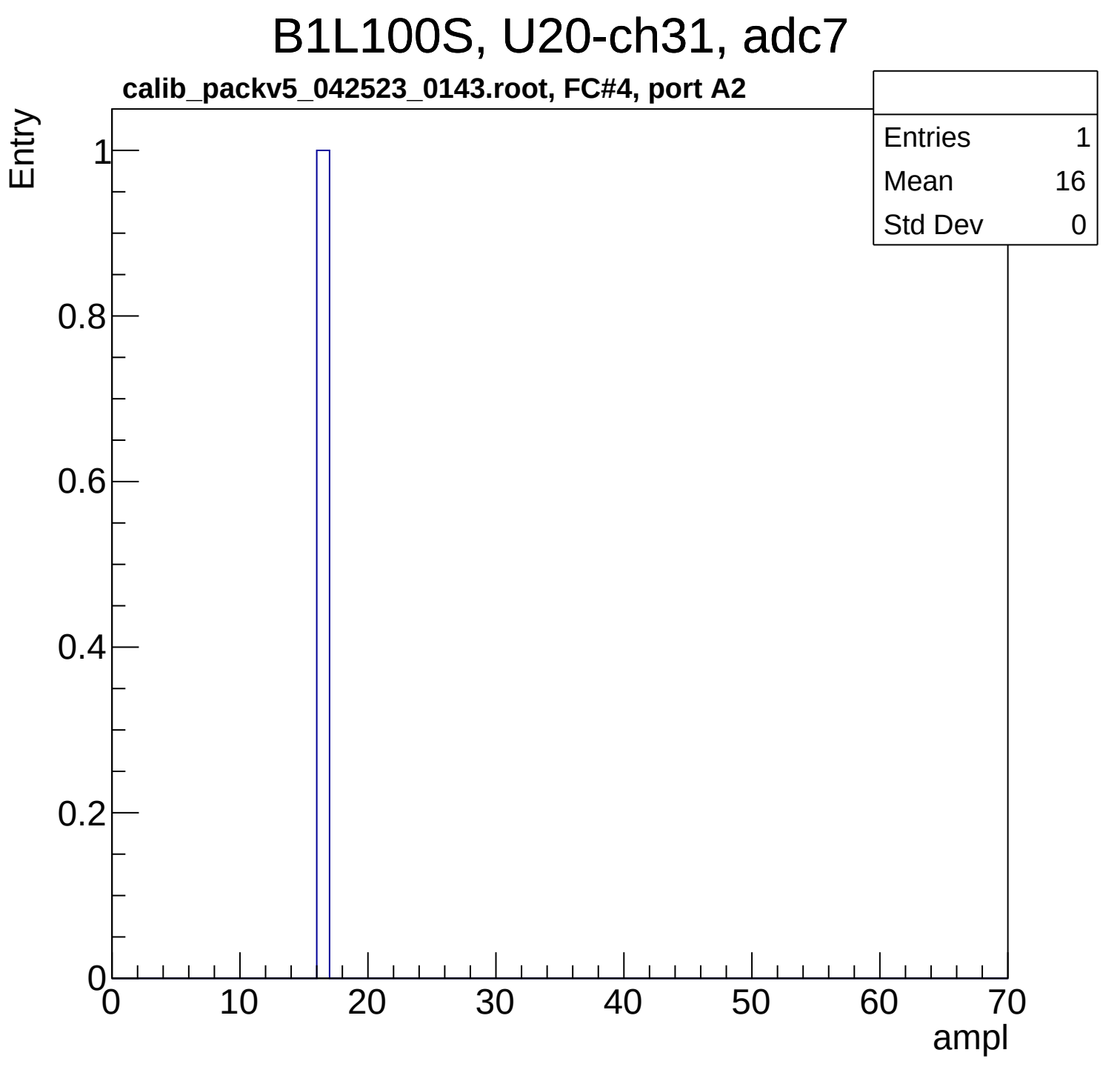

B1L100S, U20-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 62.45
Std Dev 0.6556
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 16
Std Dev 0
Entry
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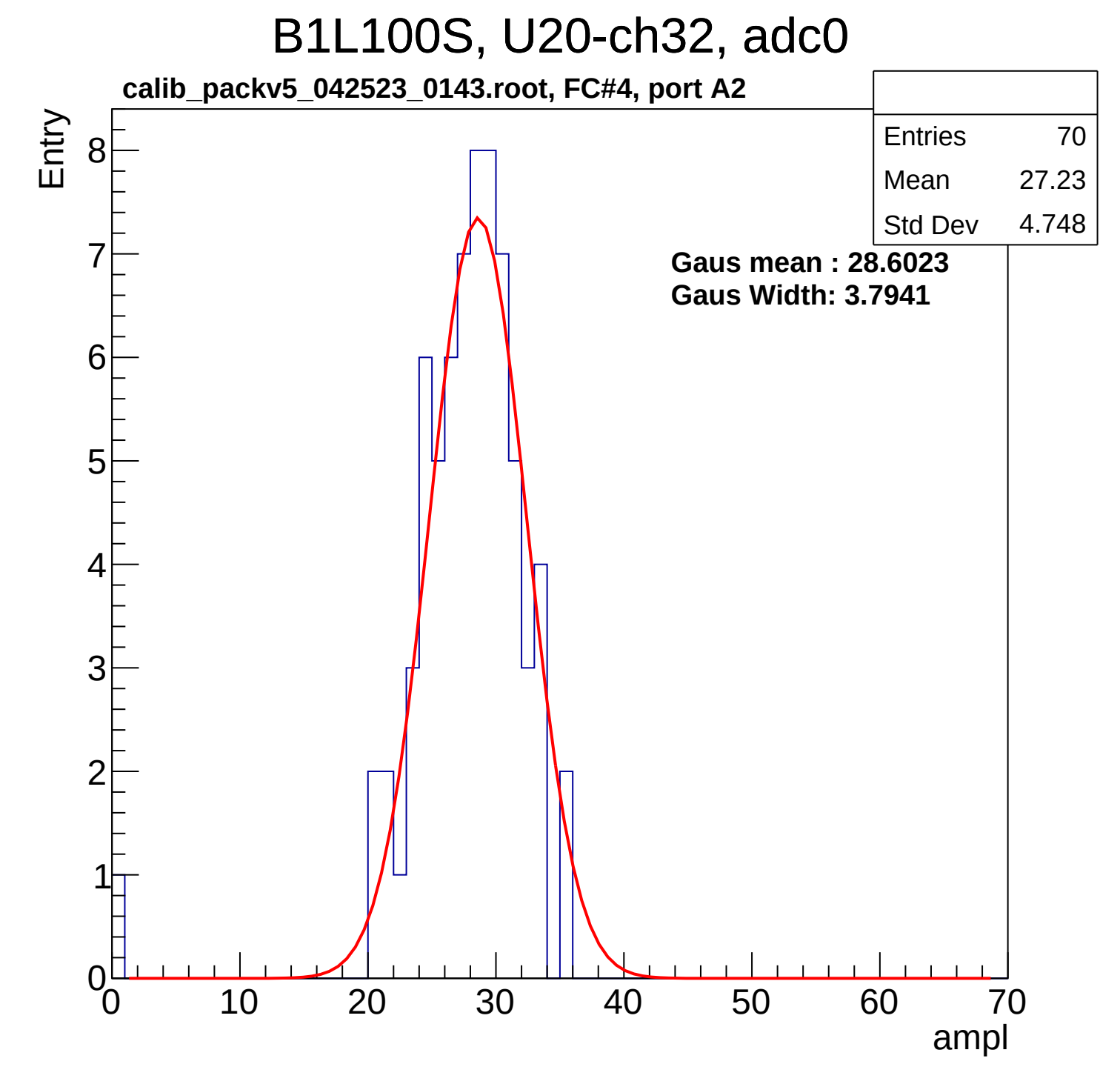
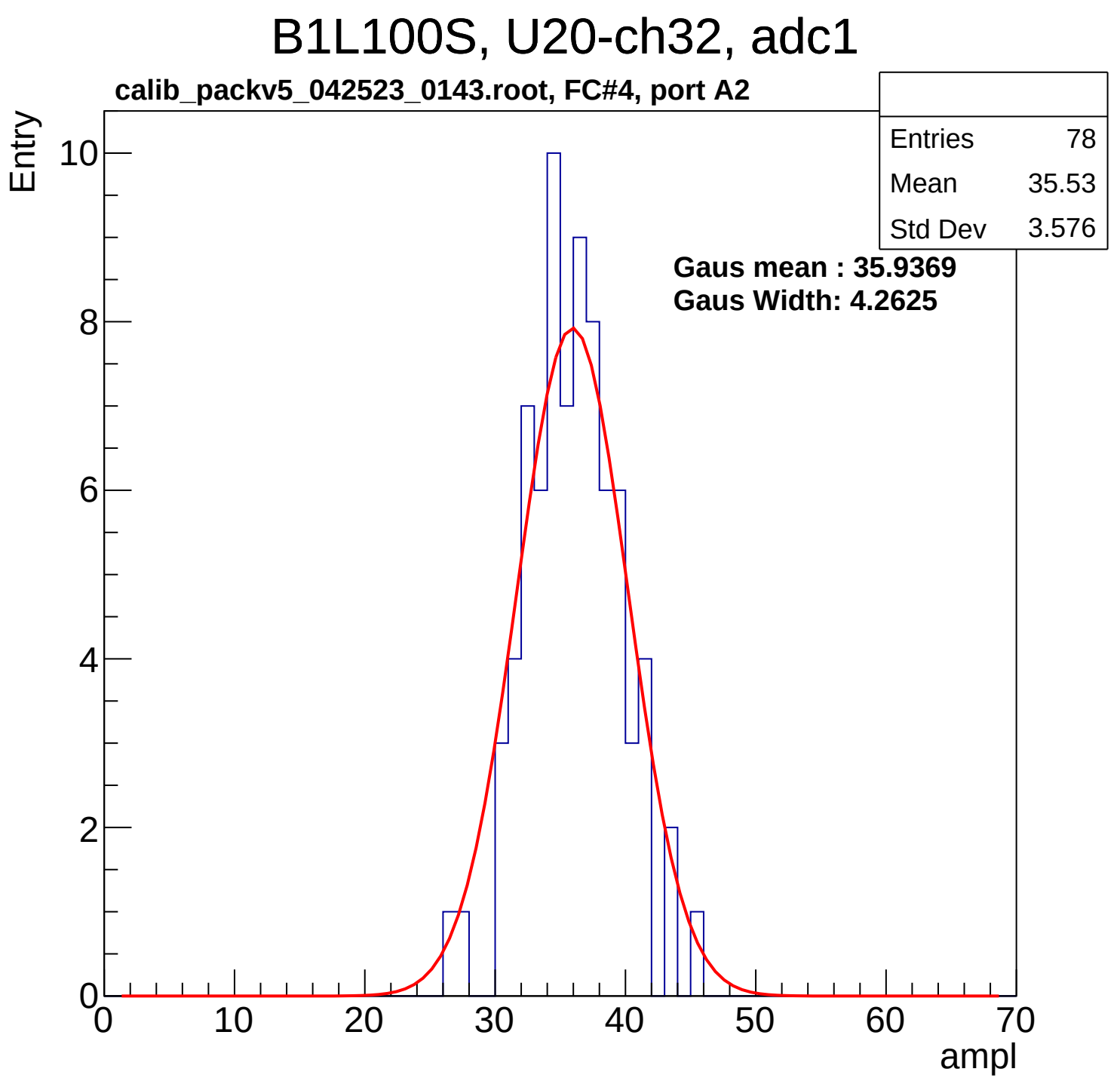
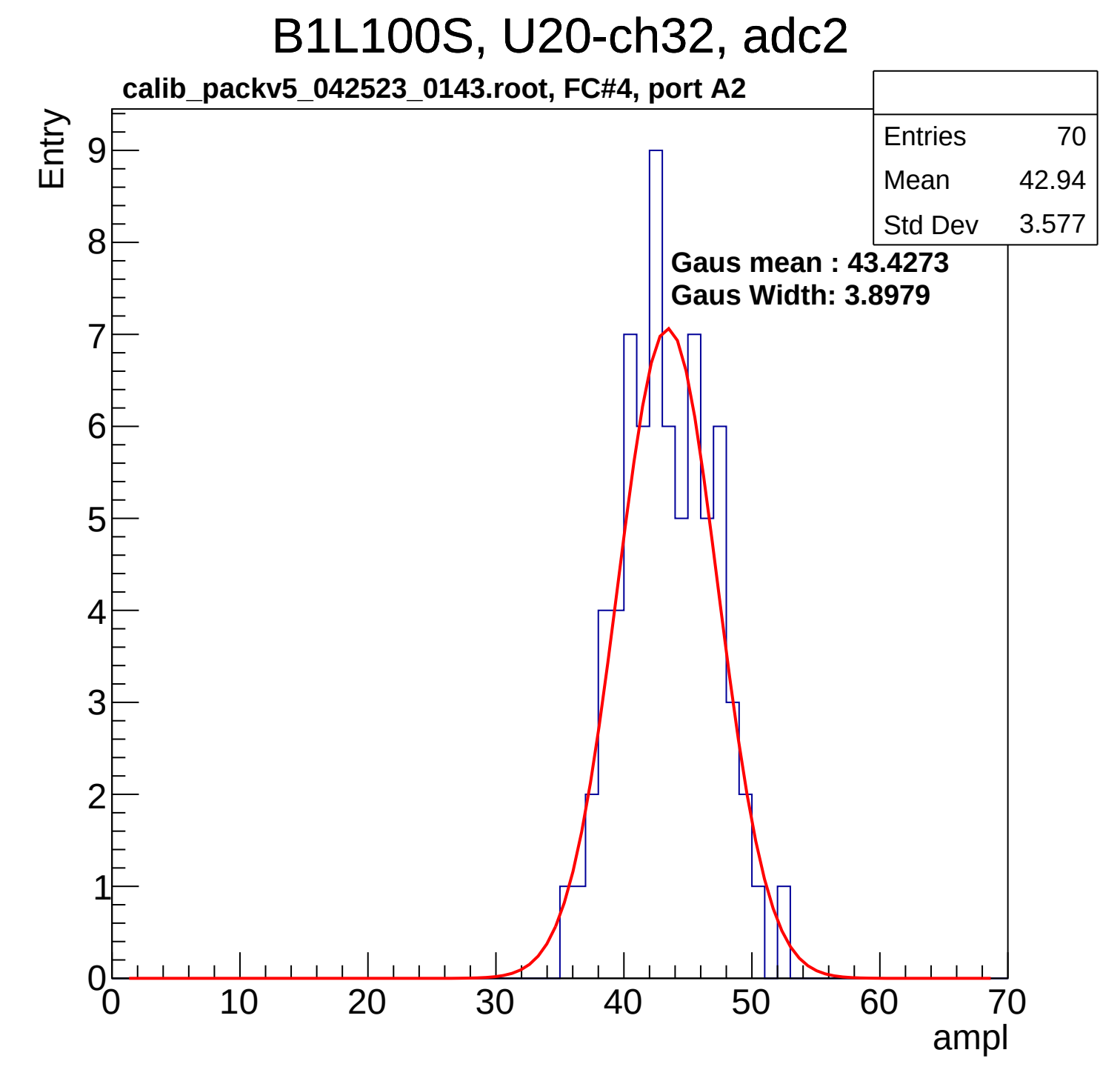
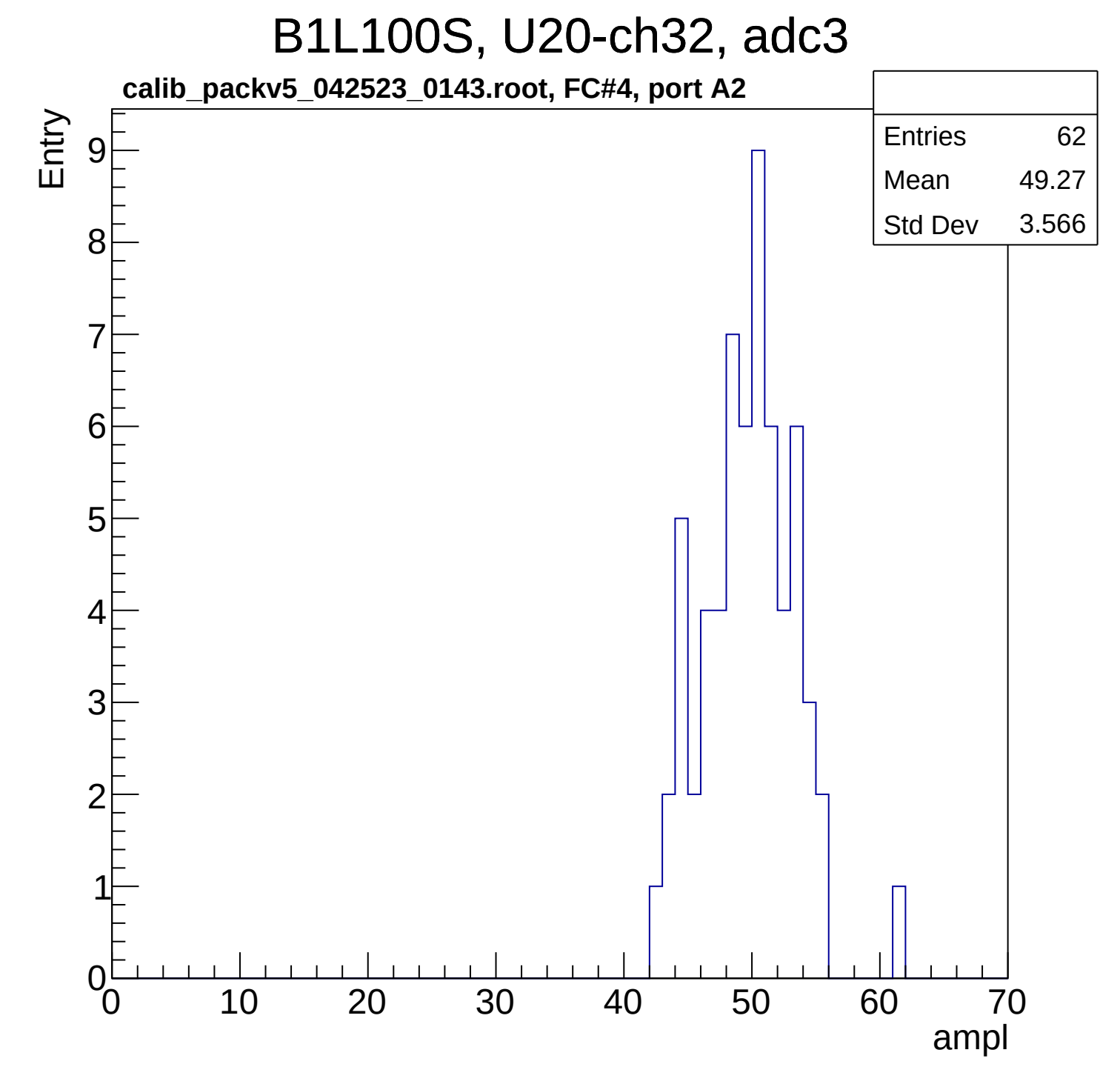
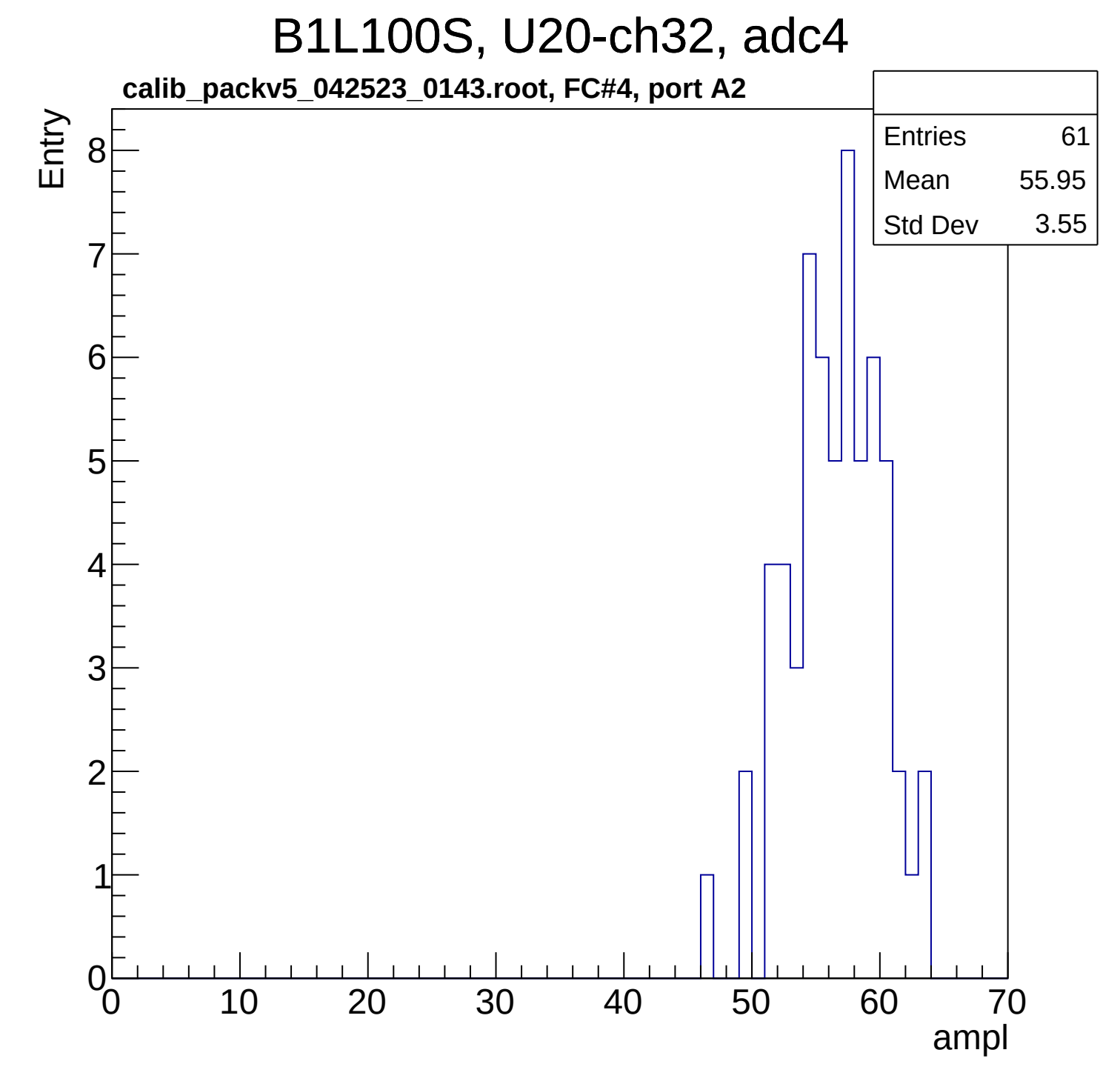
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

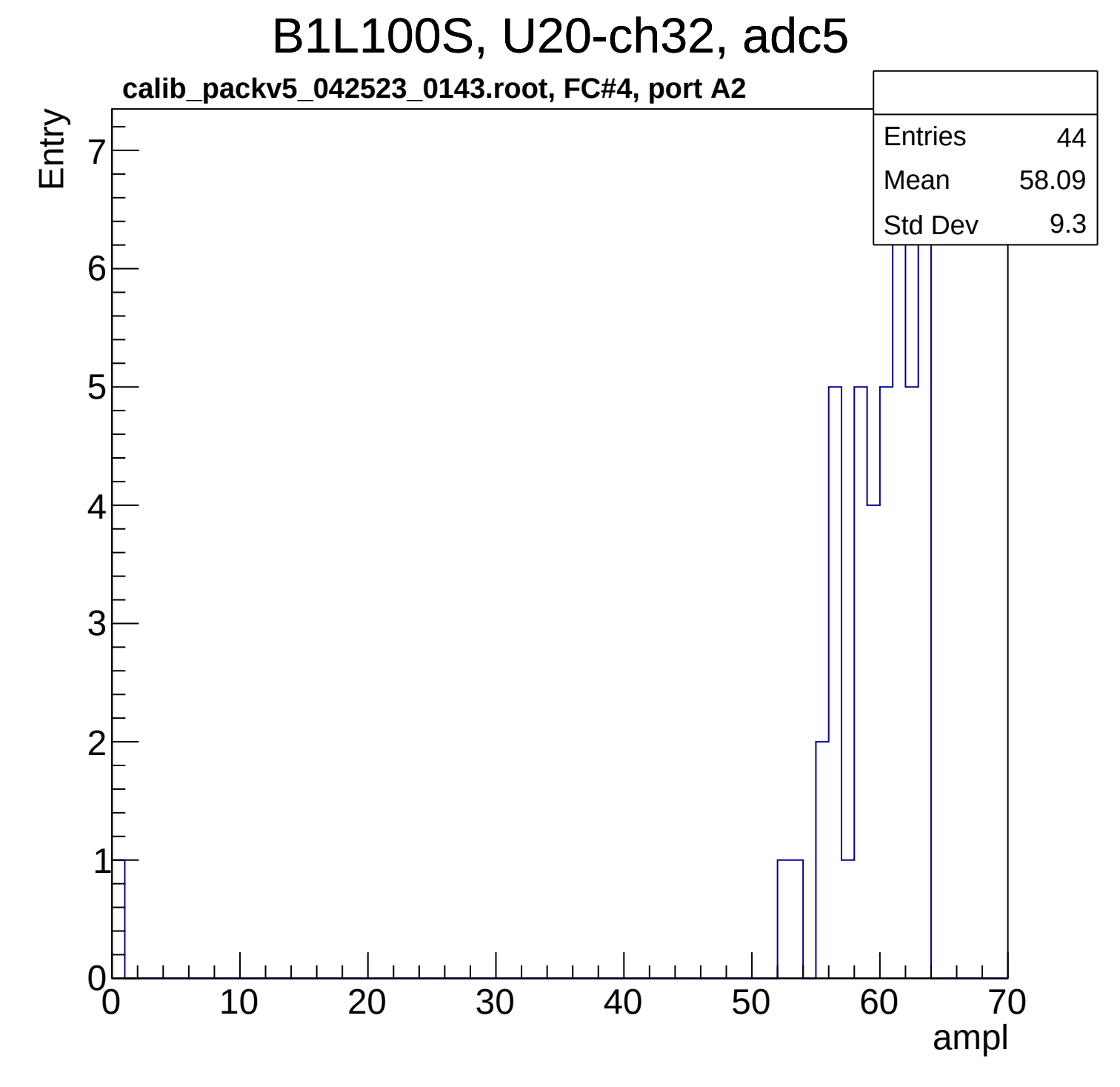

B1L100S, U20-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 27.23
Std Dev 4.748
Gaus mean : 28.6023
Gaus Width: 3.7941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

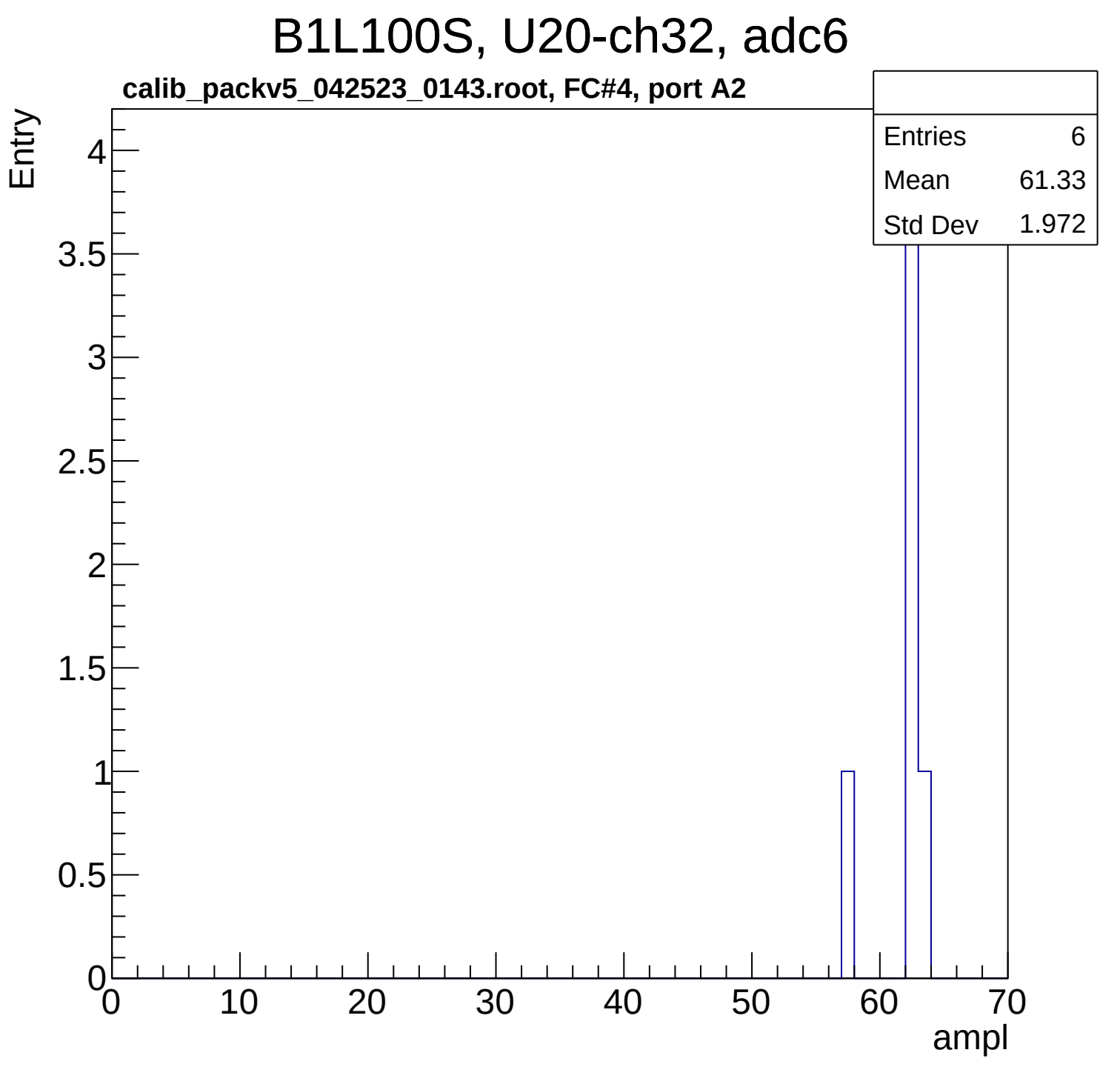

B1L100S, U20-ch32, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 35.53
Std Dev 3.576
Gaus mean : 35.9369
Gaus Width: 4.2625
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

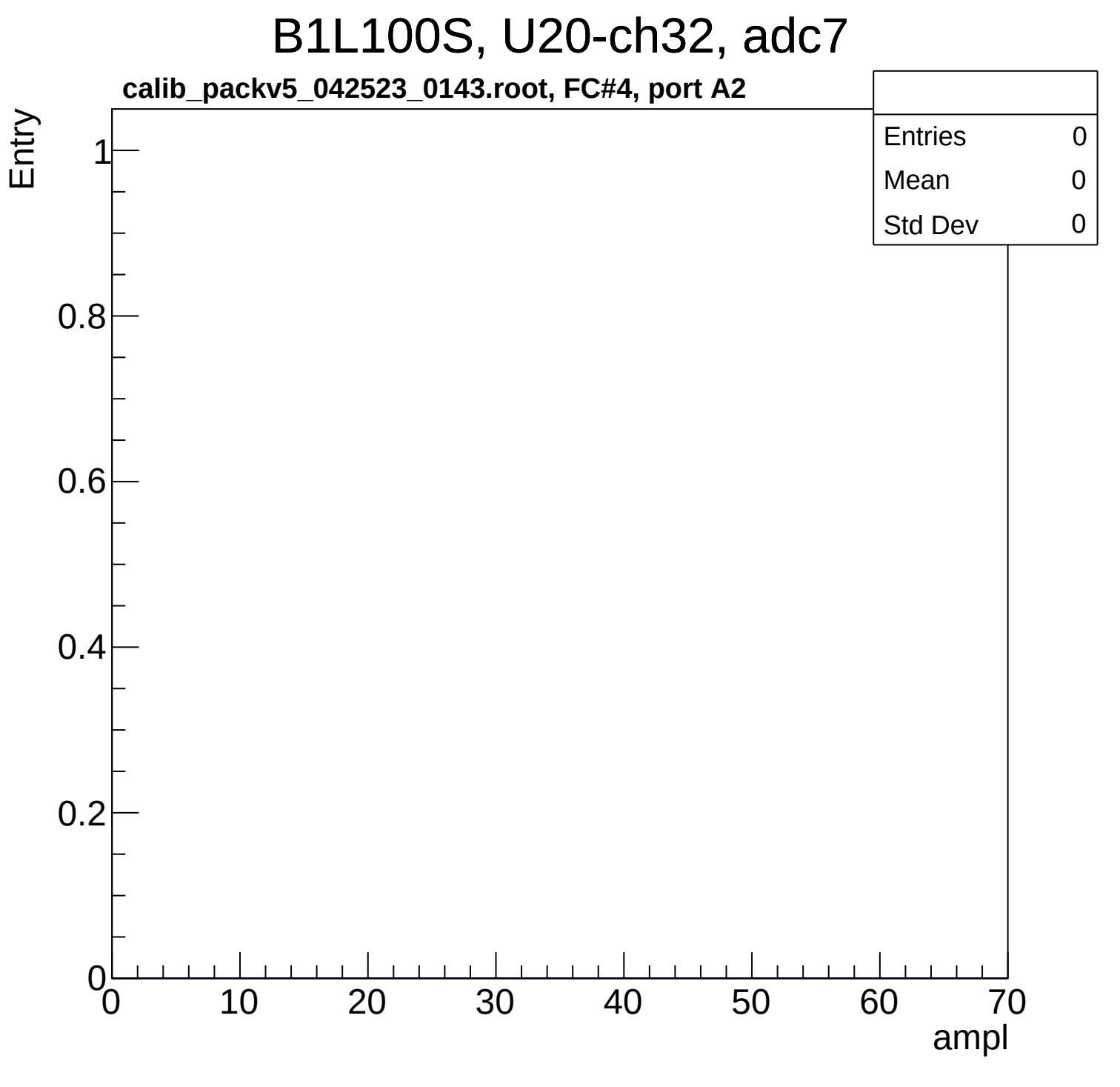

B1L100S, U20-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 42.94
Std Dev 3.577
Gaus mean : 43.4273
Gaus Width: 3.8979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

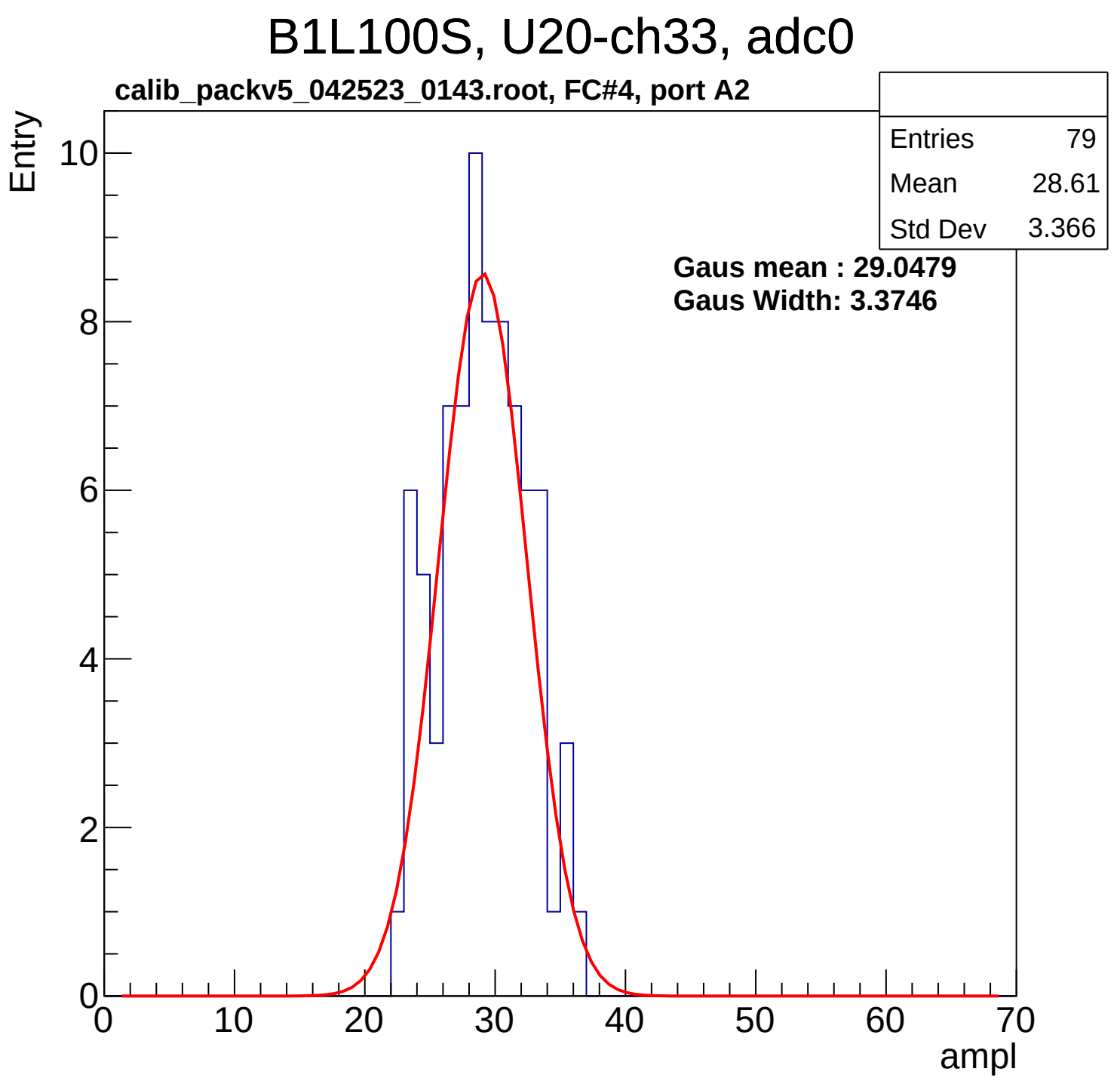

B1L100S, U20-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 49.27
Std Dev 3.566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

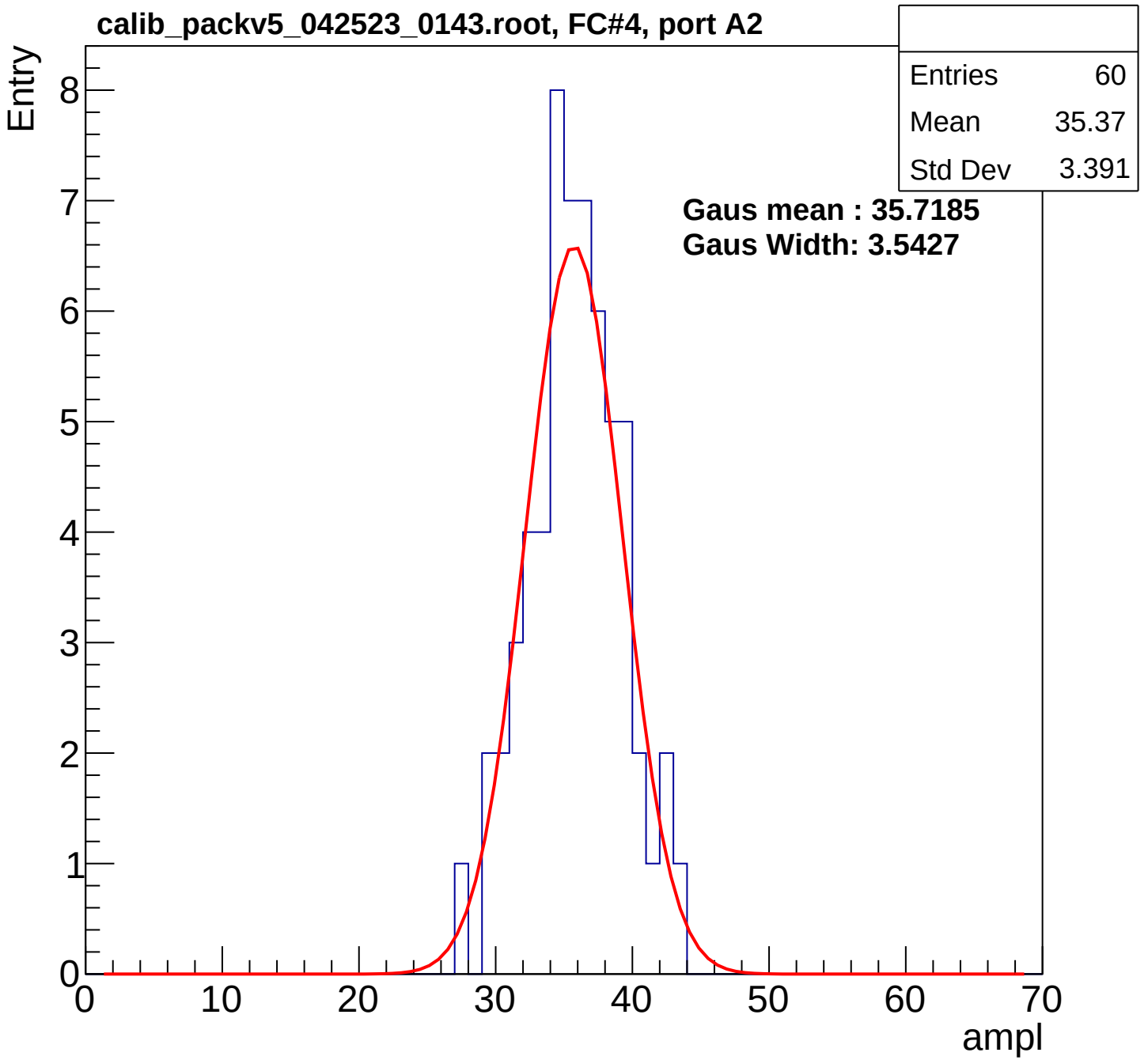

B1L100S, U20-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.95
Std Dev 3.55
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

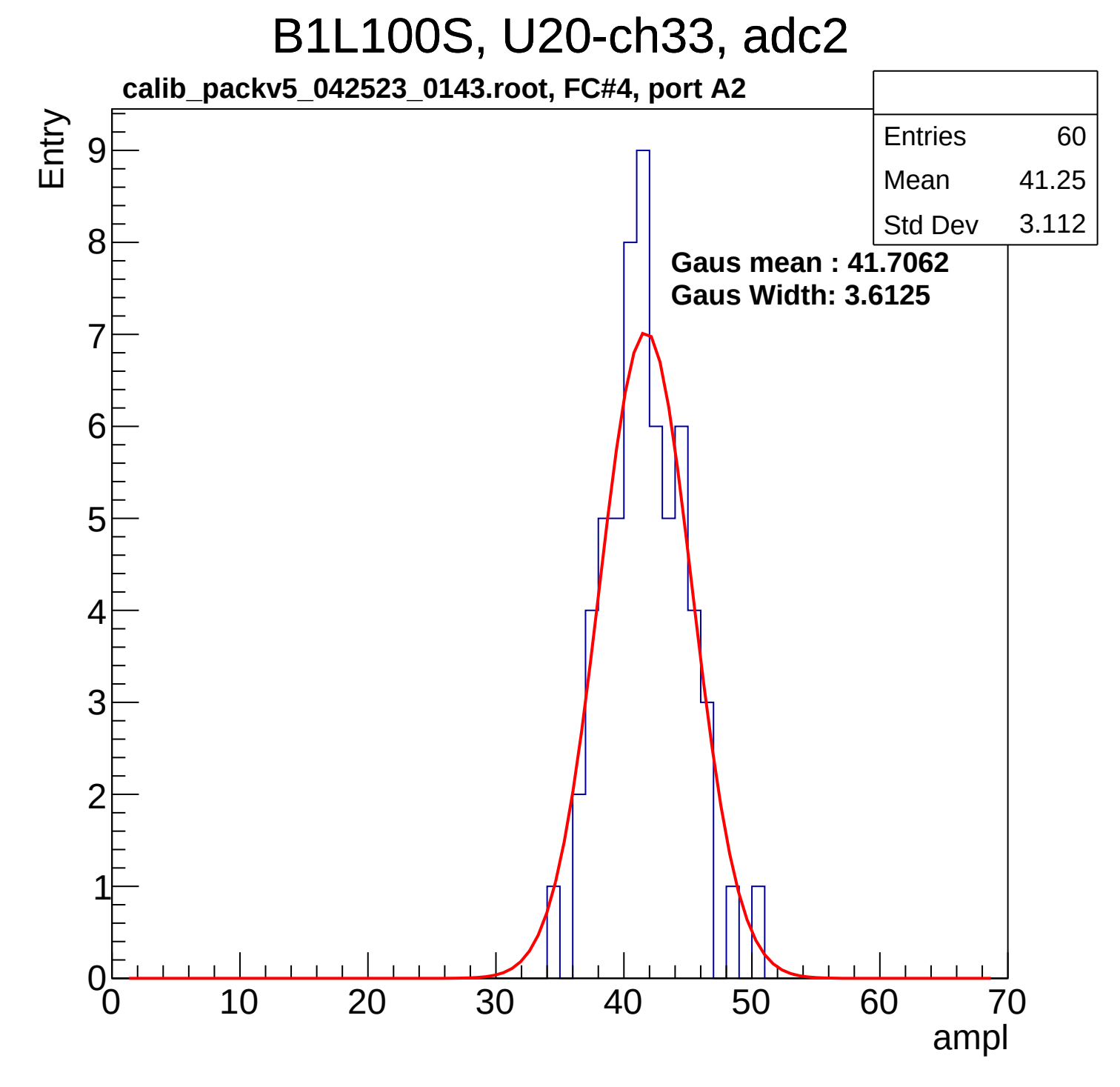

B1L100S, U20-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.09
Std Dev 9.3
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.33
Std Dev 1.972
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.61
Std Dev 3.366
Gaus mean : 29.0479
Gaus Width: 3.3746
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch33, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.37
Std Dev 3.391
Gaus mean : 35.7185
Gaus Width: 3.5427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.25
Std Dev 3.112
Gaus mean : 41.7062
Gaus Width: 3.6125
Entry
ampl
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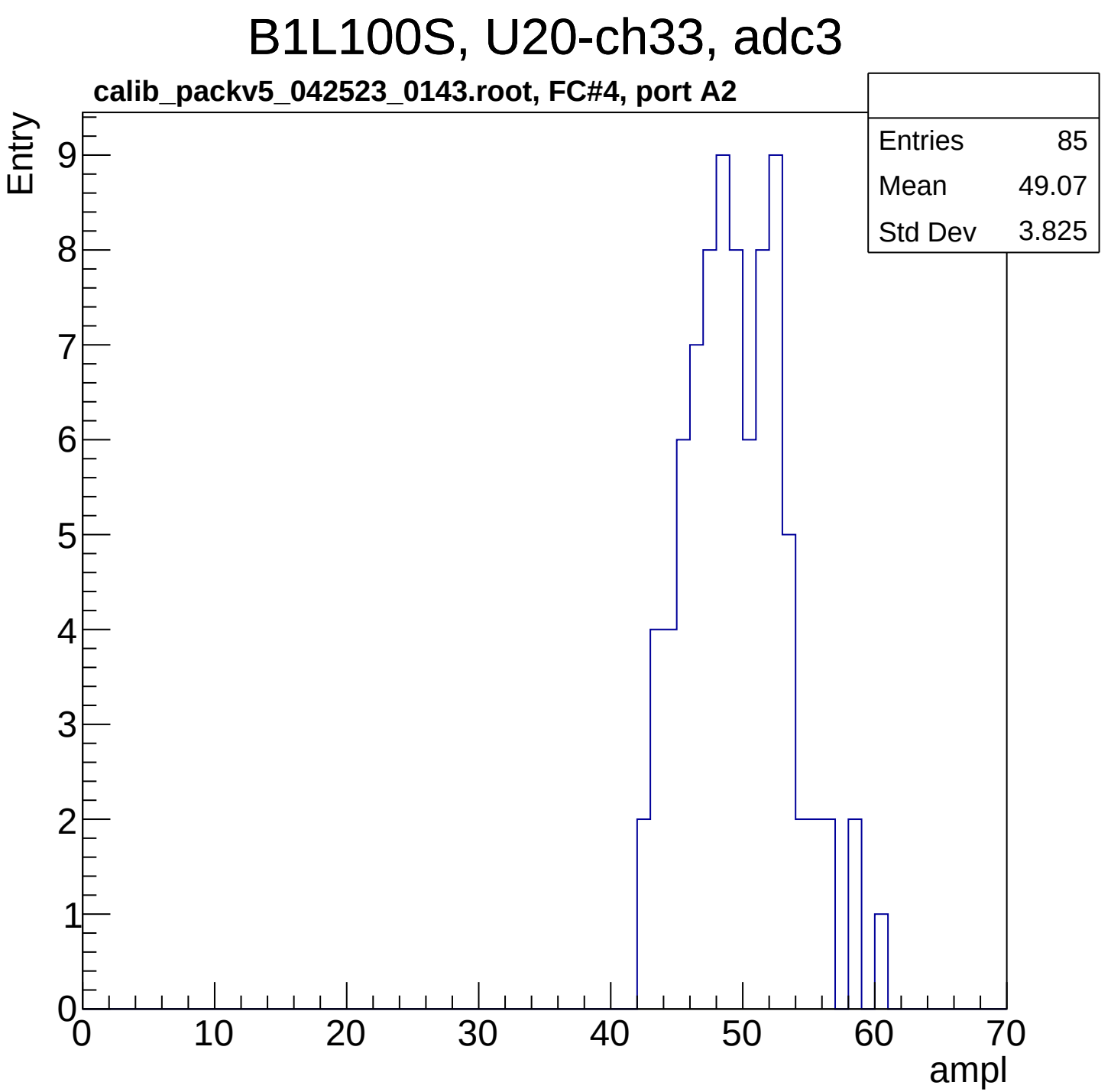
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U20-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 49.07
Std Dev 3.825
Entry
ampl
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2
3
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5
6
7
8
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0
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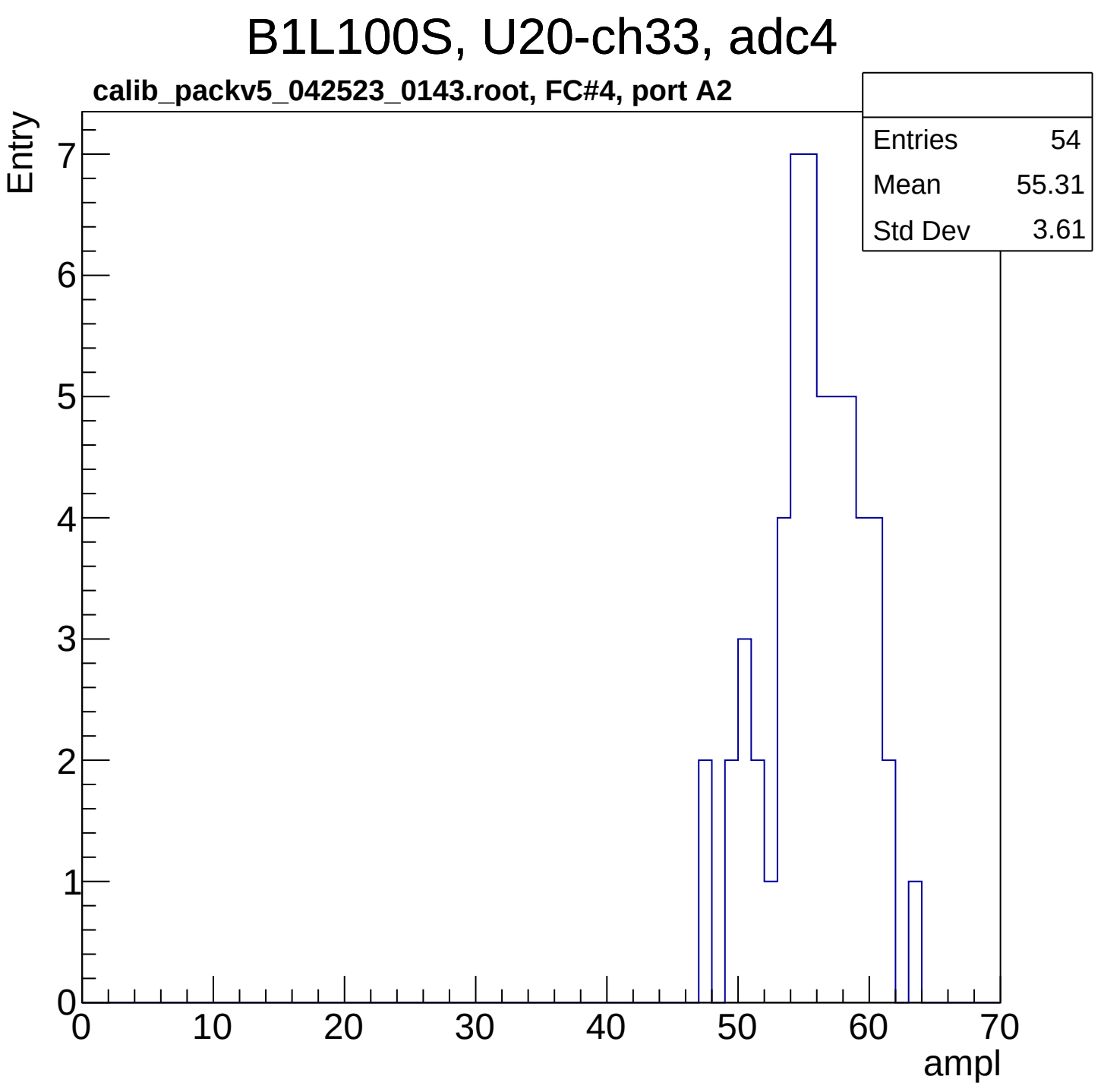
60
70


B1L100S, U20-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.31
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
0
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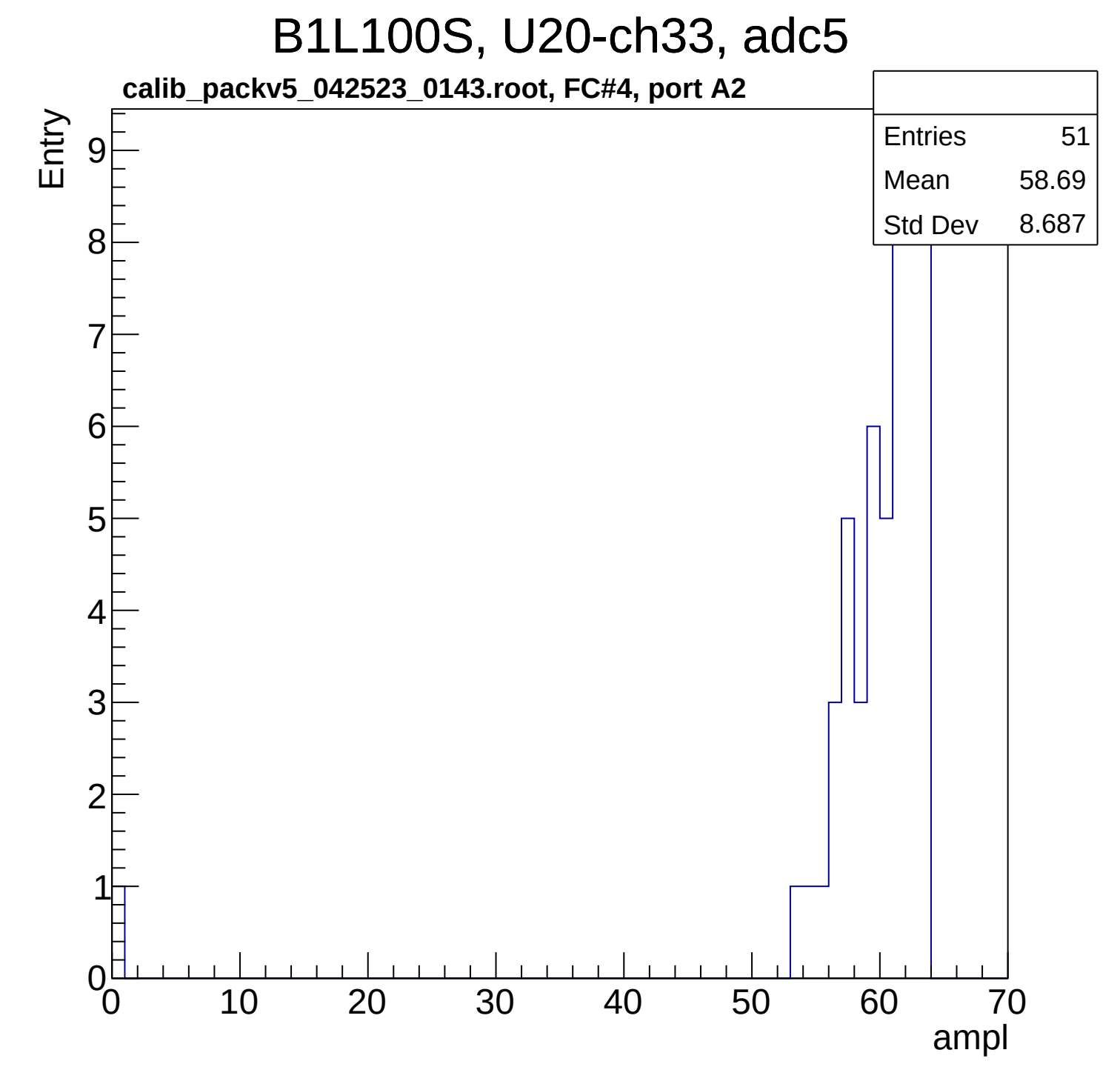
20
30
40
50
60
70


B1L100S, U20-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.69
Std Dev 8.687
Entry
ampl
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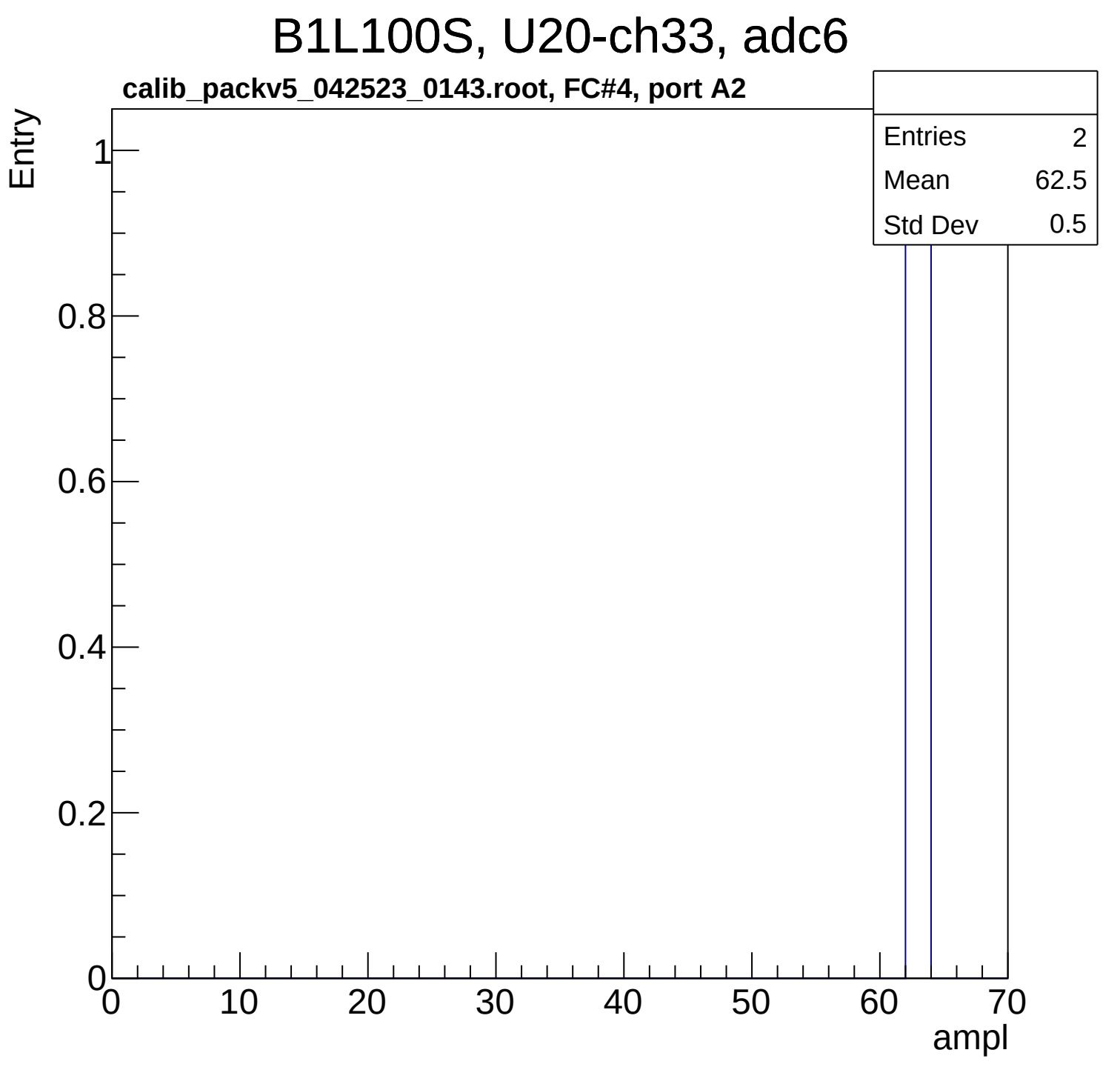
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch33, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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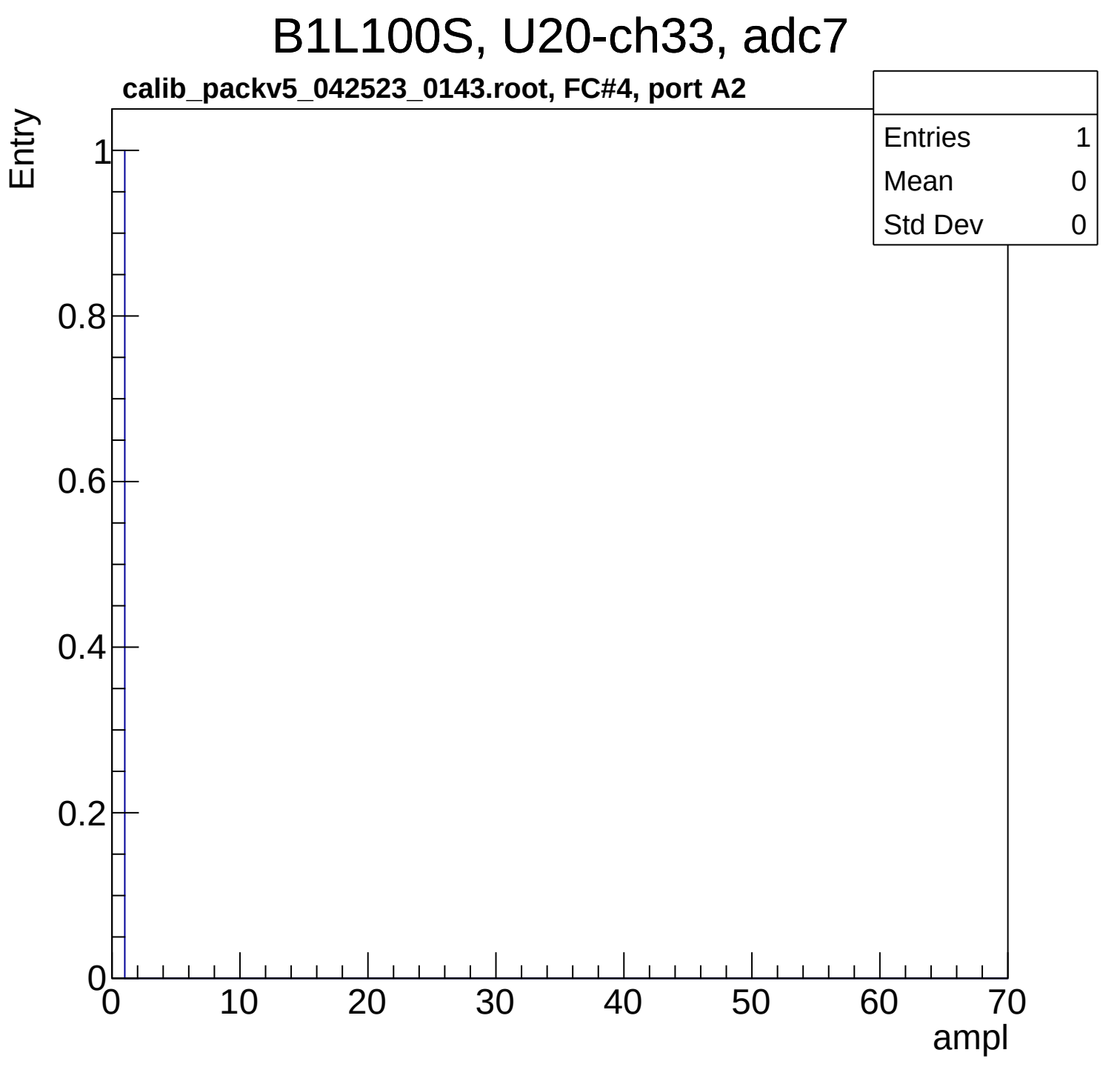
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch33, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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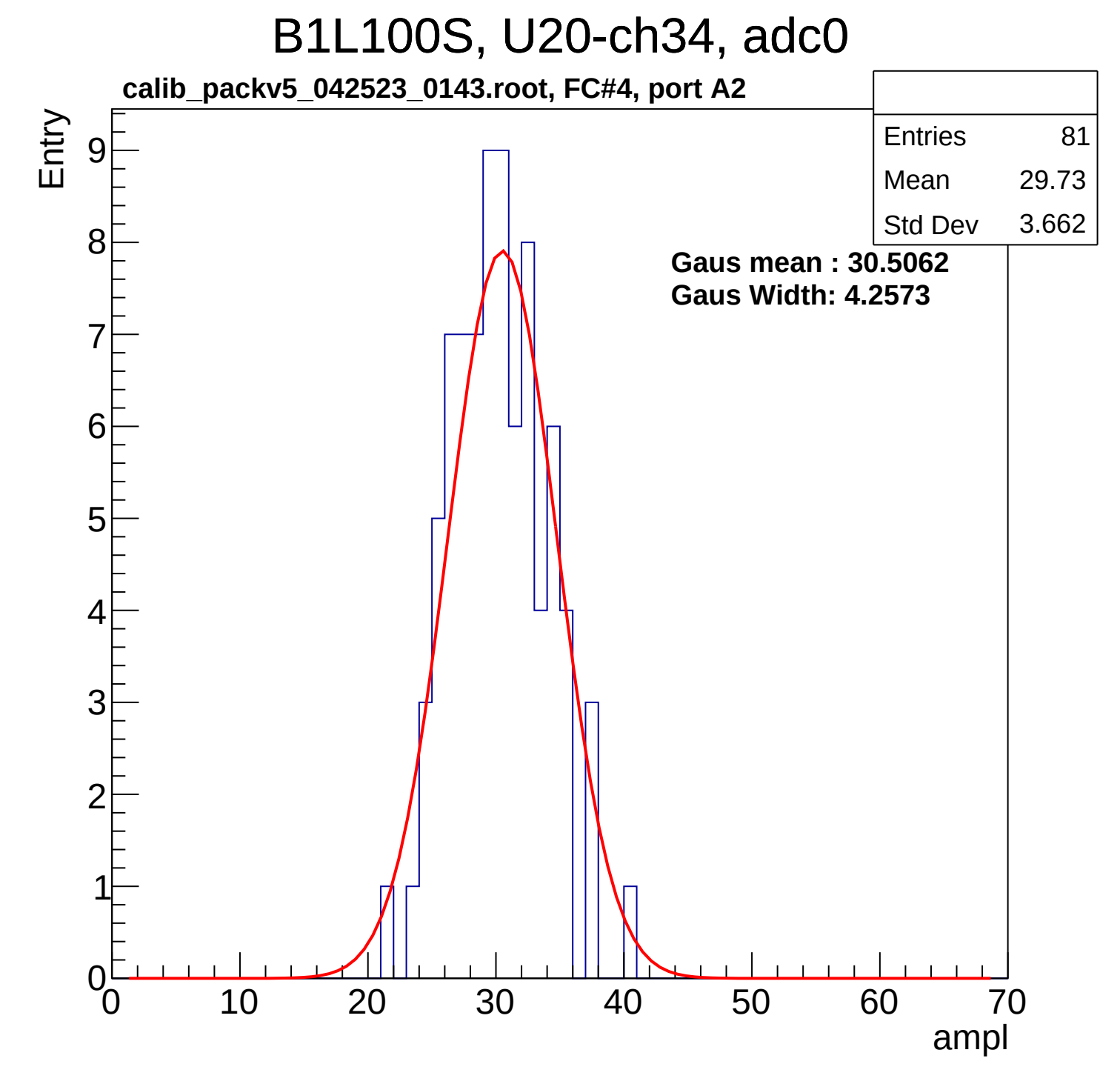
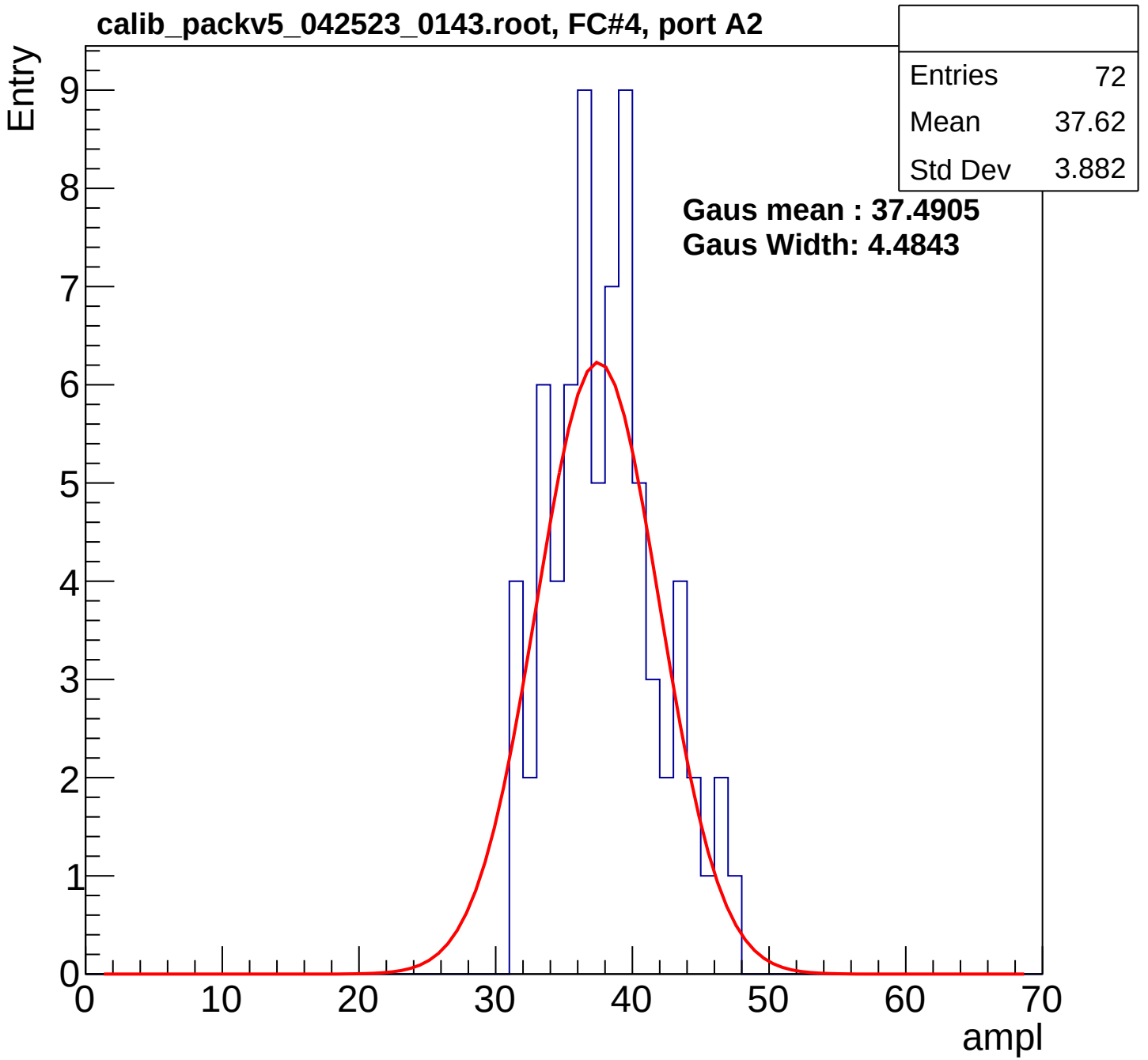
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.73
Std Dev 3.662
Gaus mean : 30.5062
Gaus Width: 4.2573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.62
Std Dev 3.882
Gaus mean : 37.4905
Gaus Width: 4.4843
Entry
ampl
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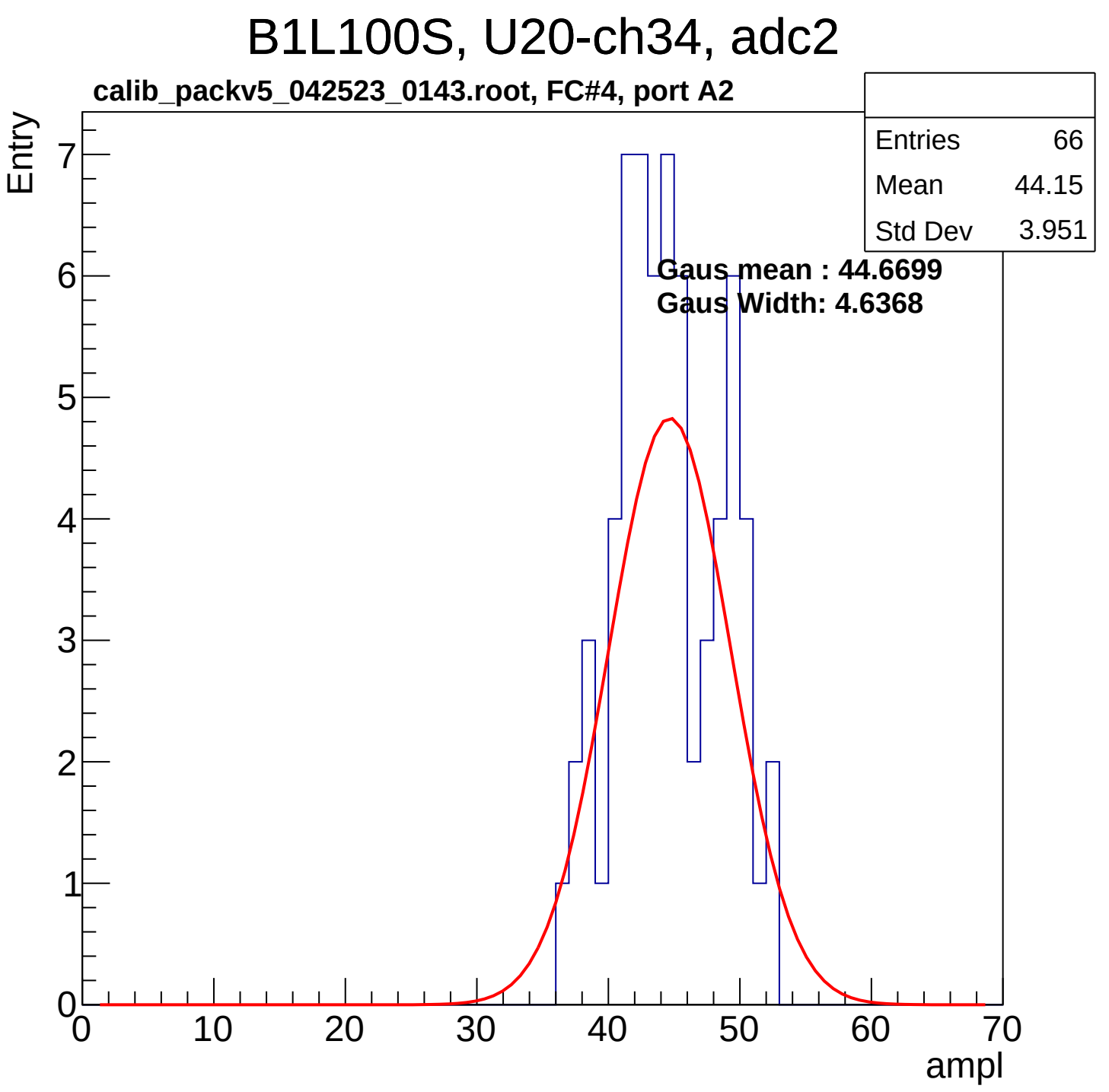
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 44.15
Std Dev 3.951
Gaus mean : 44.6699
Gaus Width: 4.6368
Entry
ampl
0
1
2
3
4
5
6
7
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60
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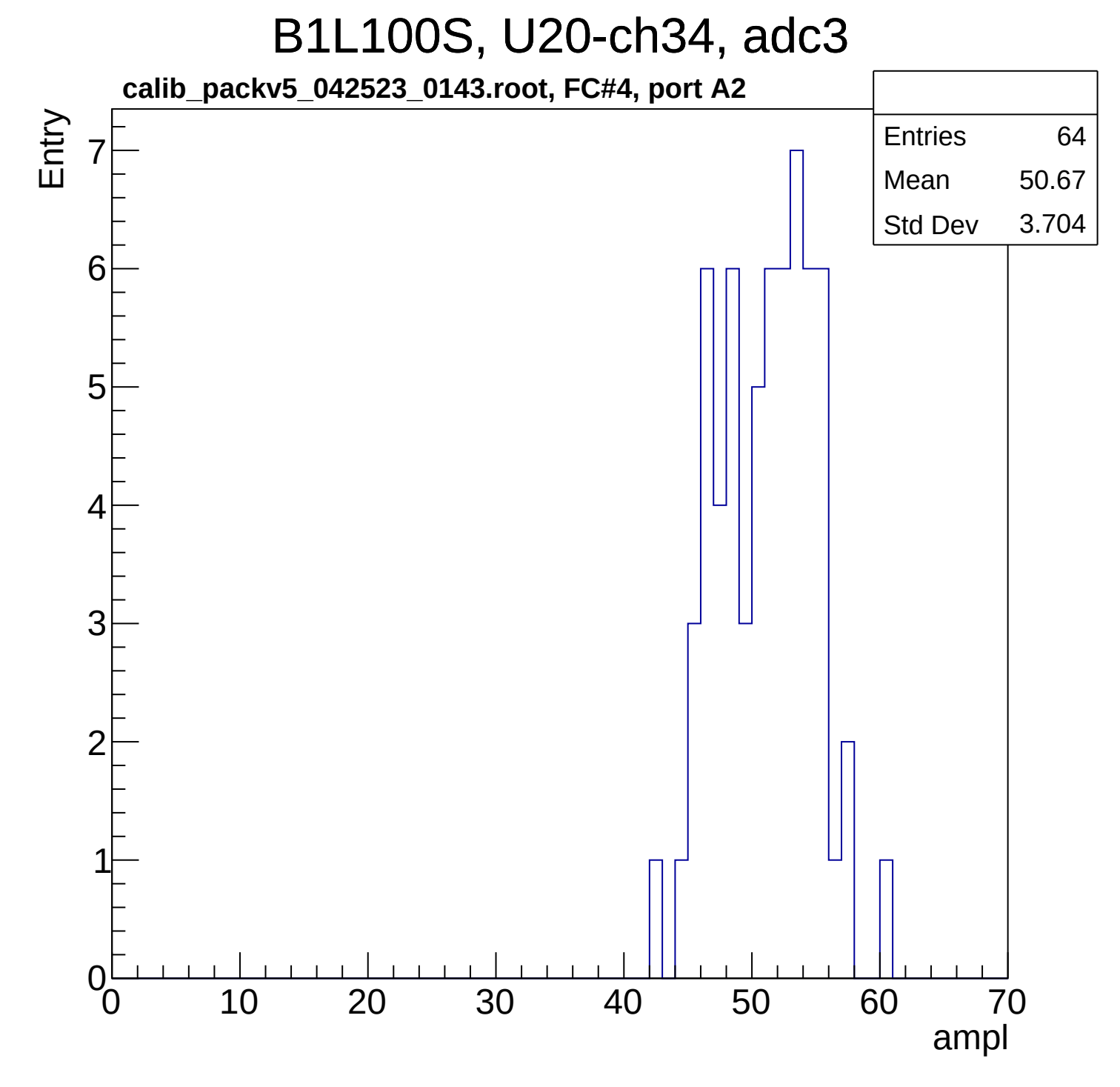
B1L100S, U20-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.67
Std Dev 3.704
Entry
ampl
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3
4
5
6
7
0
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20
30
40
50
60
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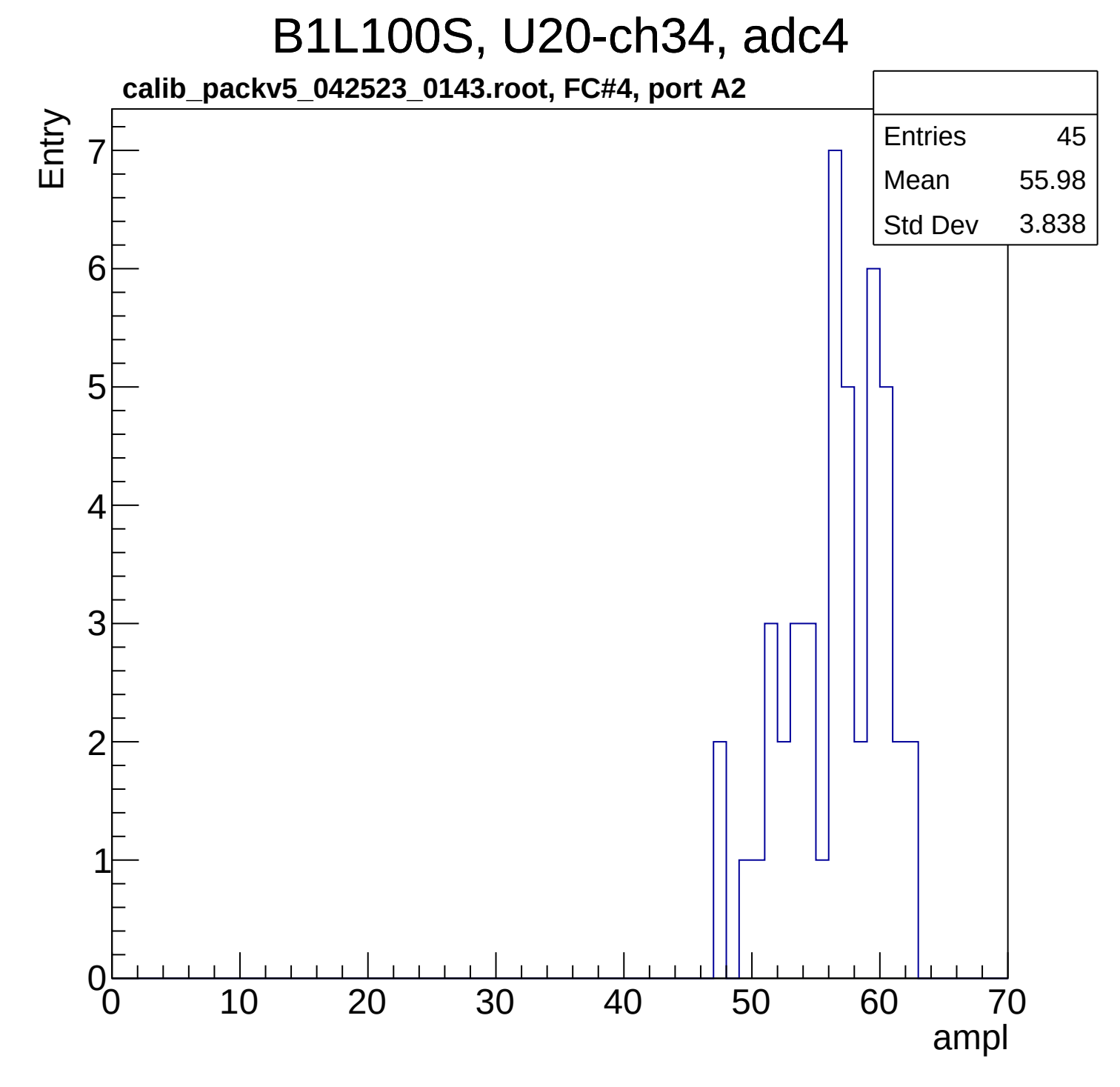
B1L100S, U20-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 55.98
Std Dev 3.838
Entry
ampl
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3
4
5
6
7
0
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40
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60
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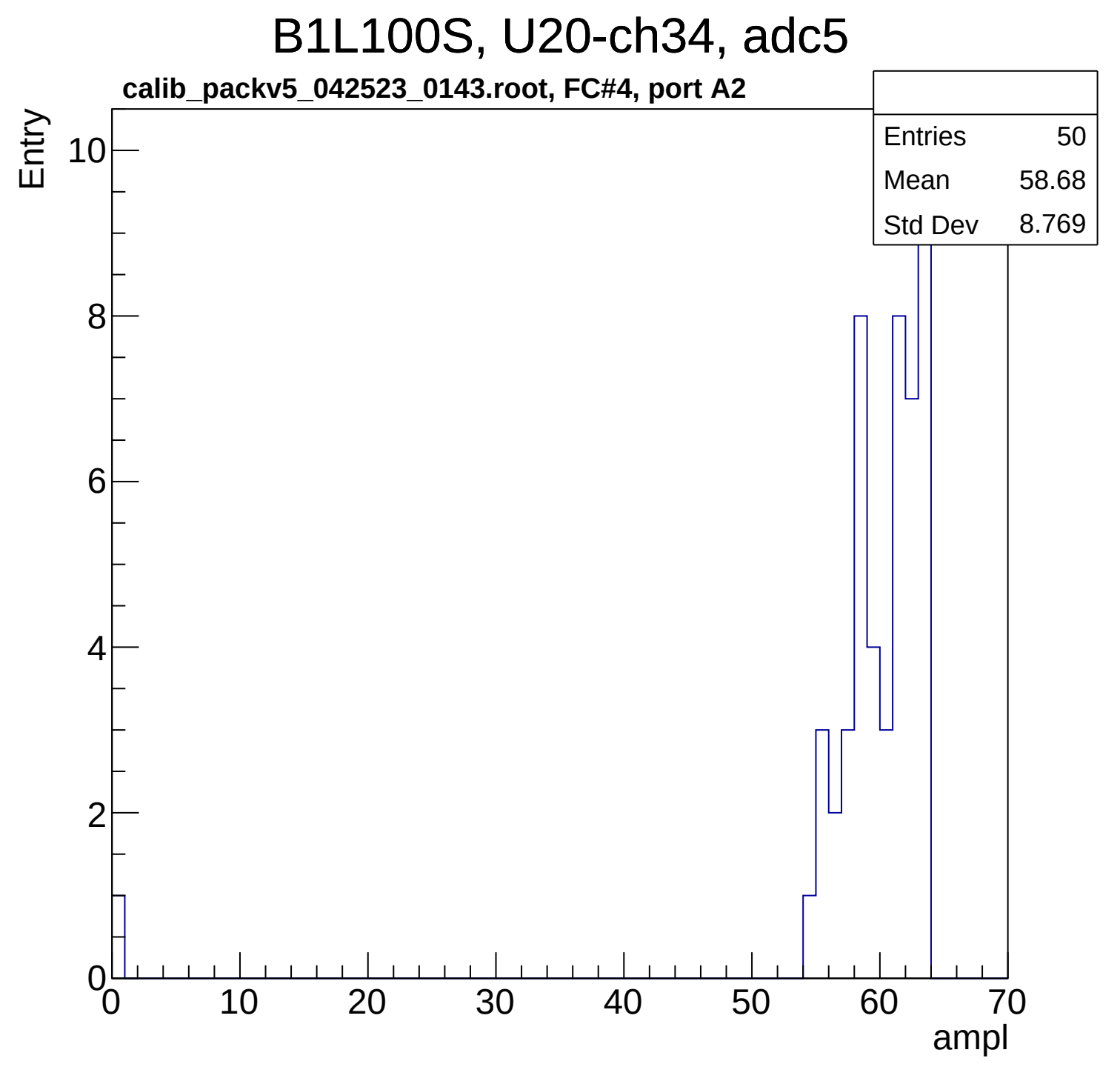
B1L100S, U20-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.68
Std Dev 8.769
Entry
ampl
0
2
4
6
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0
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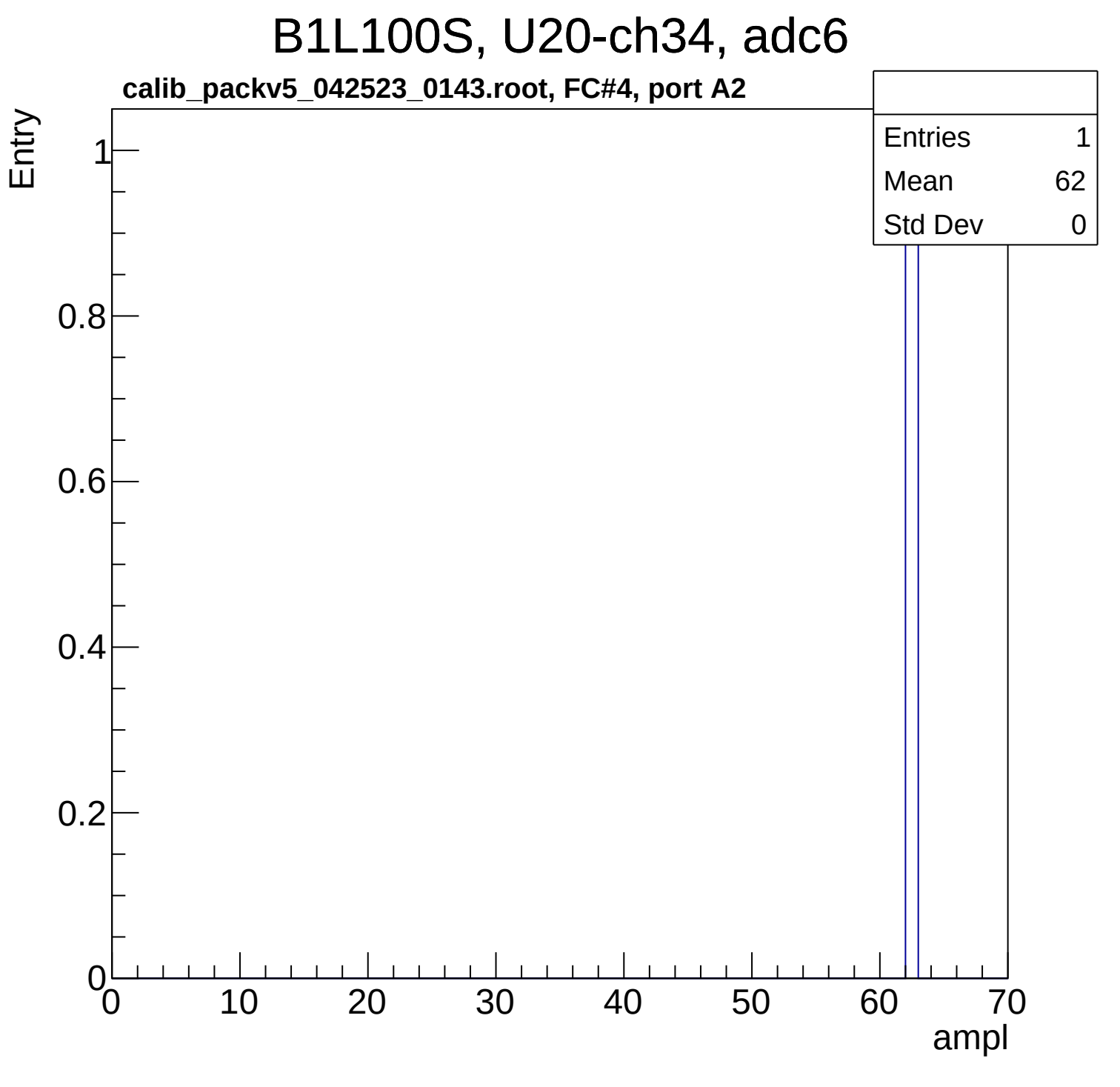
B1L100S, U20-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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0.6
0.4
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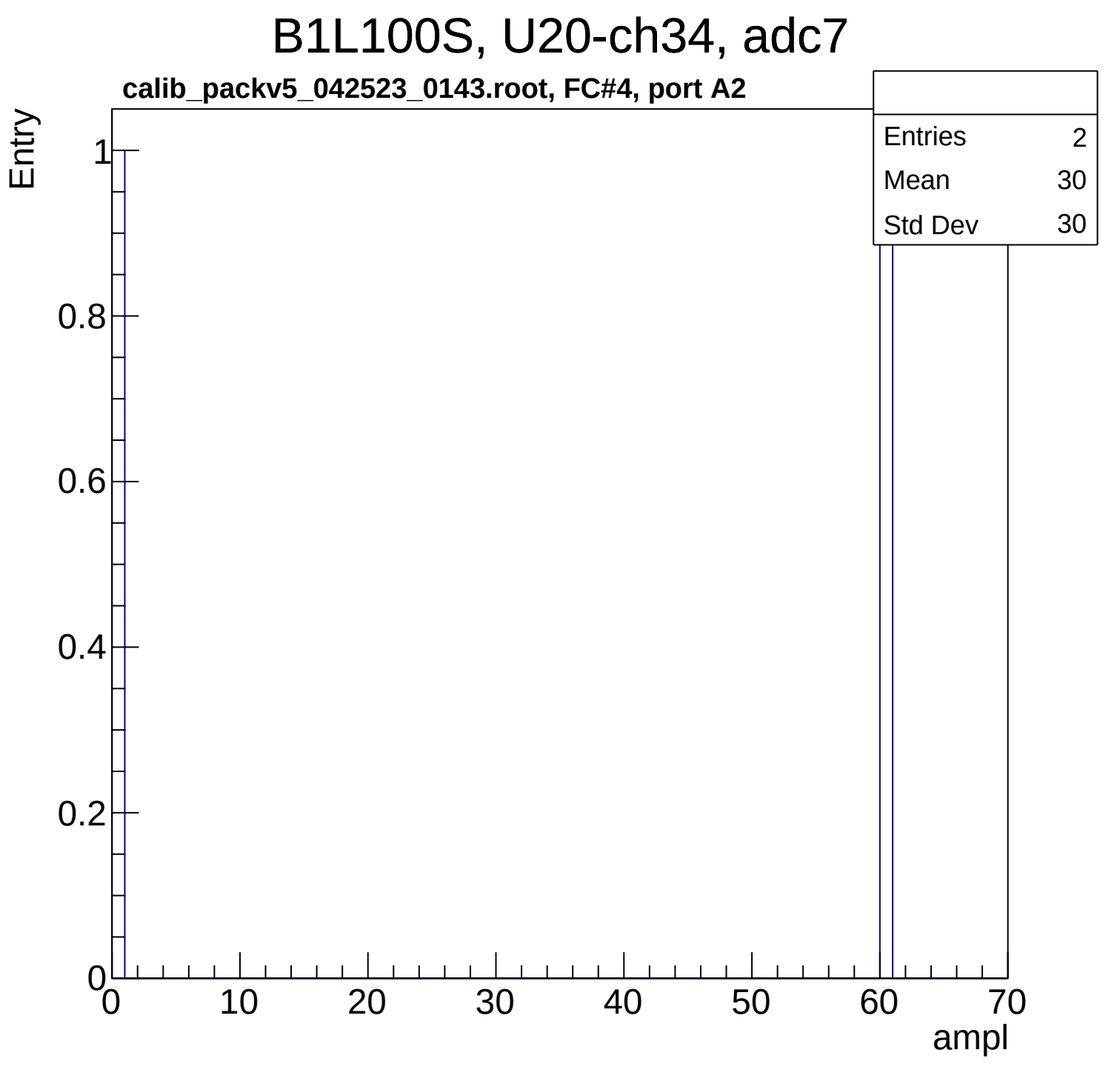
70
ampl


B1L100S, U20-ch34, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 30
Std Dev 30
Entry
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0.6
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0.2
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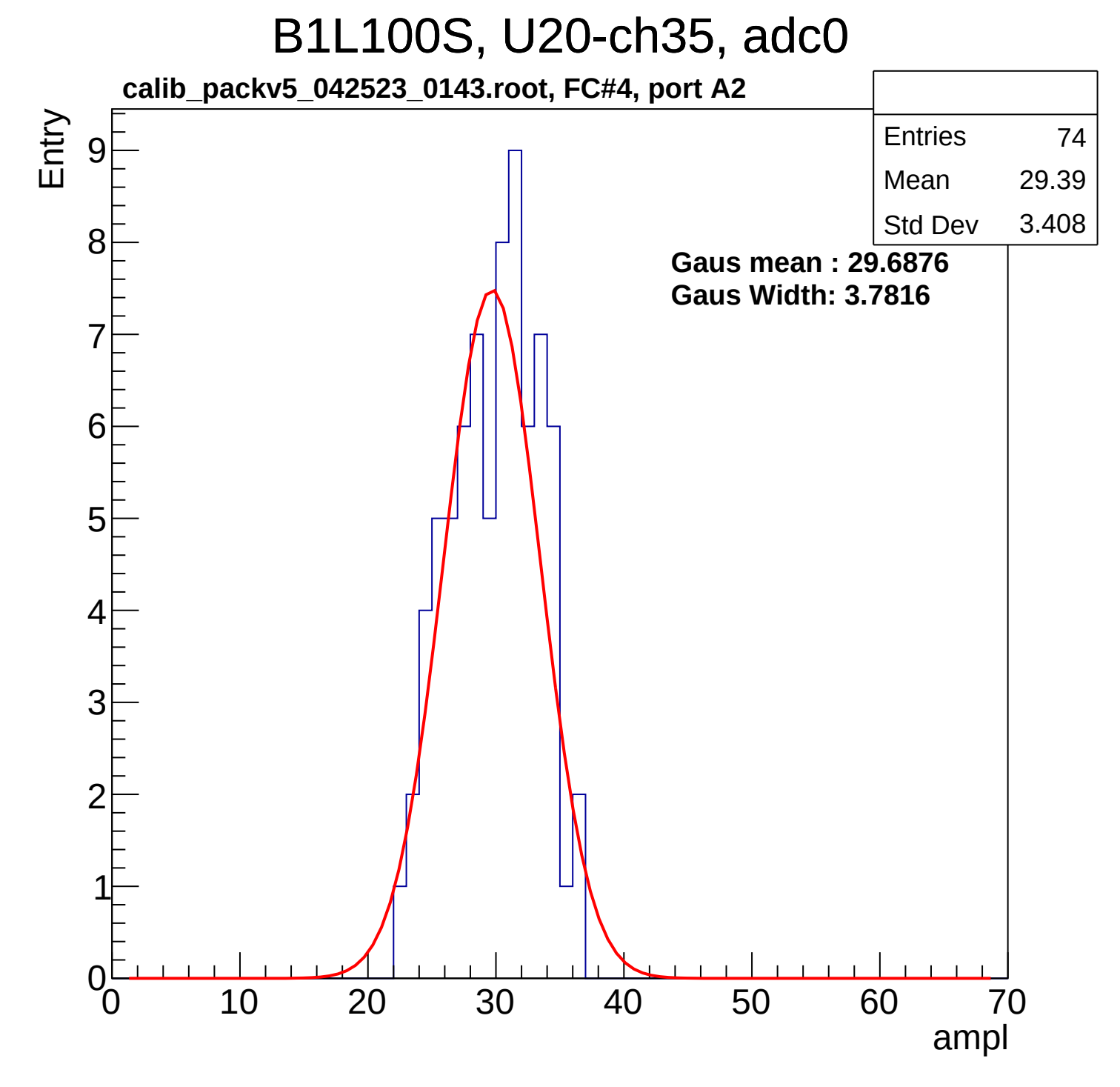
70
ampl


B1L100S, U20-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.39
Std Dev 3.408
Gaus mean : 29.6876
Gaus Width: 3.7816
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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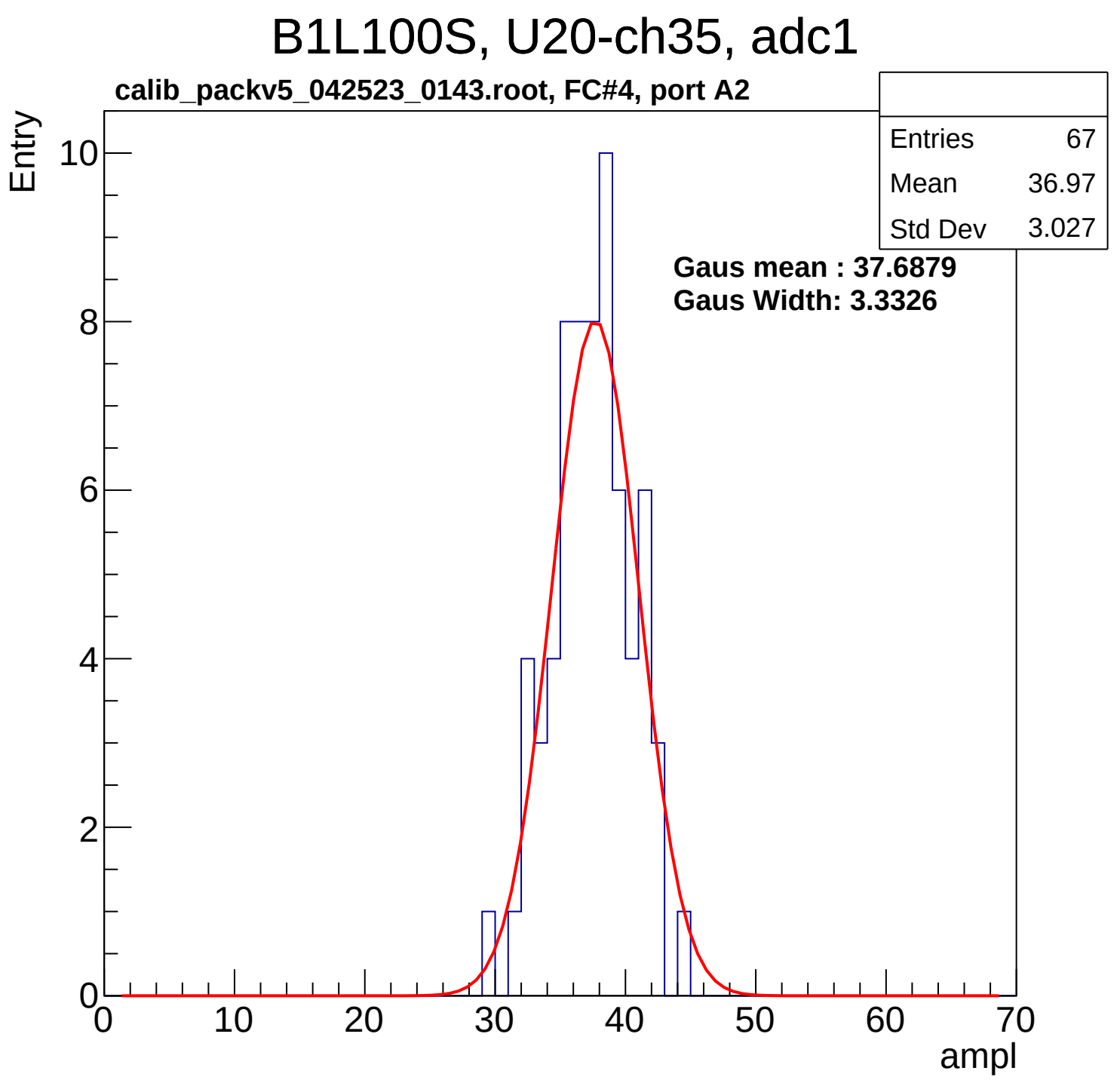
B1L100S, U20-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.97
Std Dev 3.027
Gaus mean : 37.6879
Gaus Width: 3.3326
Entry
ampl
0
2
4
6
8
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0
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40
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60
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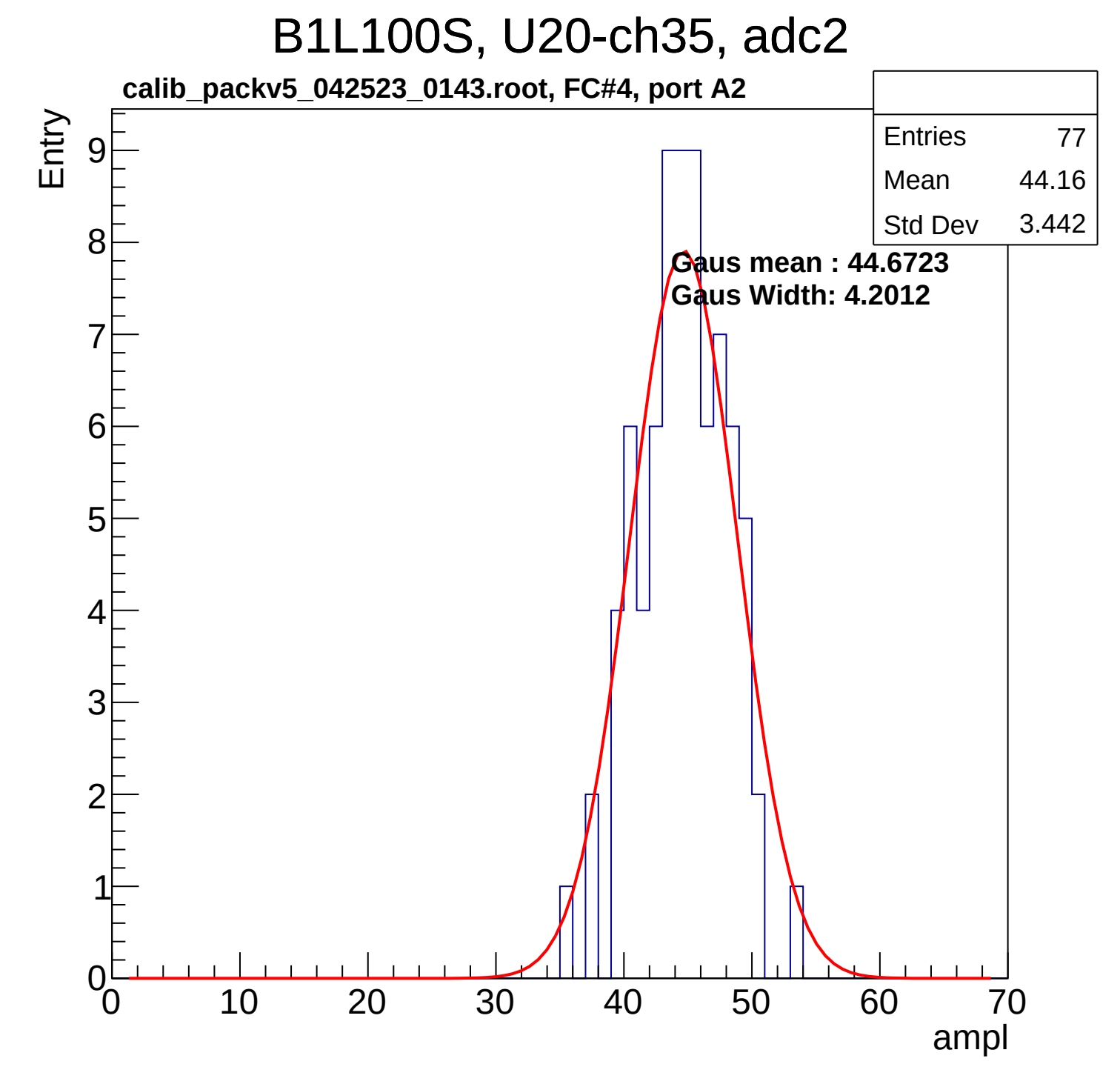
B1L100S, U20-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 44.16
Std Dev 3.442
Gaus mean : 44.6723
Gaus Width: 4.2012
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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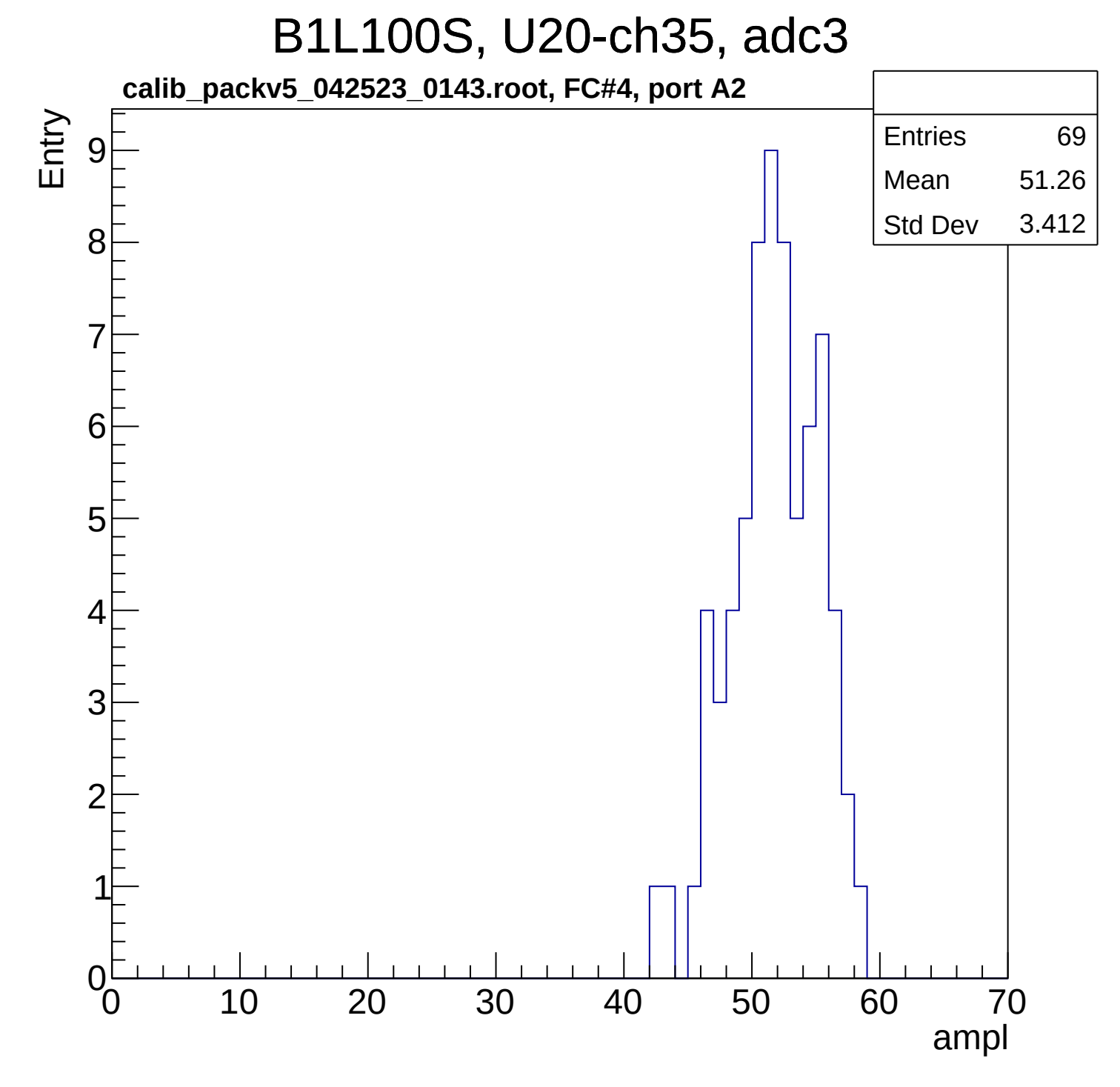
B1L100S, U20-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.26
Std Dev 3.412
Entry
ampl
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2
3
4
5
6
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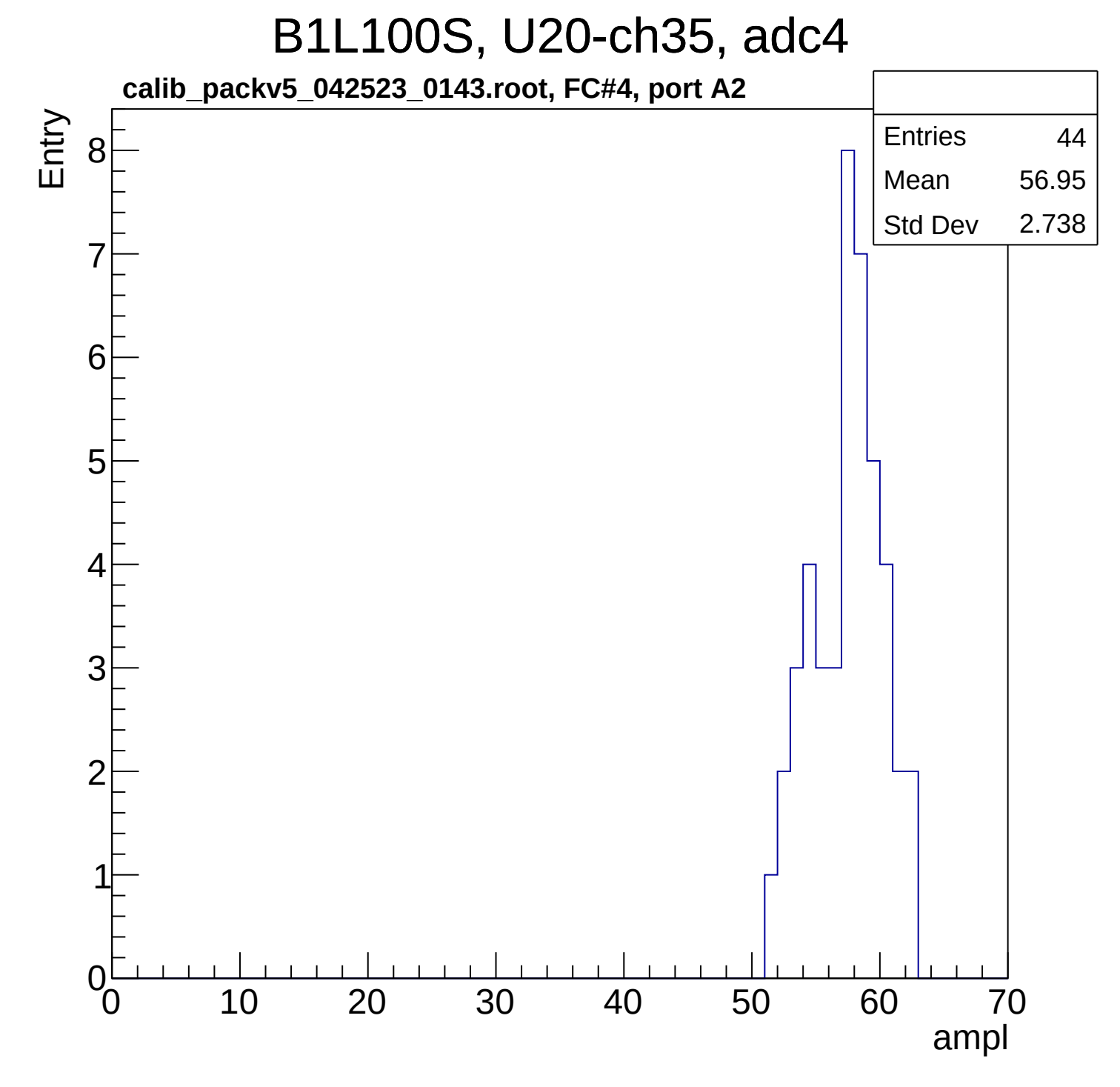
B1L100S, U20-ch35, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 56.95
Std Dev 2.738
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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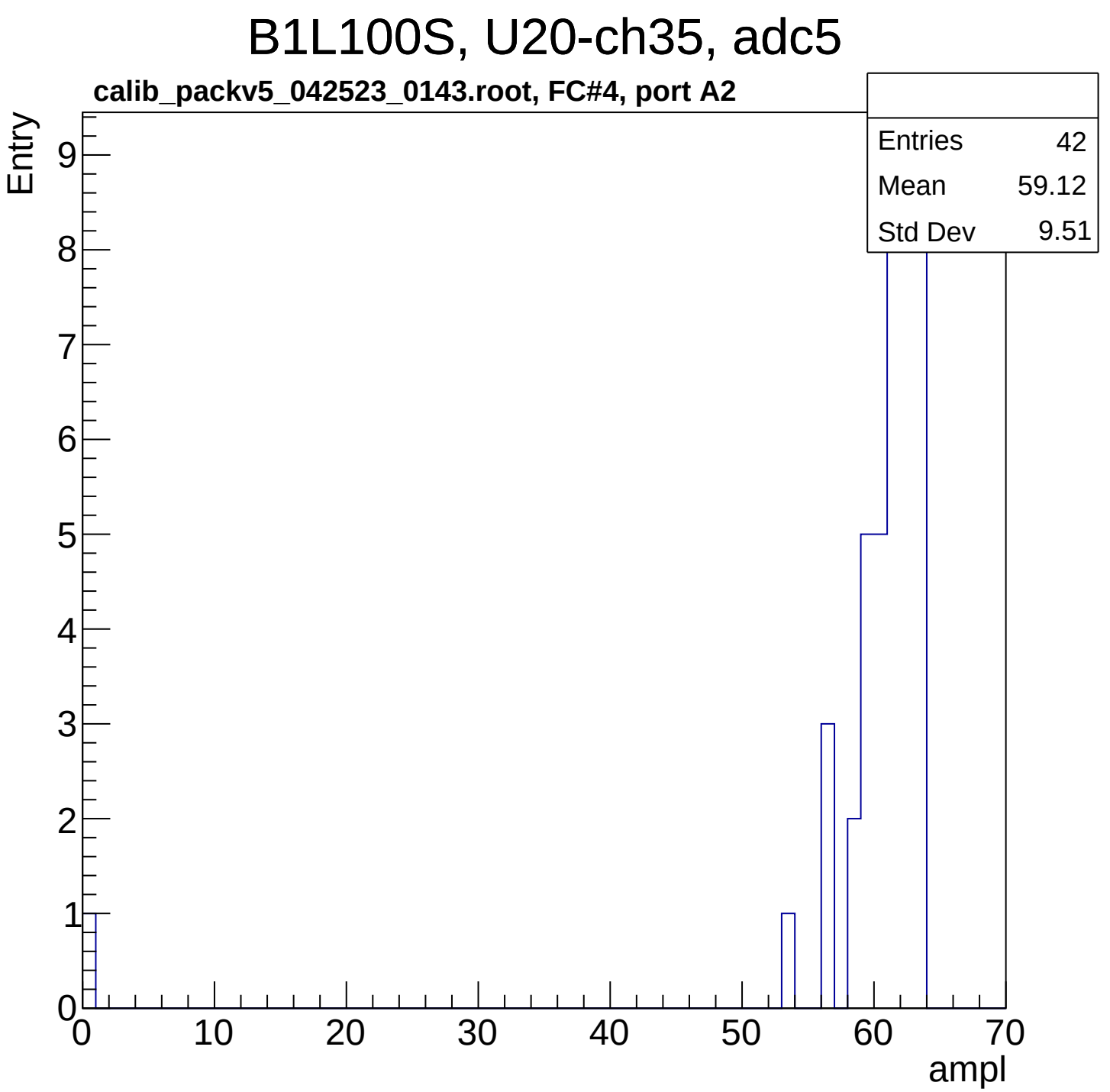
B1L100S, U20-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.12
Std Dev 9.51
Entry
ampl
0
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2
3
4
5
6
7
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0
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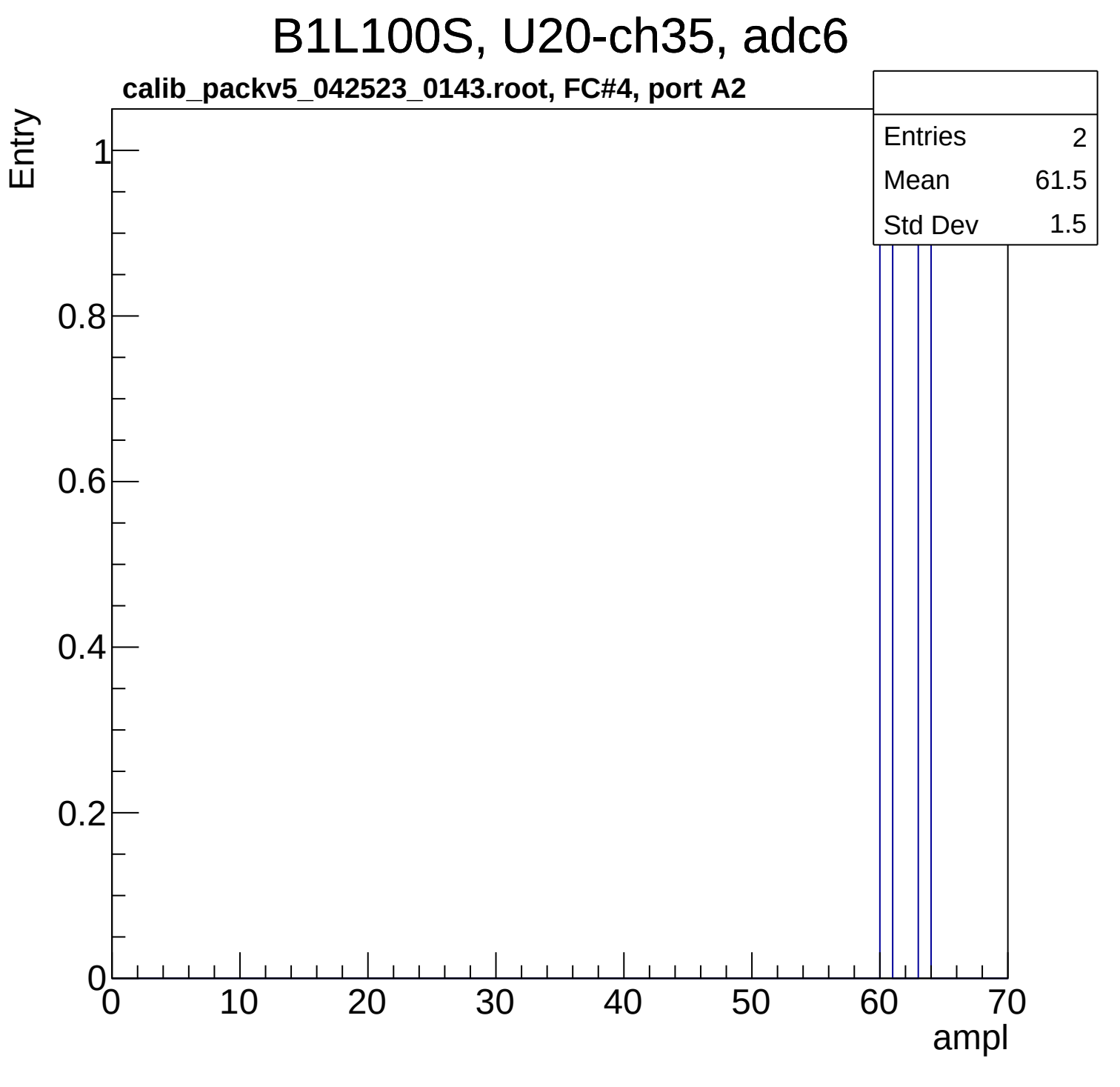
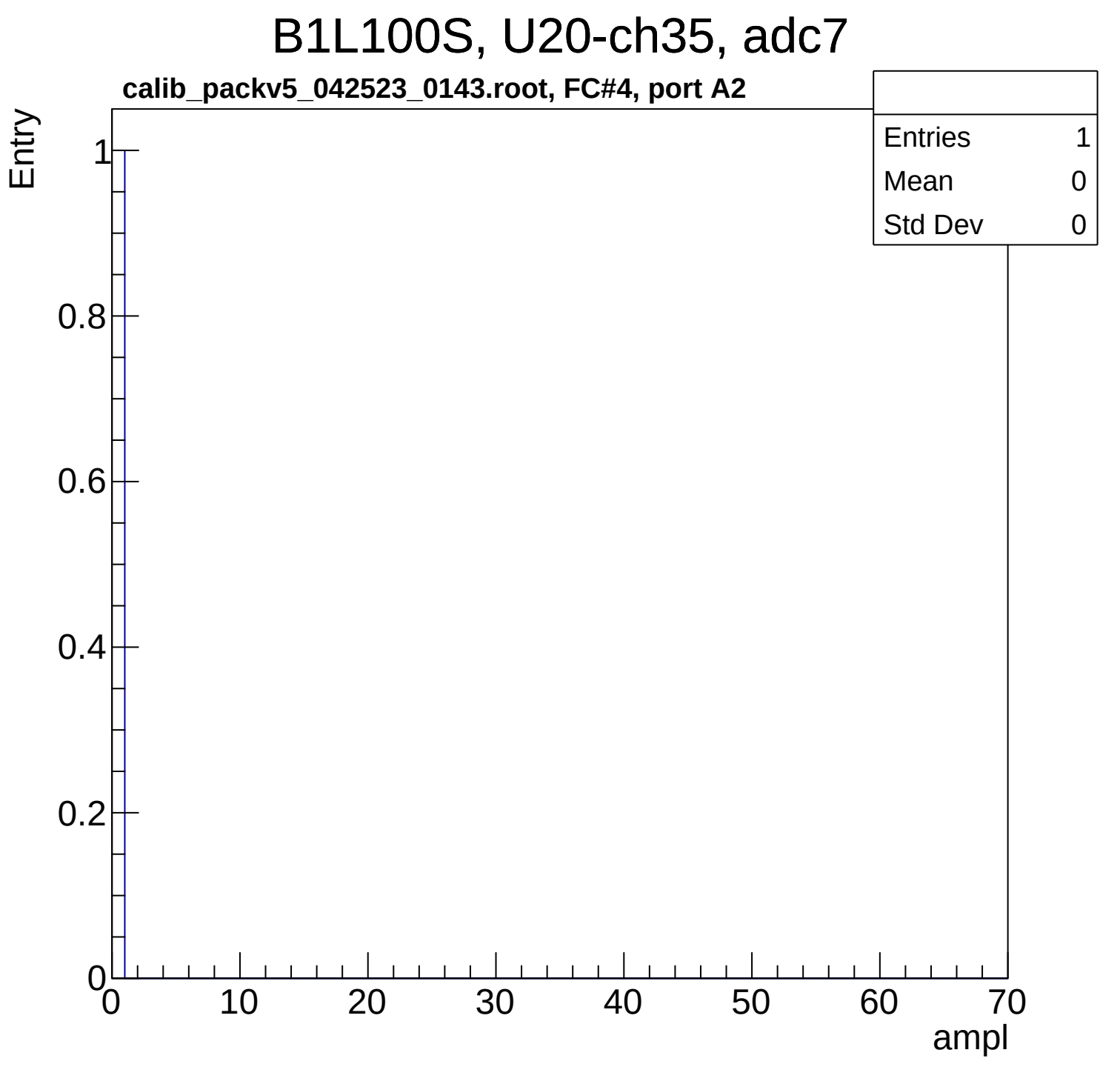
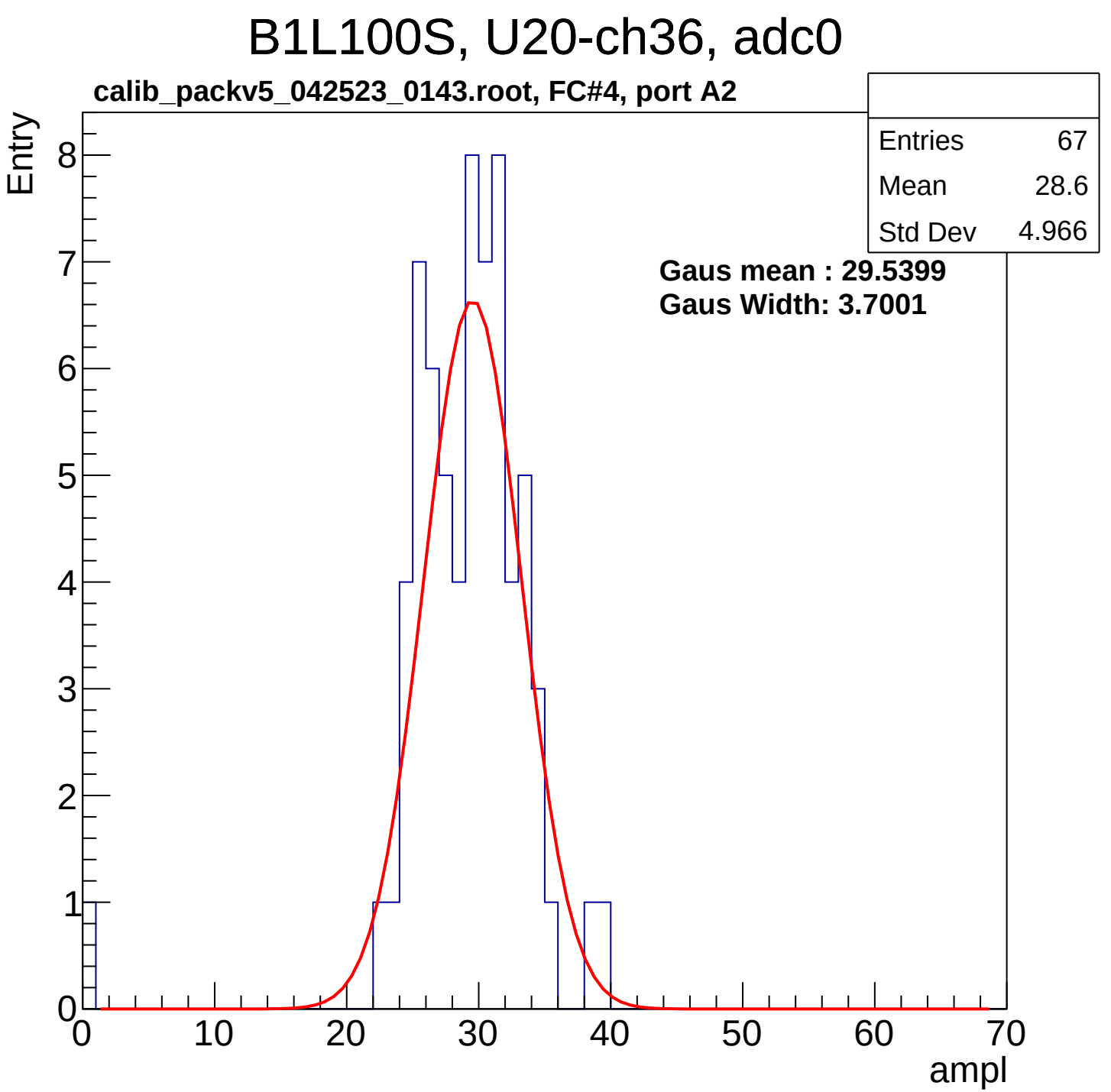
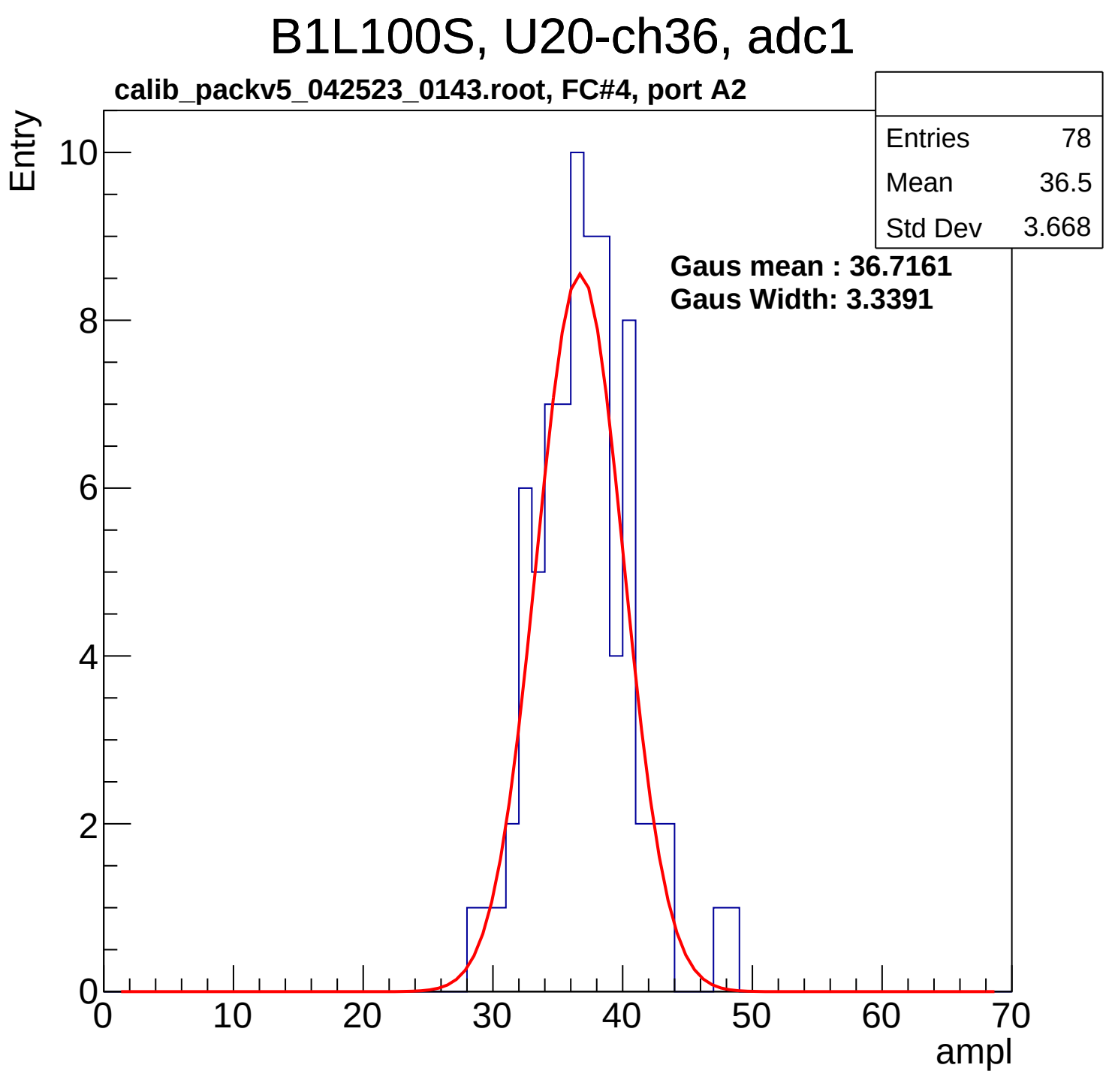

B1L100S, U20-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U20-ch35, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.6
Std Dev 4.966
Gaus mean : 29.5399
Gaus Width: 3.7001
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.5
Std Dev 3.668
Gaus mean : 36.7161
Gaus Width: 3.3391
Entry
ampl
0
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4
6
8
10
0
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20
30
40
50
60
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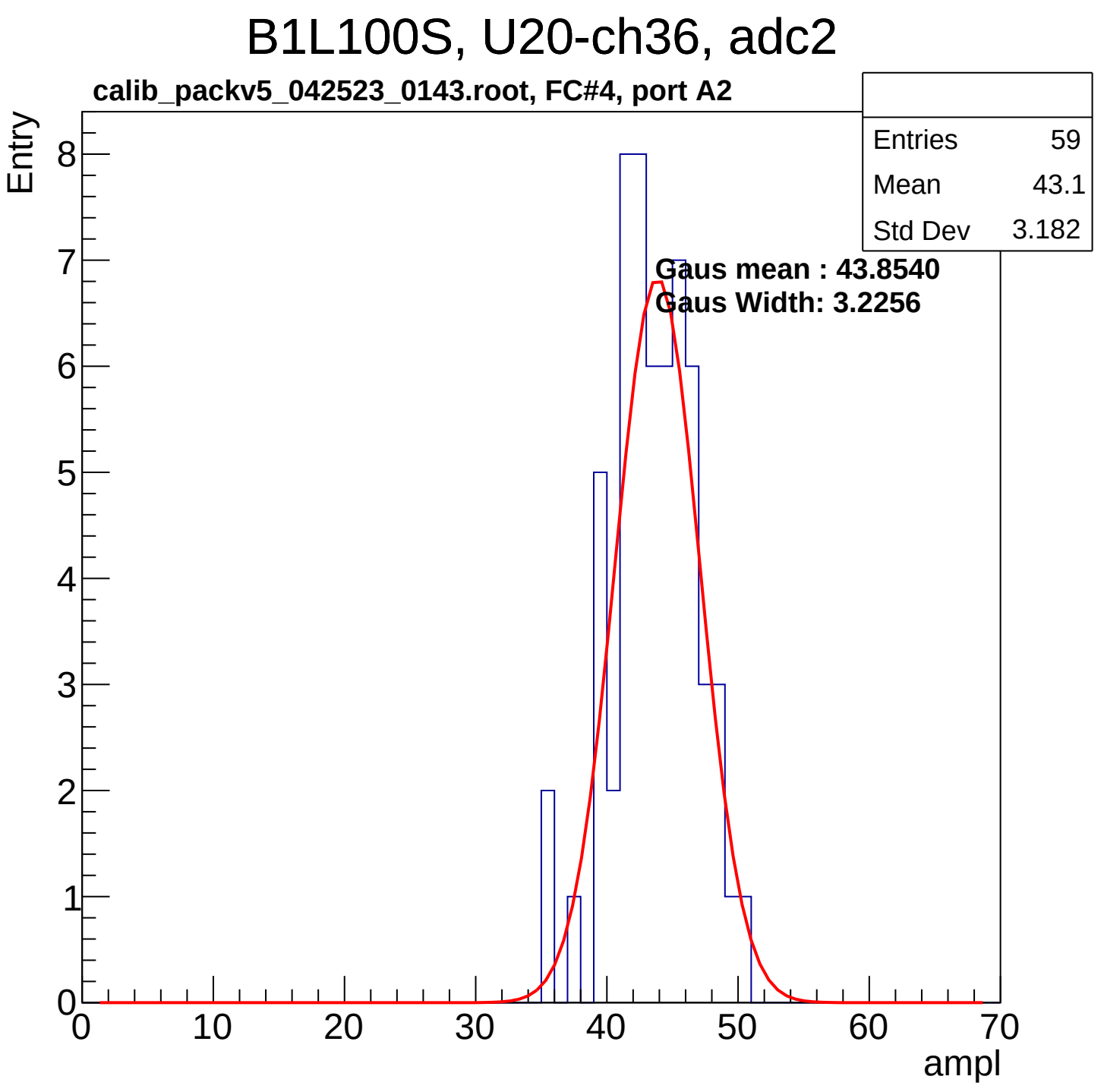
B1L100S, U20-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.1
Std Dev 3.182
Gaus mean : 43.8540
Gaus Width: 3.2256
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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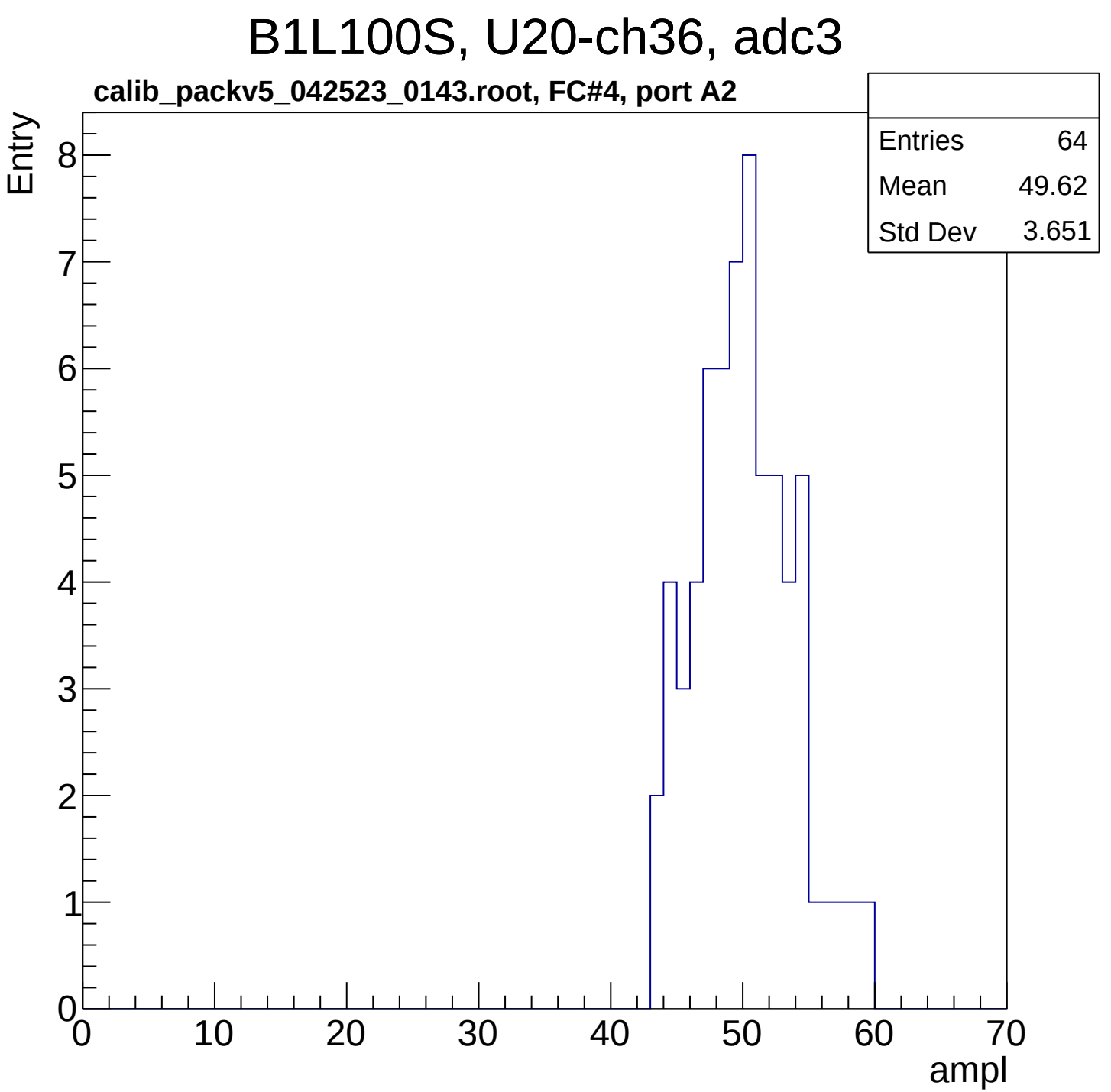
B1L100S, U20-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 49.62
Std Dev 3.651
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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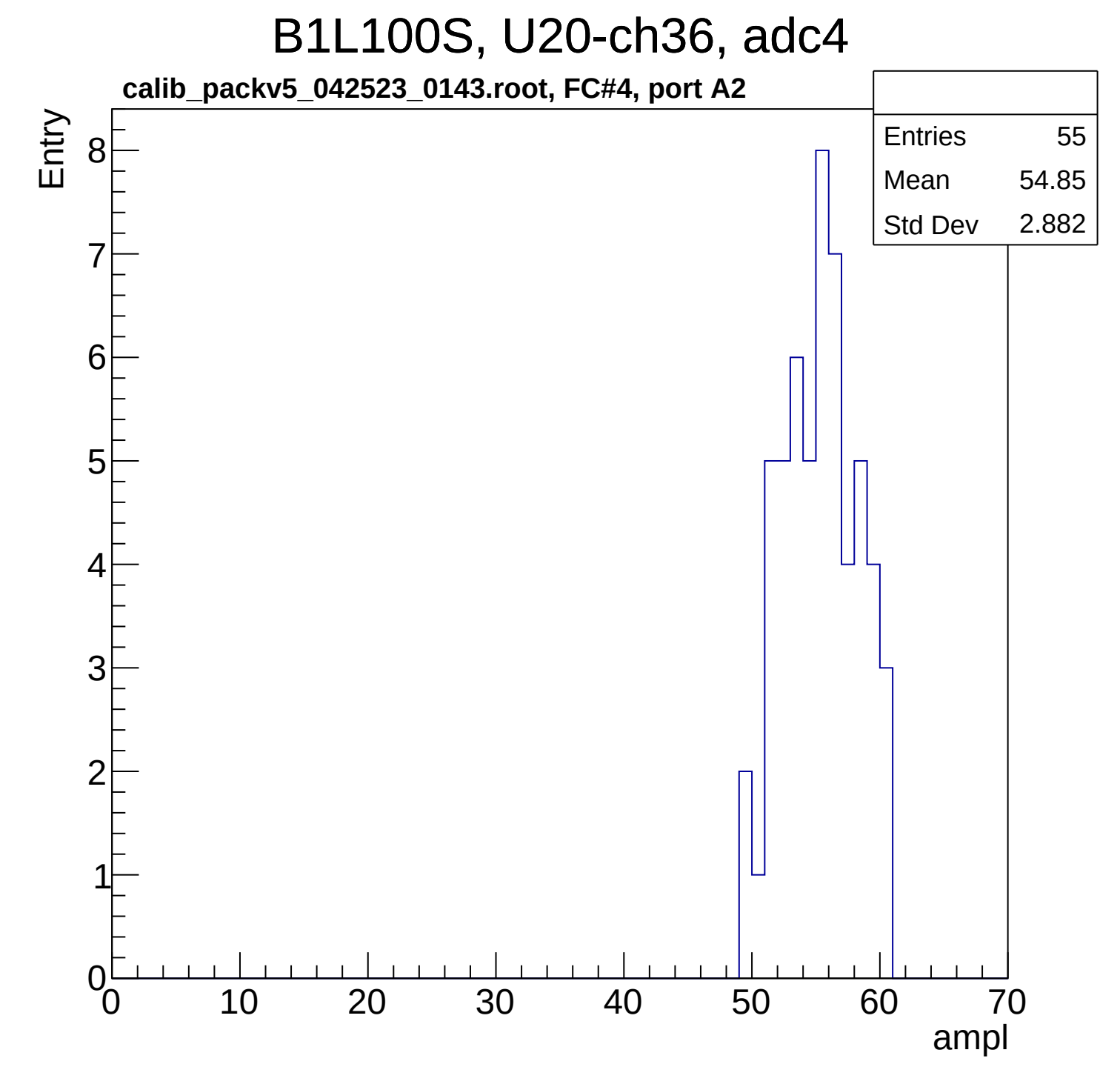
B1L100S, U20-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 54.85
Std Dev 2.882
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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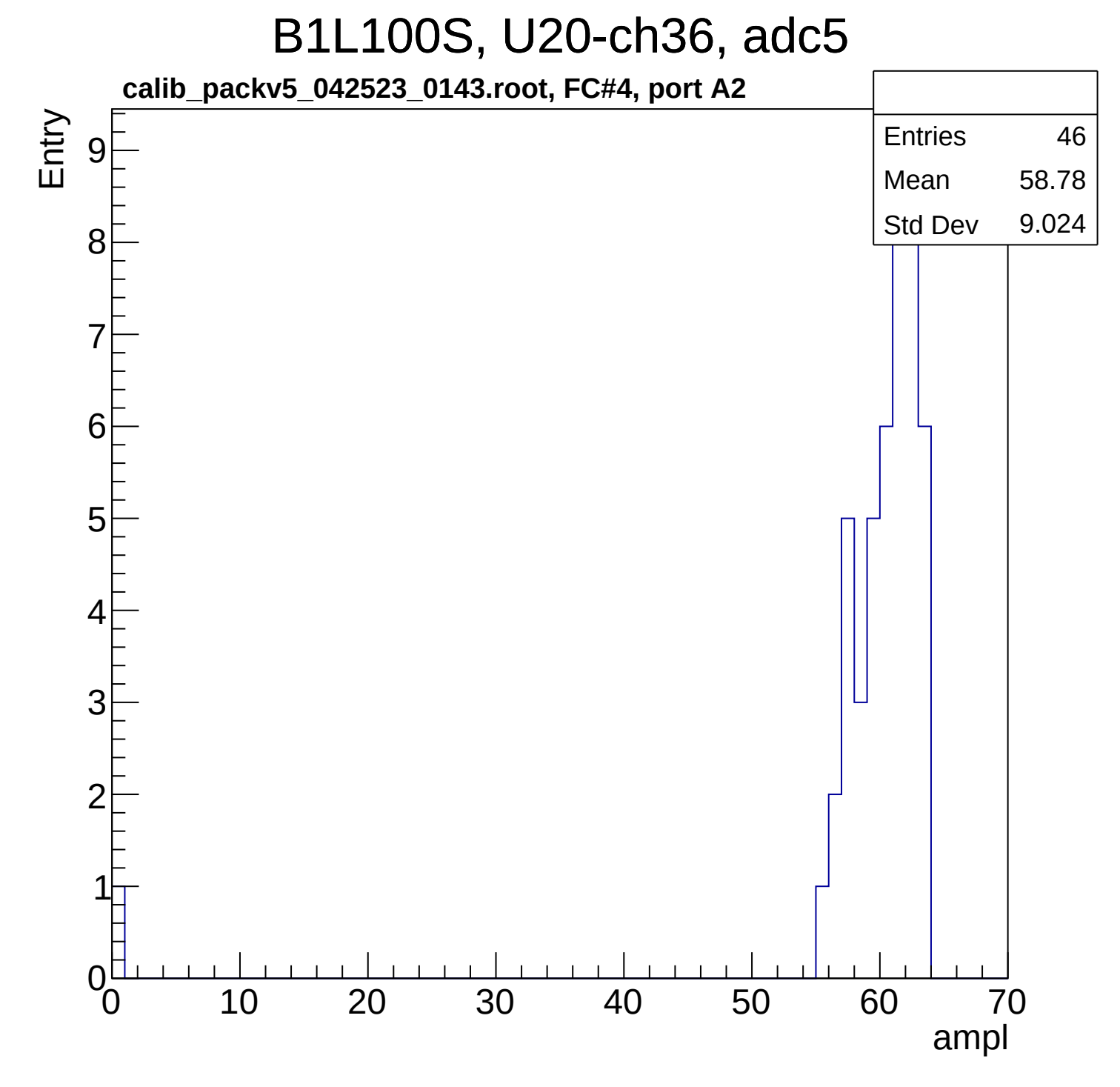
B1L100S, U20-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.78
Std Dev 9.024
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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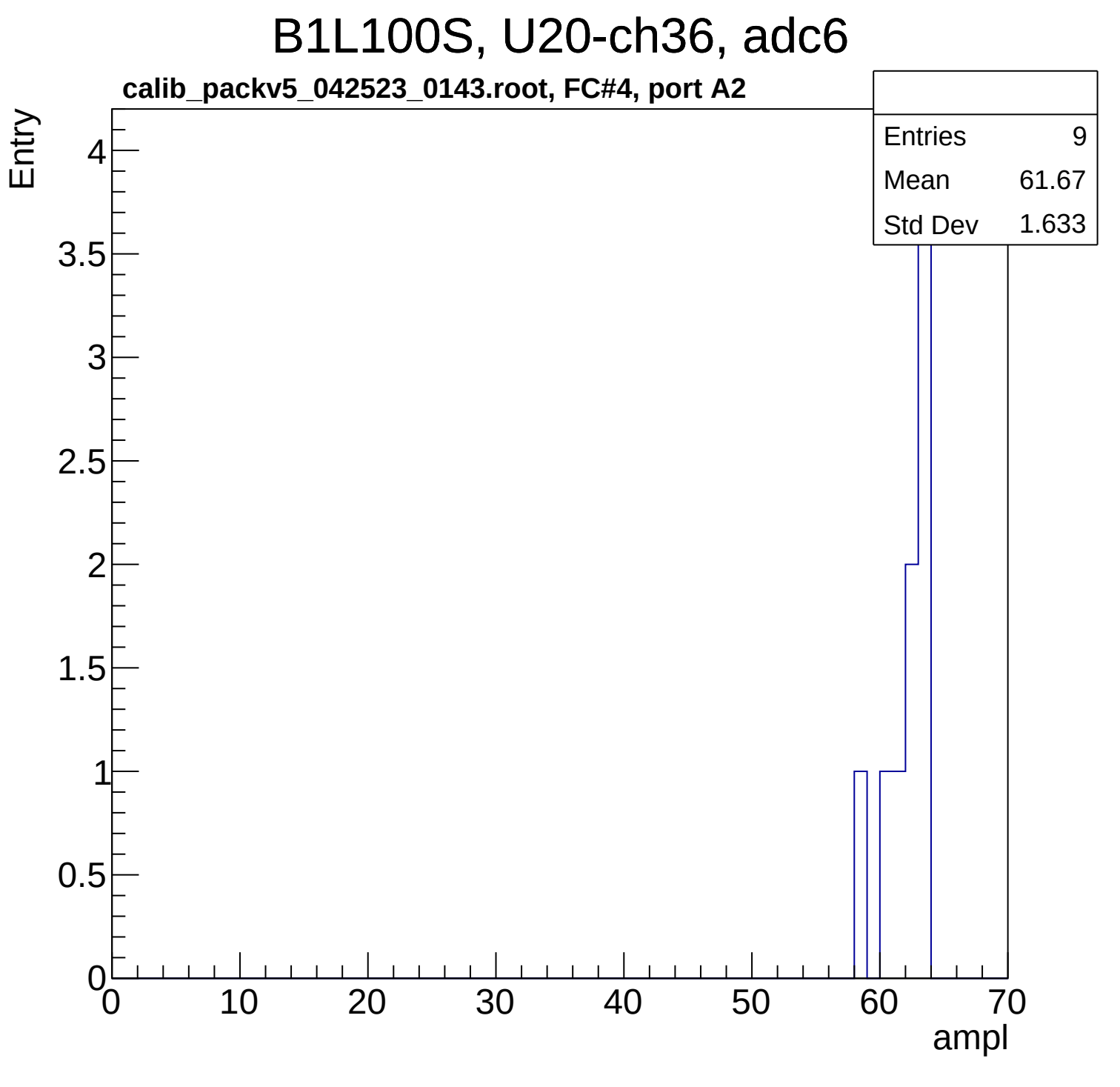
B1L100S, U20-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.67
Std Dev 1.633
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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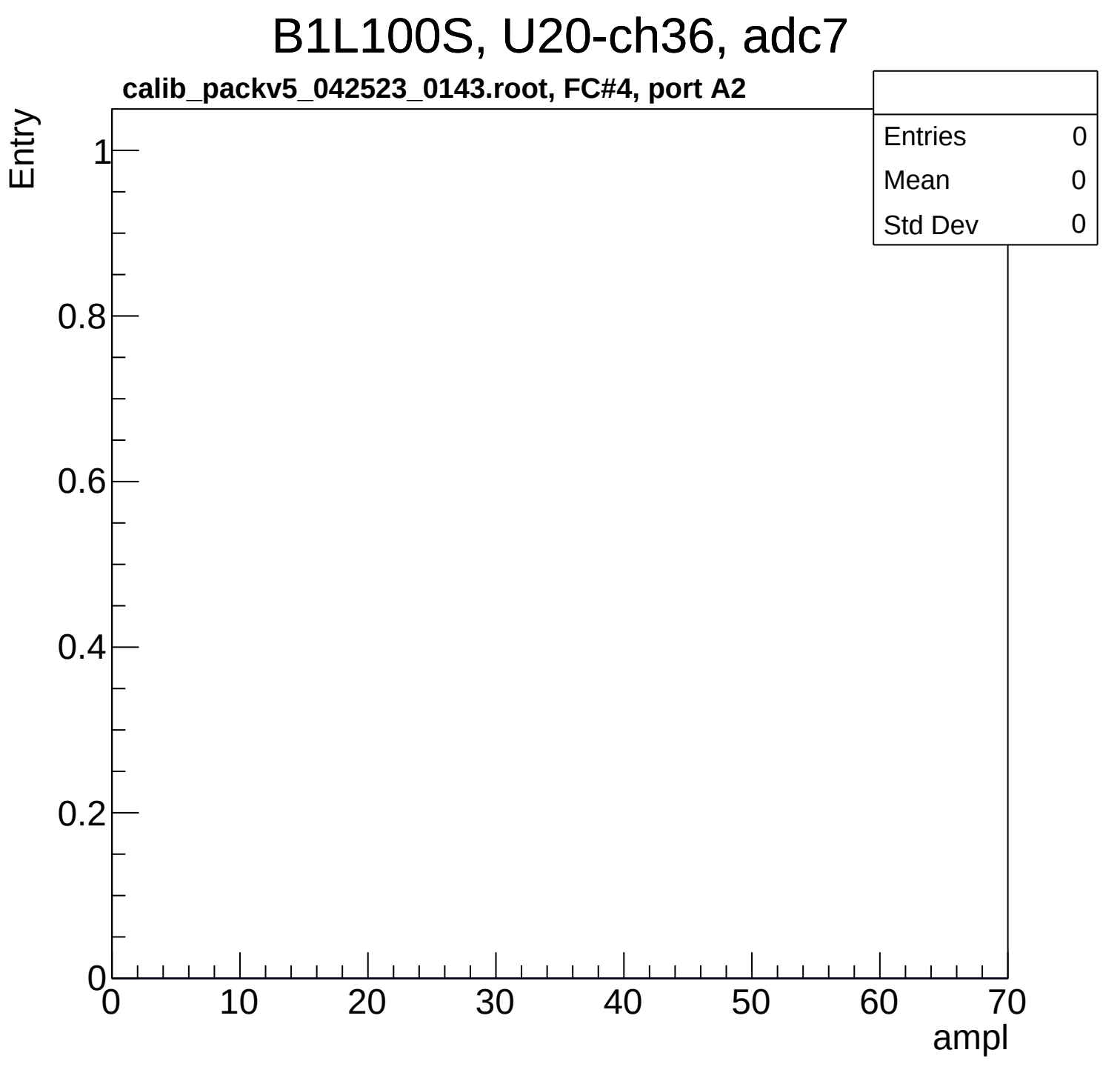
40
50
60
70


B1L100S, U20-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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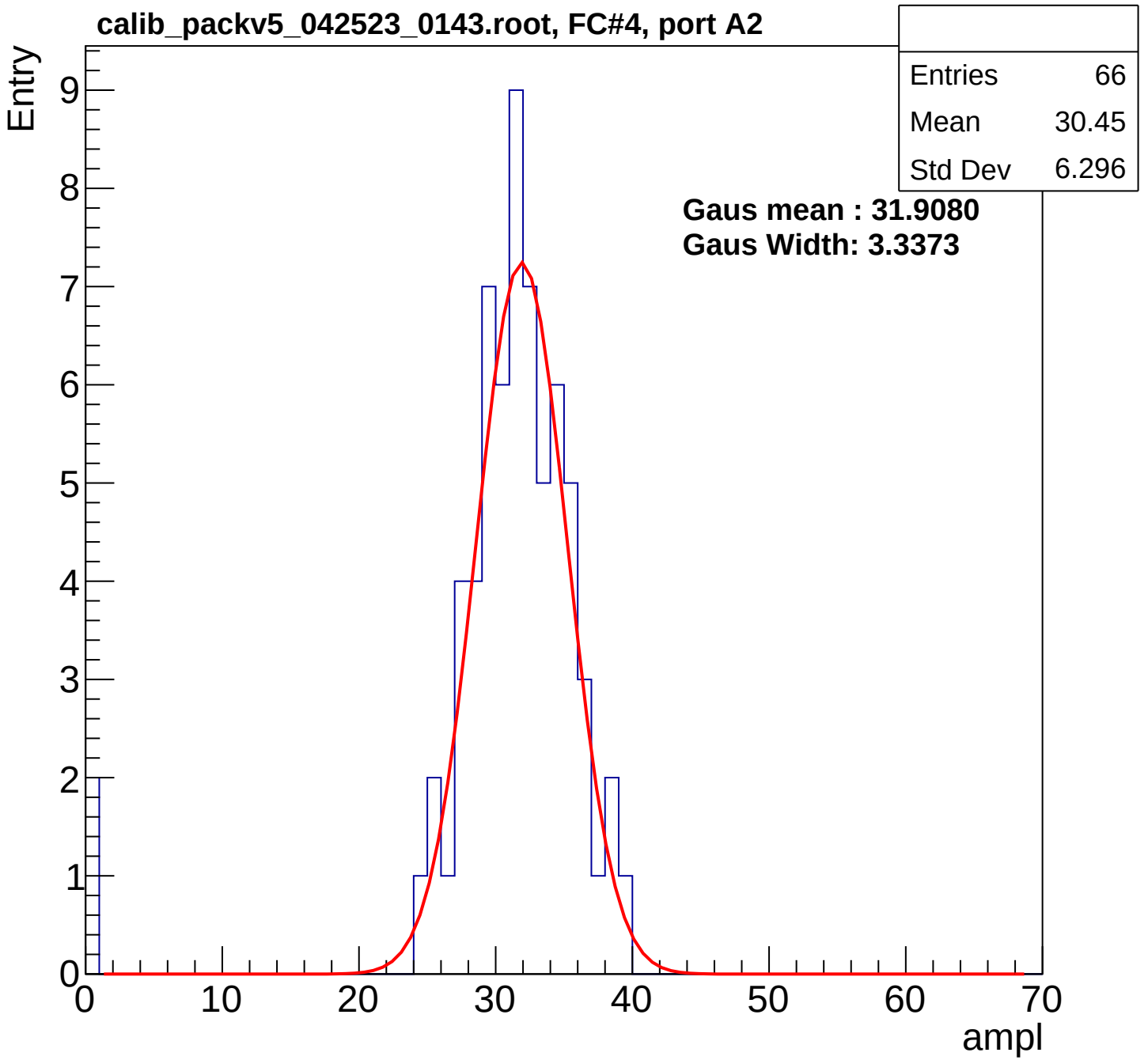
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 30.45
Std Dev 6.296
Gaus mean : 31.9080
Gaus Width: 3.3373
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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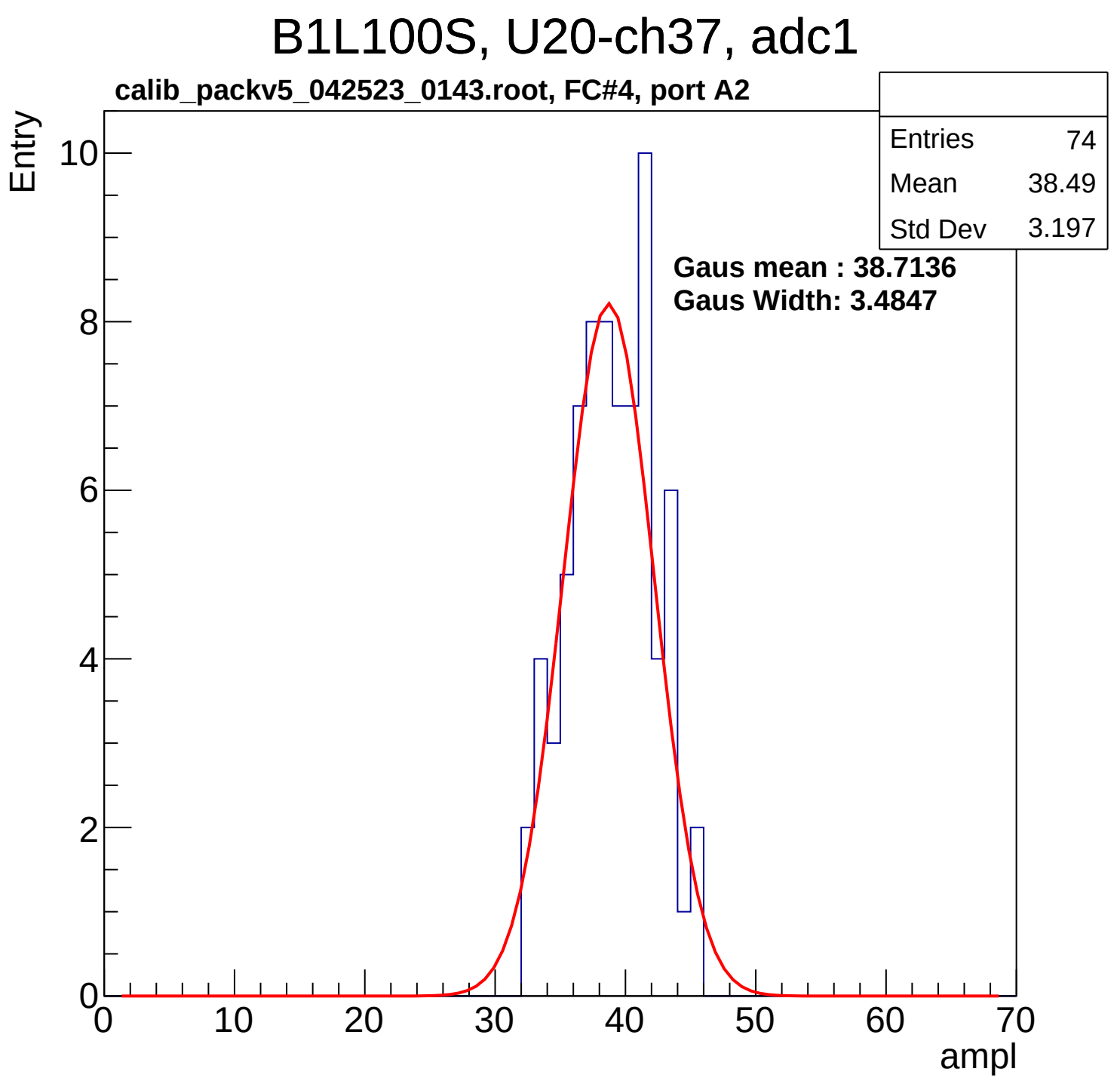
B1L100S, U20-ch37, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 38.49
Std Dev 3.197
Gaus mean : 38.7136
Gaus Width: 3.4847
Entry
ampl
0
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4
6
8
10
0
10
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40
50
60
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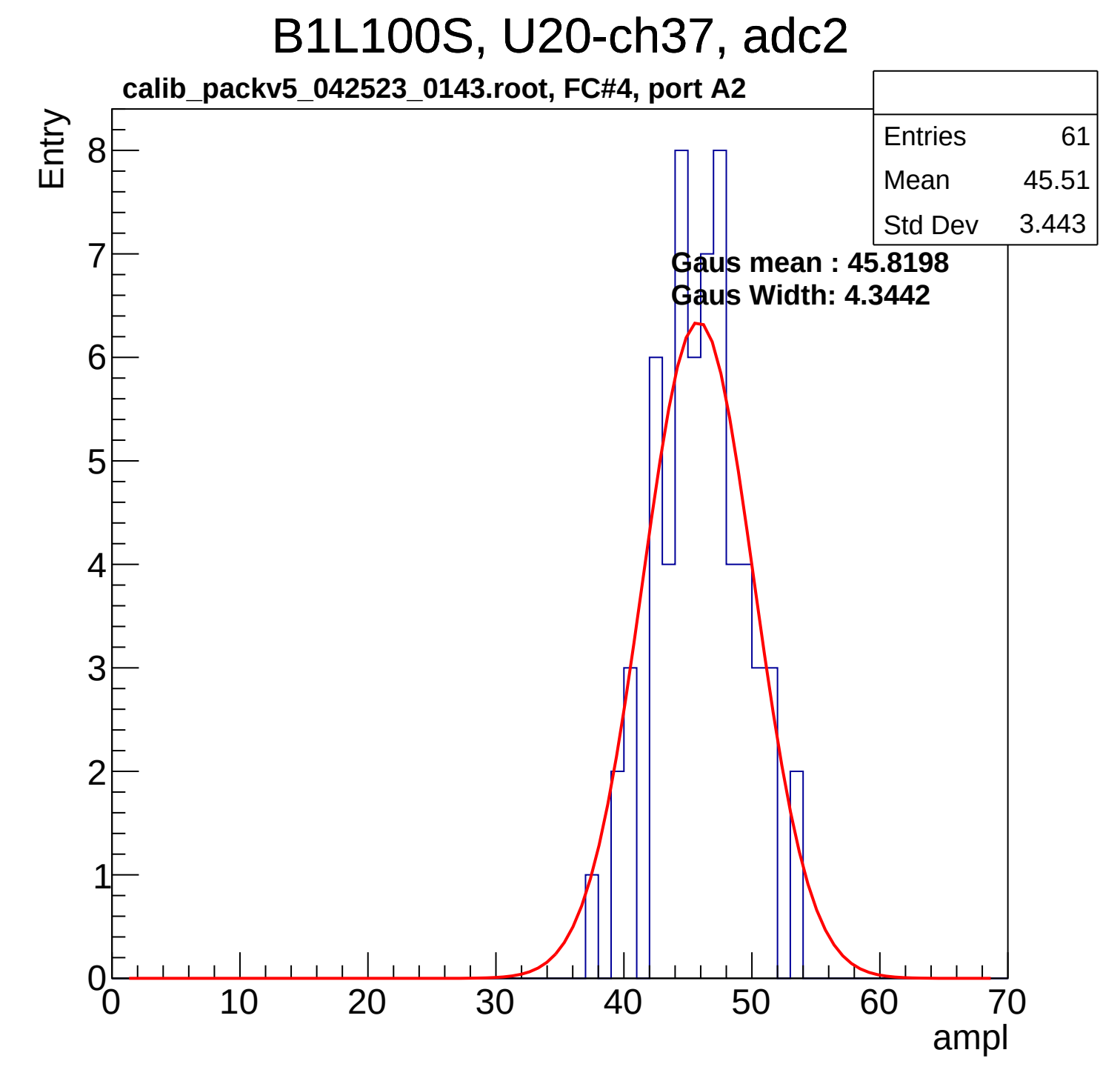
B1L100S, U20-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 45.51
Std Dev 3.443
Gaus mean : 45.8198
Gaus Width: 4.3442
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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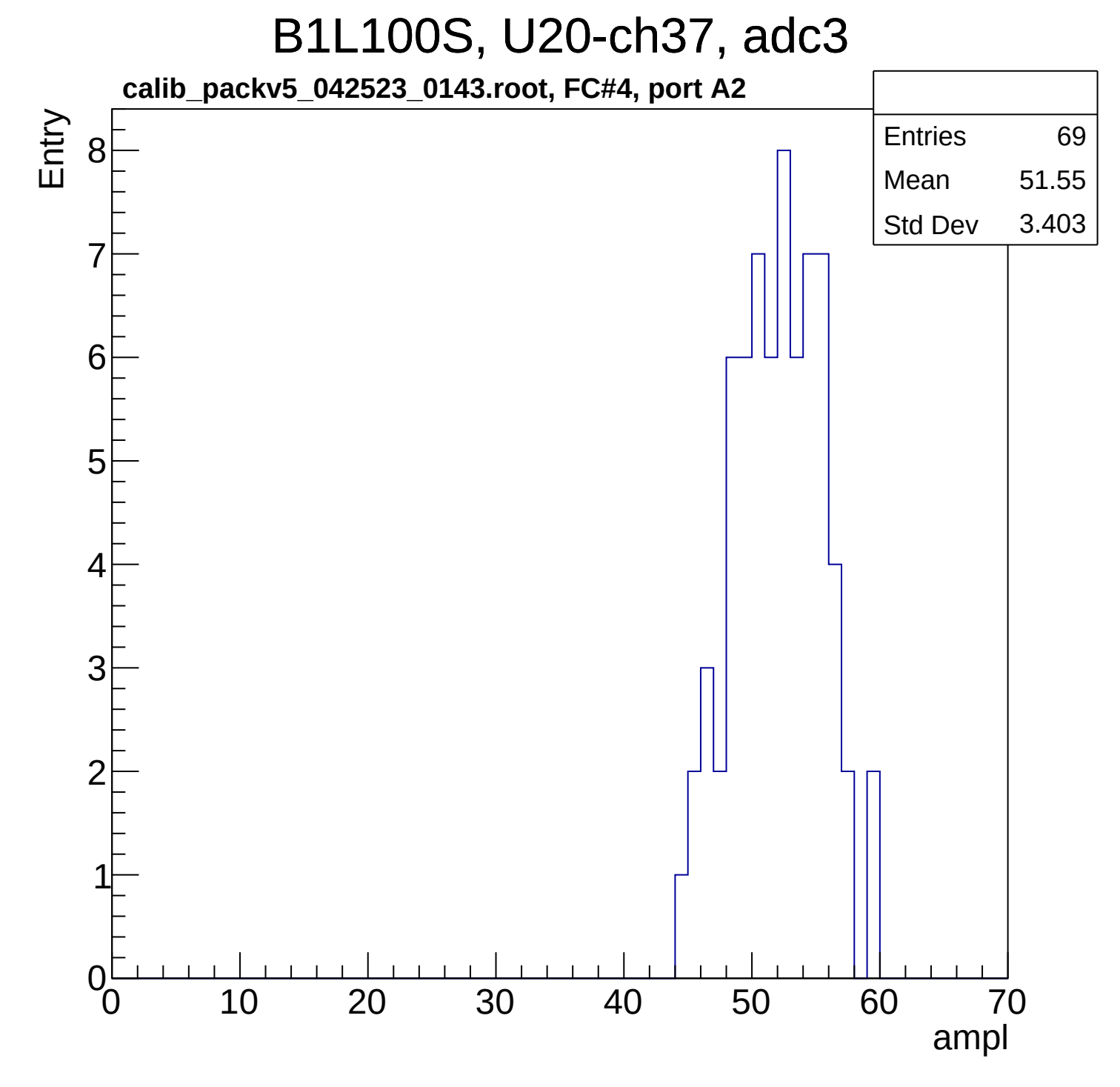
B1L100S, U20-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.55
Std Dev 3.403
Entry
ampl
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2
3
4
5
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0
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60
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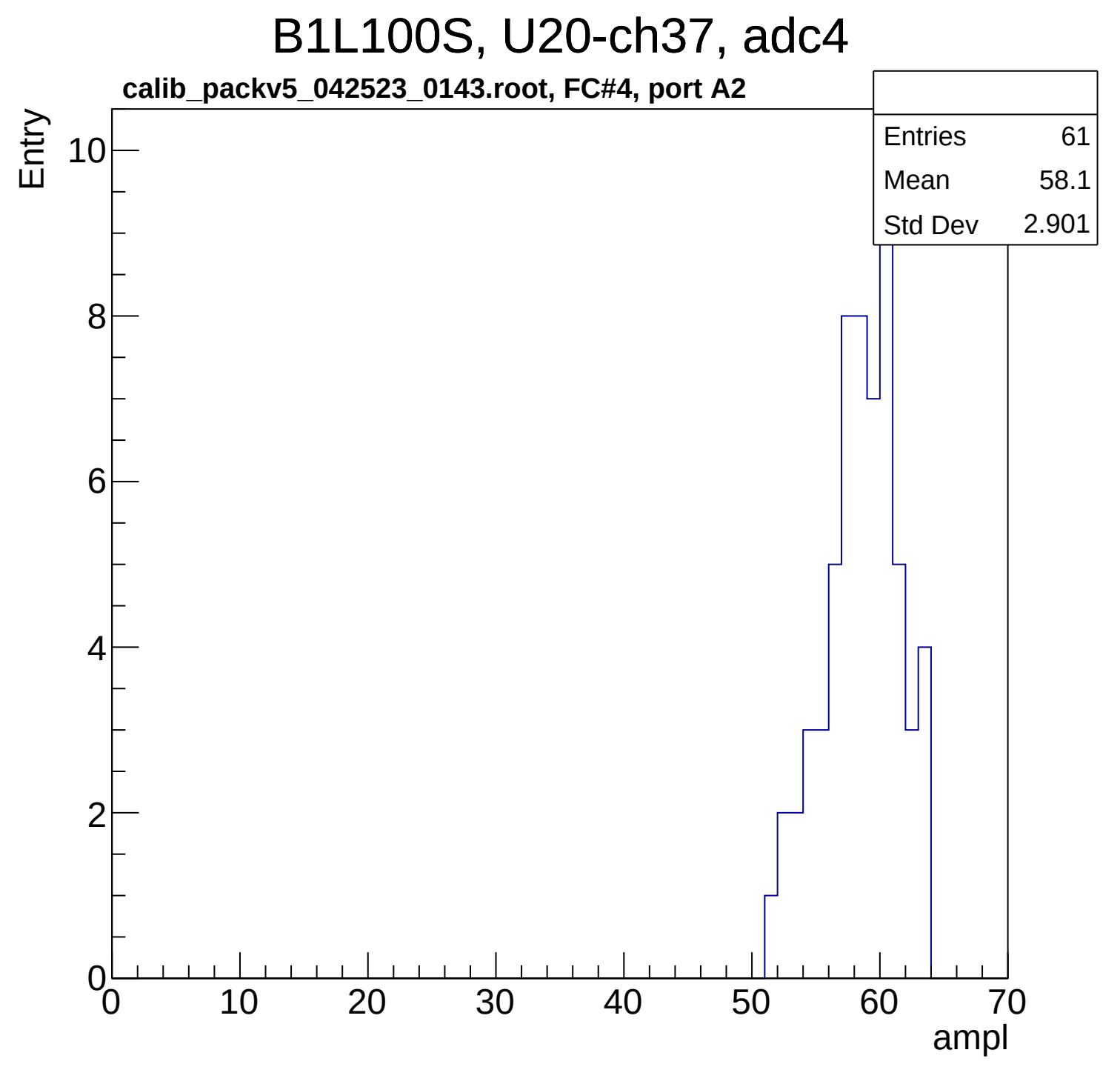
B1L100S, U20-ch37, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 58.1
Std Dev 2.901
Entry
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10
0
10
20
30
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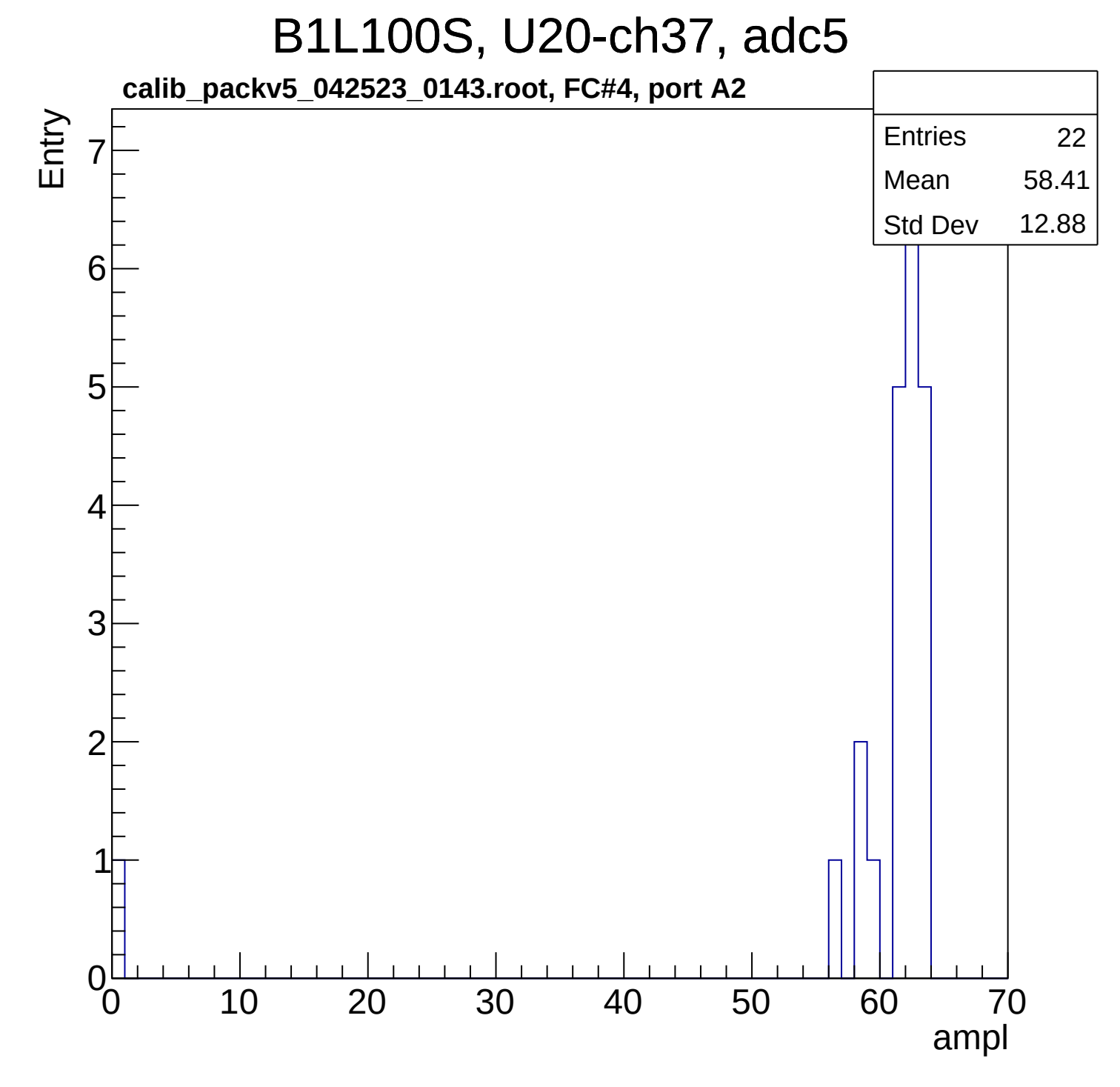
50
60
70
ampl


B1L100S, U20-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 58.41
Std Dev 12.88
Entry
ampl
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4
5
6
7
0
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40
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60
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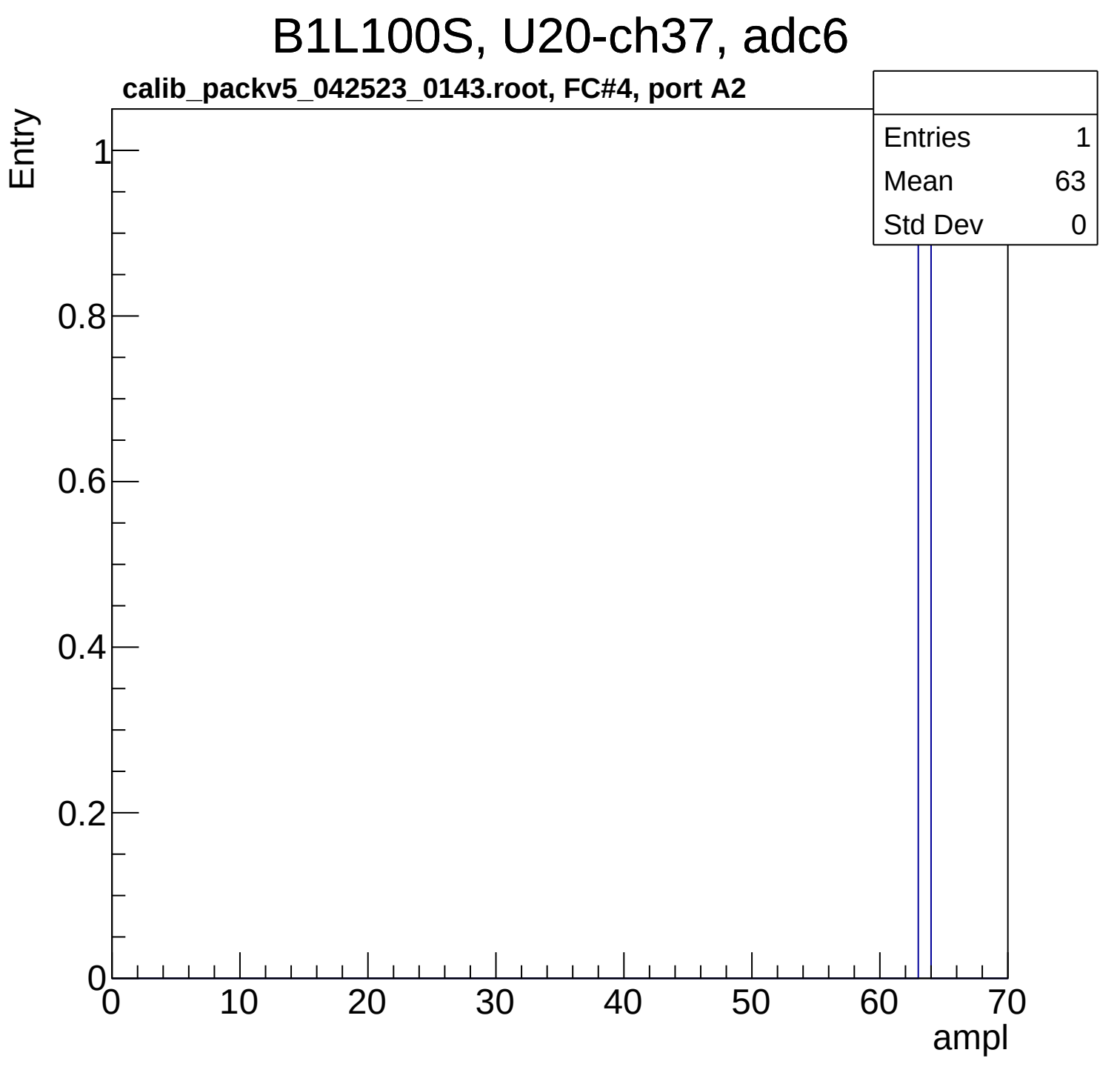
B1L100S, U20-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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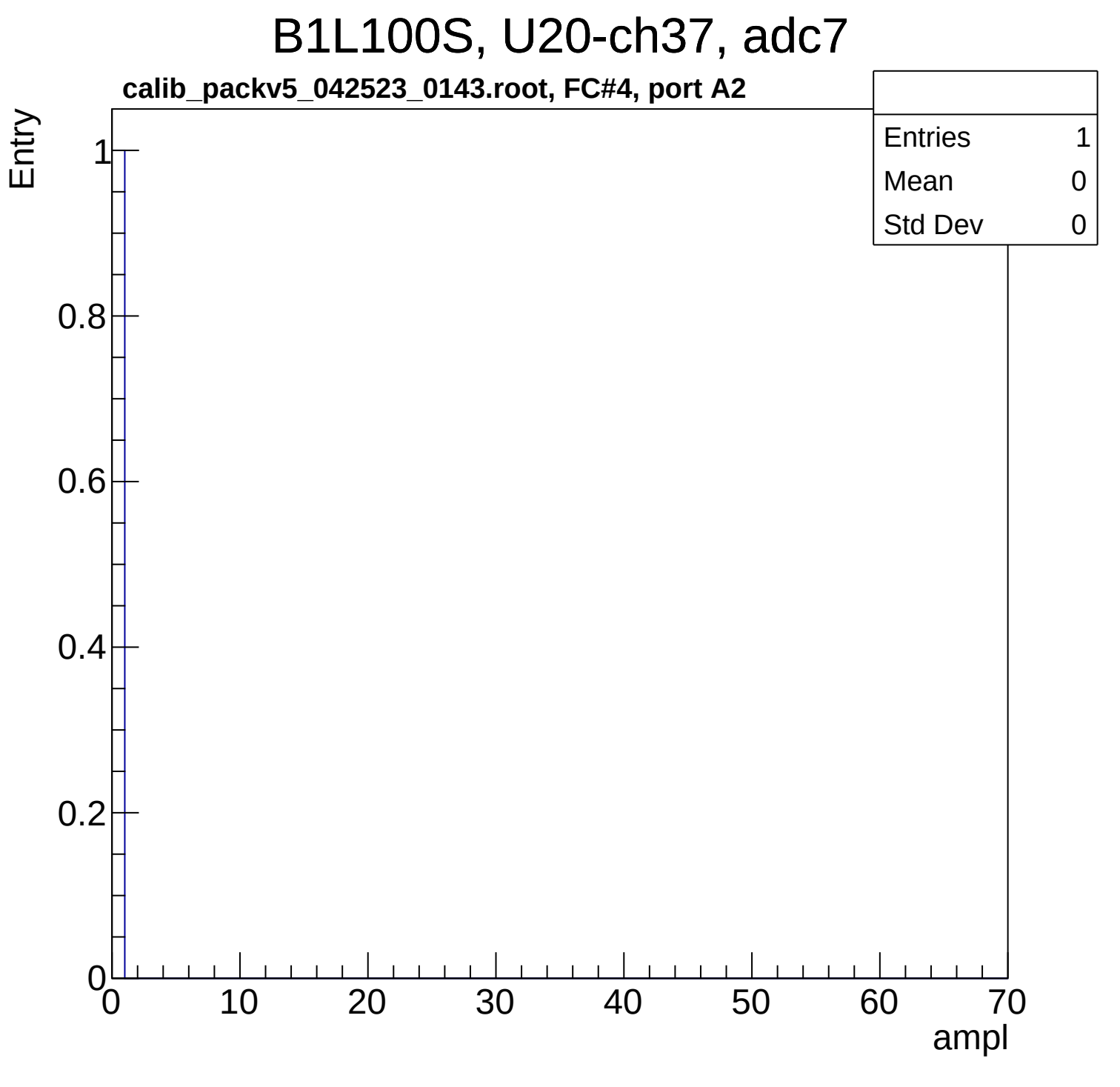
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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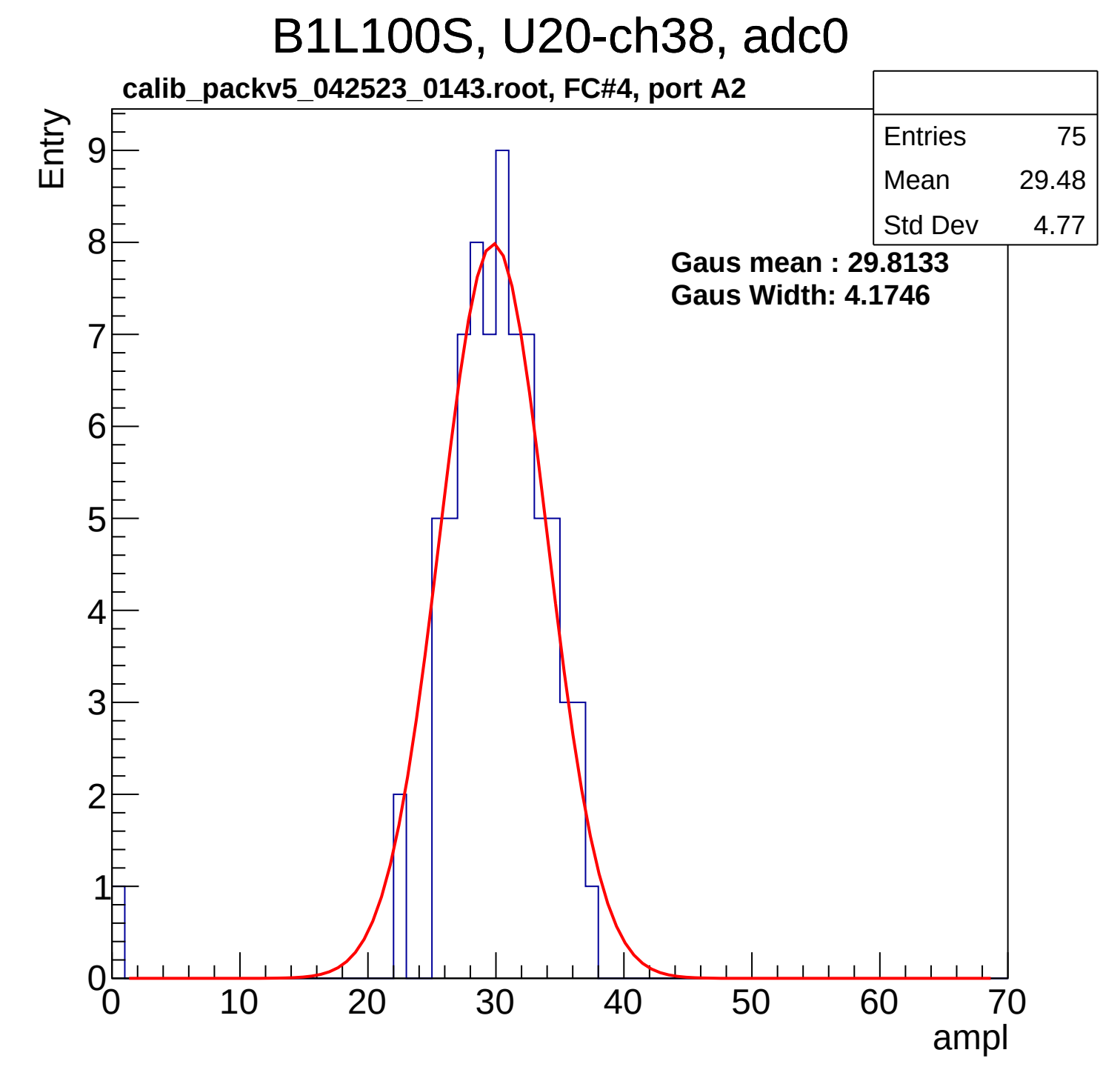
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.48
Std Dev 4.77
Gaus mean : 29.8133
Gaus Width: 4.1746
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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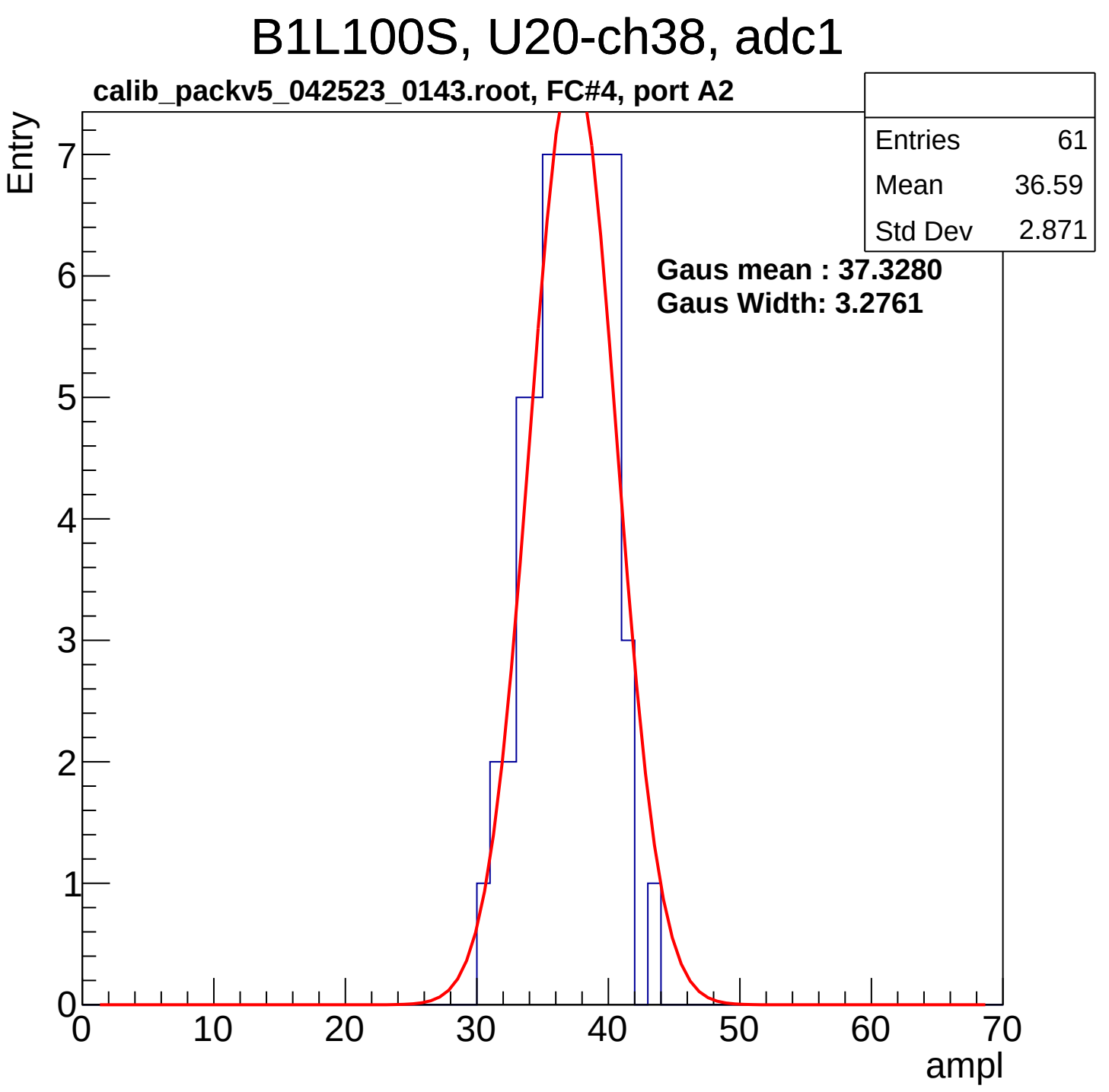
B1L100S, U20-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 36.59
Std Dev 2.871
Gaus mean : 37.3280
Gaus Width: 3.2761
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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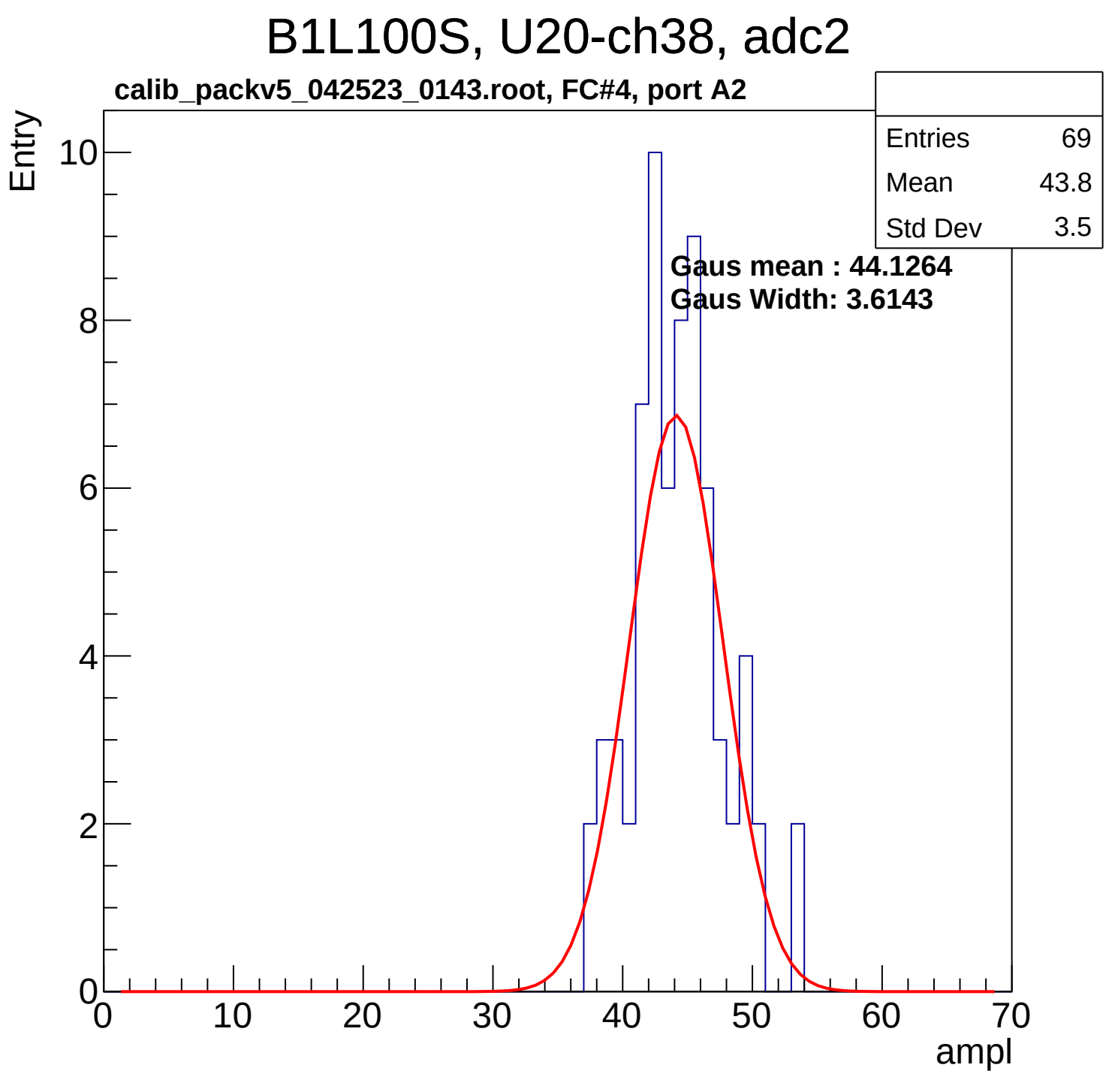
B1L100S, U20-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.8
Std Dev 3.5
Gaus mean : 44.1264
Gaus Width: 3.6143
Entry
ampl
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4
6
8
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0
10
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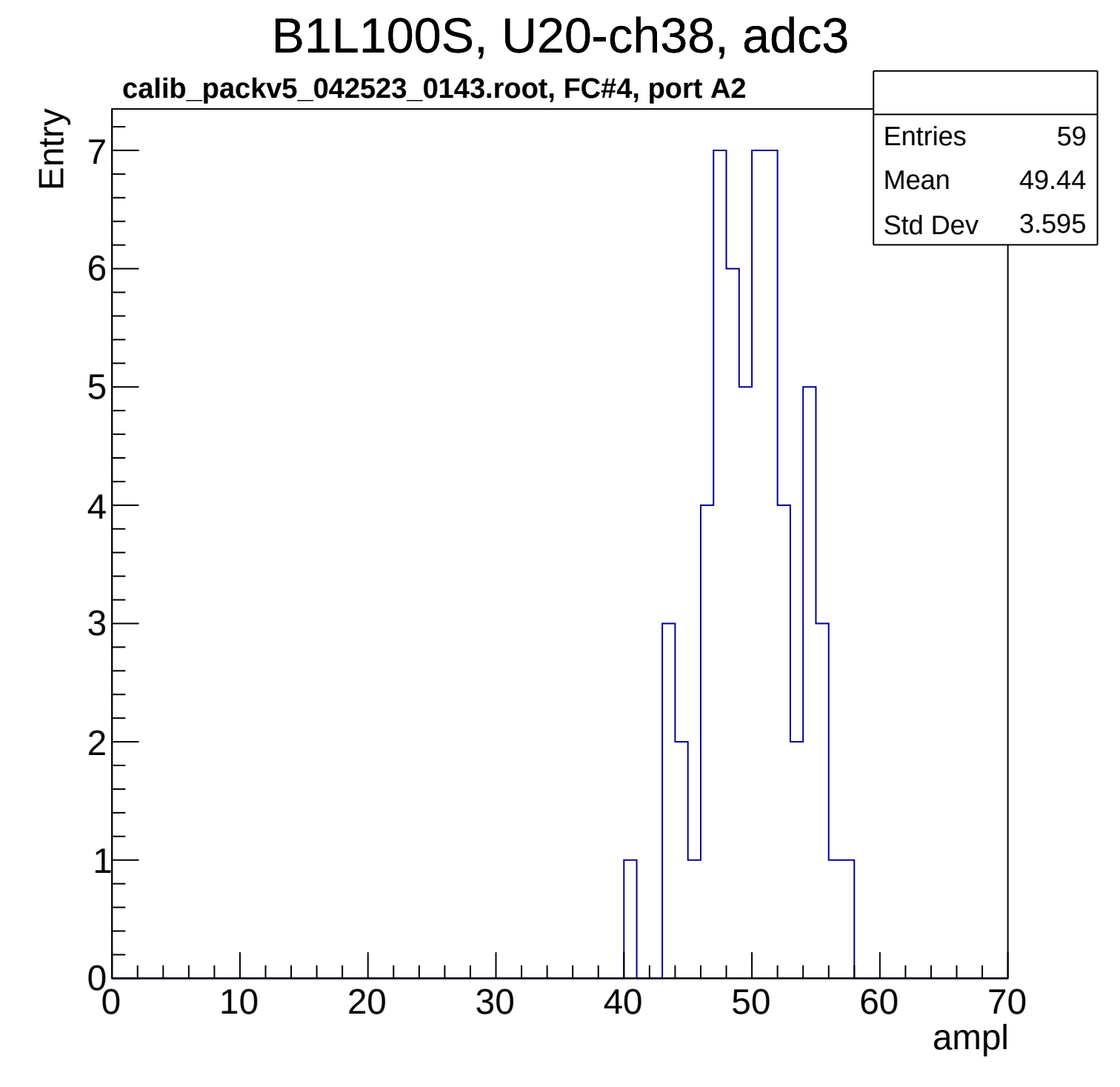
B1L100S, U20-ch38, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 49.44
Std Dev 3.595
Entry
ampl
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2
3
4
5
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7
0
10
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60
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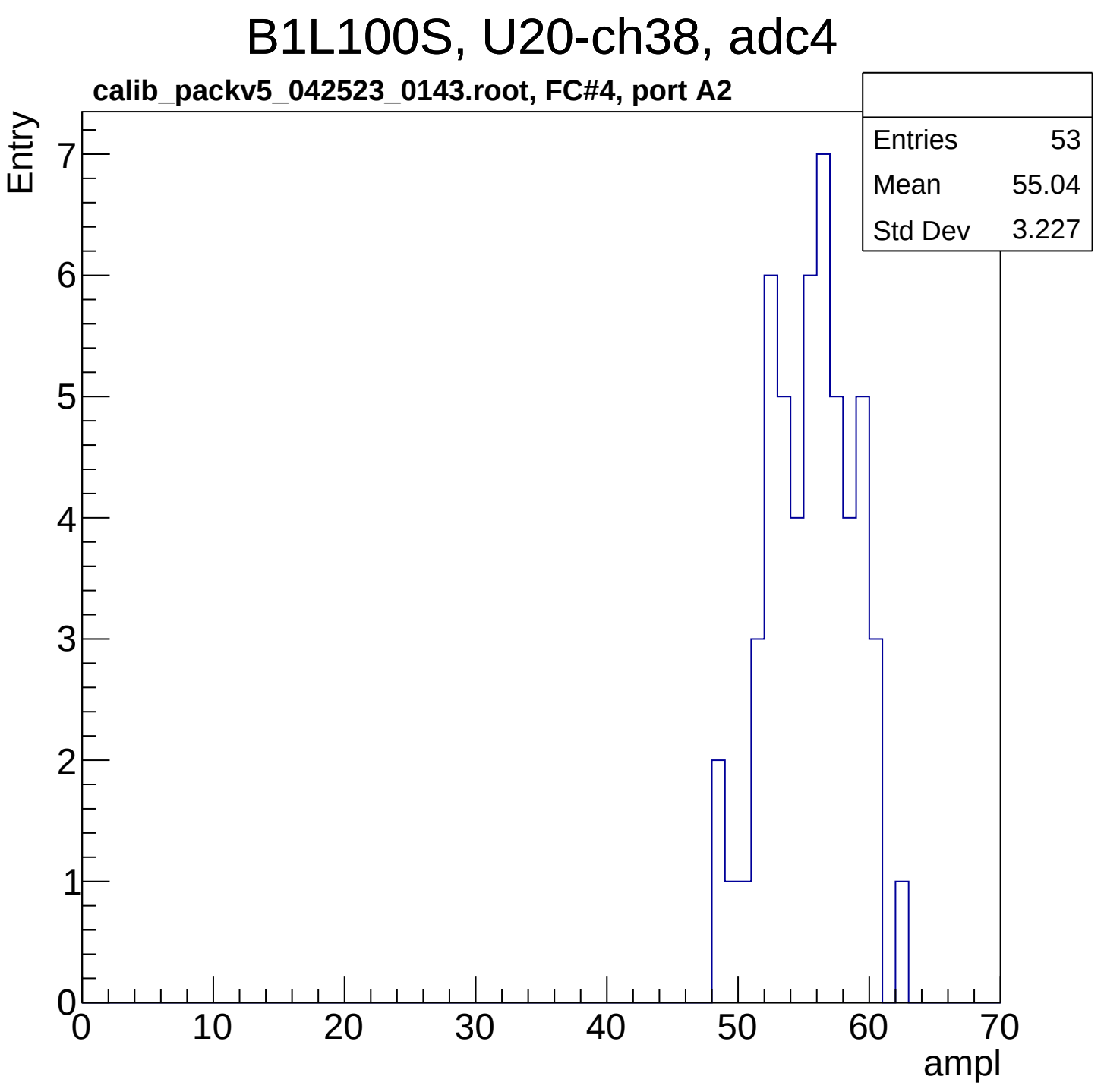
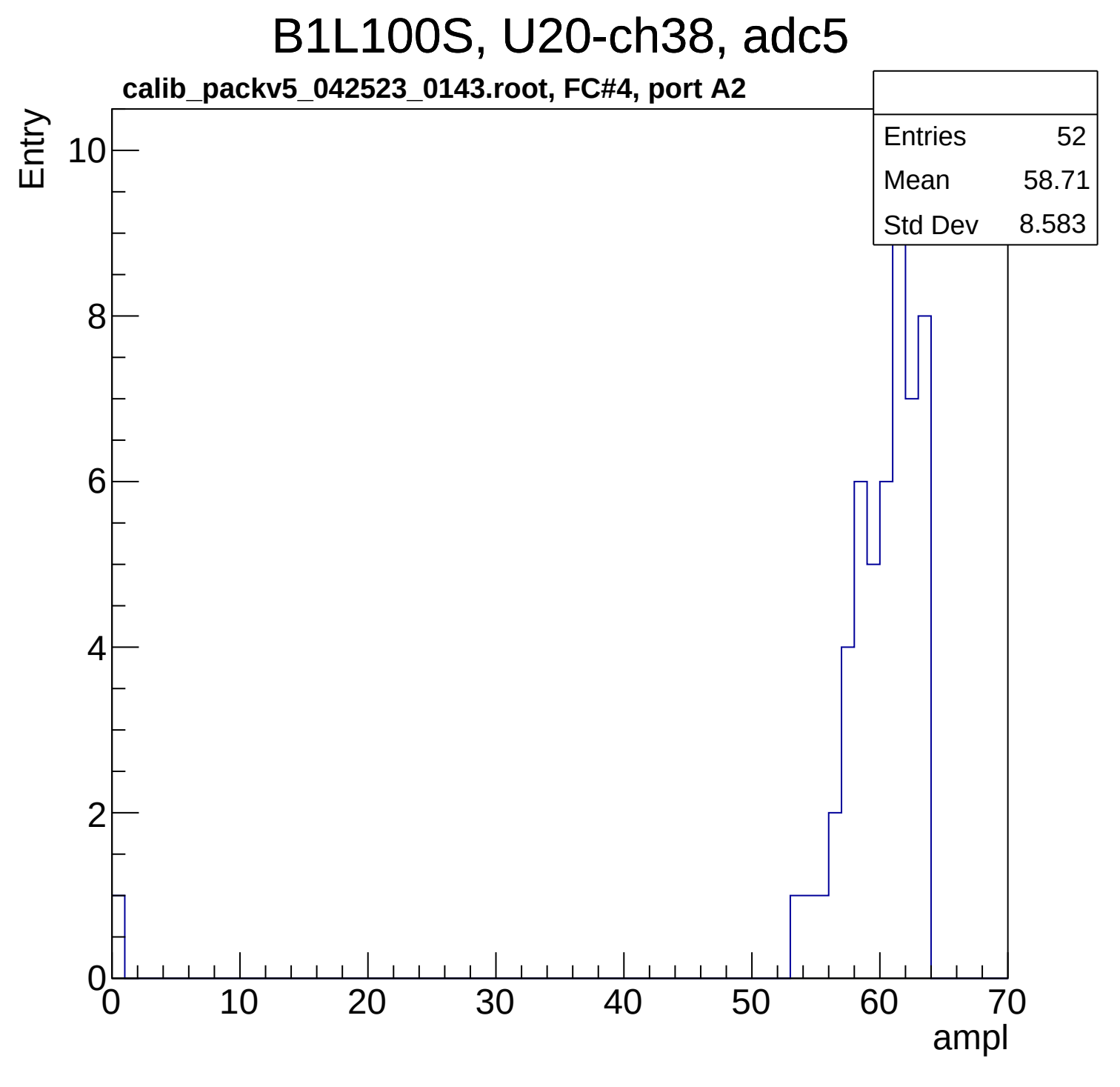

B1L100S, U20-ch38, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.04
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.71
Std Dev 8.583
Entry
ampl
0
2
4
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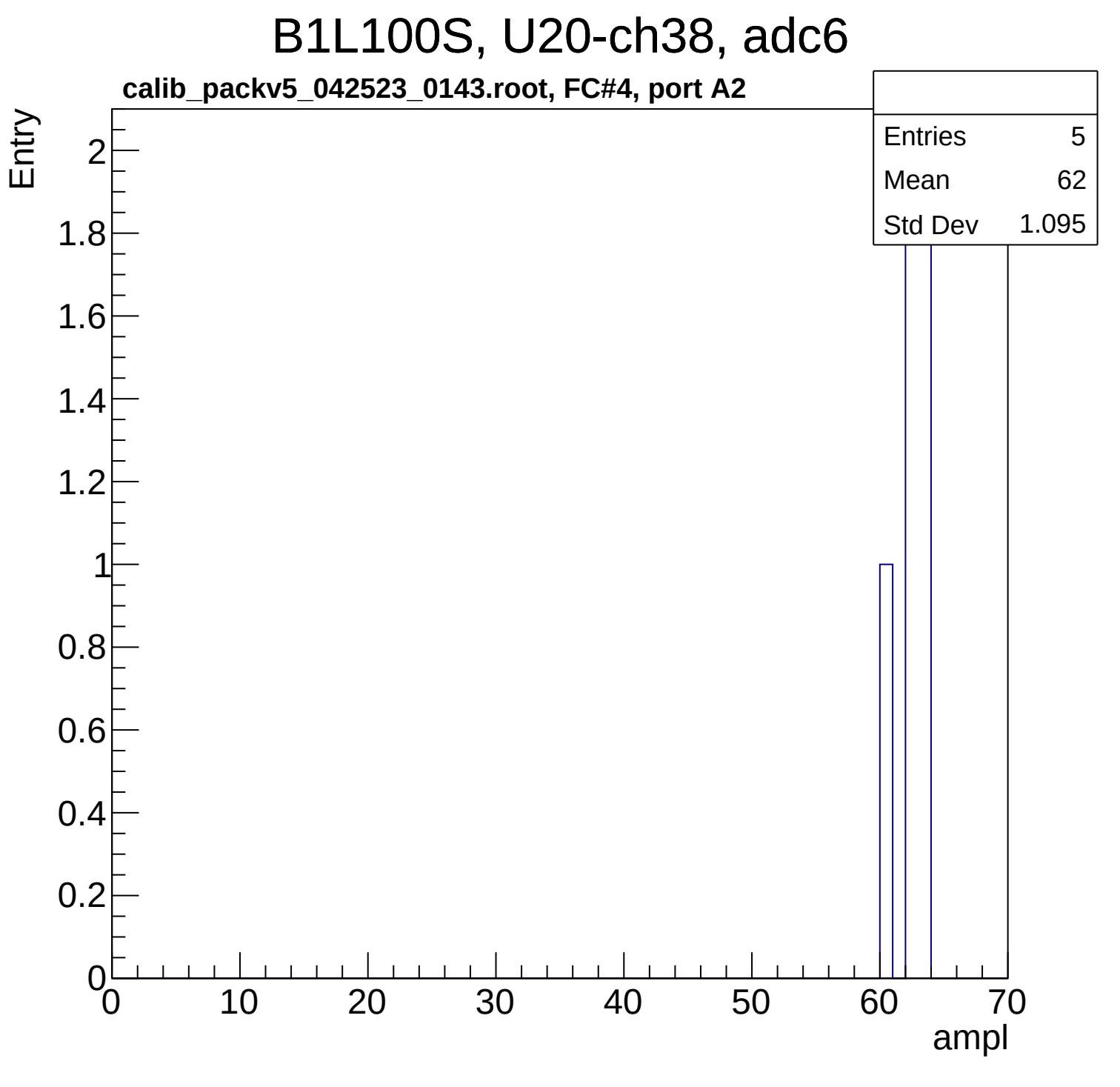
B1L100S, U20-ch38, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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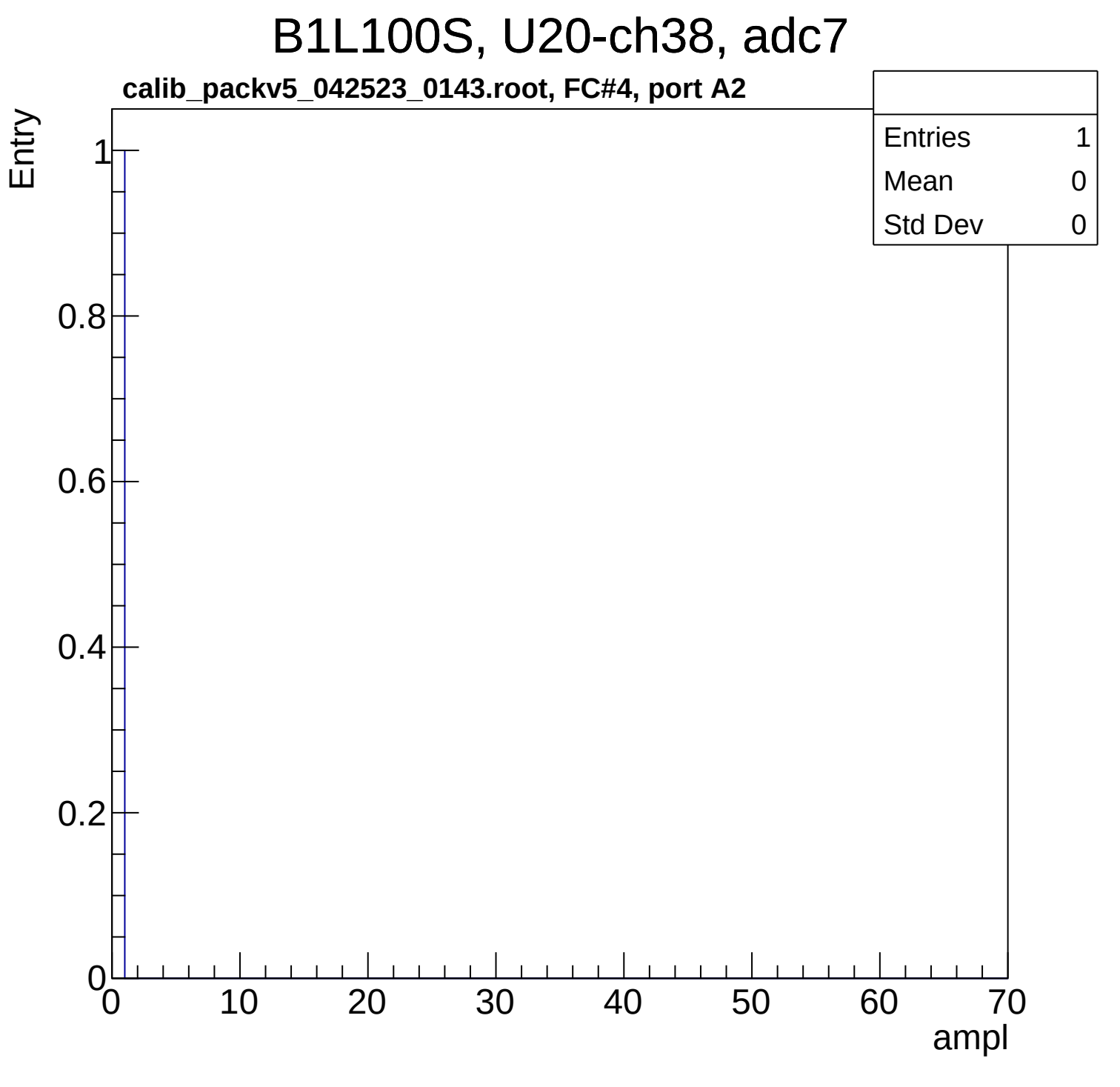
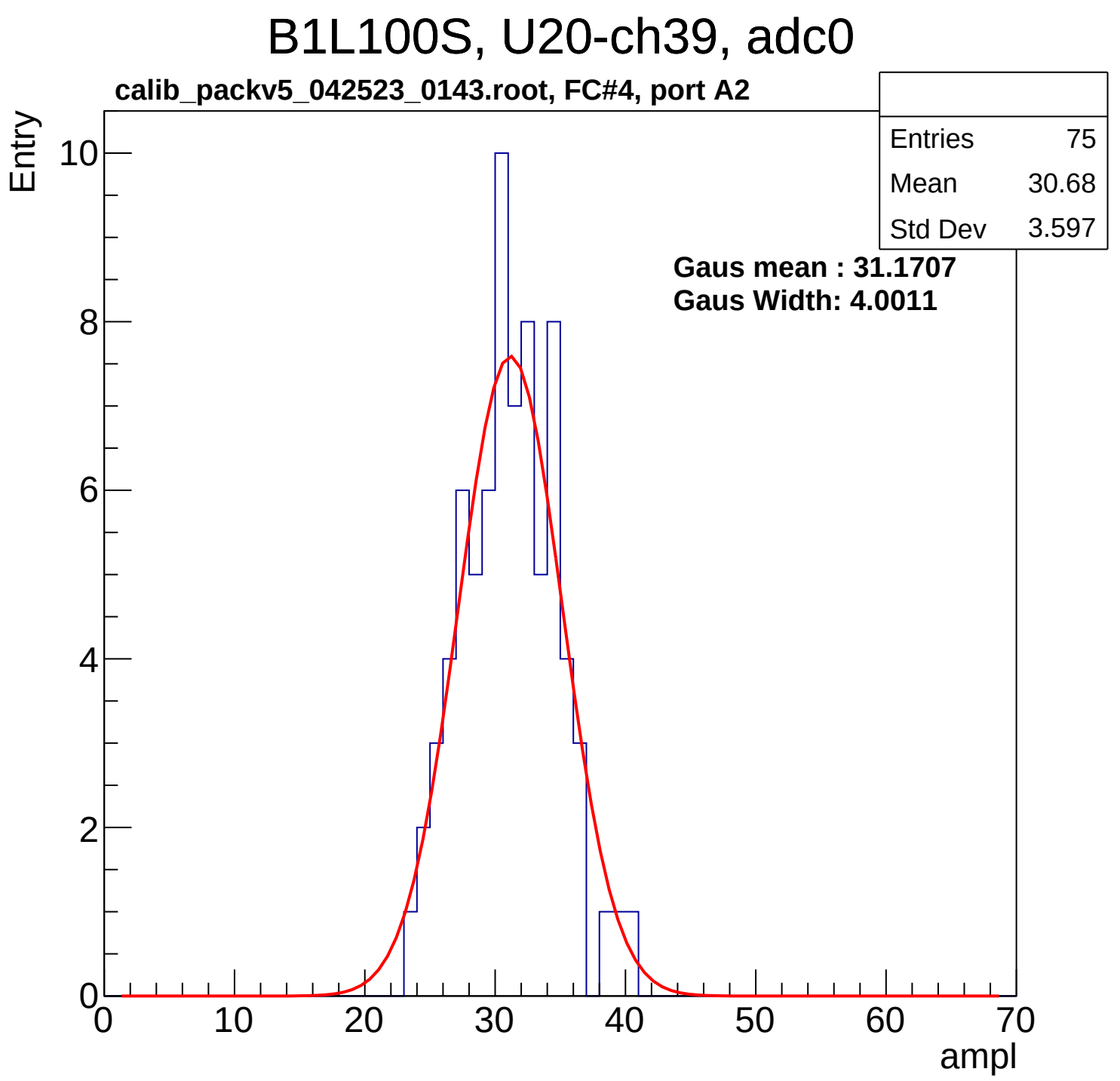

B1L100S, U20-ch38, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.68
Std Dev 3.597
Gaus mean : 31.1707
Gaus Width: 4.0011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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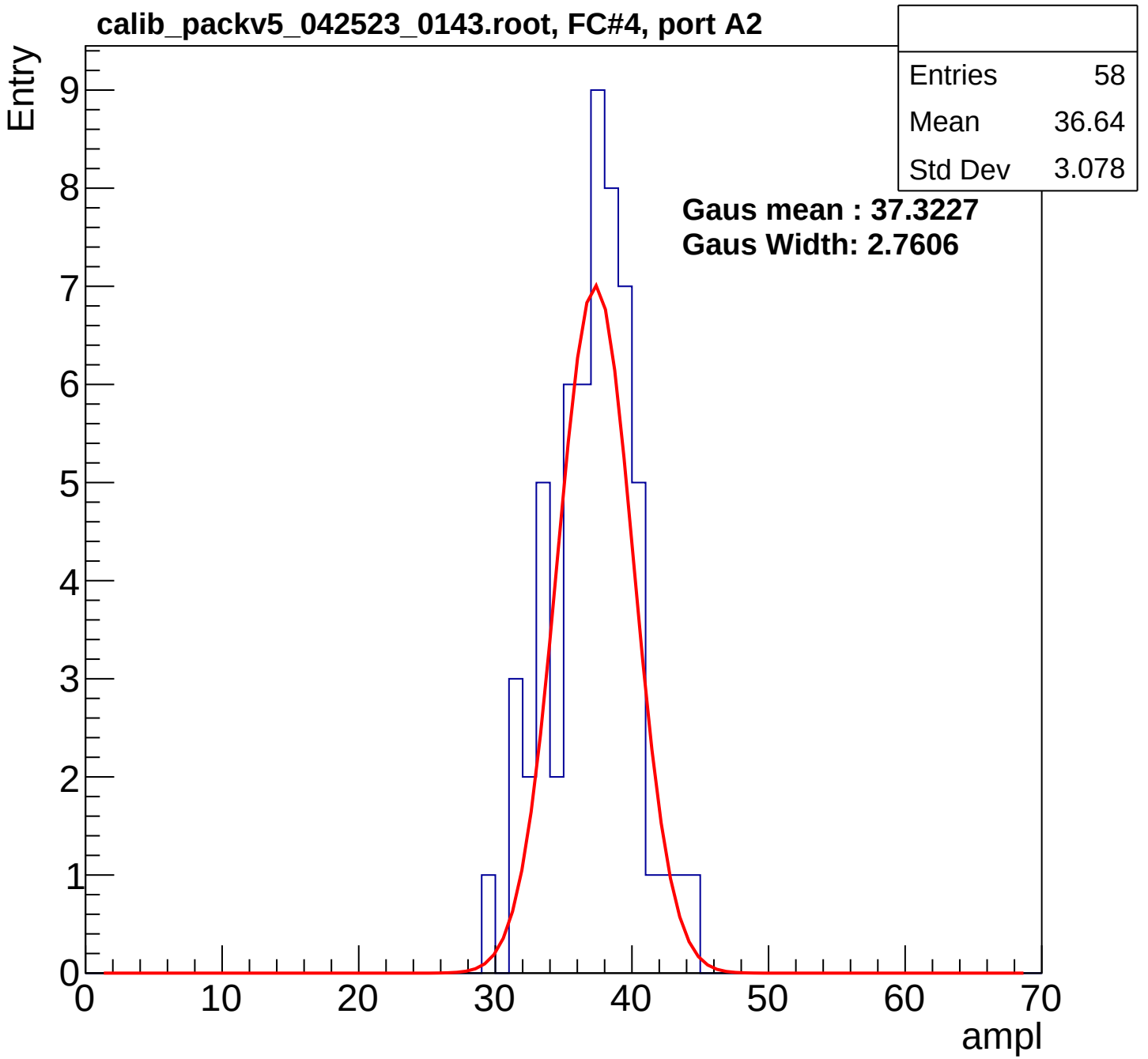
B1L100S, U20-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 36.64
Std Dev 3.078
Gaus mean : 37.3227
Gaus Width: 2.7606
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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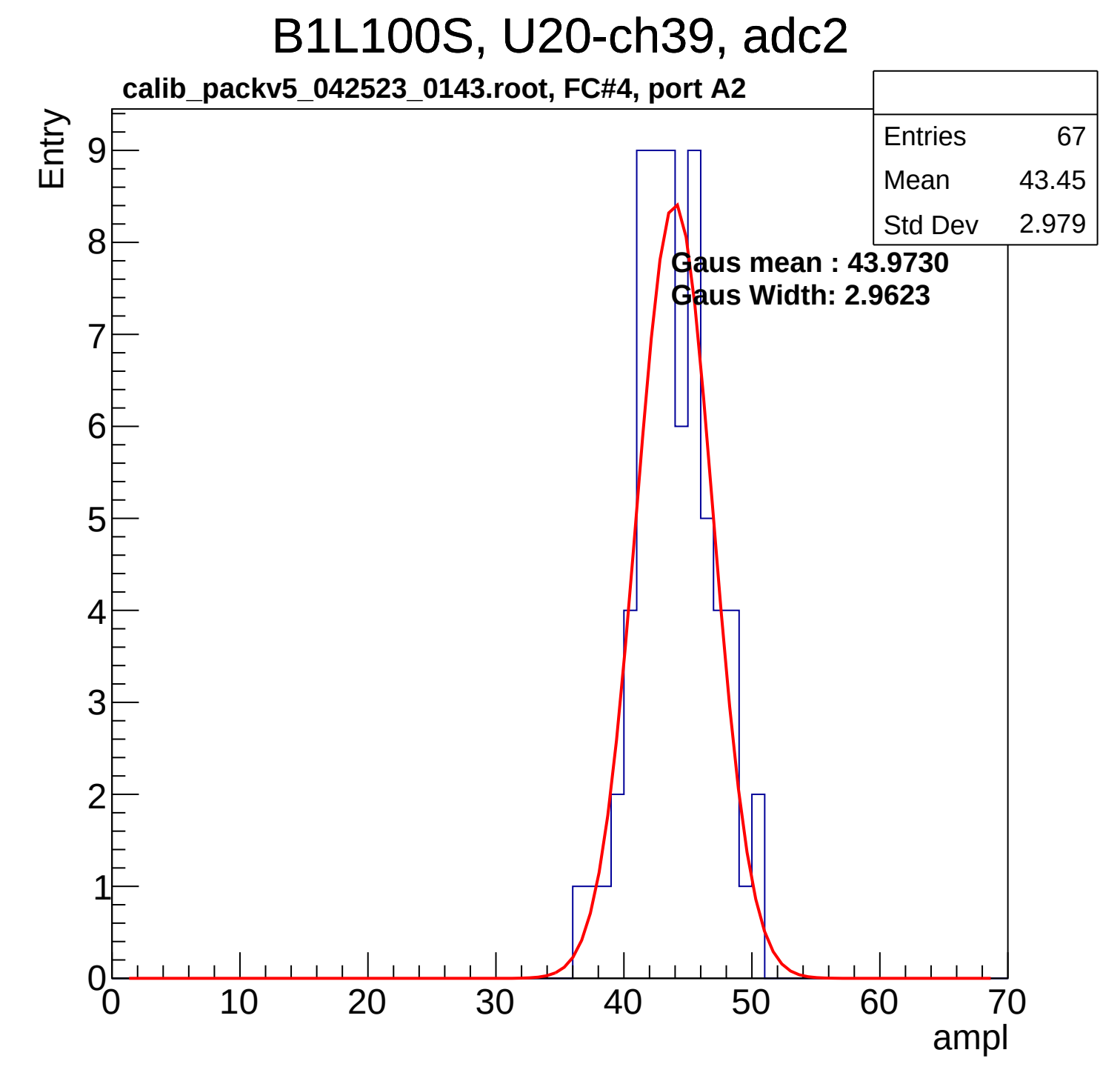
B1L100S, U20-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43.45
Std Dev 2.979
Gaus mean : 43.9730
Gaus Width: 2.9623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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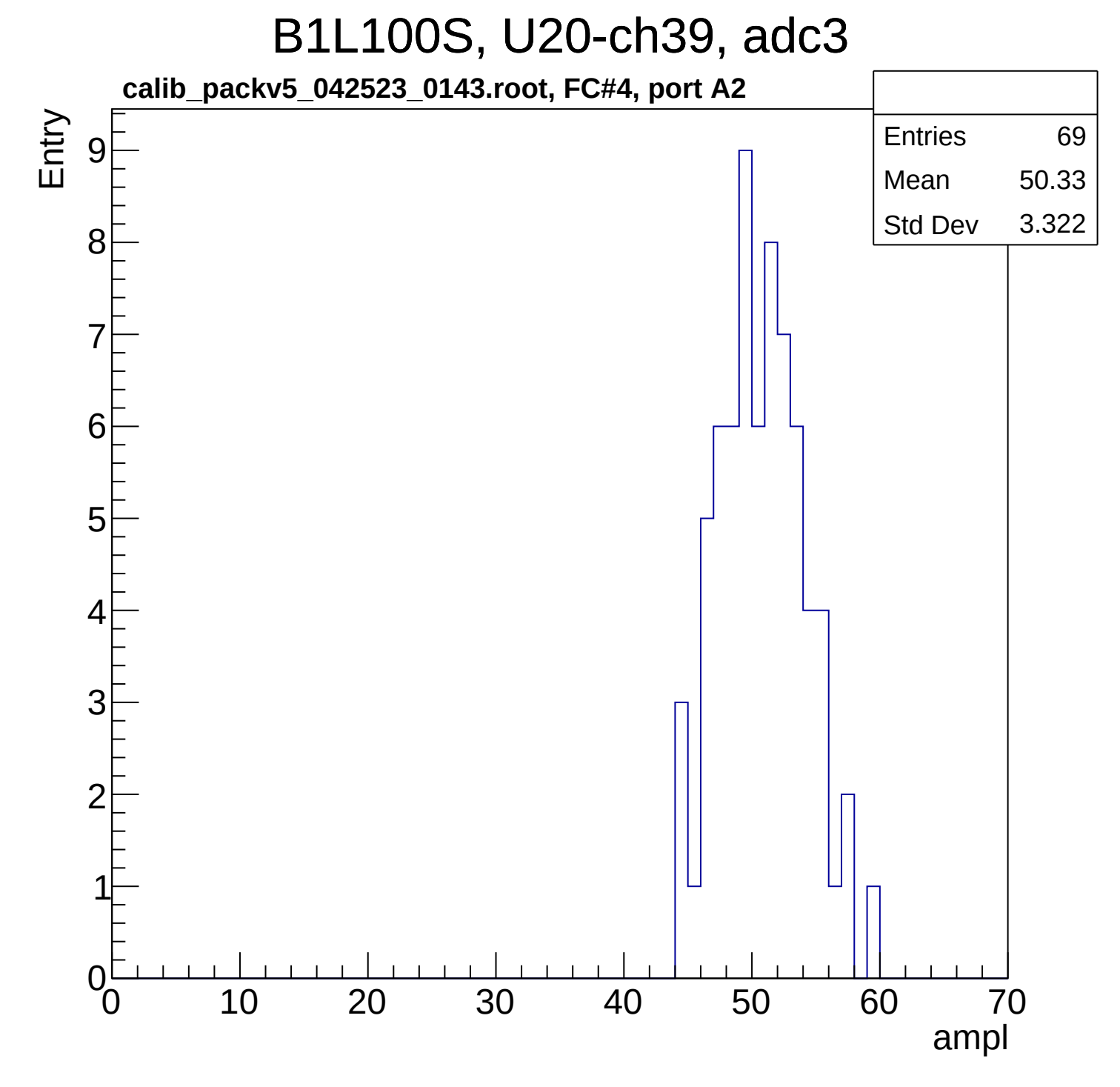
B1L100S, U20-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 50.33
Std Dev 3.322
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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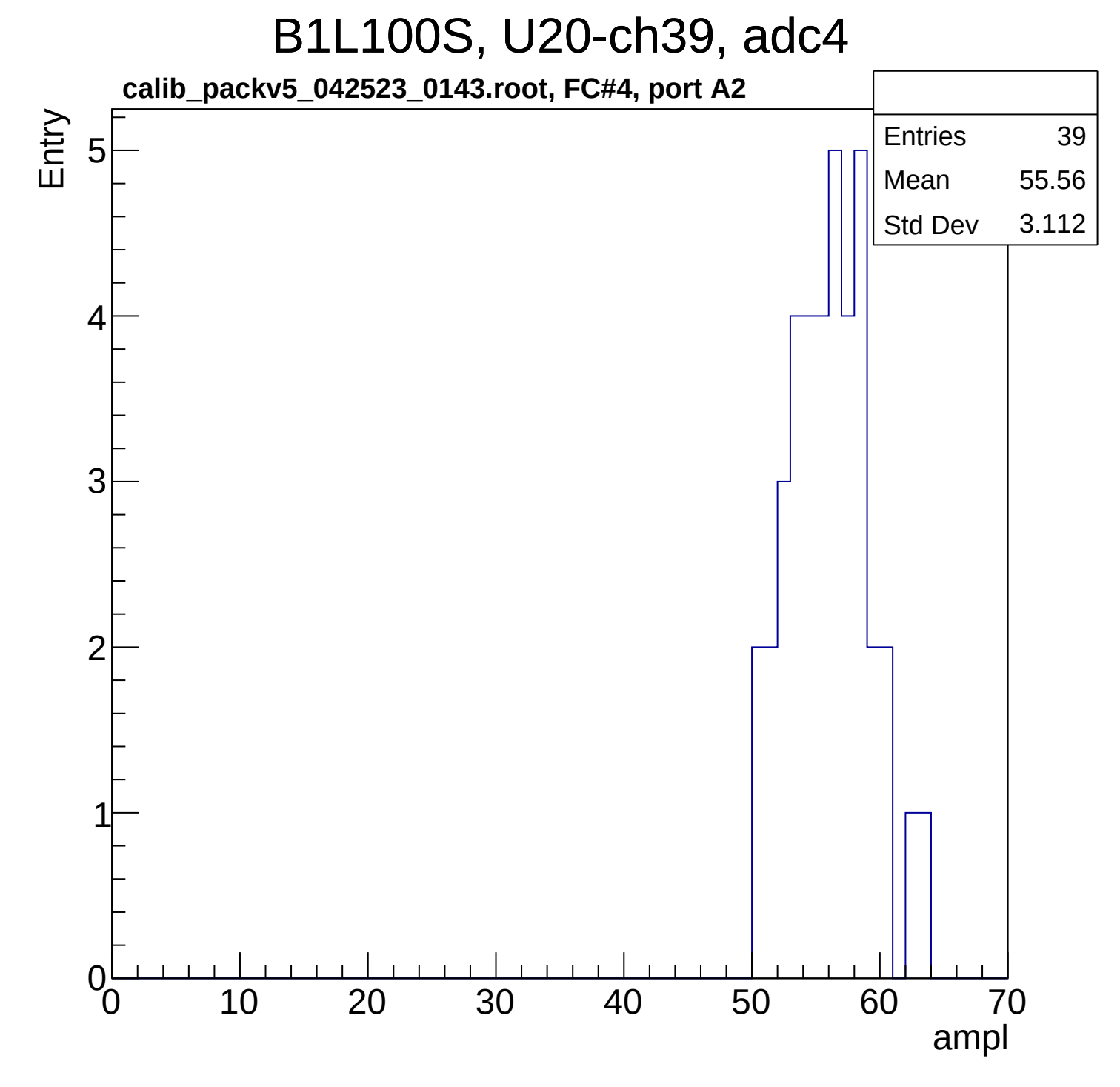
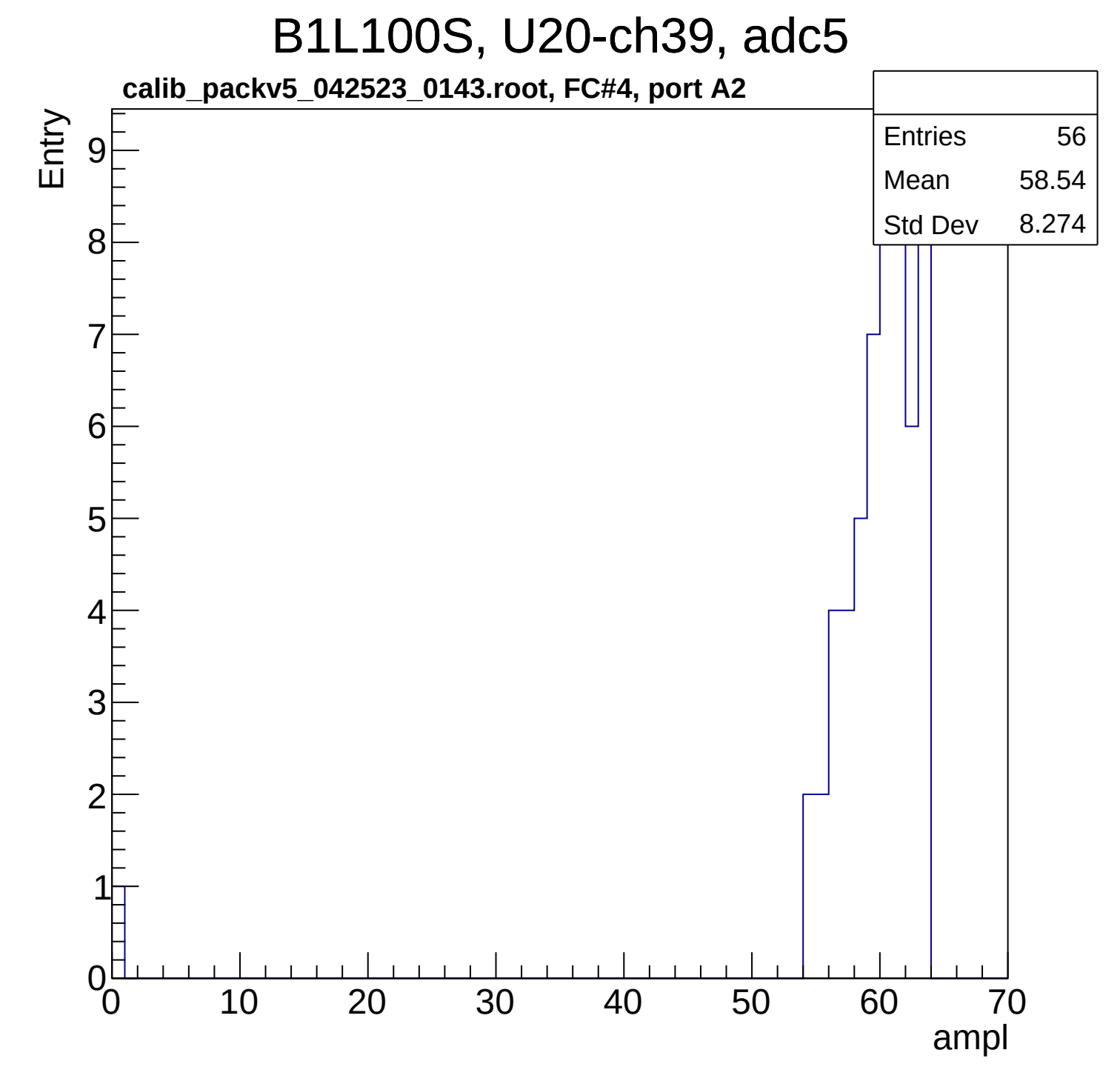
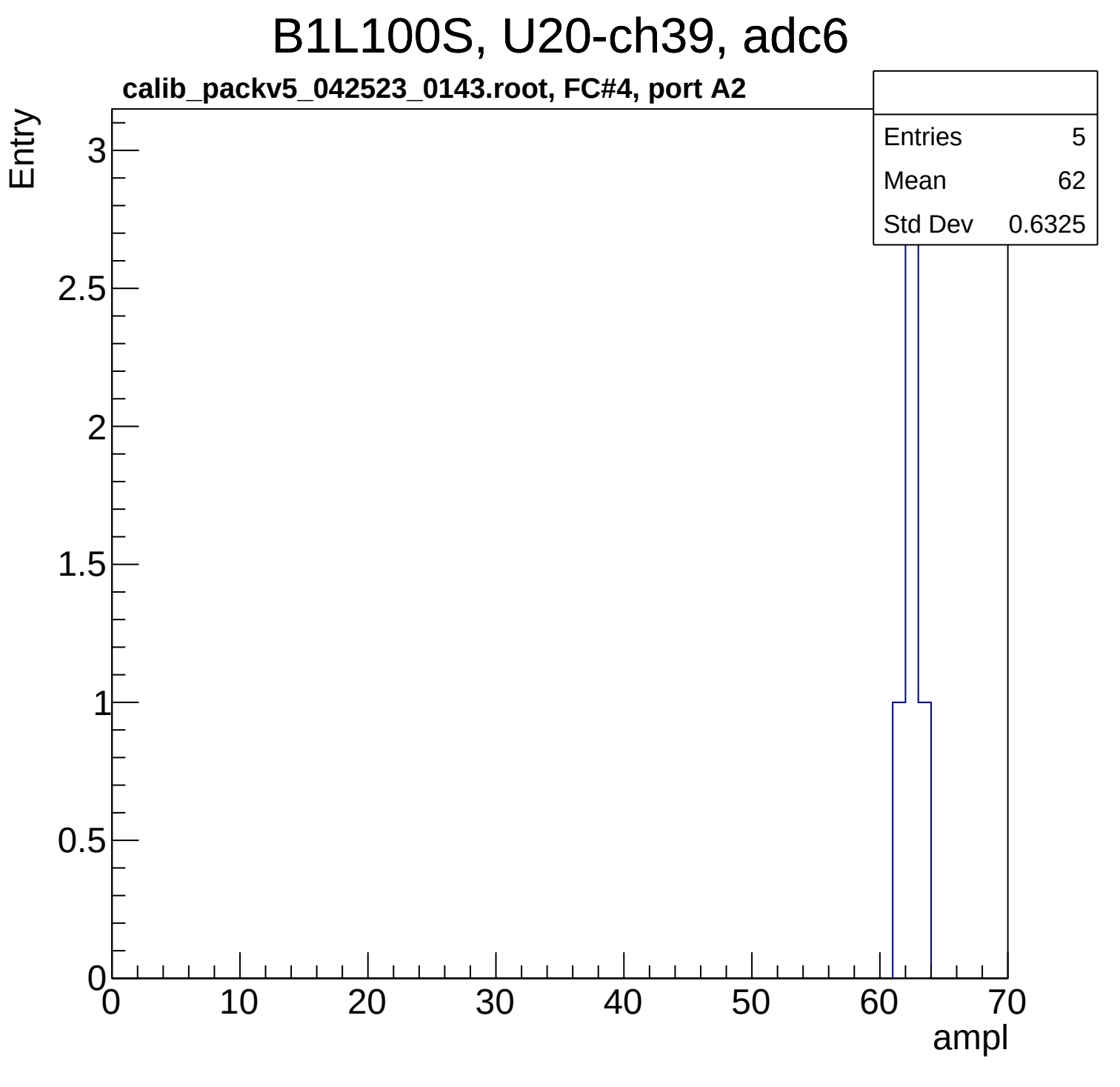
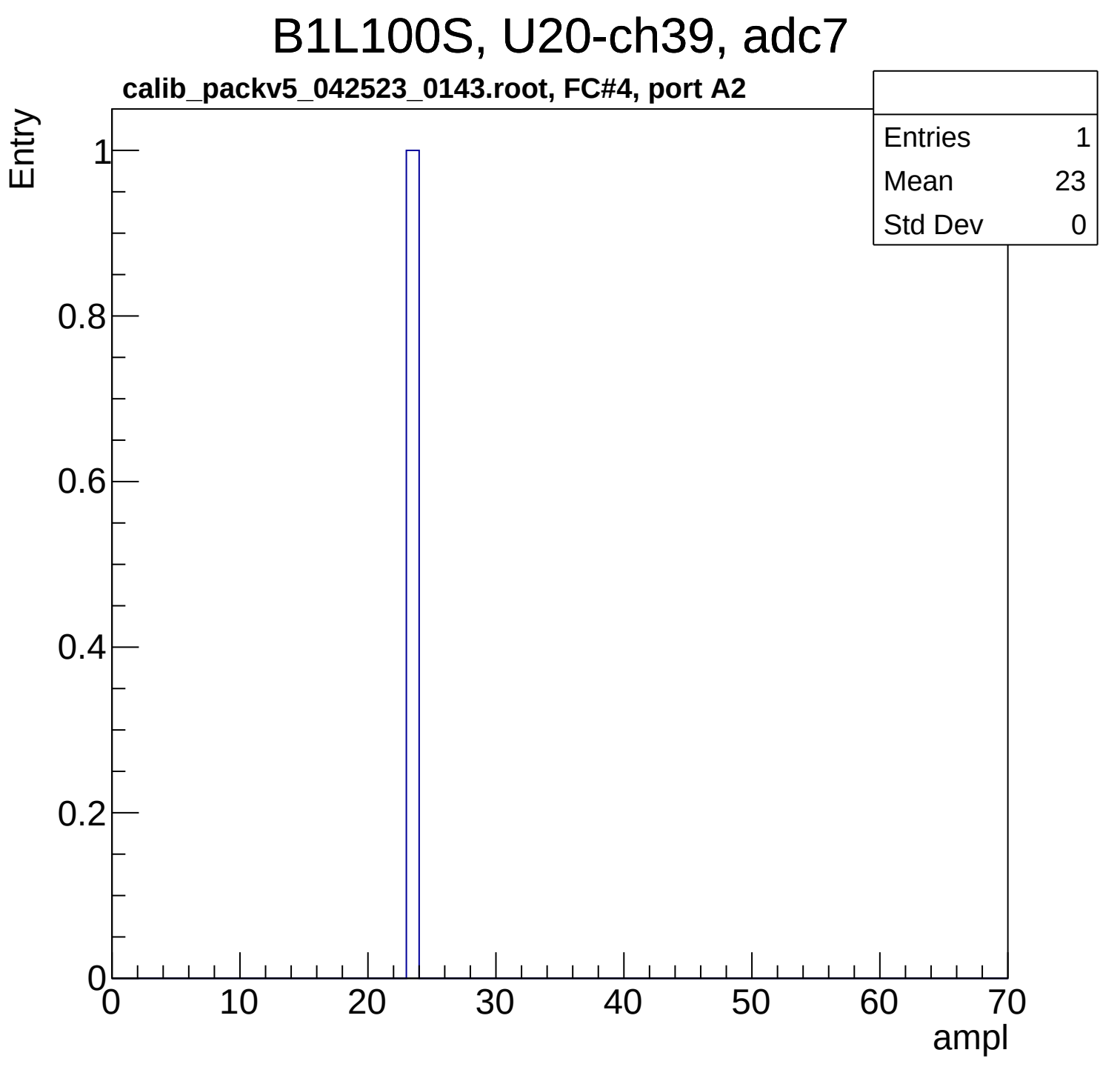

B1L100S, U20-ch39, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 55.56
Std Dev 3.112
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.54
Std Dev 8.274
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 0.6325
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
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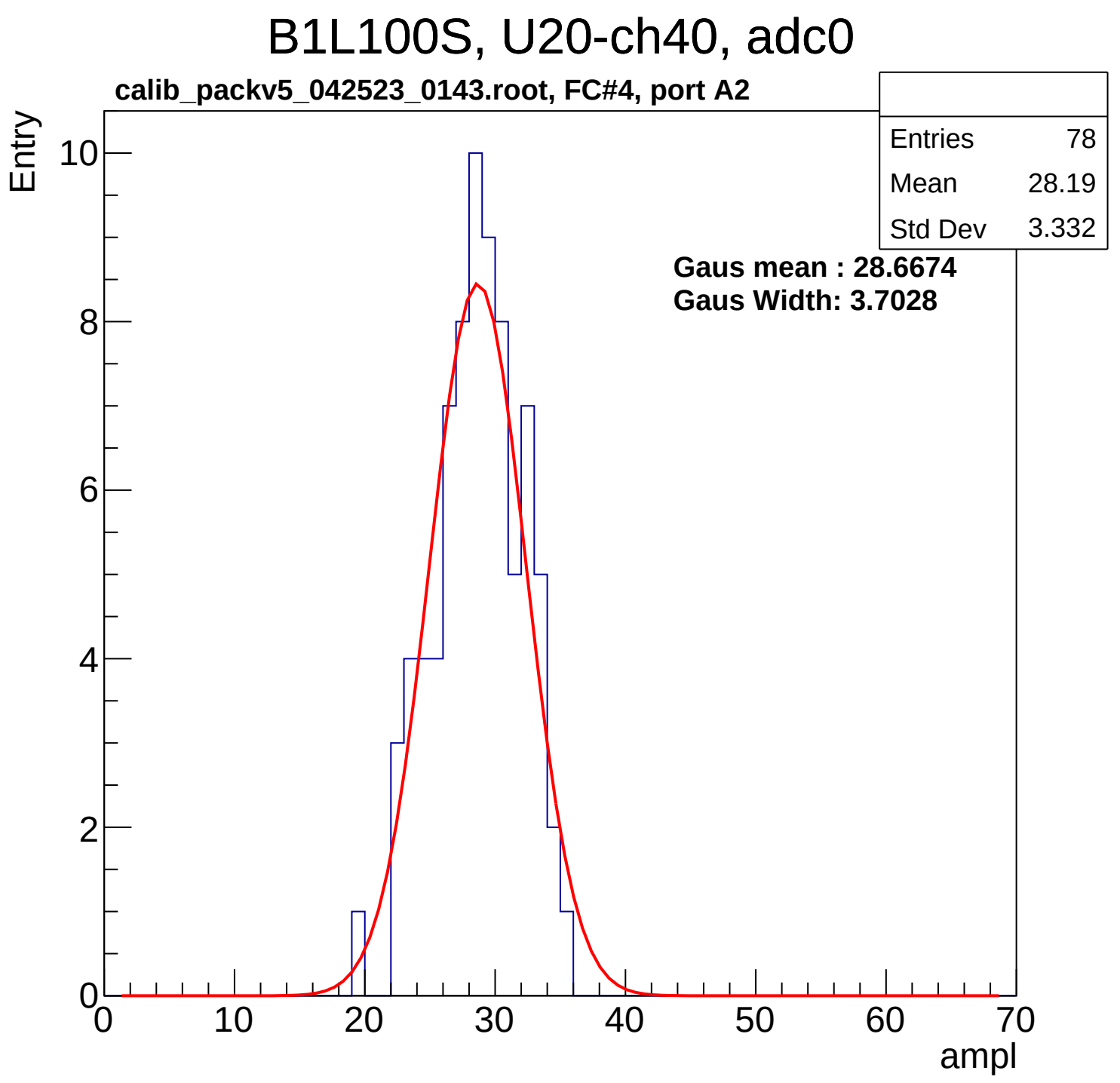
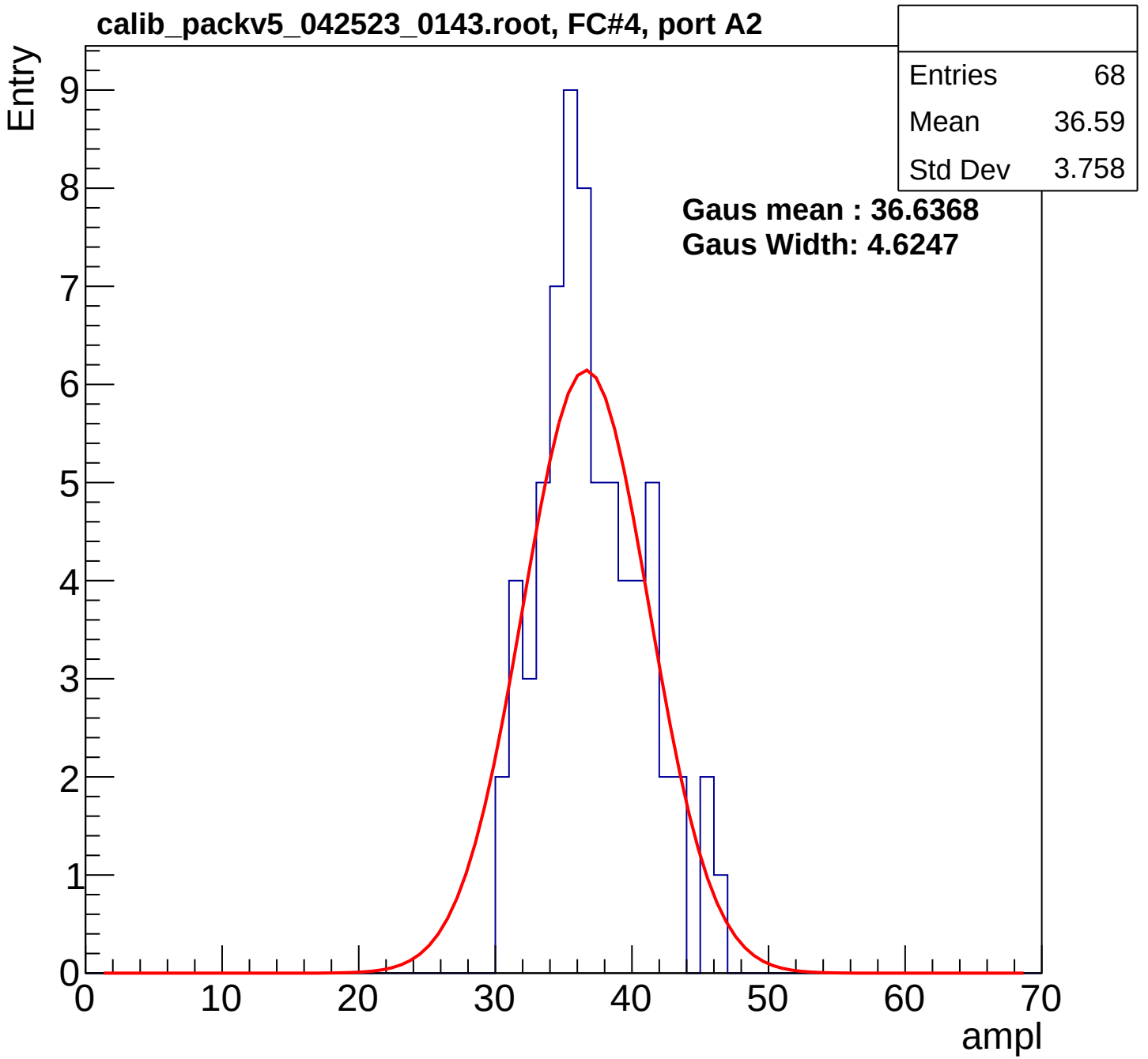
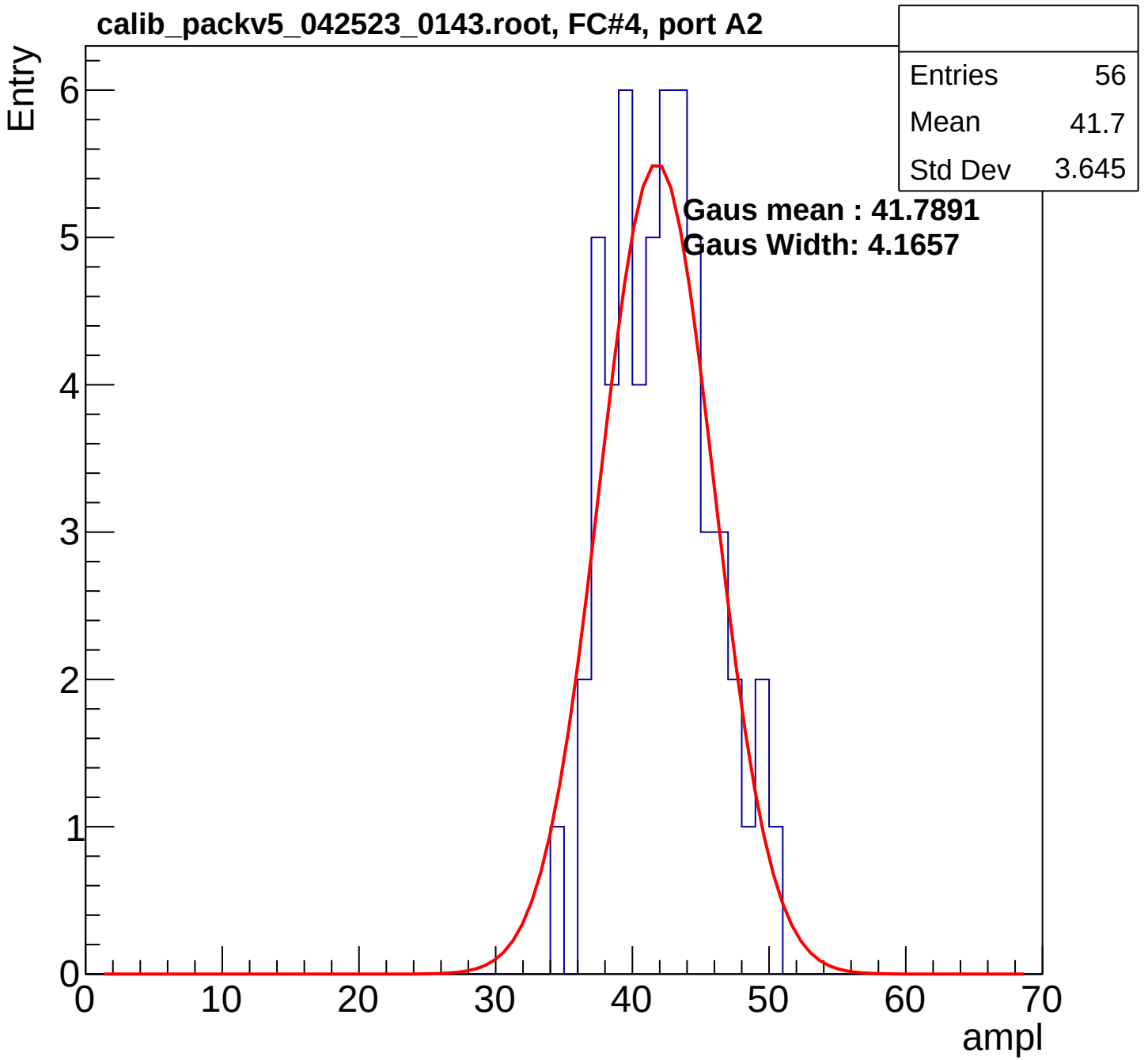
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 28.19
Std Dev 3.332
Gaus mean : 28.6674
Gaus Width: 3.7028
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.59
Std Dev 3.758
Gaus mean : 36.6368
Gaus Width: 4.6247
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 41.7
Std Dev 3.645
Gaus mean : 41.7891
Gaus Width: 4.1657
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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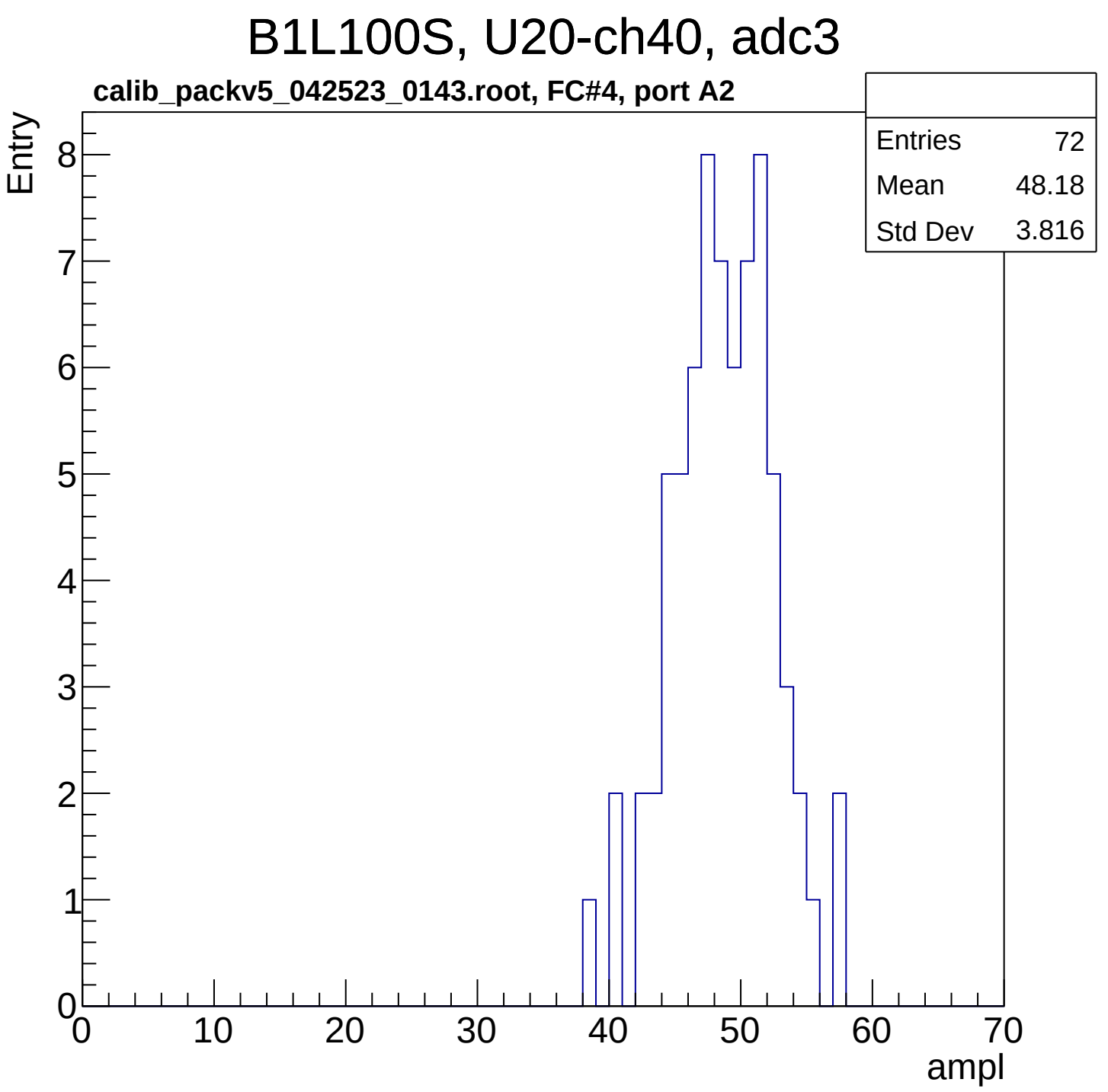
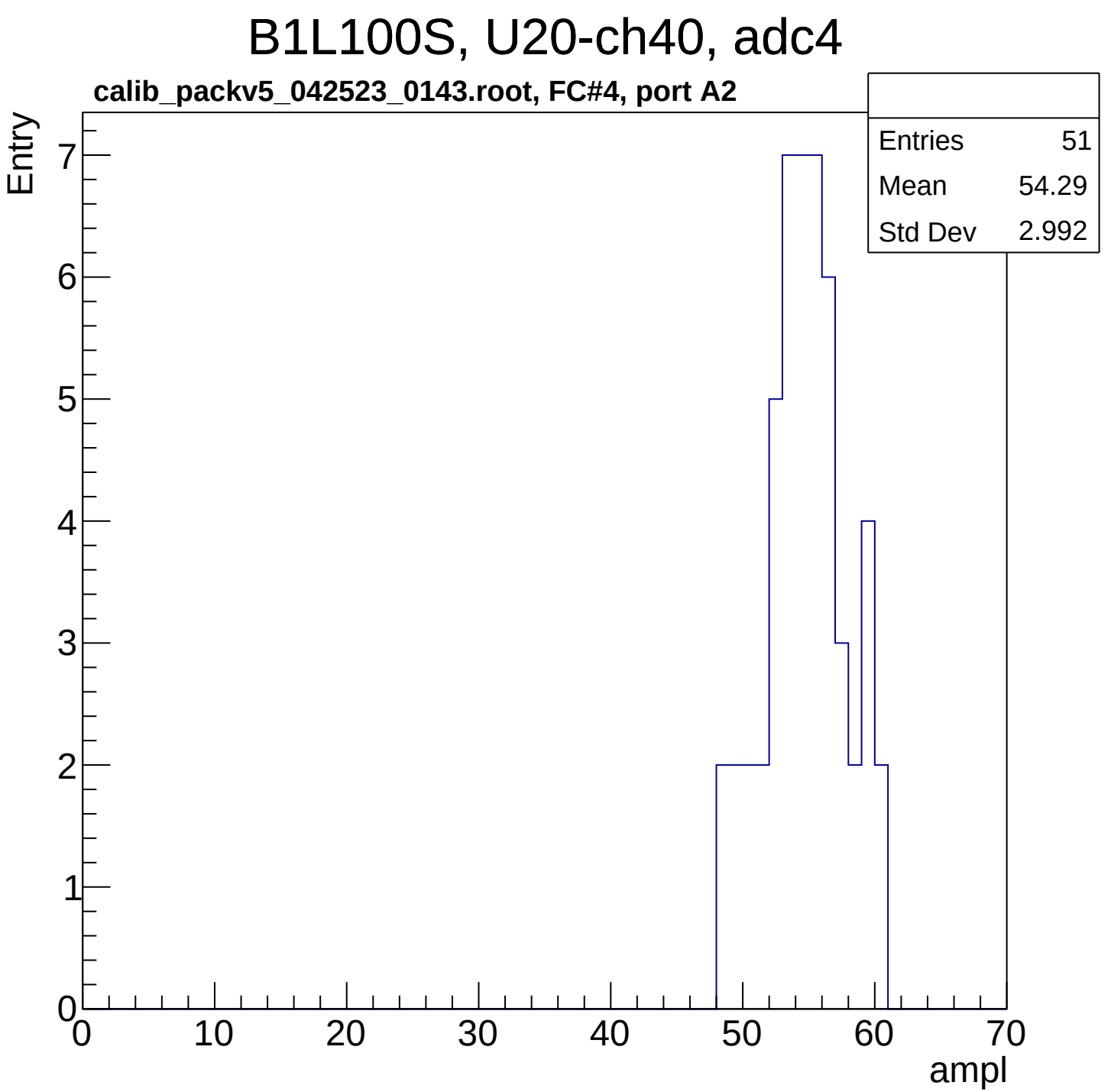
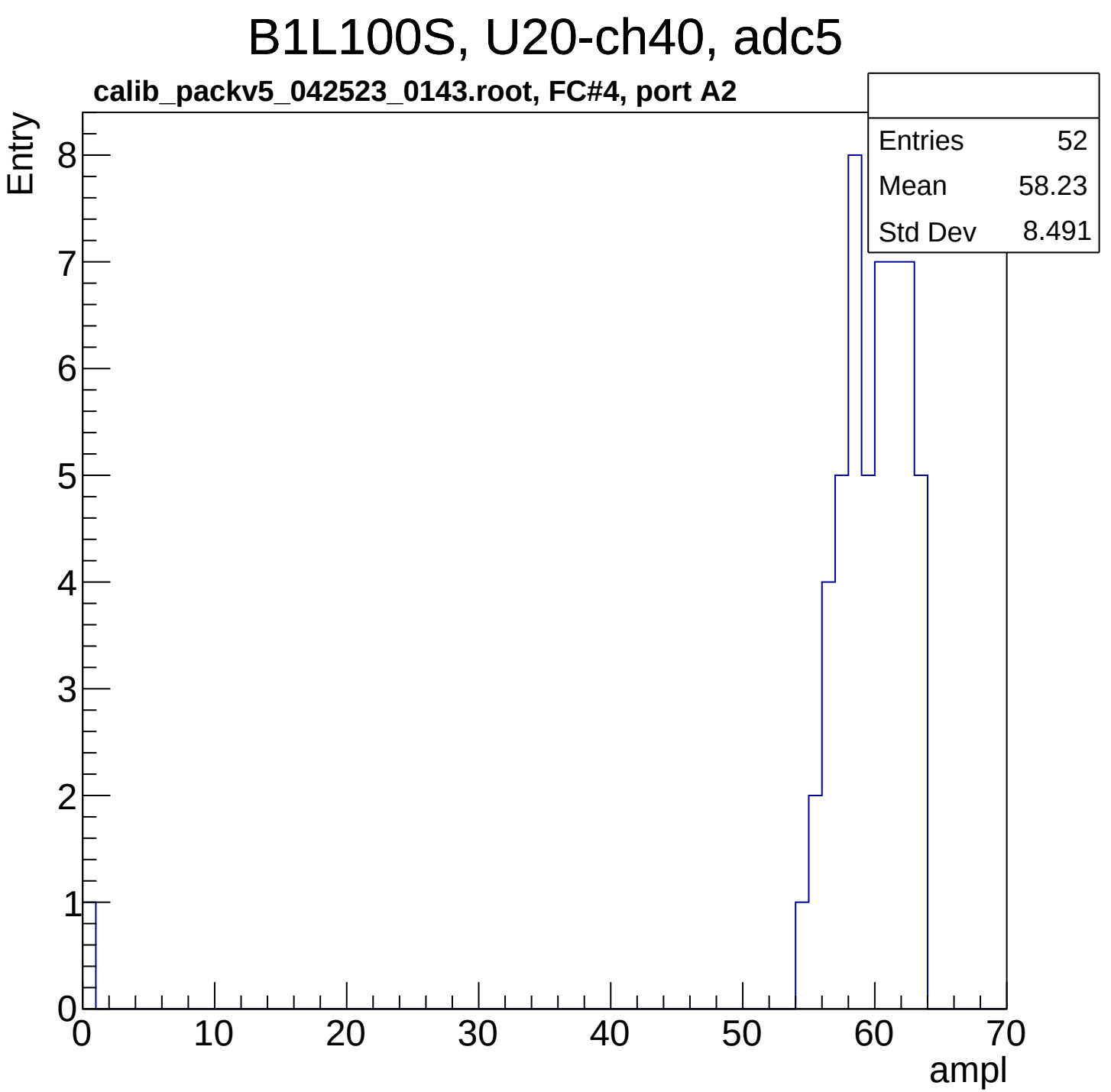

B1L100S, U20-ch40, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 48.18
Std Dev 3.816
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.29
Std Dev 2.992
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.23
Std Dev 8.491
Entry
ampl
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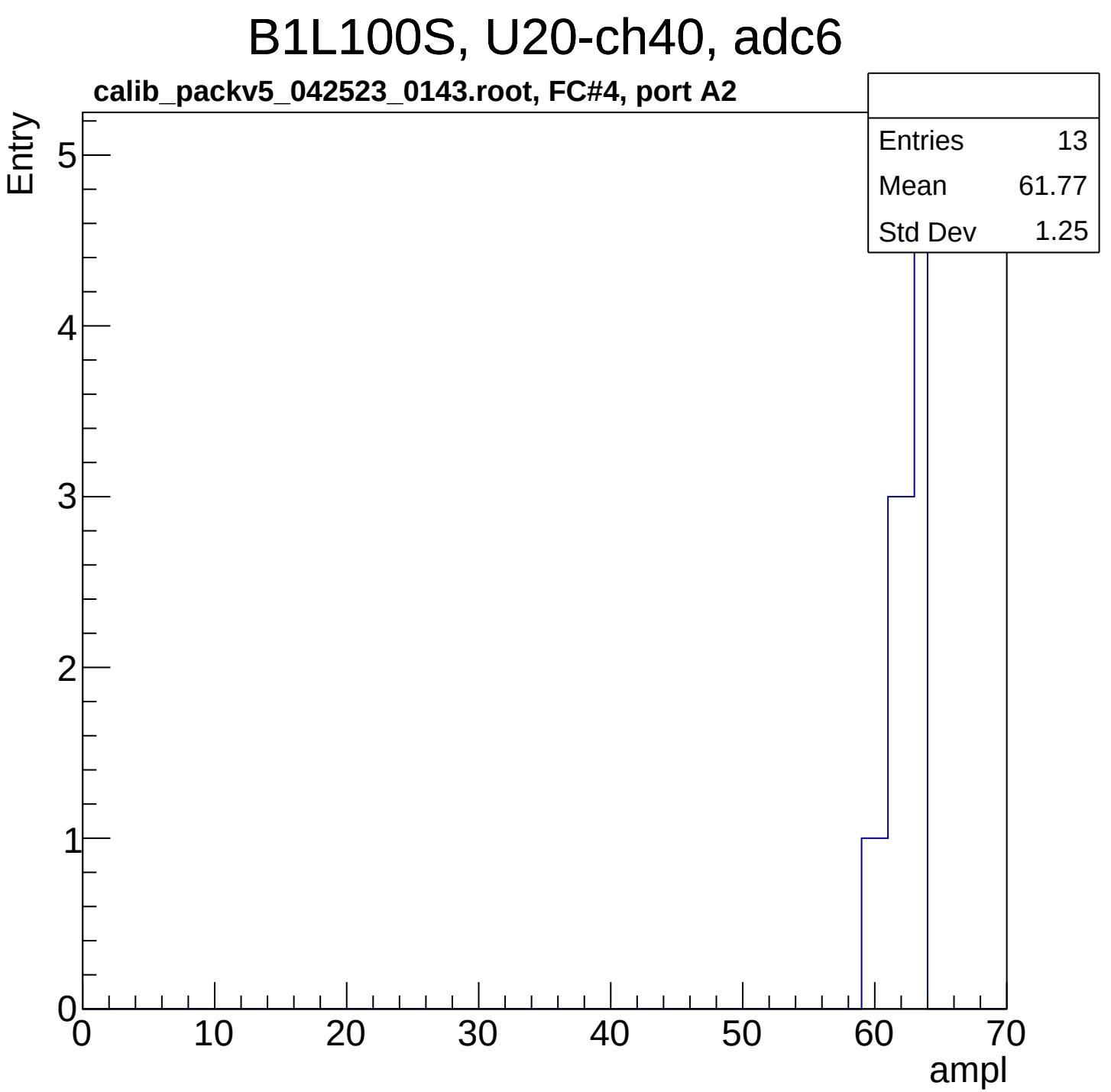
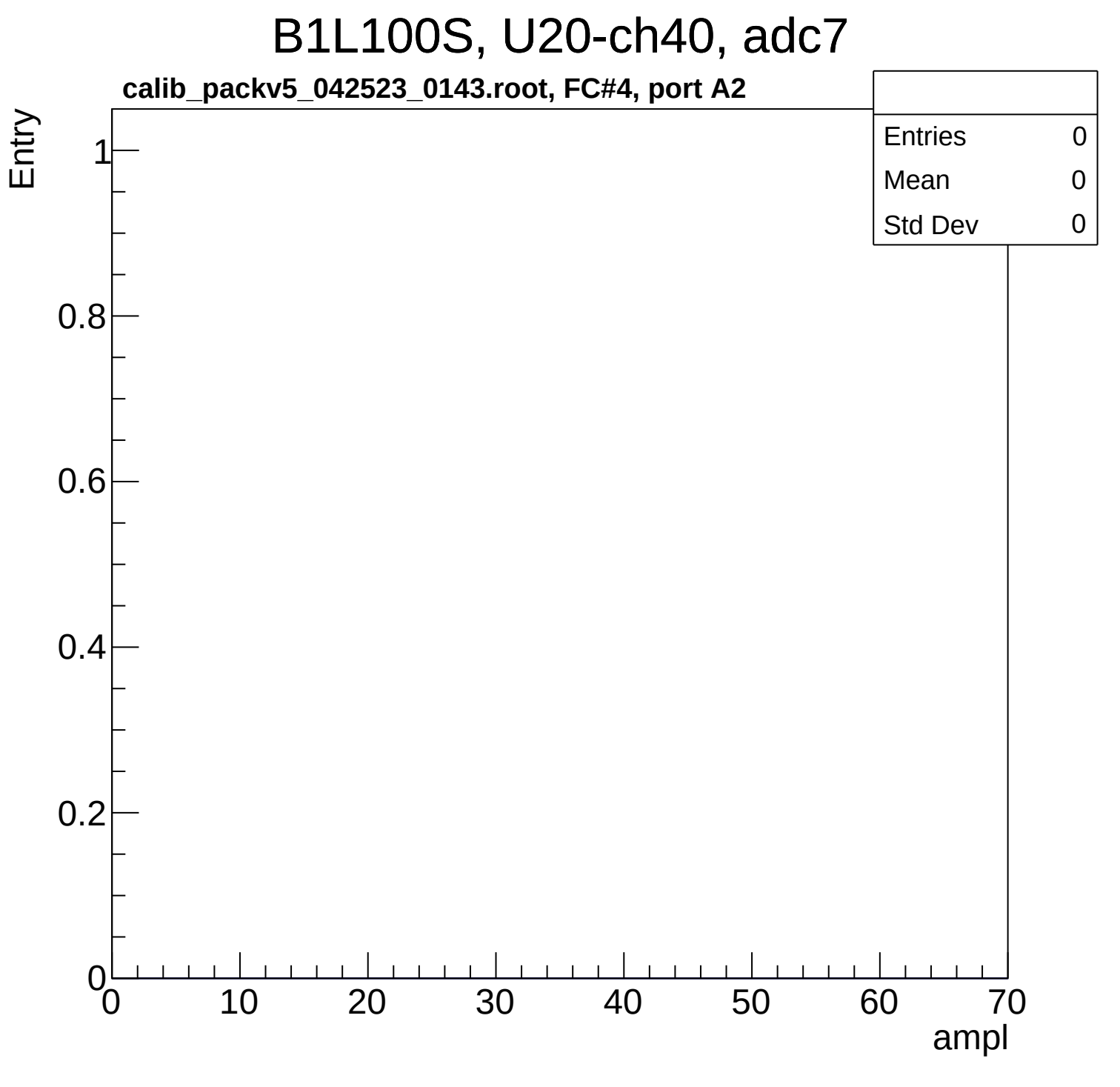
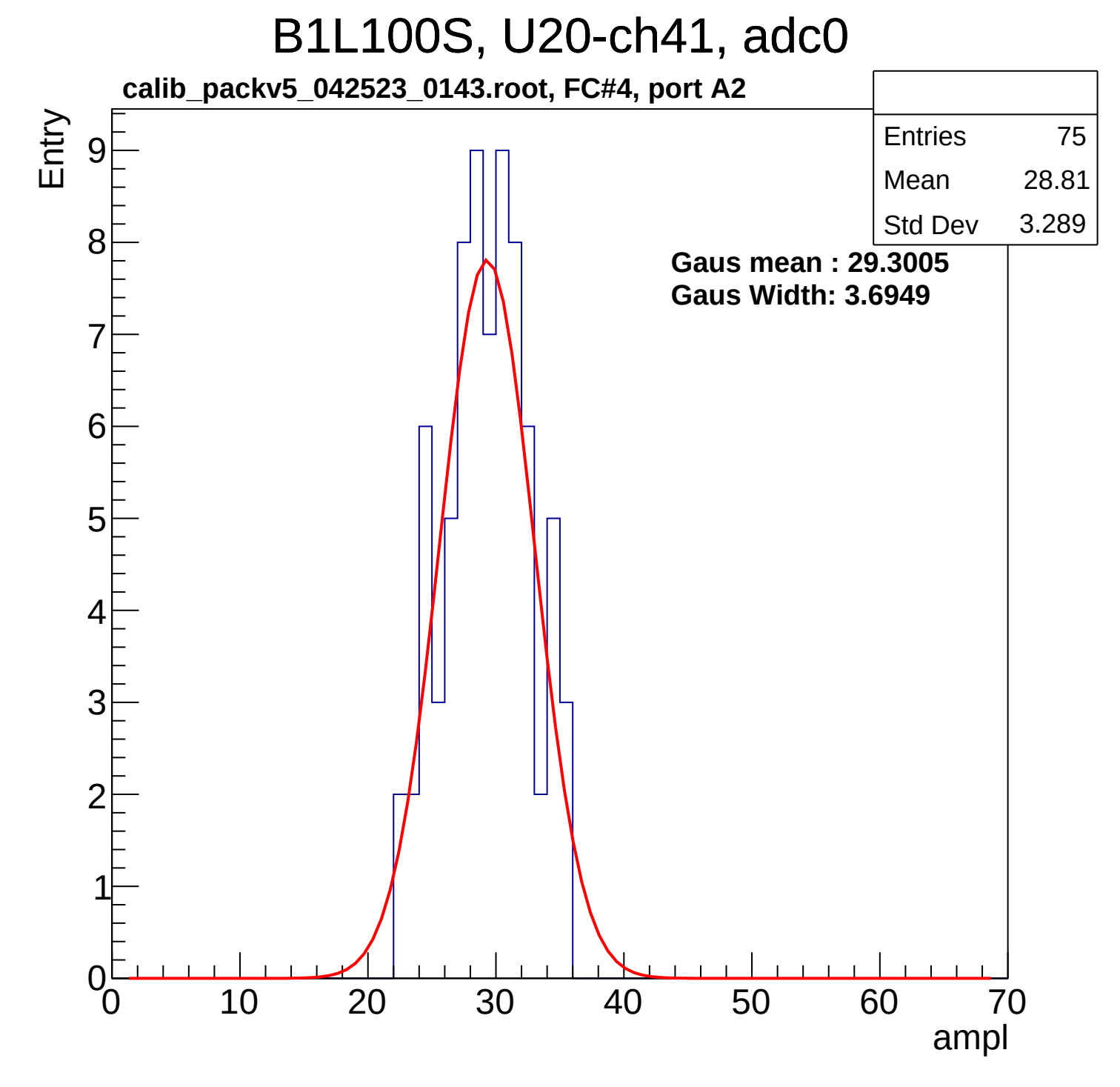
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch40, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 61.77
Std Dev 1.25
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch41, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.81
Std Dev 3.289
Gaus mean : 29.3005
Gaus Width: 3.6949
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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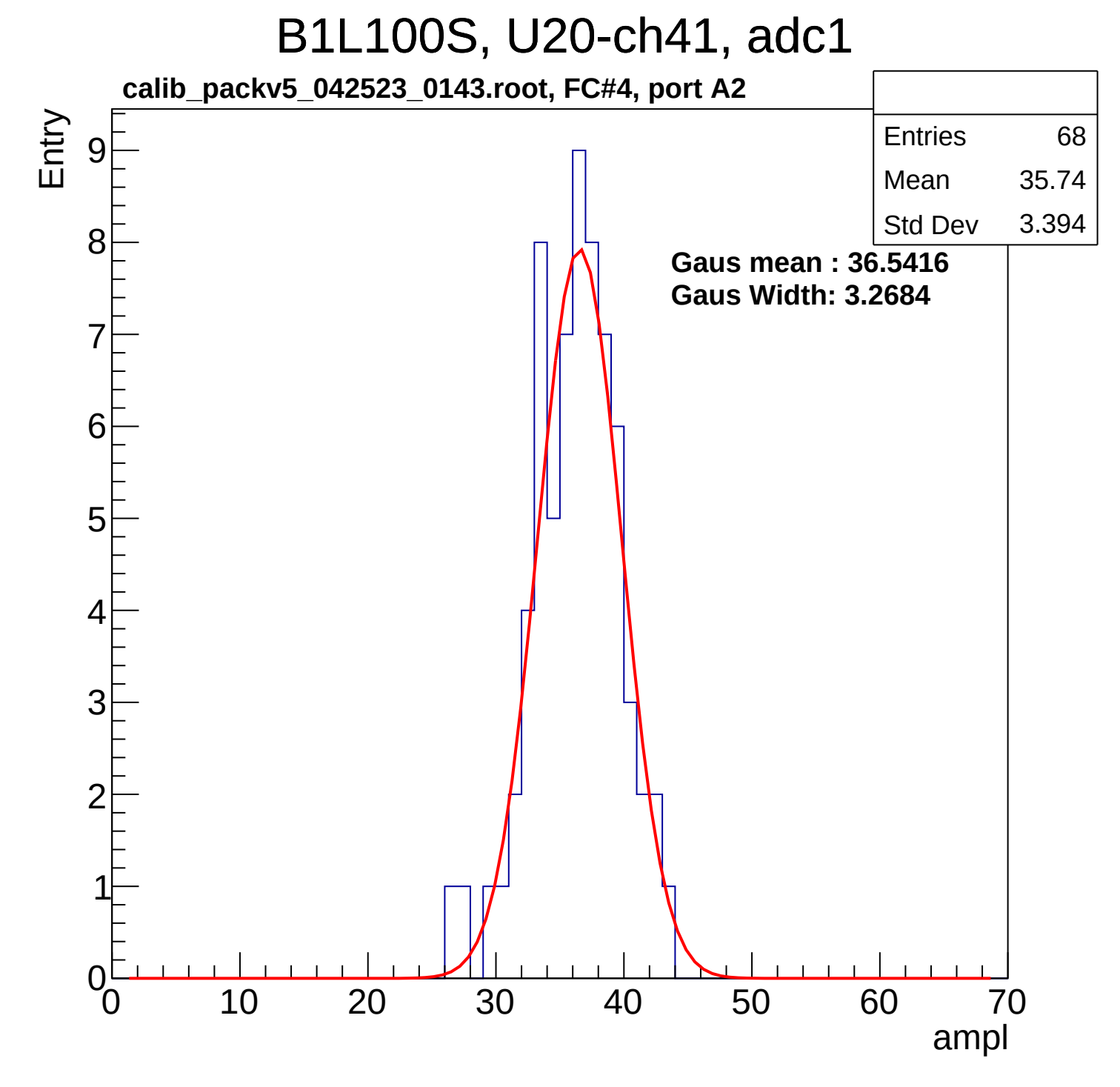
B1L100S, U20-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 35.74
Std Dev 3.394
Gaus mean : 36.5416
Gaus Width: 3.2684
Entry
ampl
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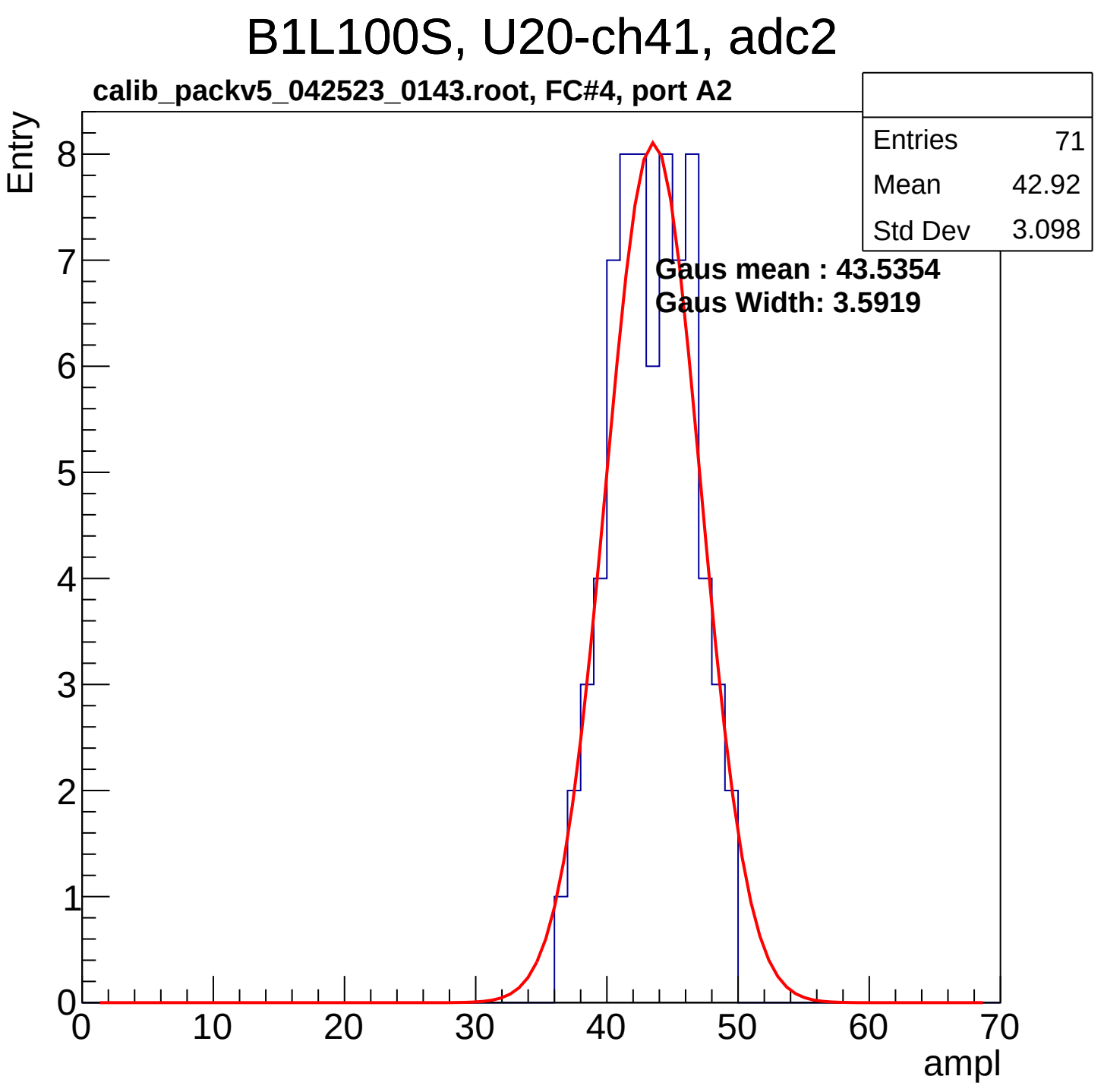
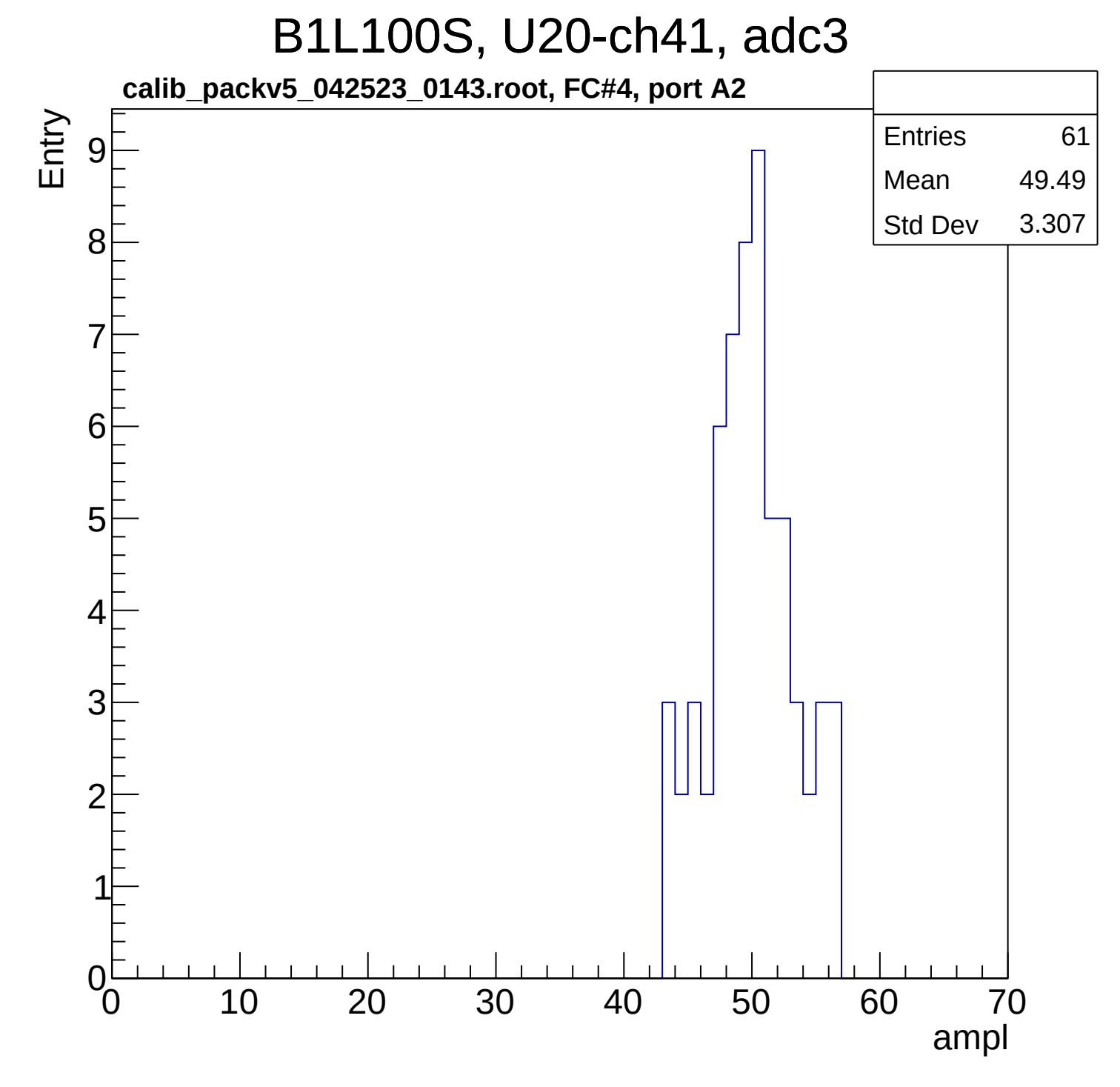
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.92
Std Dev 3.098
Gaus mean : 43.5354
Gaus Width: 3.5919
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.49
Std Dev 3.307
Entry
ampl
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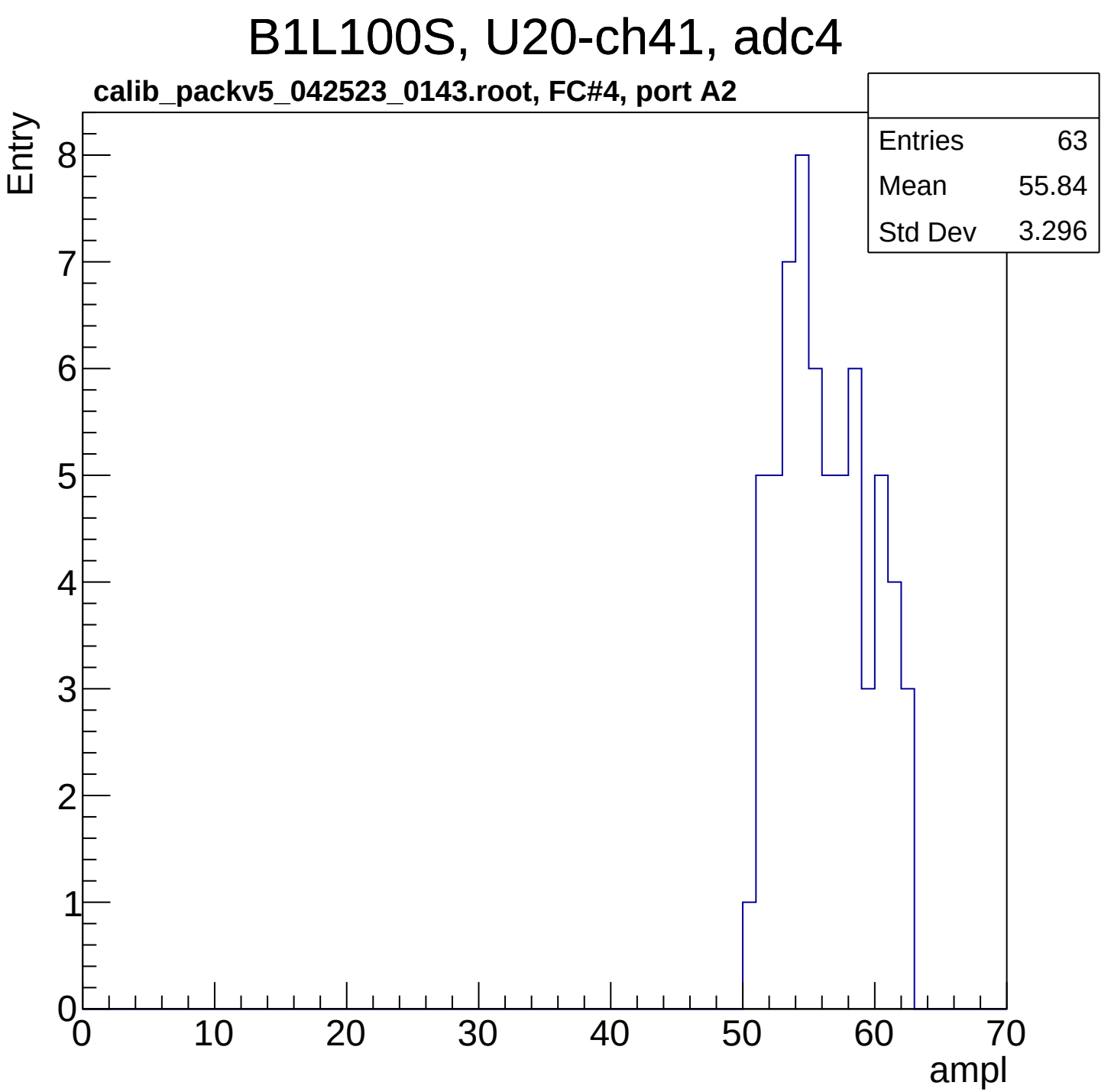
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch41, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.84
Std Dev 3.296
Entry
ampl
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4
5
6
7
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0
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20
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40
50
60
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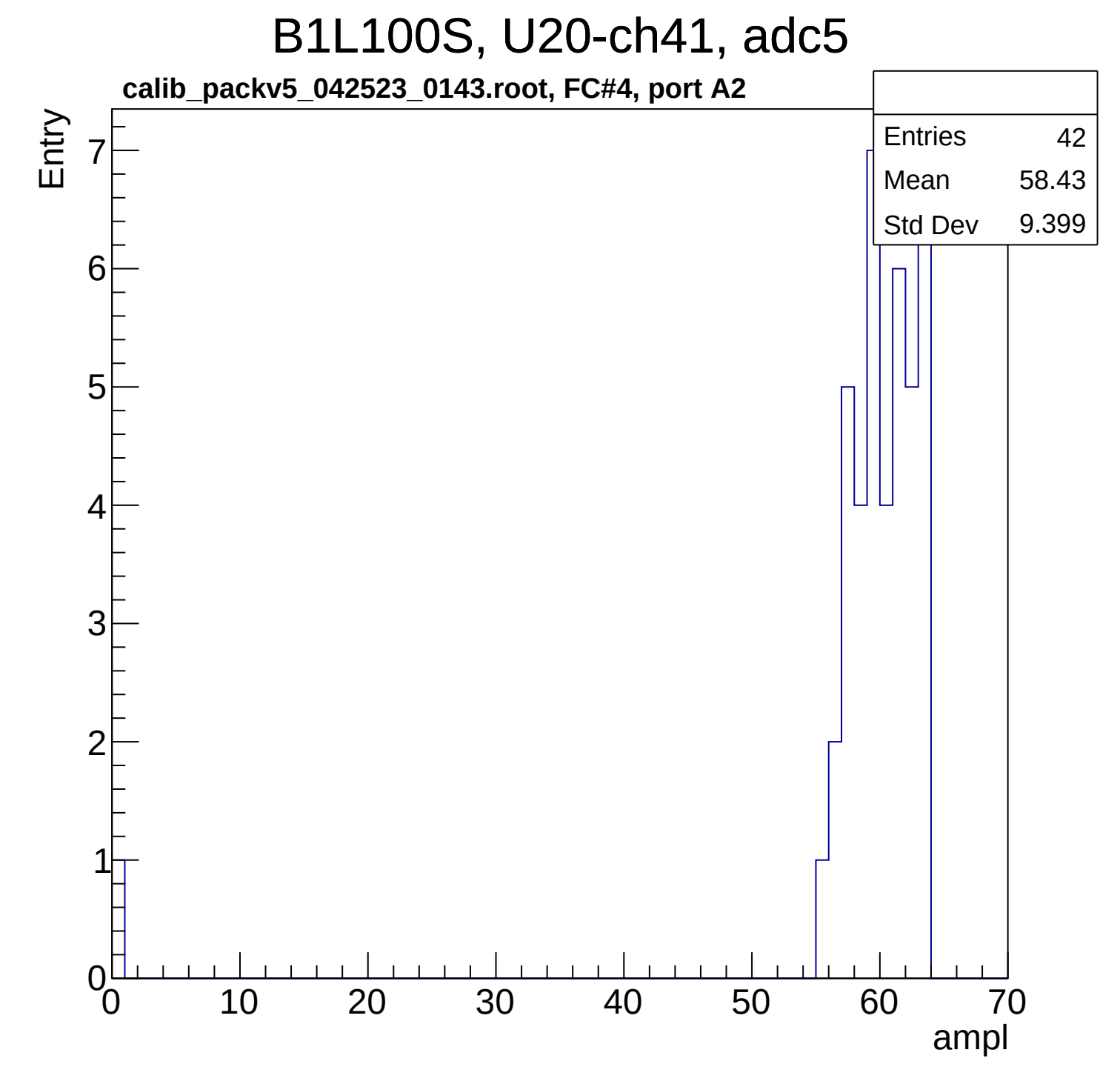
B1L100S, U20-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.43
Std Dev 9.399
Entry
ampl
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3
4
5
6
7
0
10
20
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40
50
60
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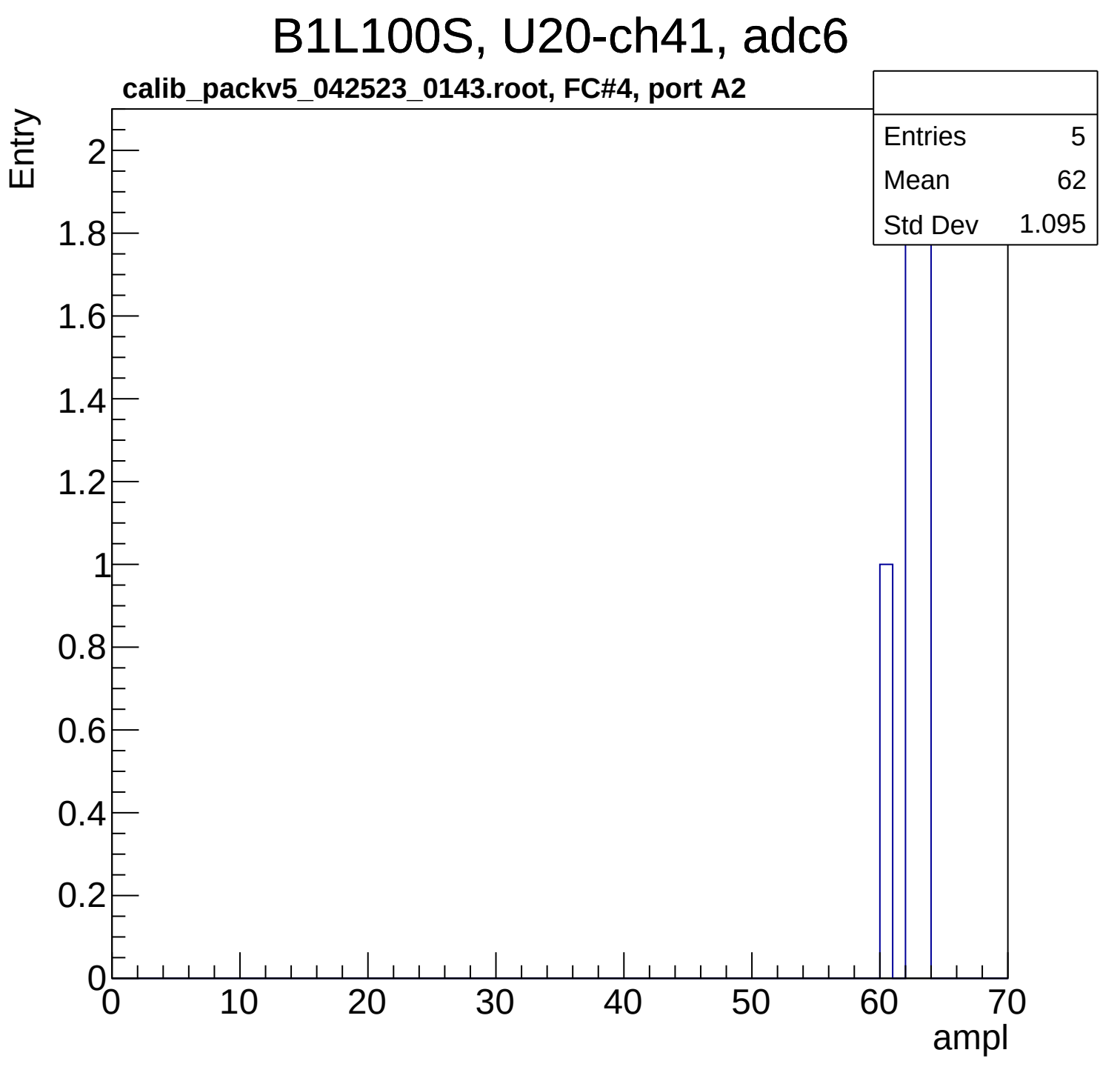
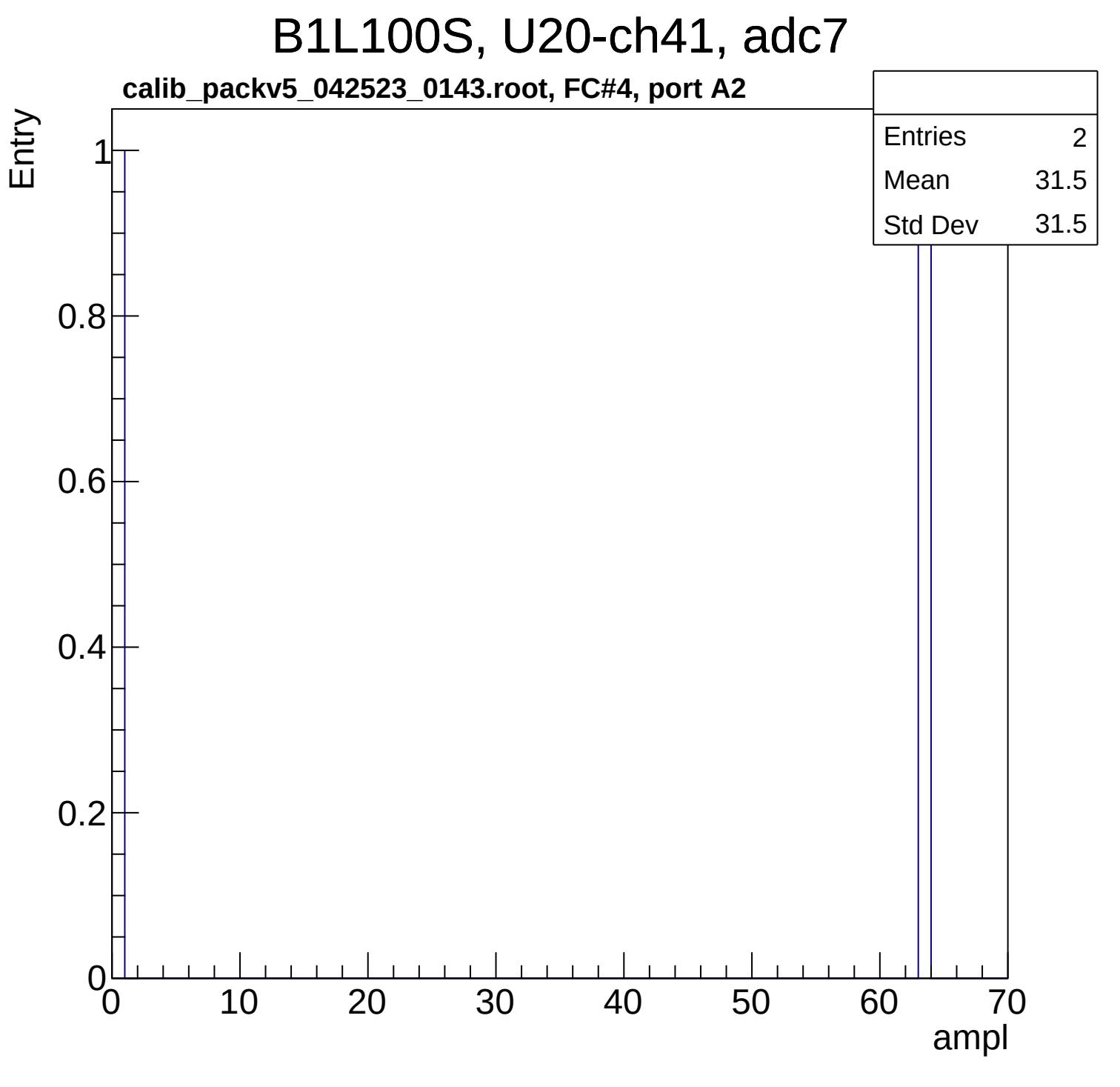

B1L100S, U20-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
0
10
20
30
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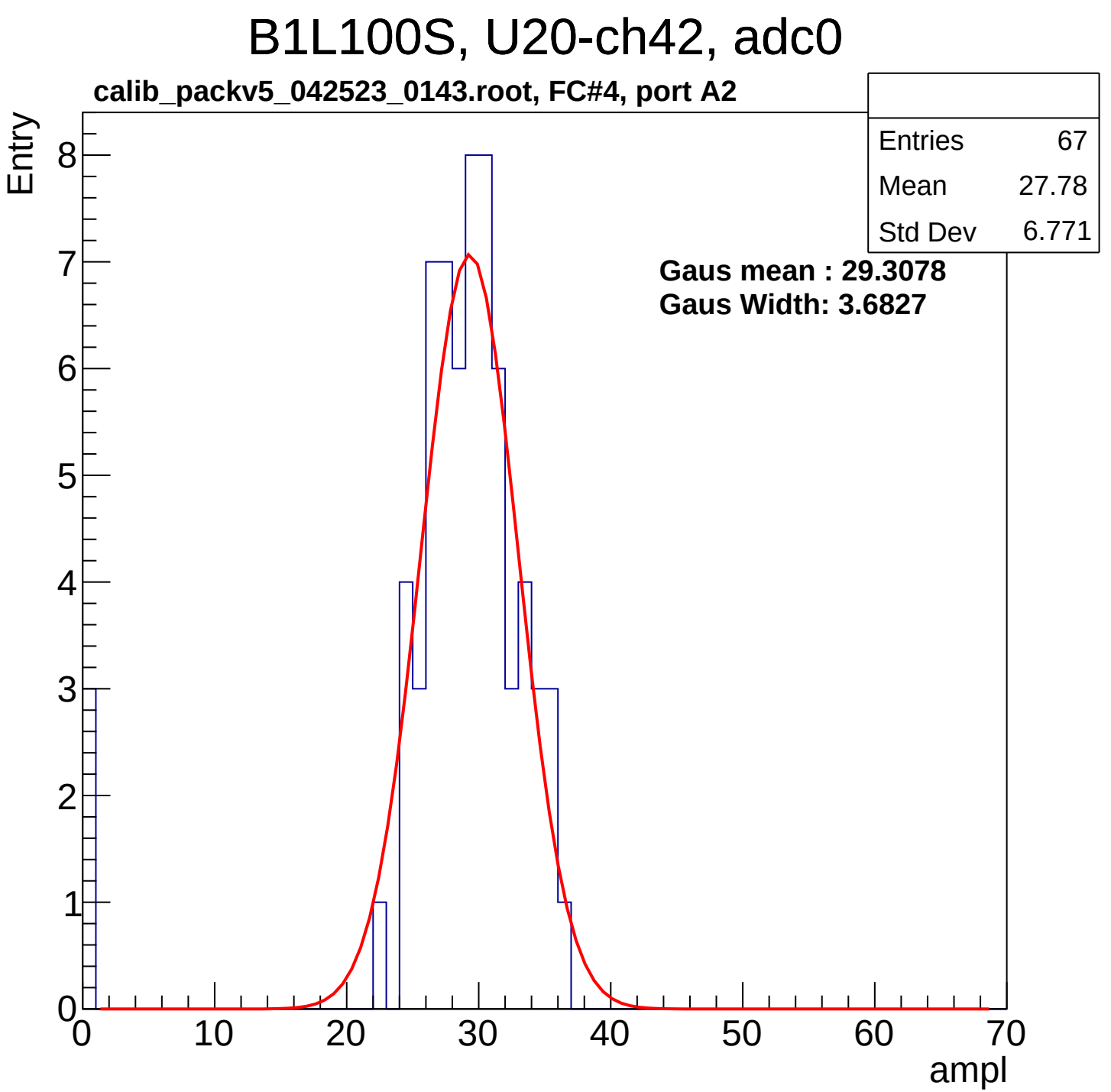
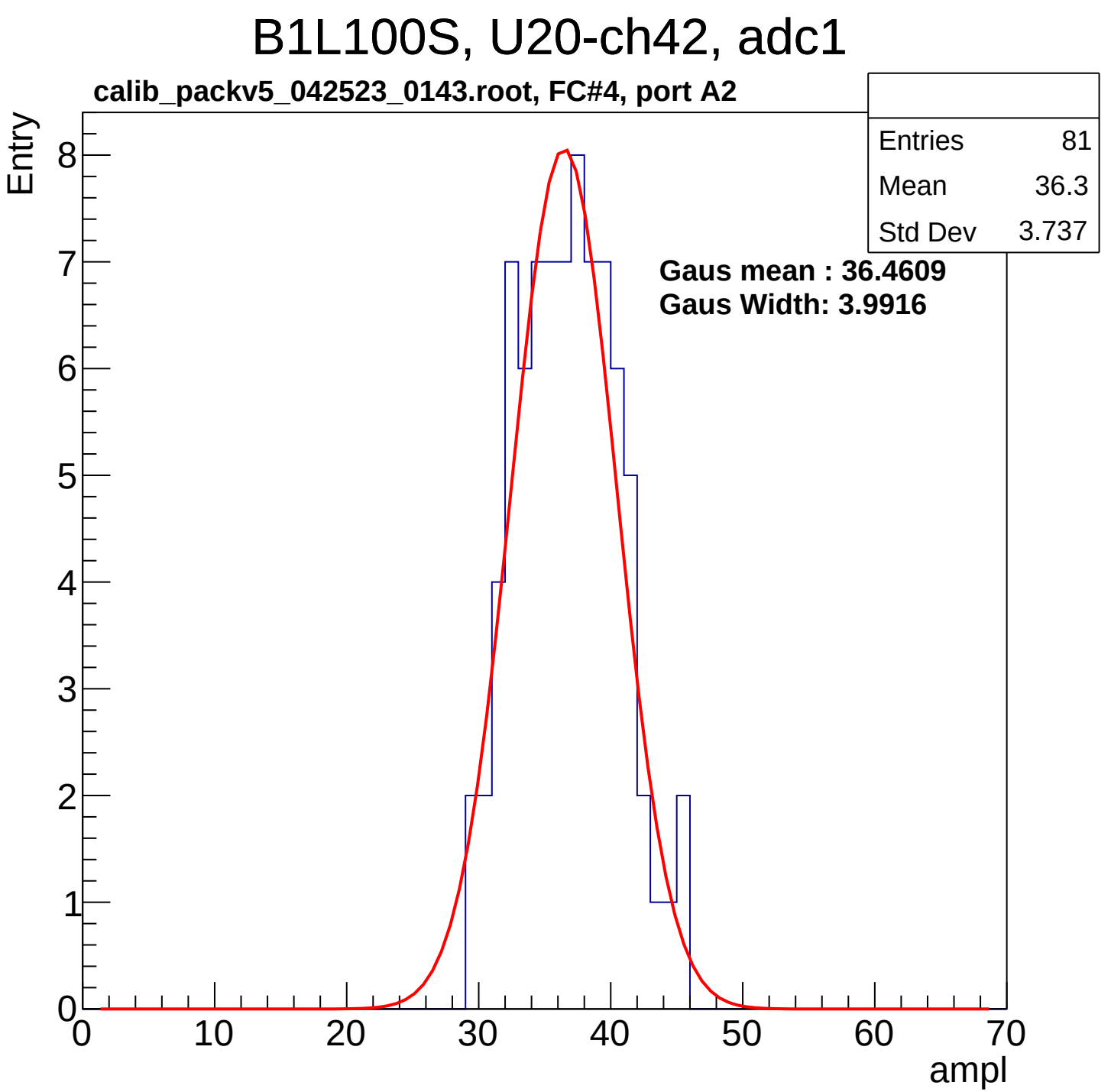
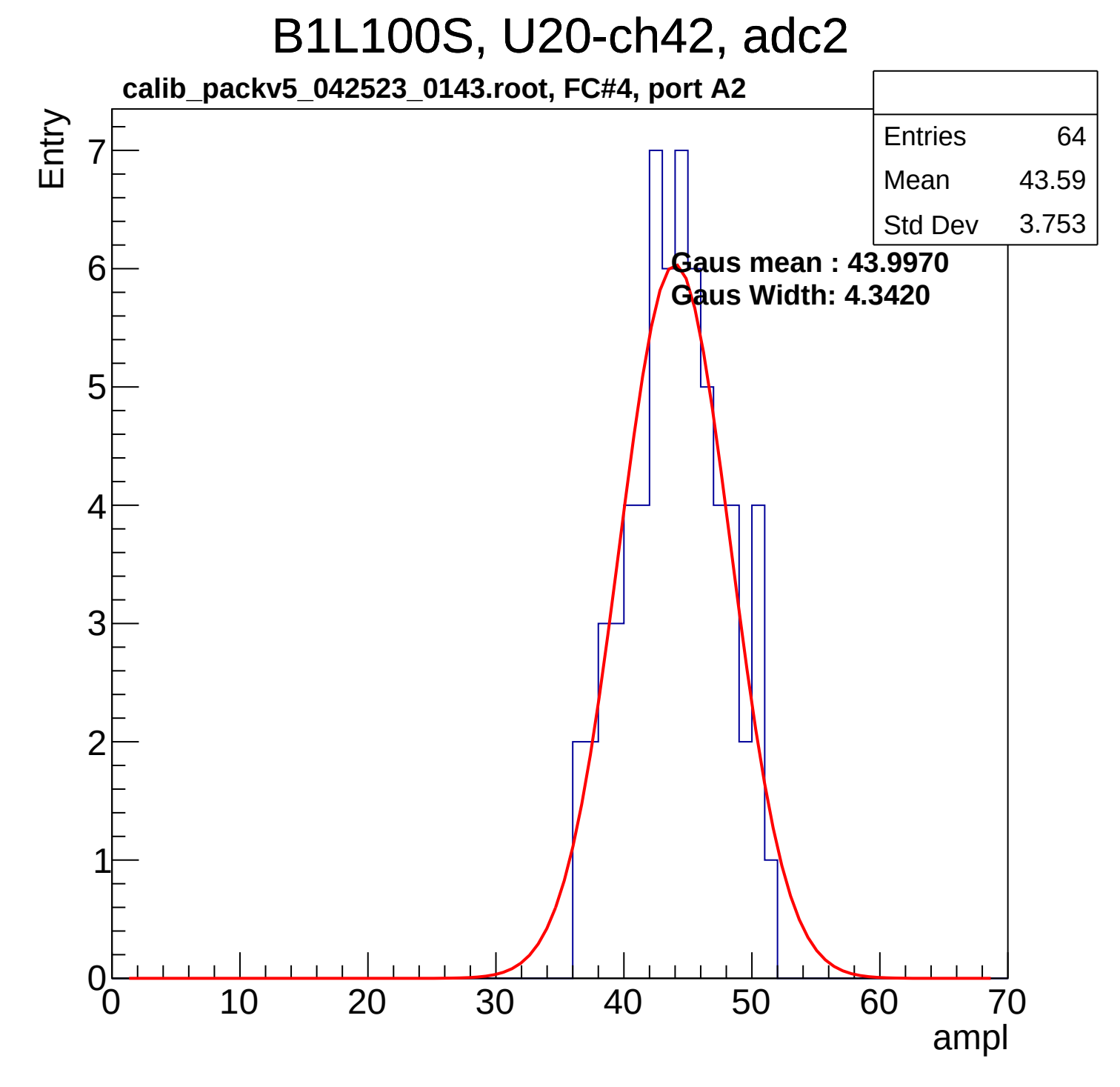
50
60
70
ampl


B1L100S, U20-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.78
Std Dev 6.771
Gaus mean : 29.3078
Gaus Width: 3.6827
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch42, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 36.3
Std Dev 3.737
Gaus mean : 36.4609
Gaus Width: 3.9916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.59
Std Dev 3.753
Gaus mean : 43.9970
Gaus Width: 4.3420
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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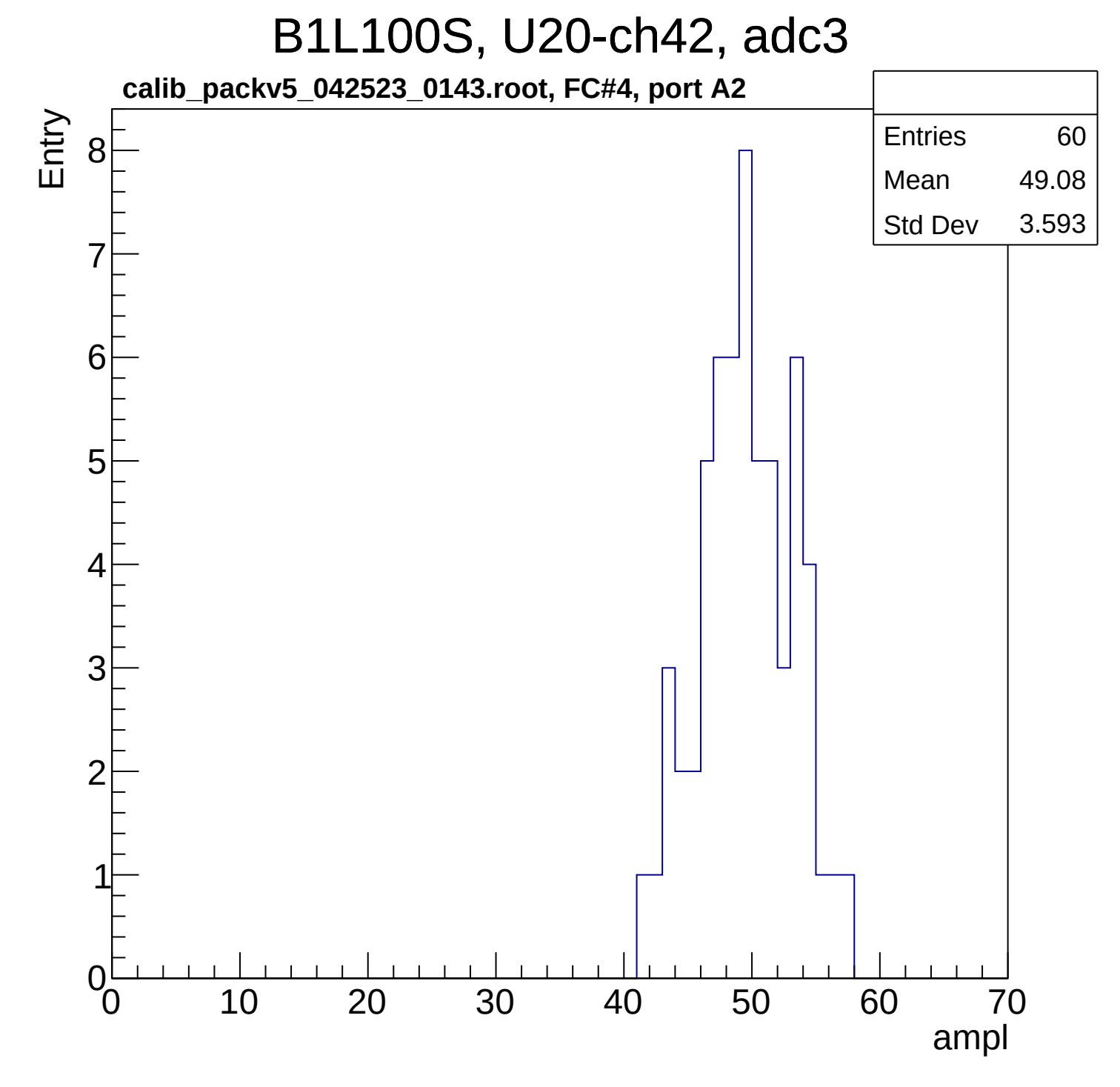
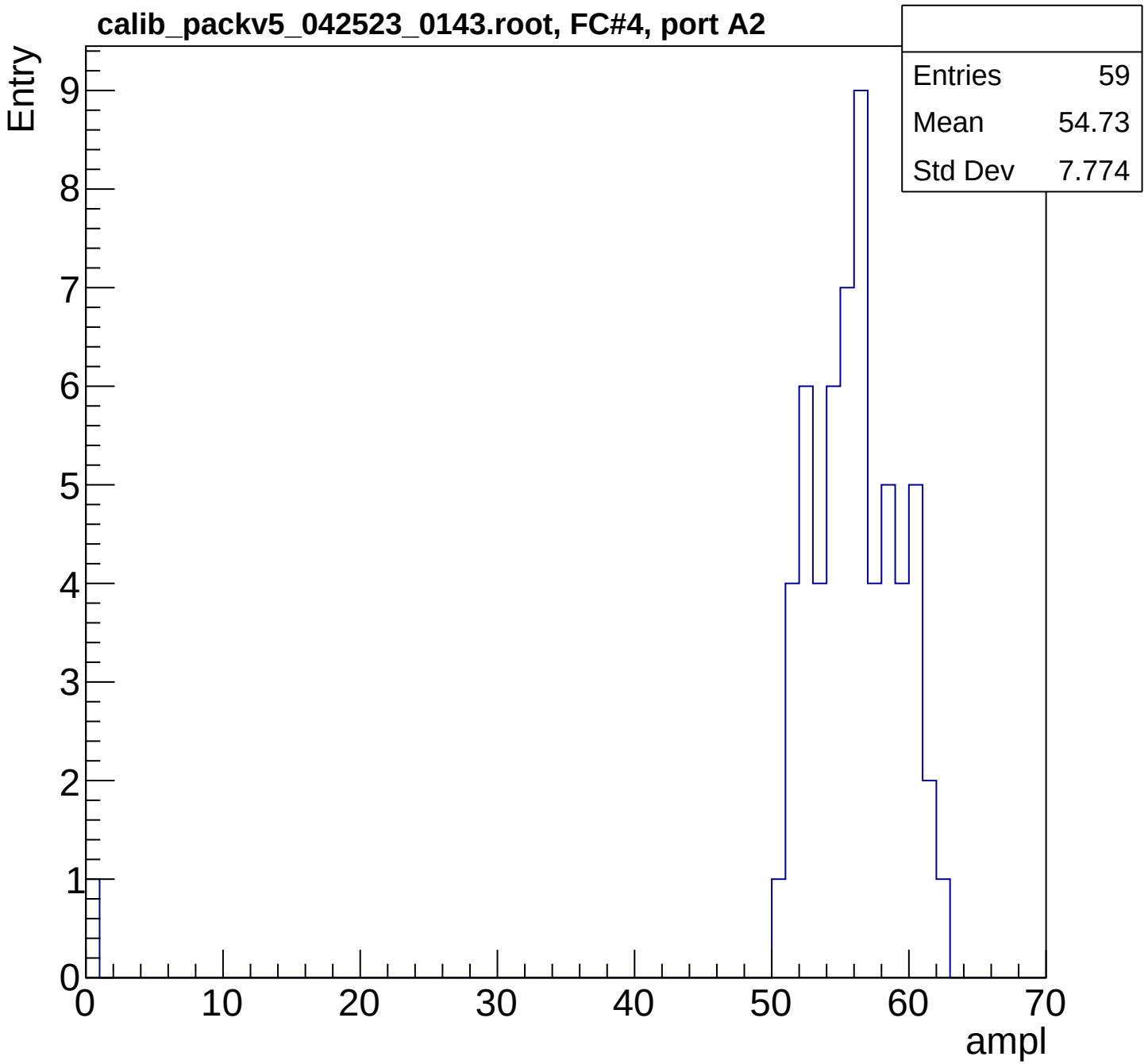
60
70


B1L100S, U20-ch42, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.08
Std Dev 3.593
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch42, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.73
Std Dev 7.774
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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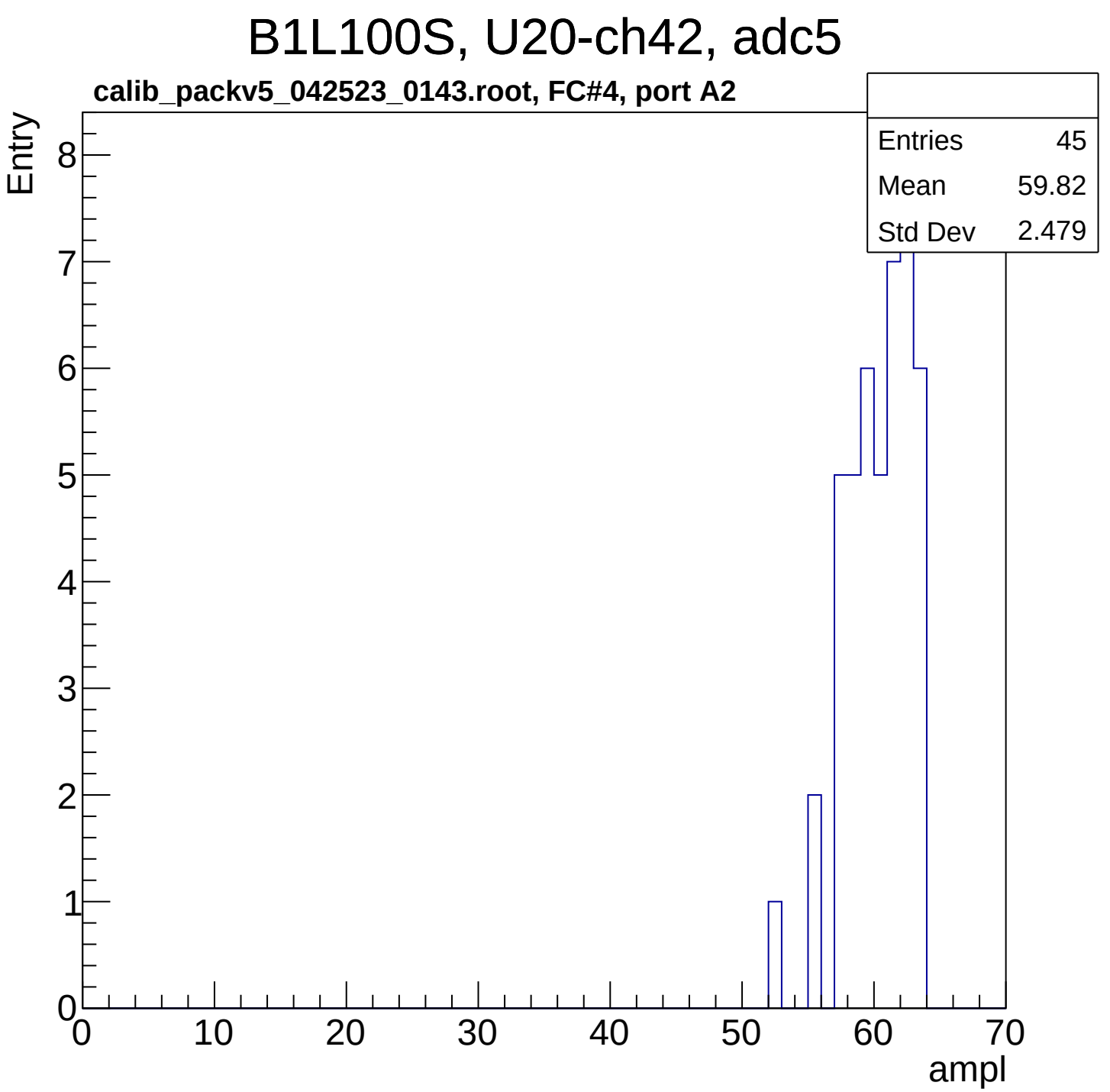
B1L100S, U20-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.82
Std Dev 2.479
Entry
ampl
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2
3
4
5
6
7
8
0
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40
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60
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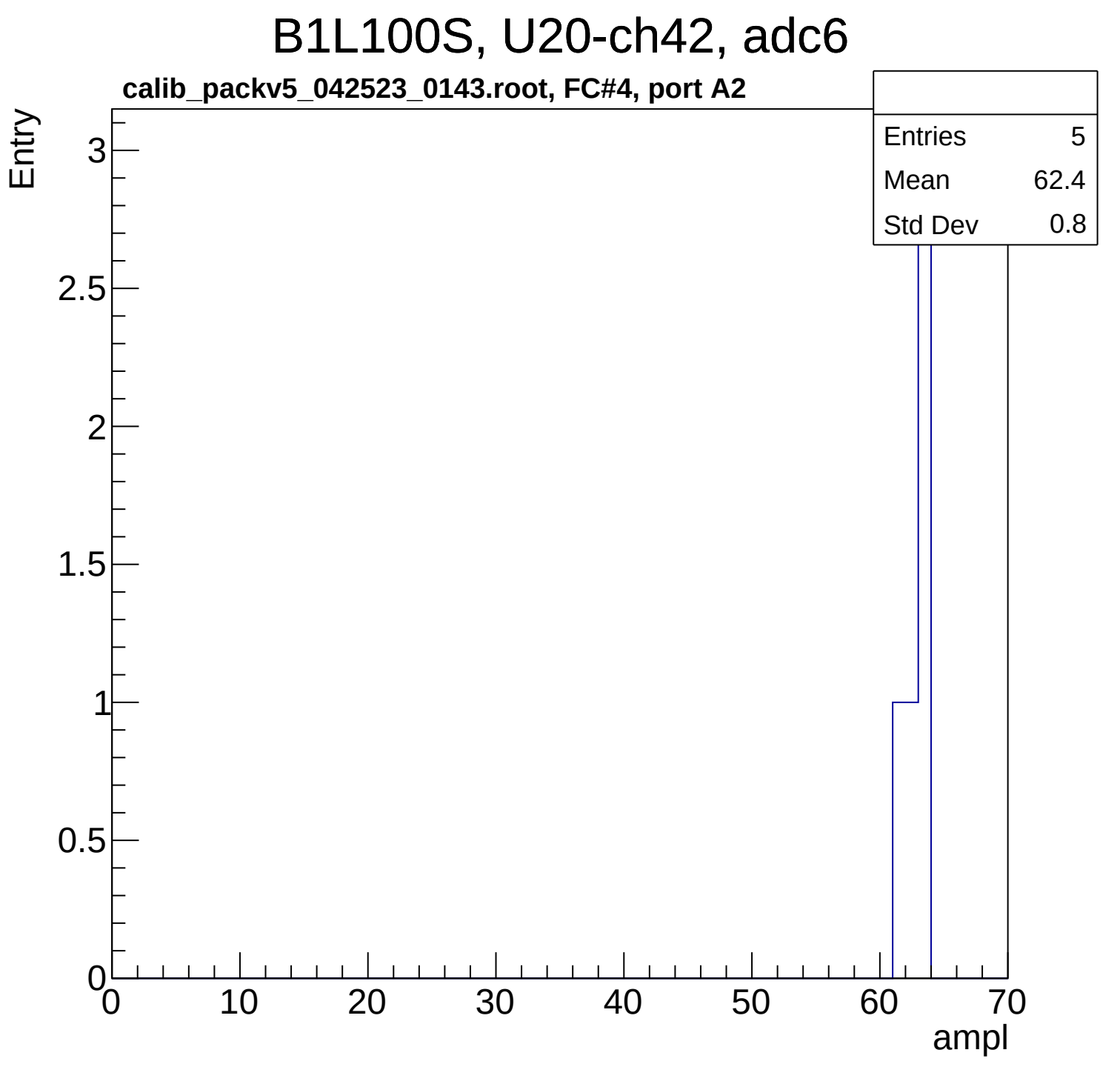
B1L100S, U20-ch42, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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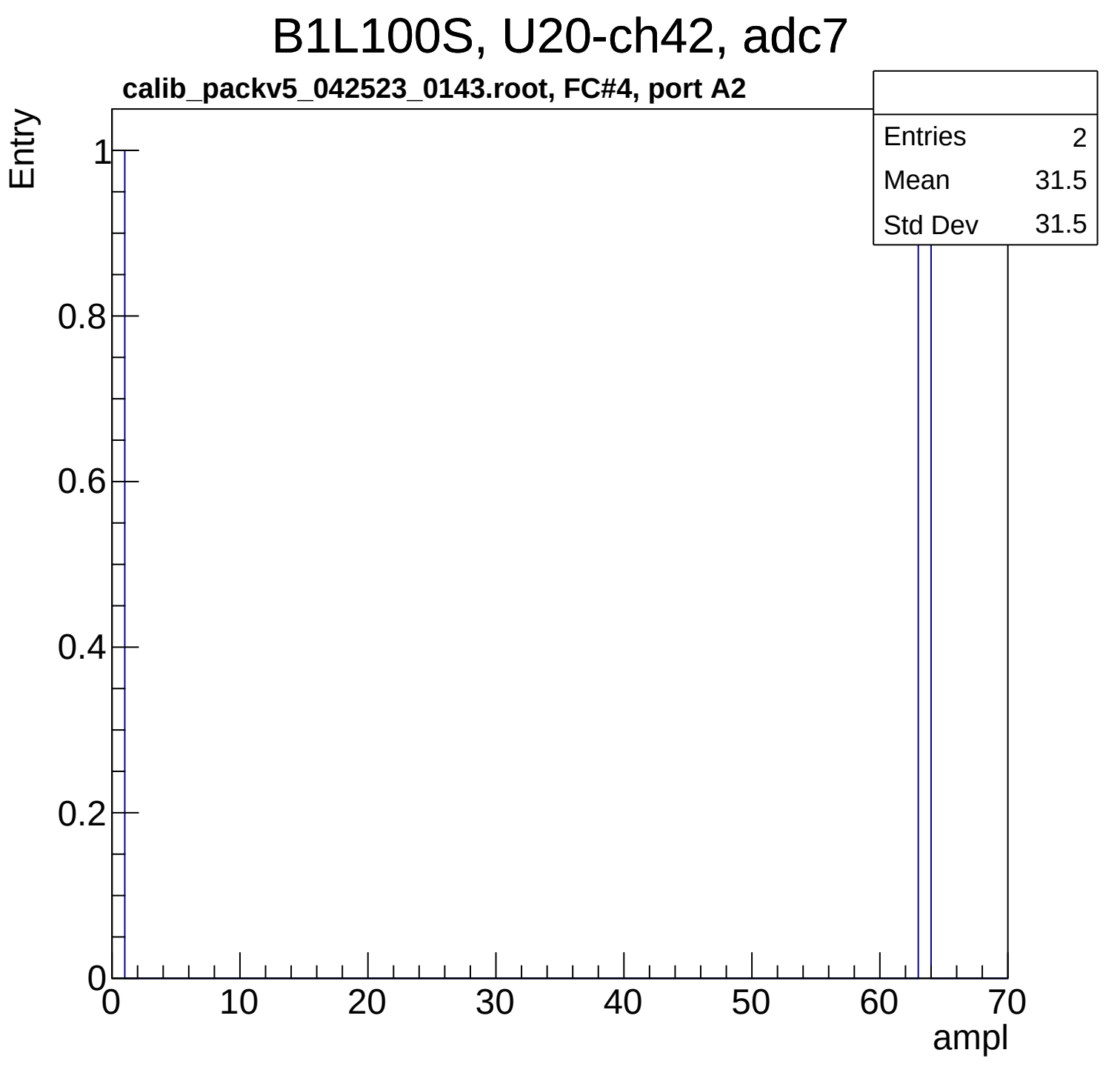
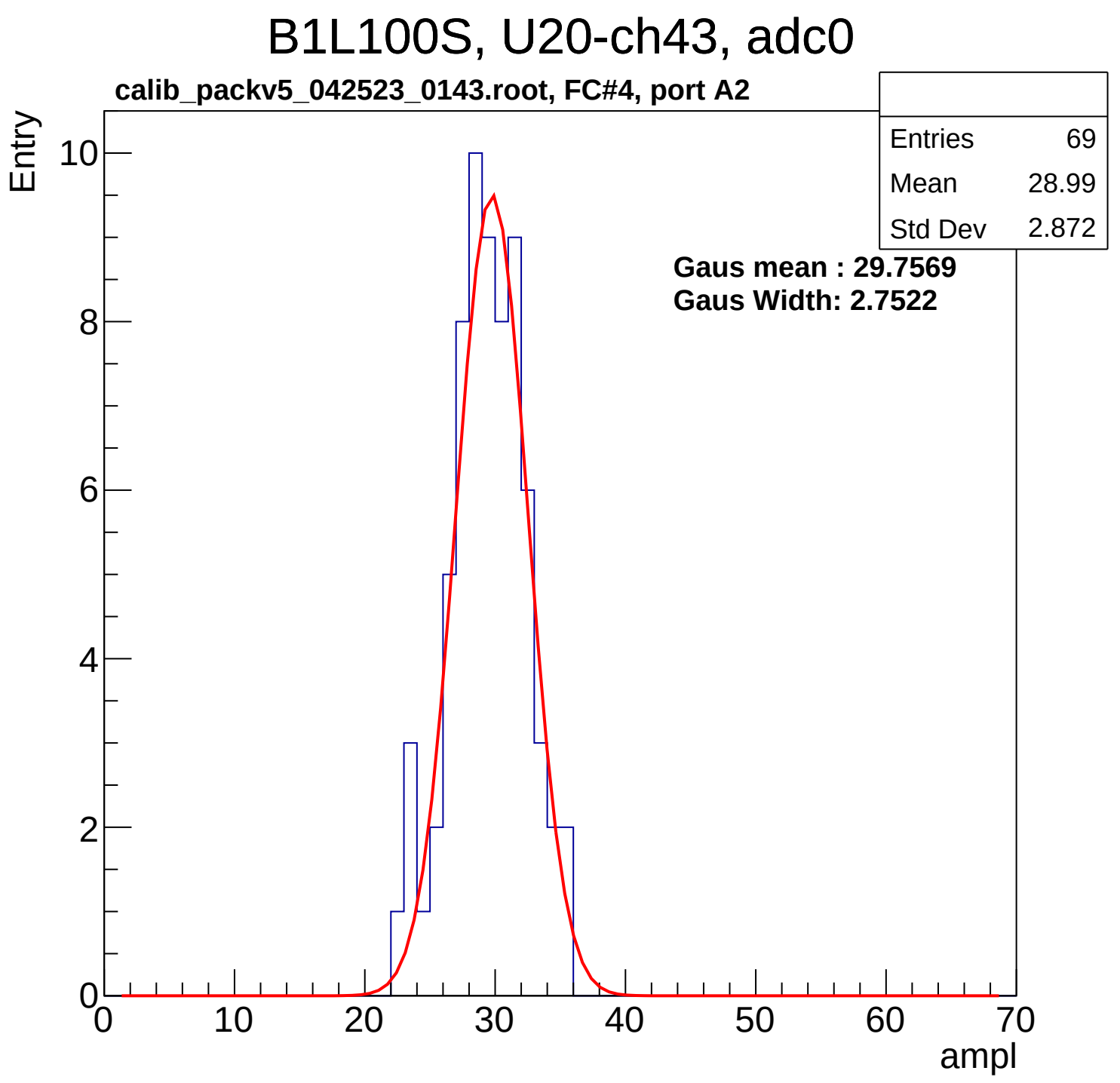

B1L100S, U20-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch43, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.99
Std Dev 2.872
Gaus mean : 29.7569
Gaus Width: 2.7522
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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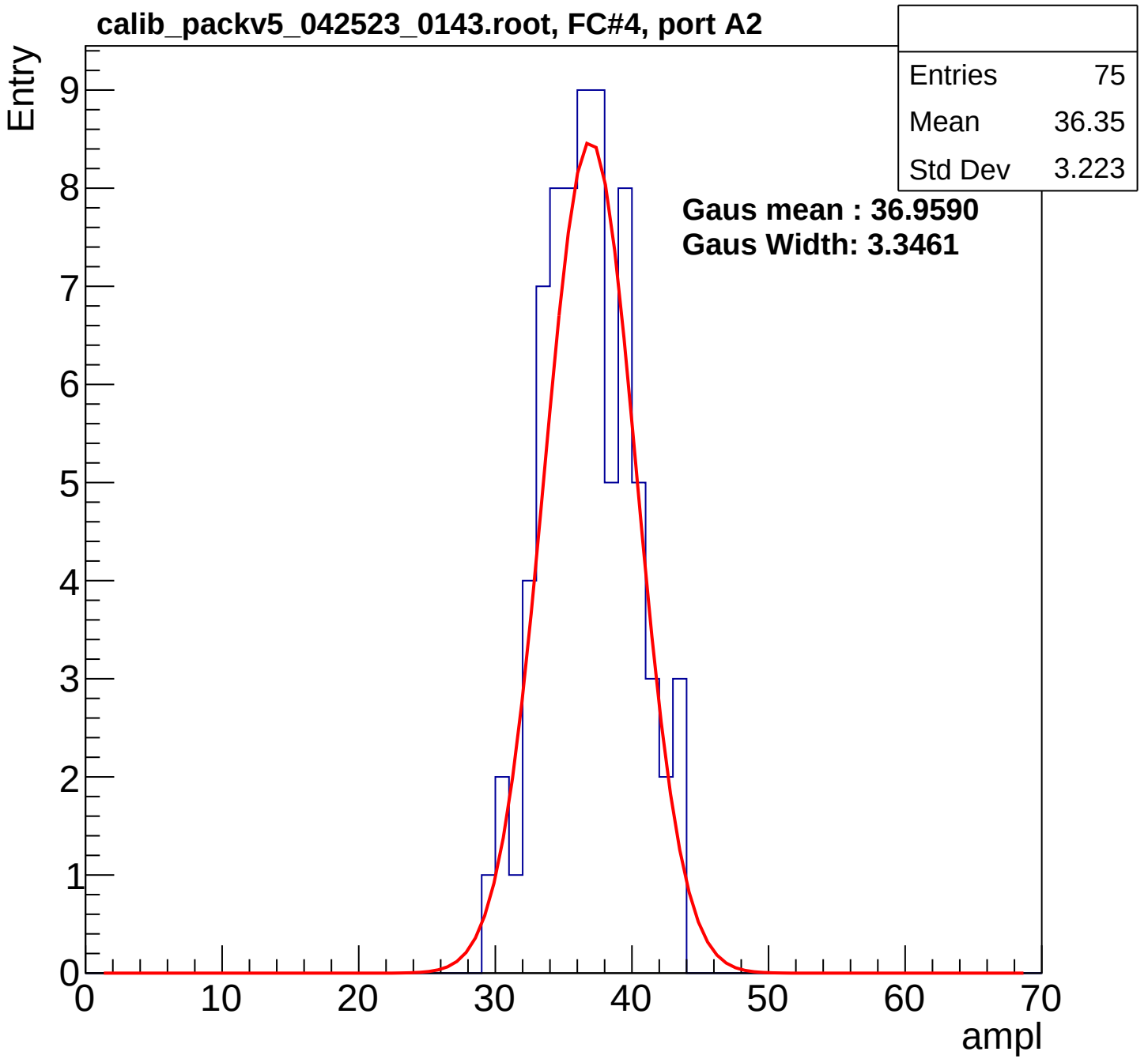
B1L100S, U20-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.35
Std Dev 3.223
Gaus mean : 36.9590
Gaus Width: 3.3461
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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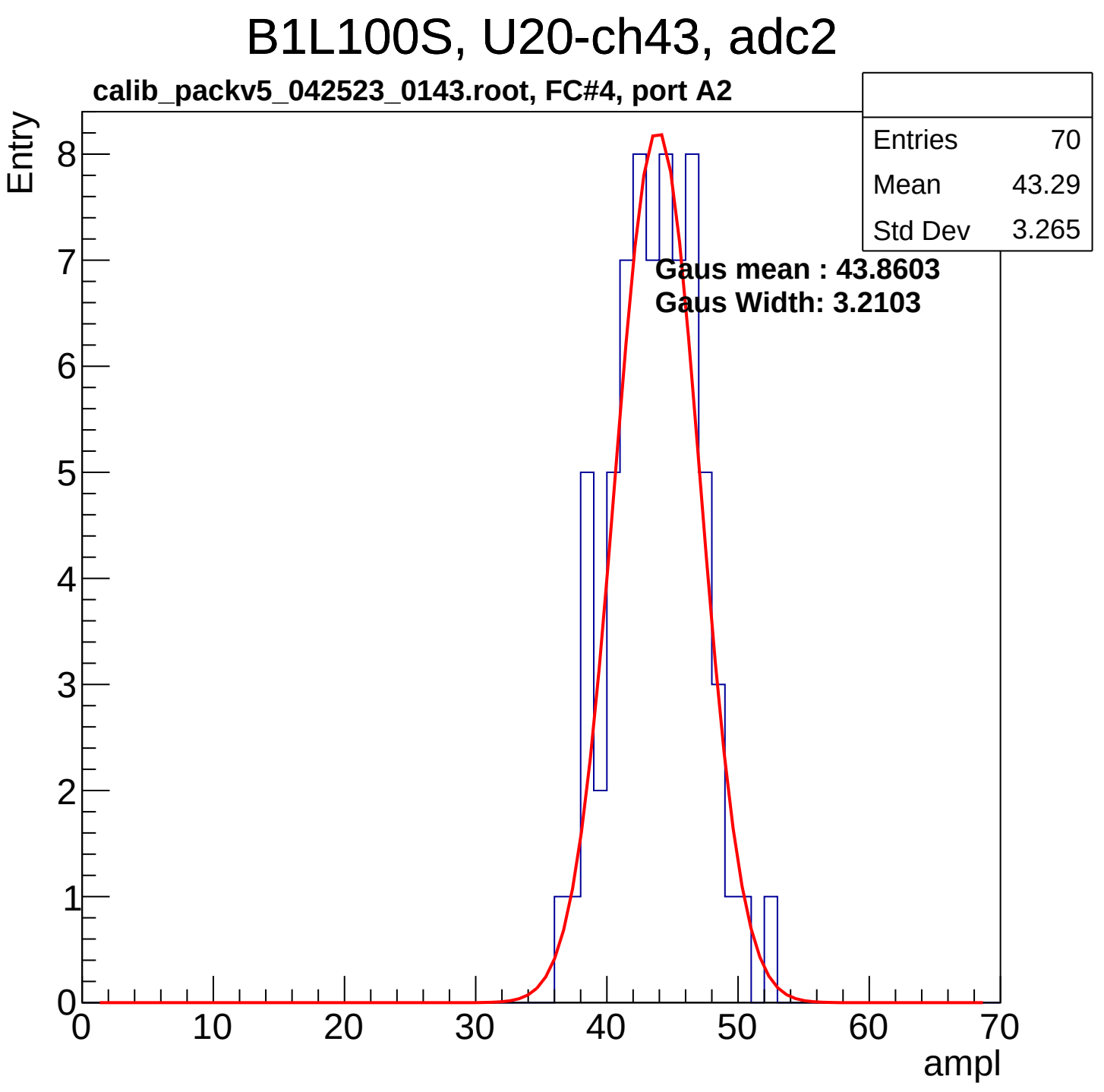
B1L100S, U20-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 43.29
Std Dev 3.265
Gaus mean : 43.8603
Gaus Width: 3.2103
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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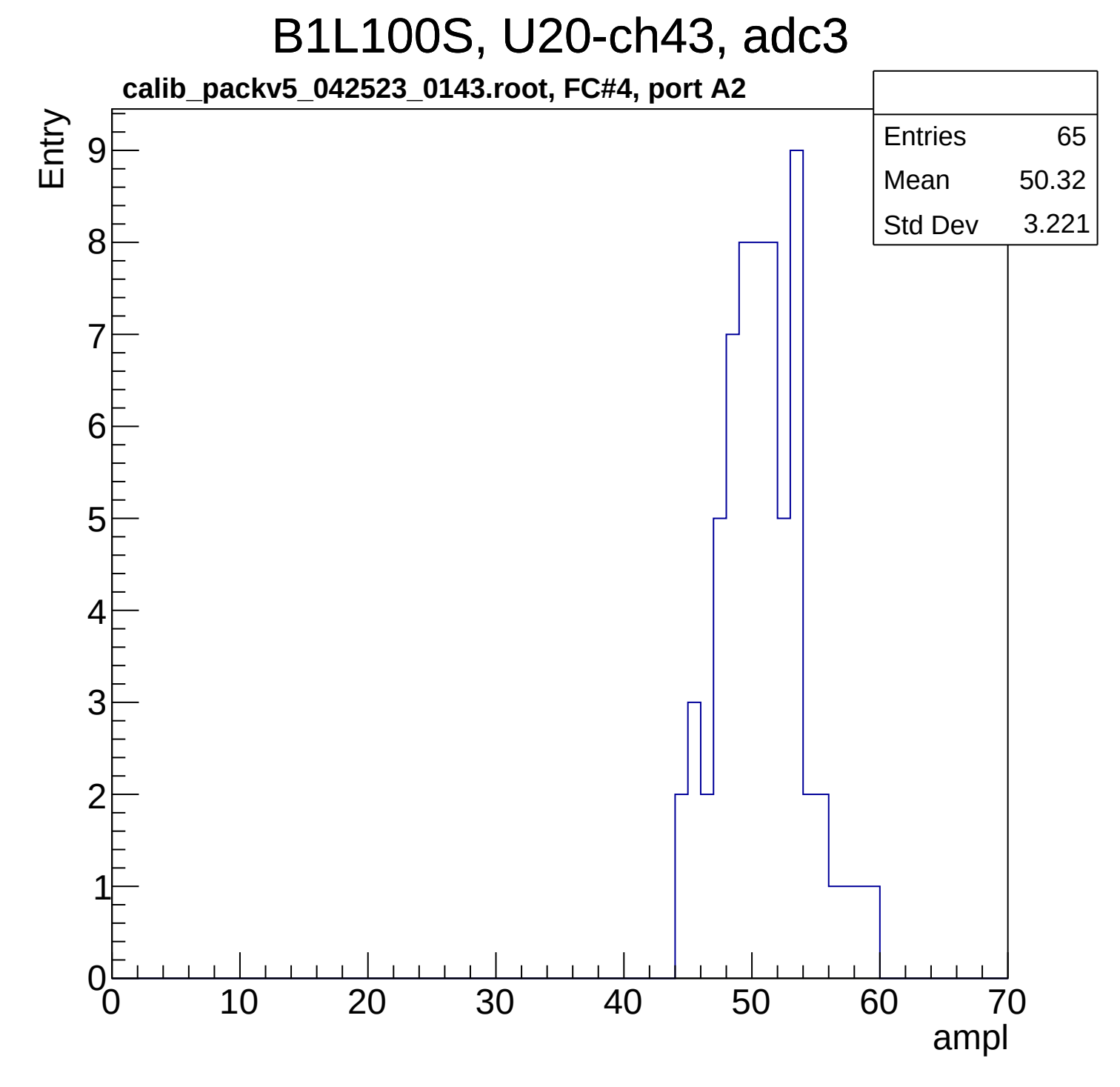
B1L100S, U20-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.32
Std Dev 3.221
Entry
ampl
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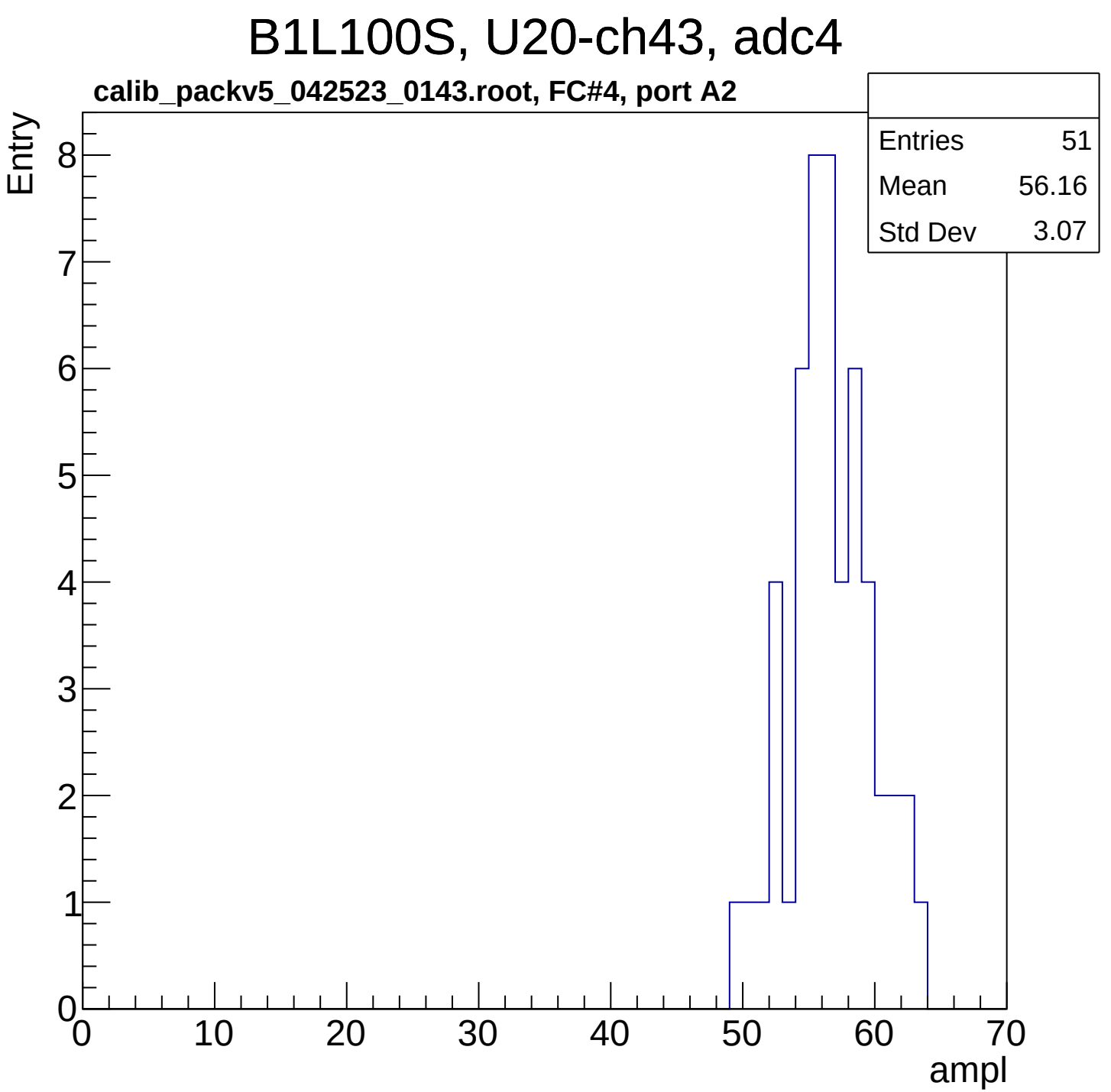
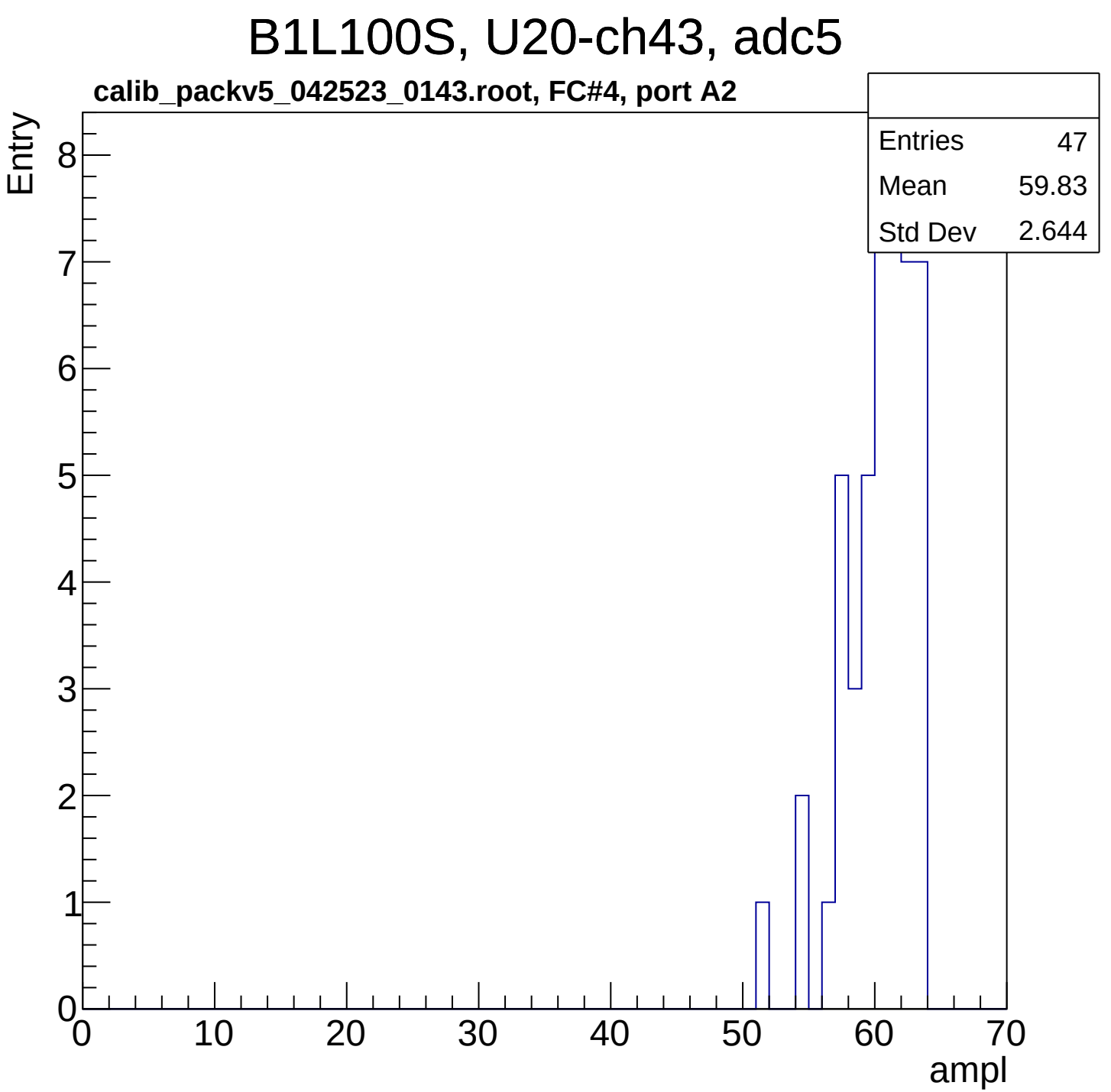
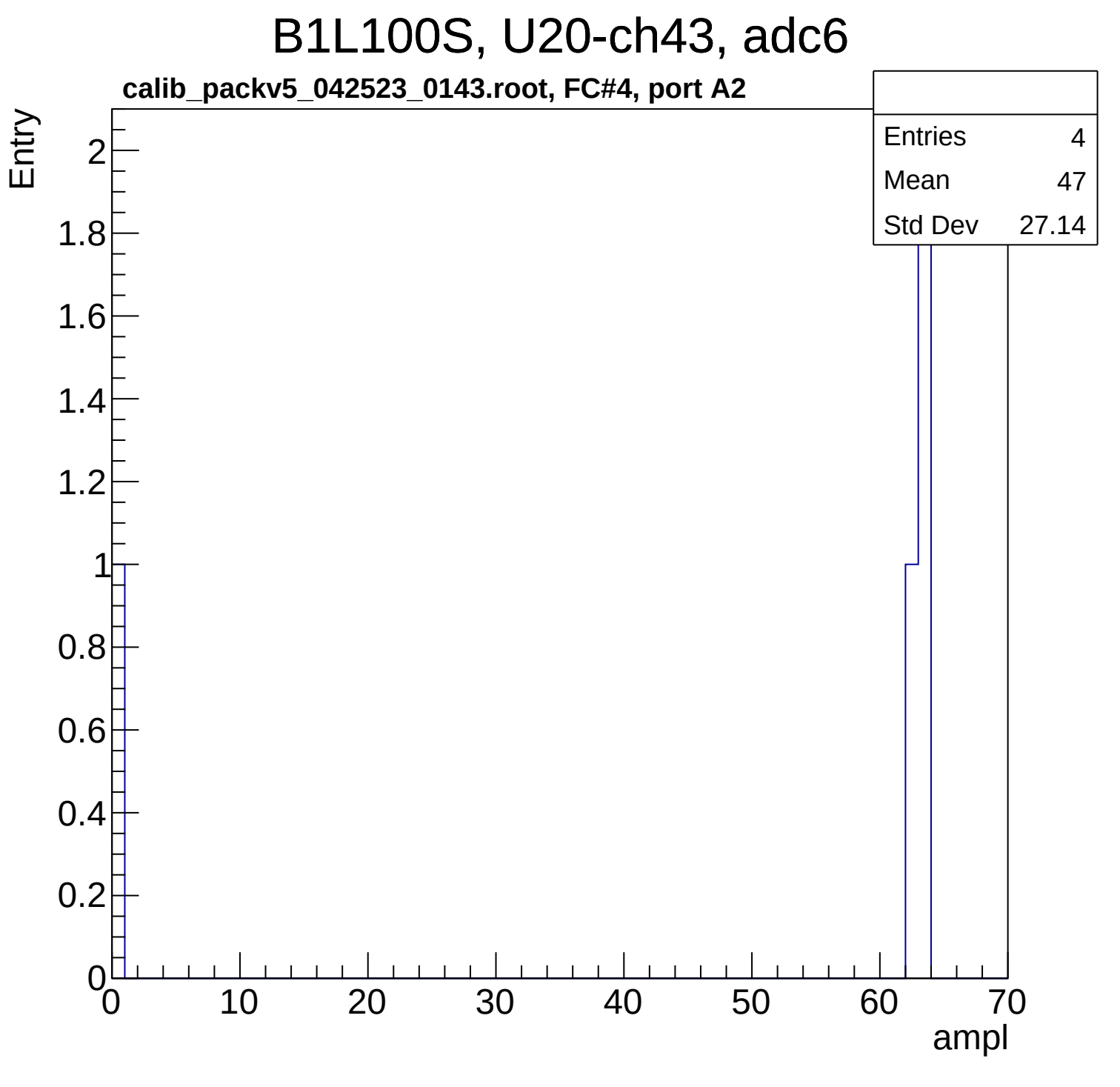
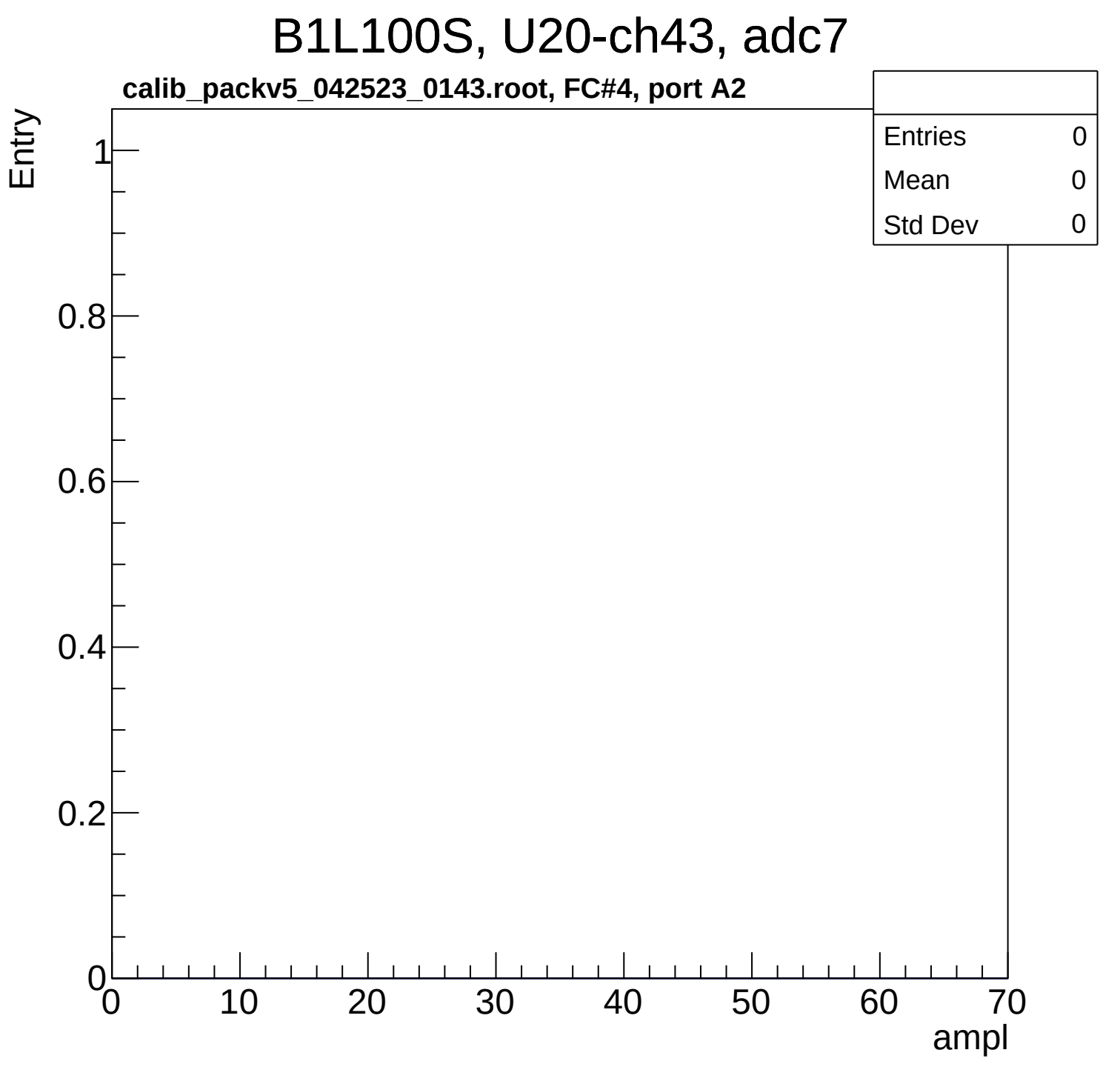
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.16
Std Dev 3.07
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch43, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.83
Std Dev 2.644
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch43, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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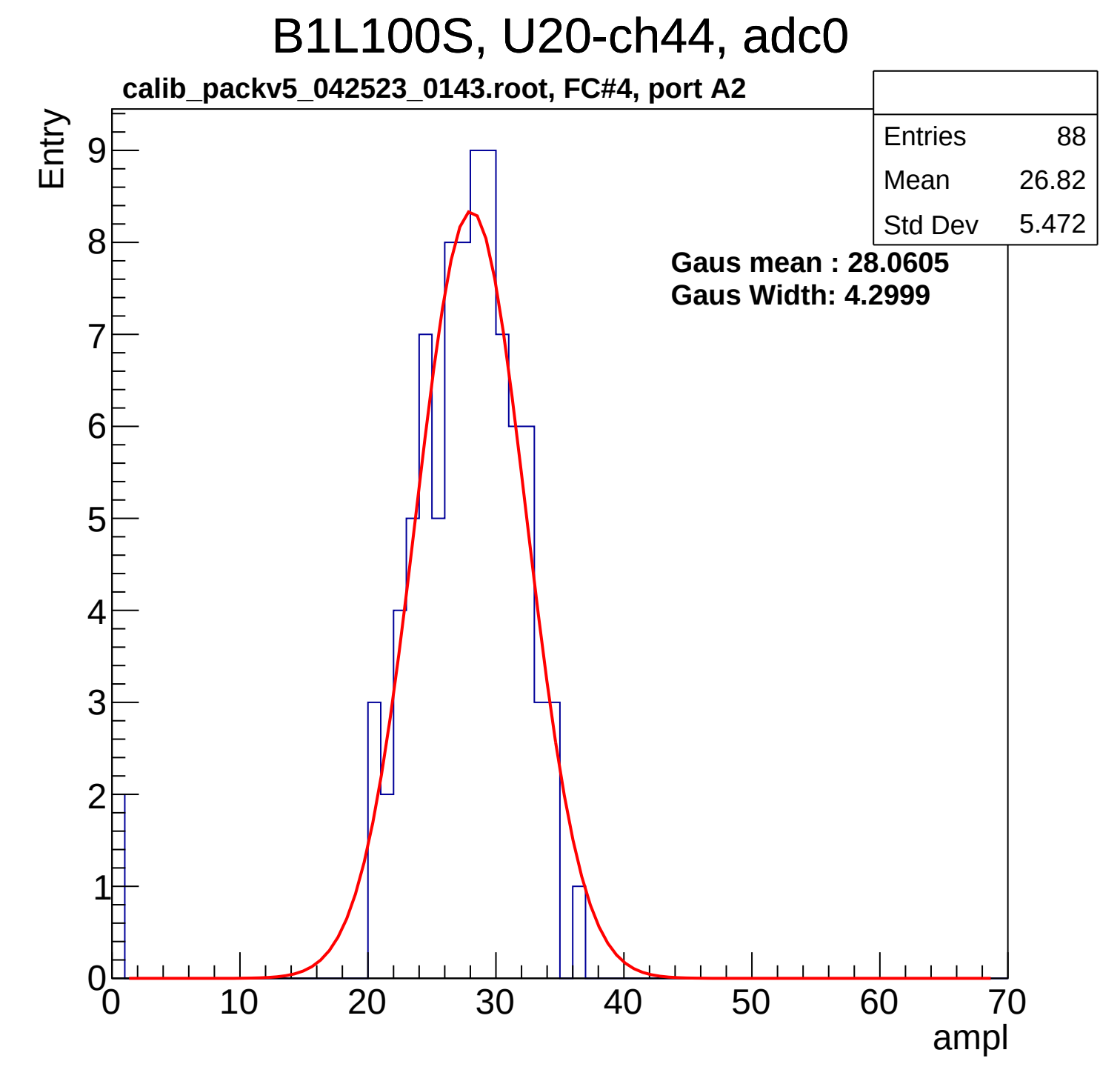
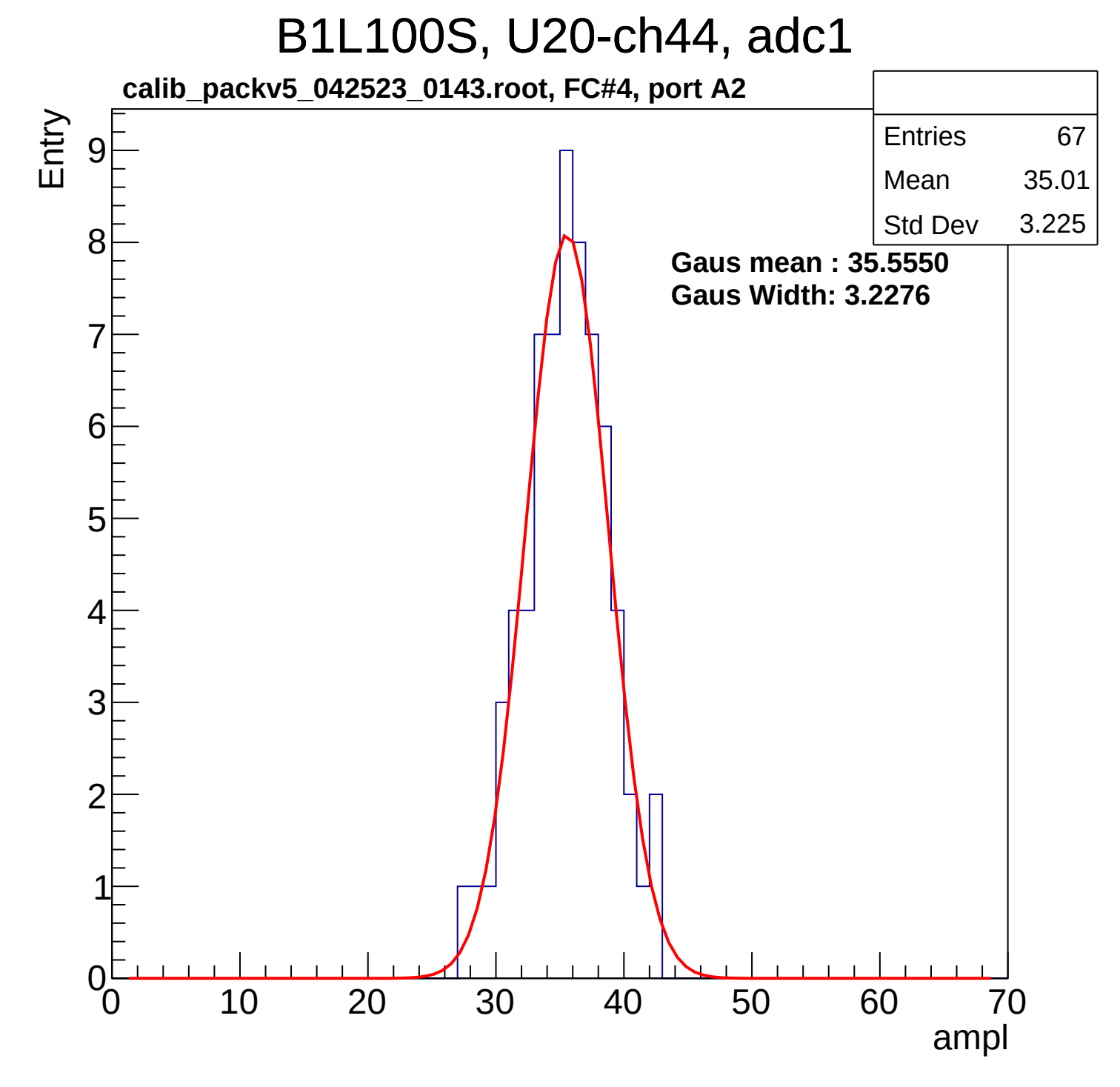
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 26.82
Std Dev 5.472
Gaus mean : 28.0605
Gaus Width: 4.2999
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 35.01
Std Dev 3.225
Gaus mean : 35.5550
Gaus Width: 3.2276
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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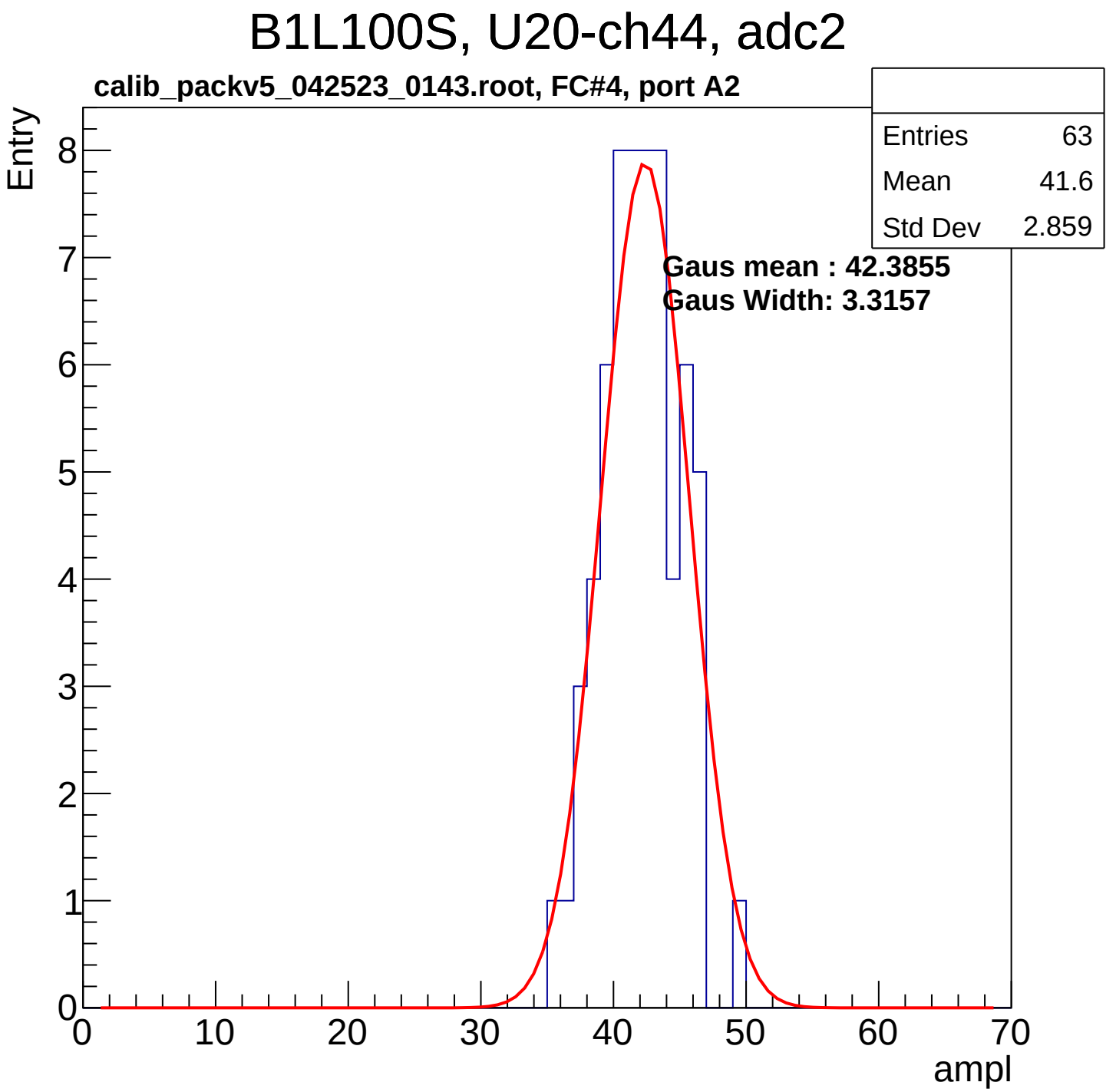
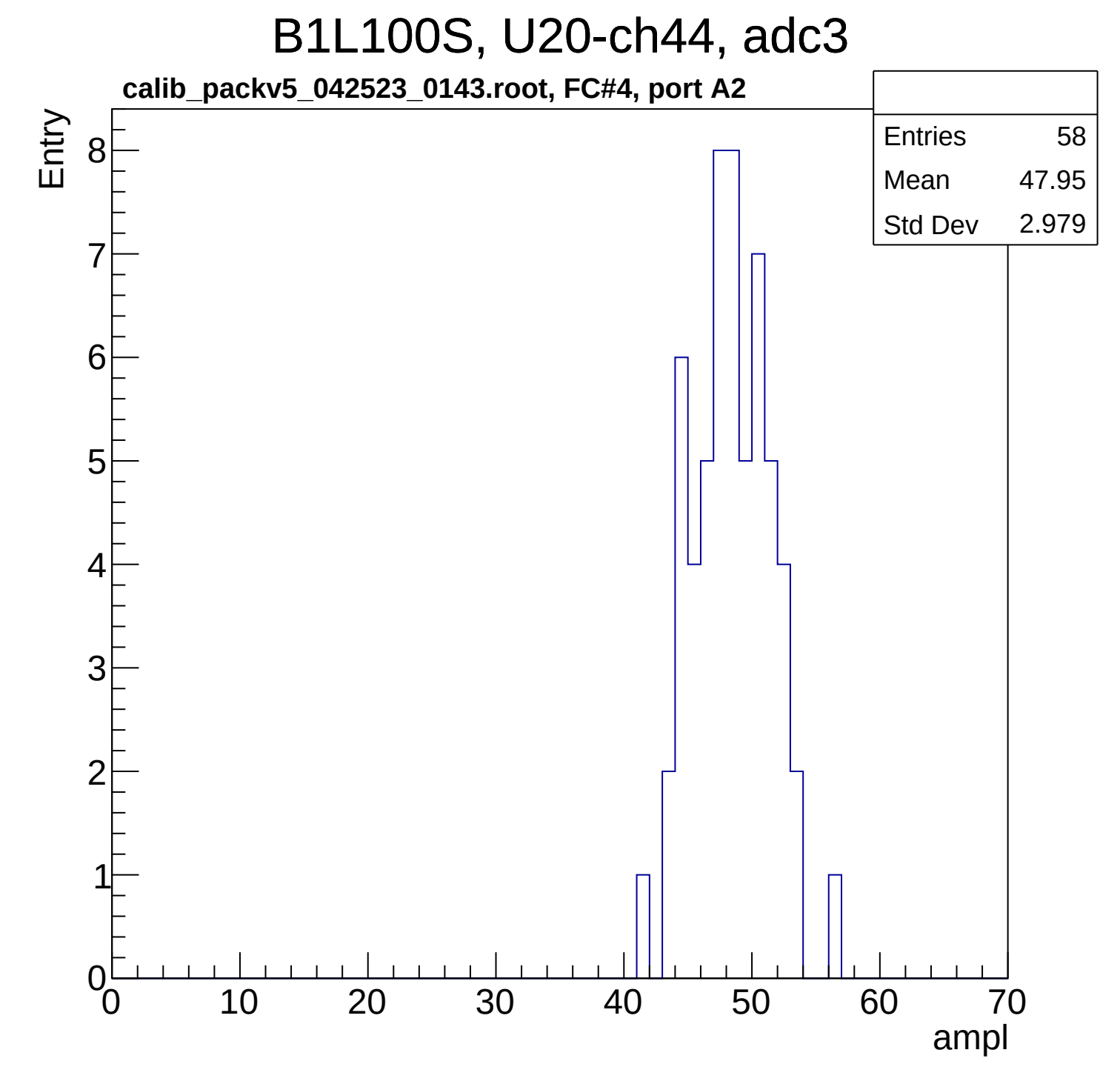

B1L100S, U20-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 41.6
Std Dev 2.859
Gaus mean : 42.3855
Gaus Width: 3.3157
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch44, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 47.95
Std Dev 2.979
Entry
ampl
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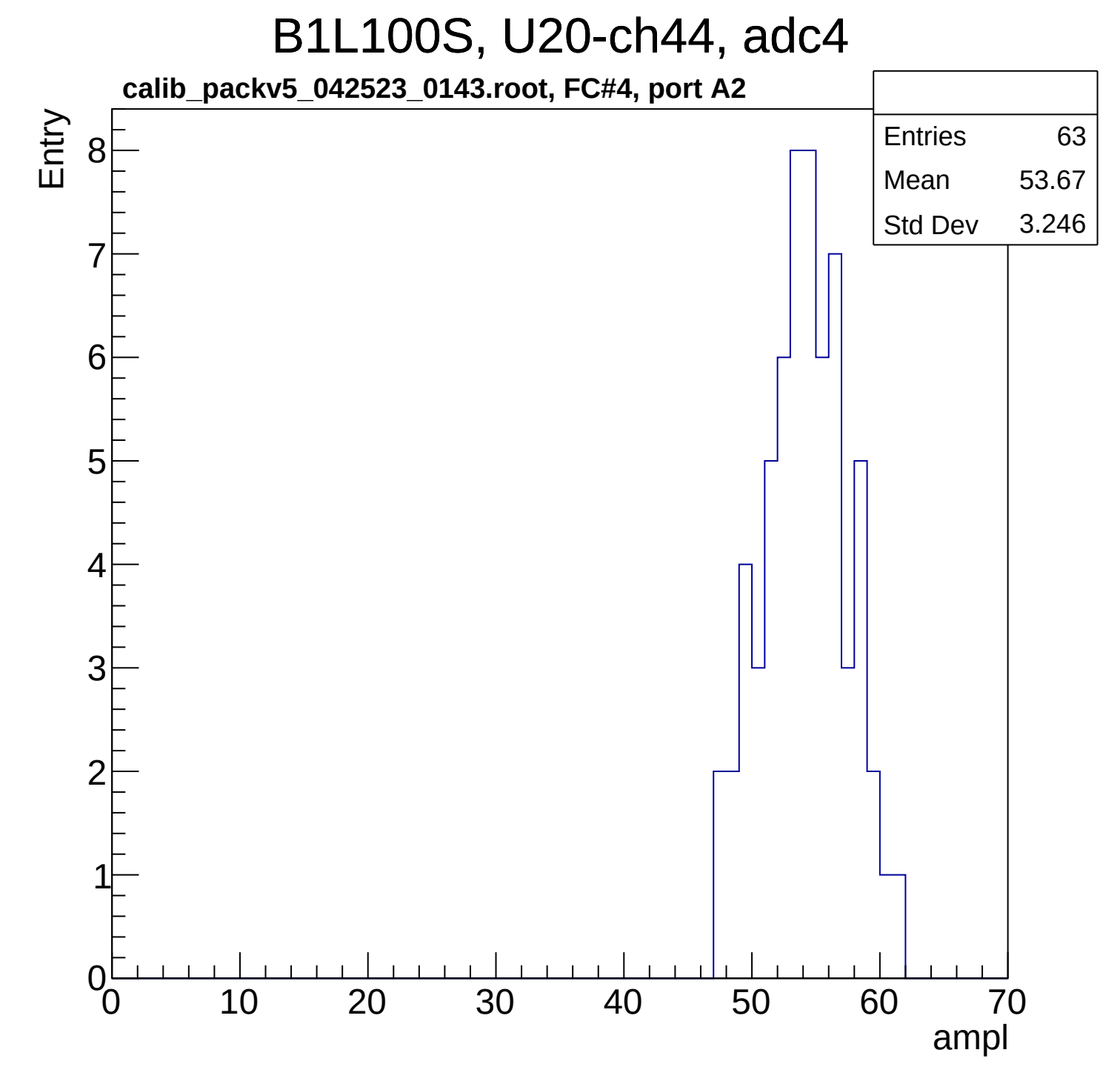
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 53.67
Std Dev 3.246
Entry
ampl
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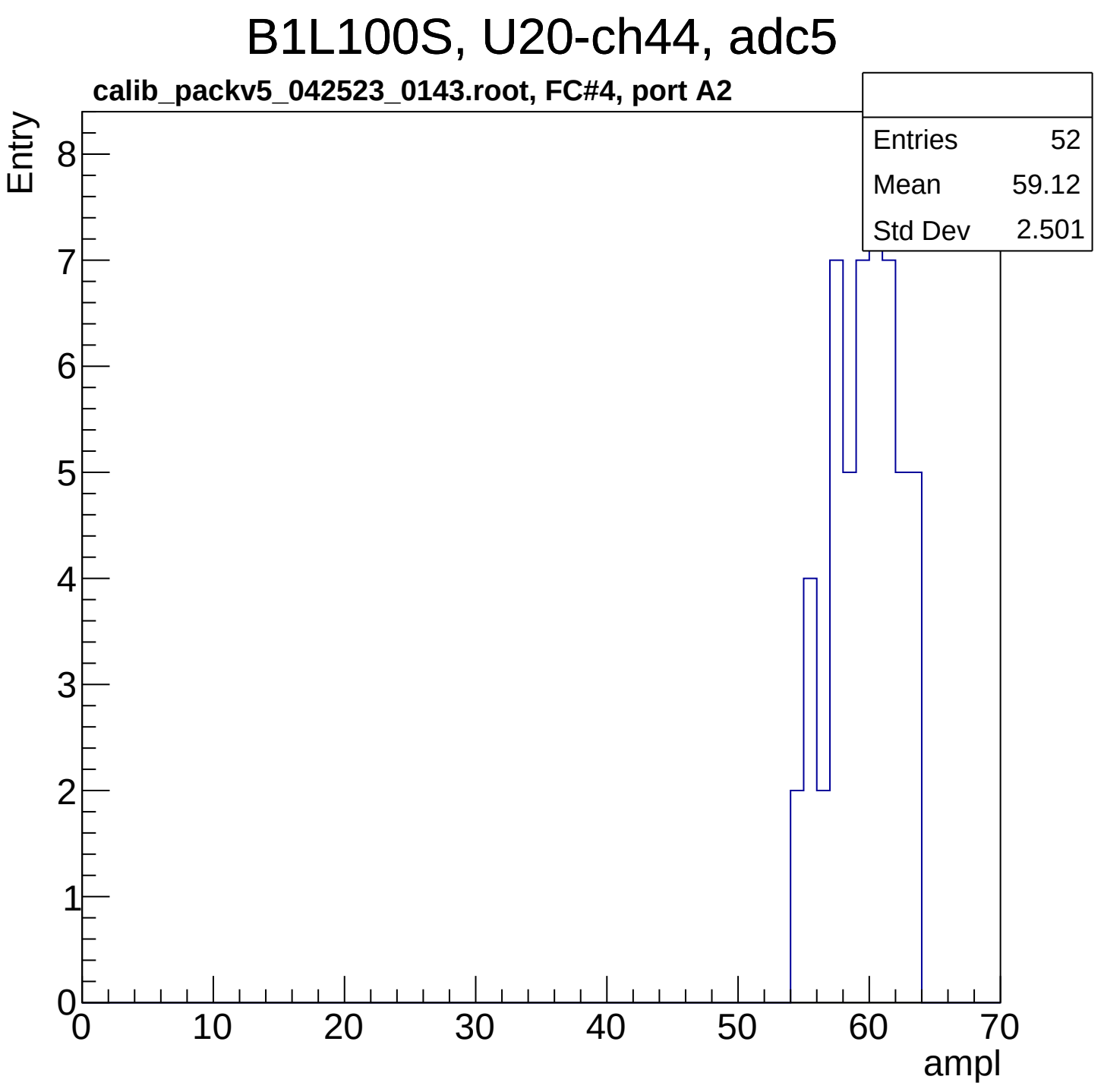
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 59.12
Std Dev 2.501
Entry
ampl
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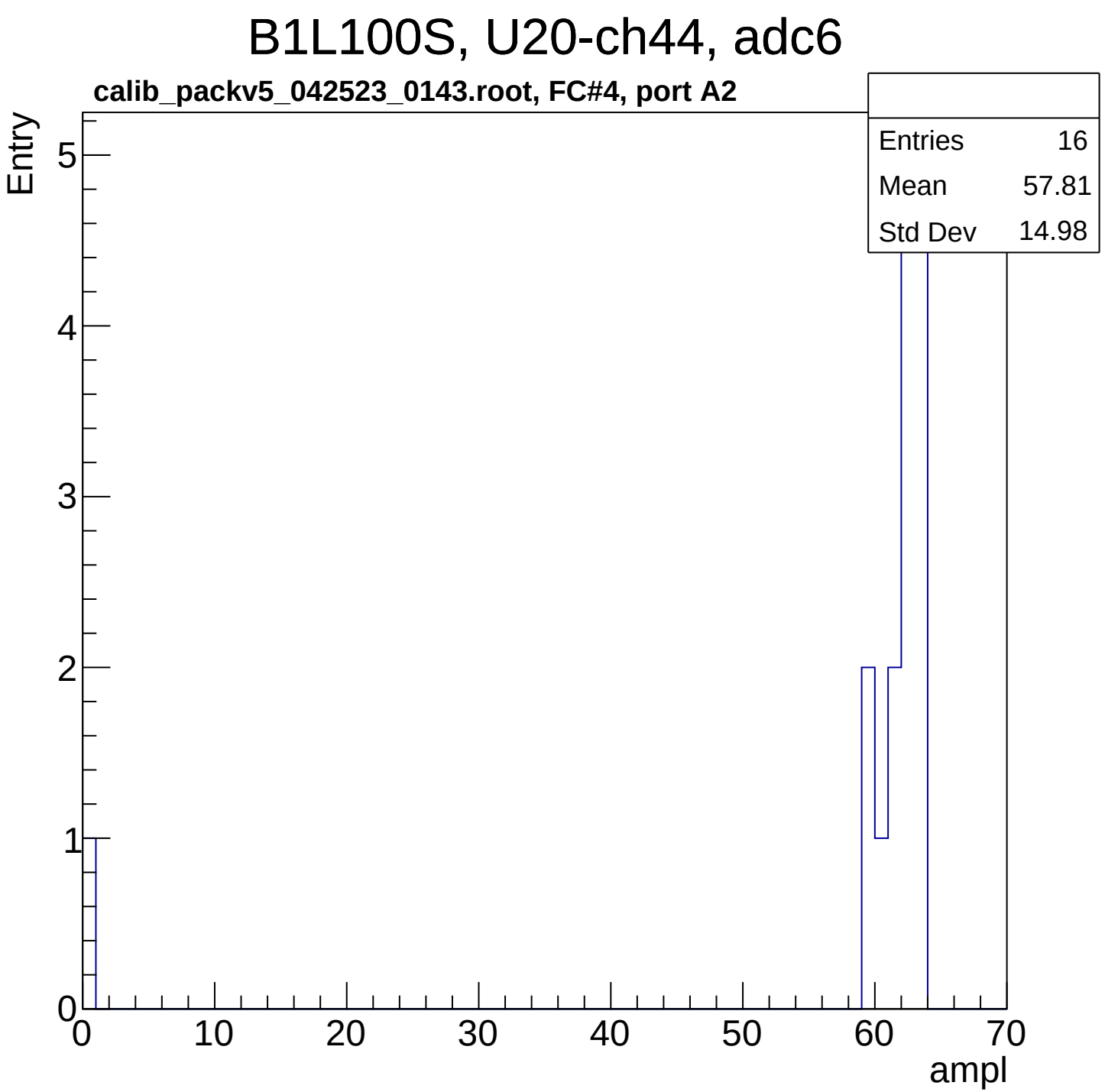
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 57.81
Std Dev 14.98
Entry
ampl
0
1
2
3
4
5
0
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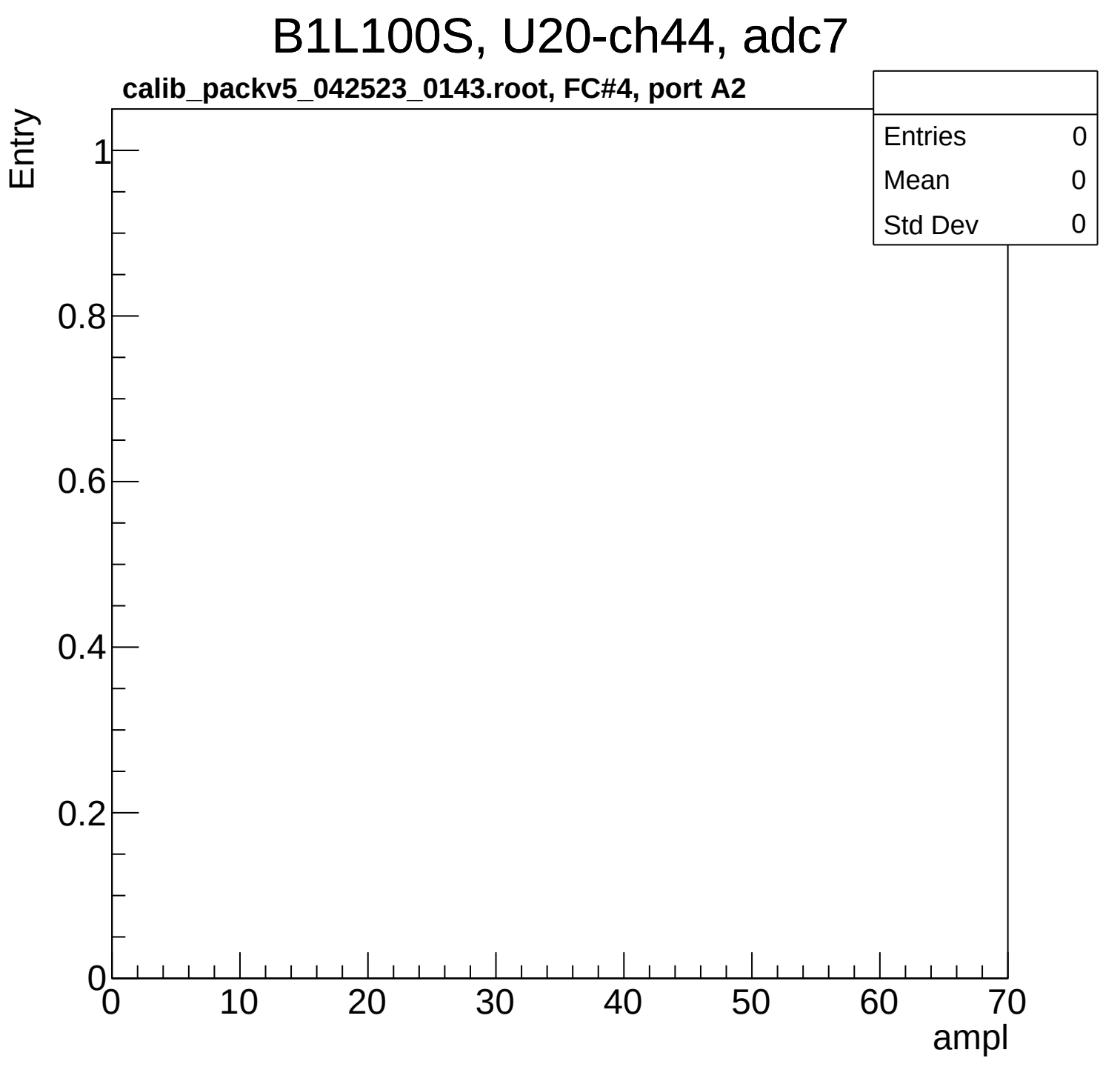
20
30
40
50
60
70


B1L100S, U20-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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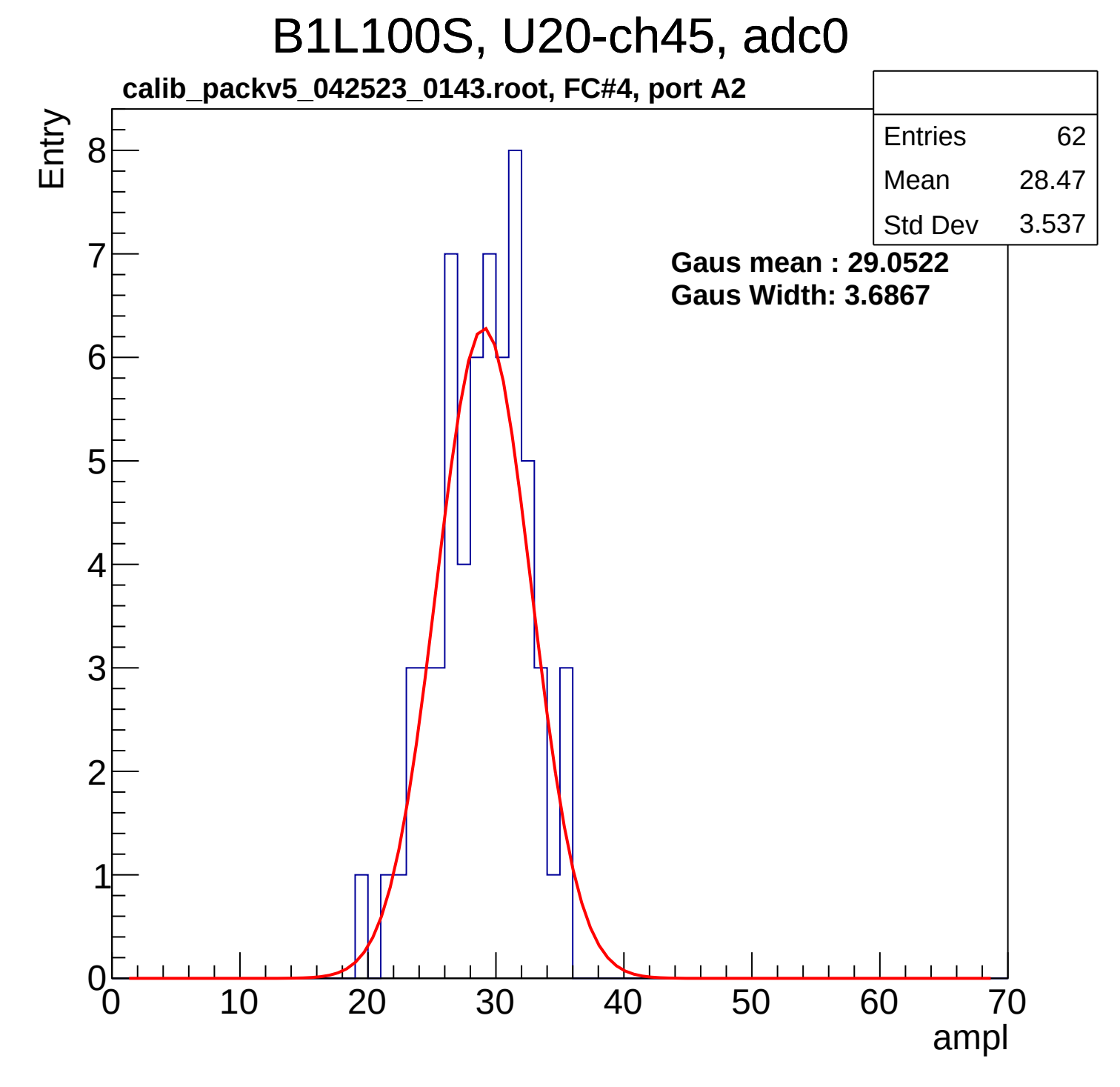
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.47
Std Dev 3.537
Gaus mean : 29.0522
Gaus Width: 3.6867
Entry
ampl
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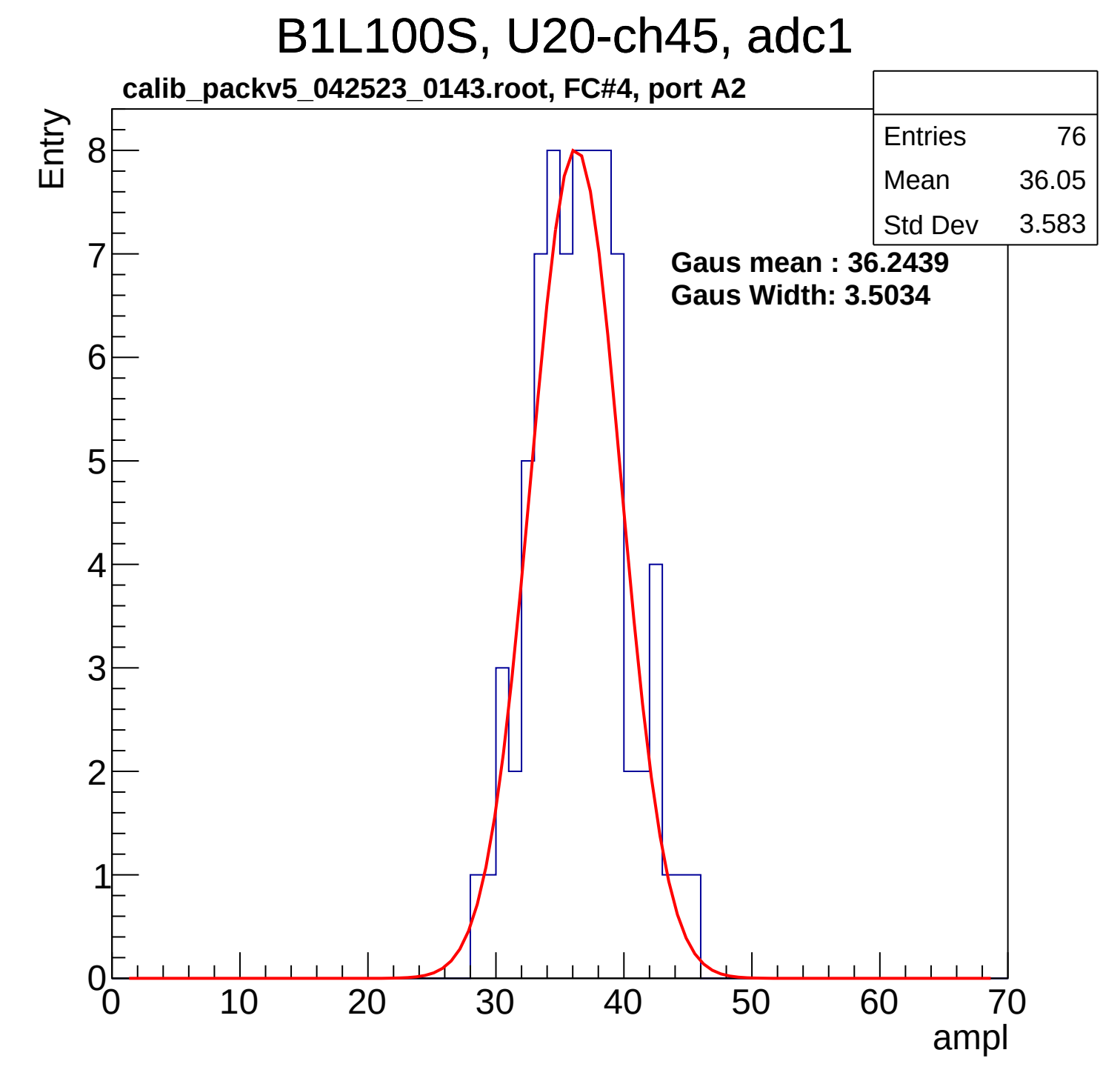
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch45, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.05
Std Dev 3.583
Gaus mean : 36.2439
Gaus Width: 3.5034
Entry
ampl
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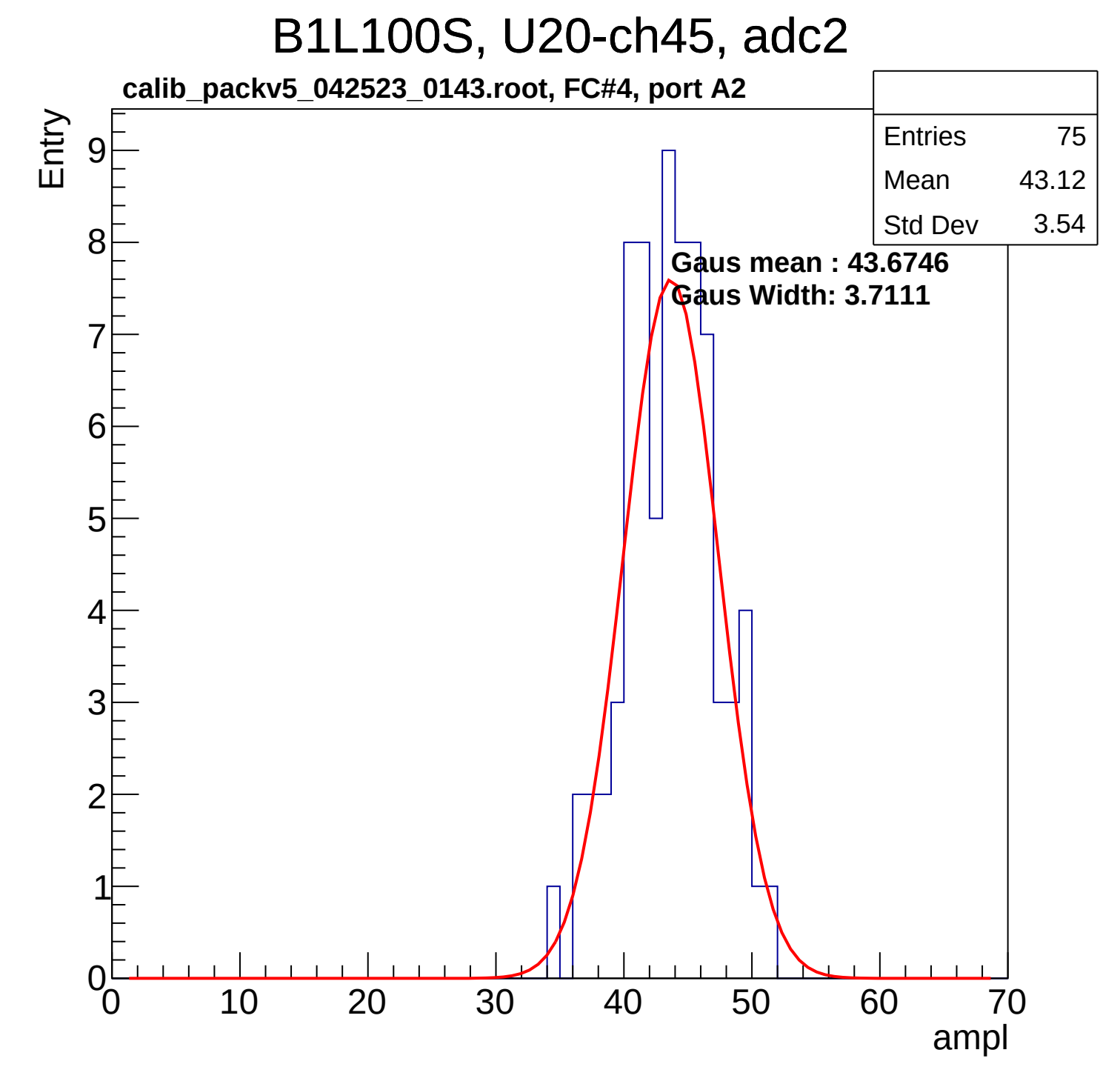
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch45, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.12
Std Dev 3.54
Gaus mean : 43.6746
Gaus Width: 3.7111
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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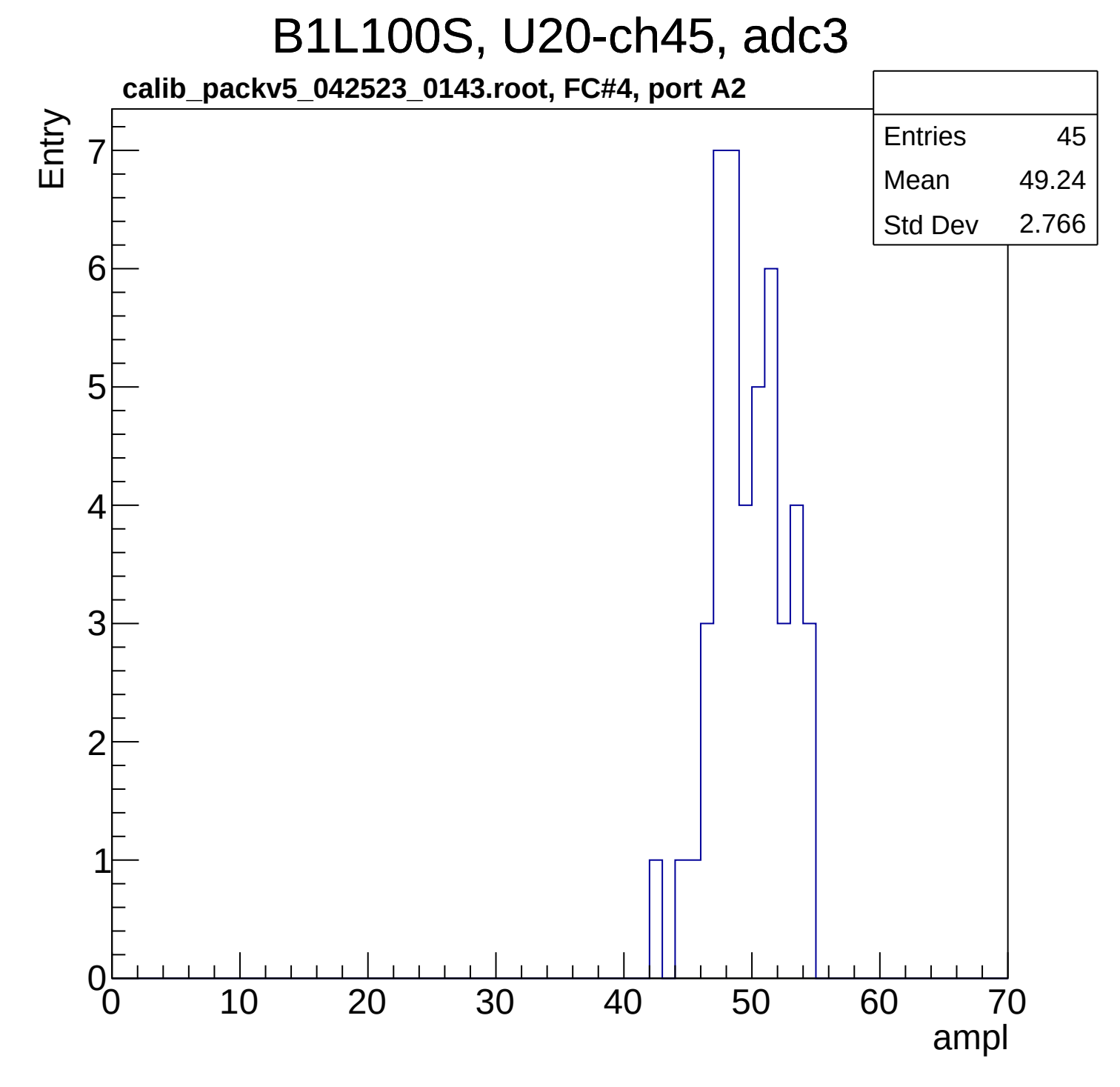
B1L100S, U20-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 49.24
Std Dev 2.766
Entry
ampl
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4
5
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0
10
20
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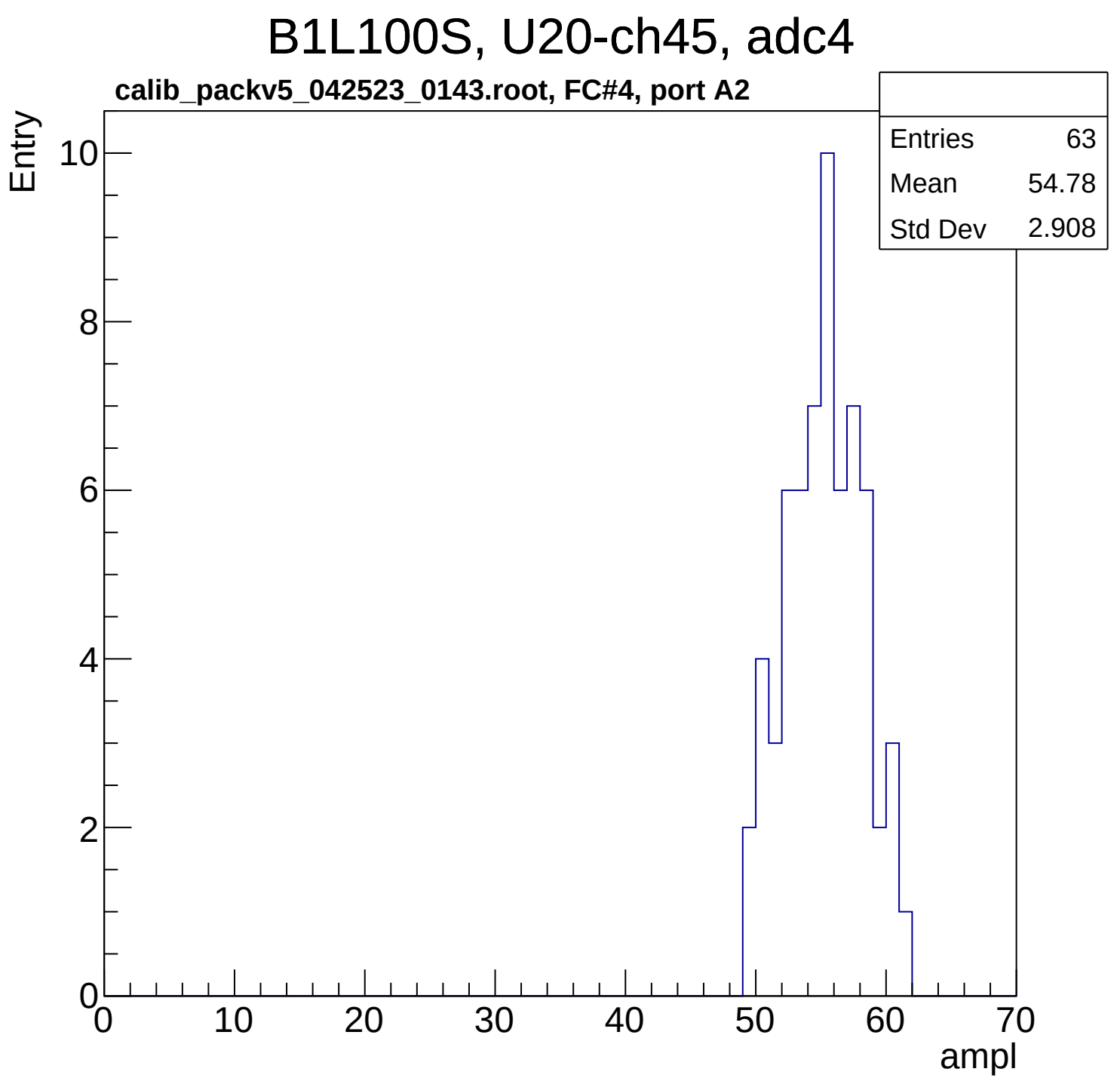
40
50
60
70


B1L100S, U20-ch45, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 54.78
Std Dev 2.908
Entry
0
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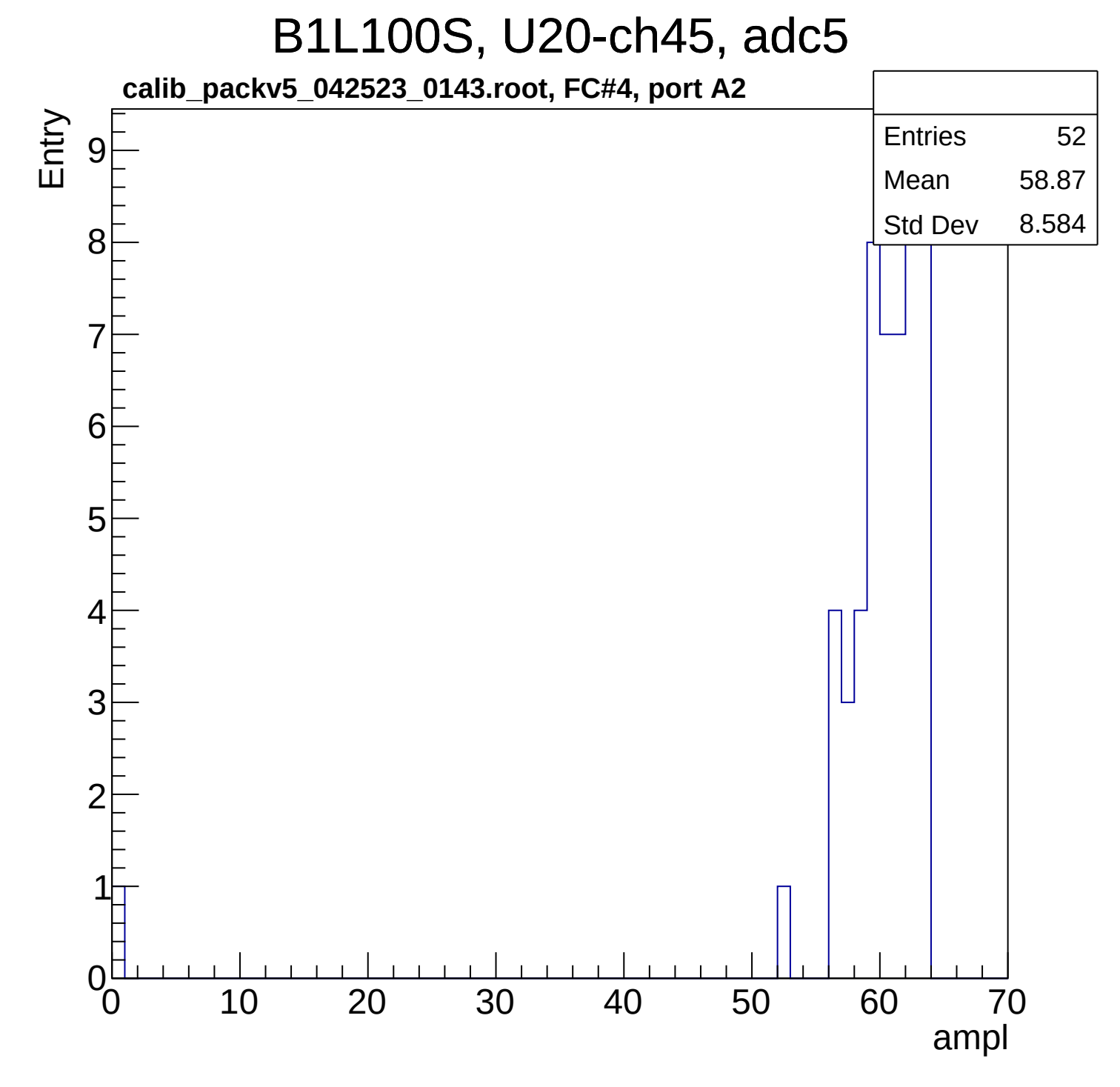
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.87
Std Dev 8.584
Entry
ampl
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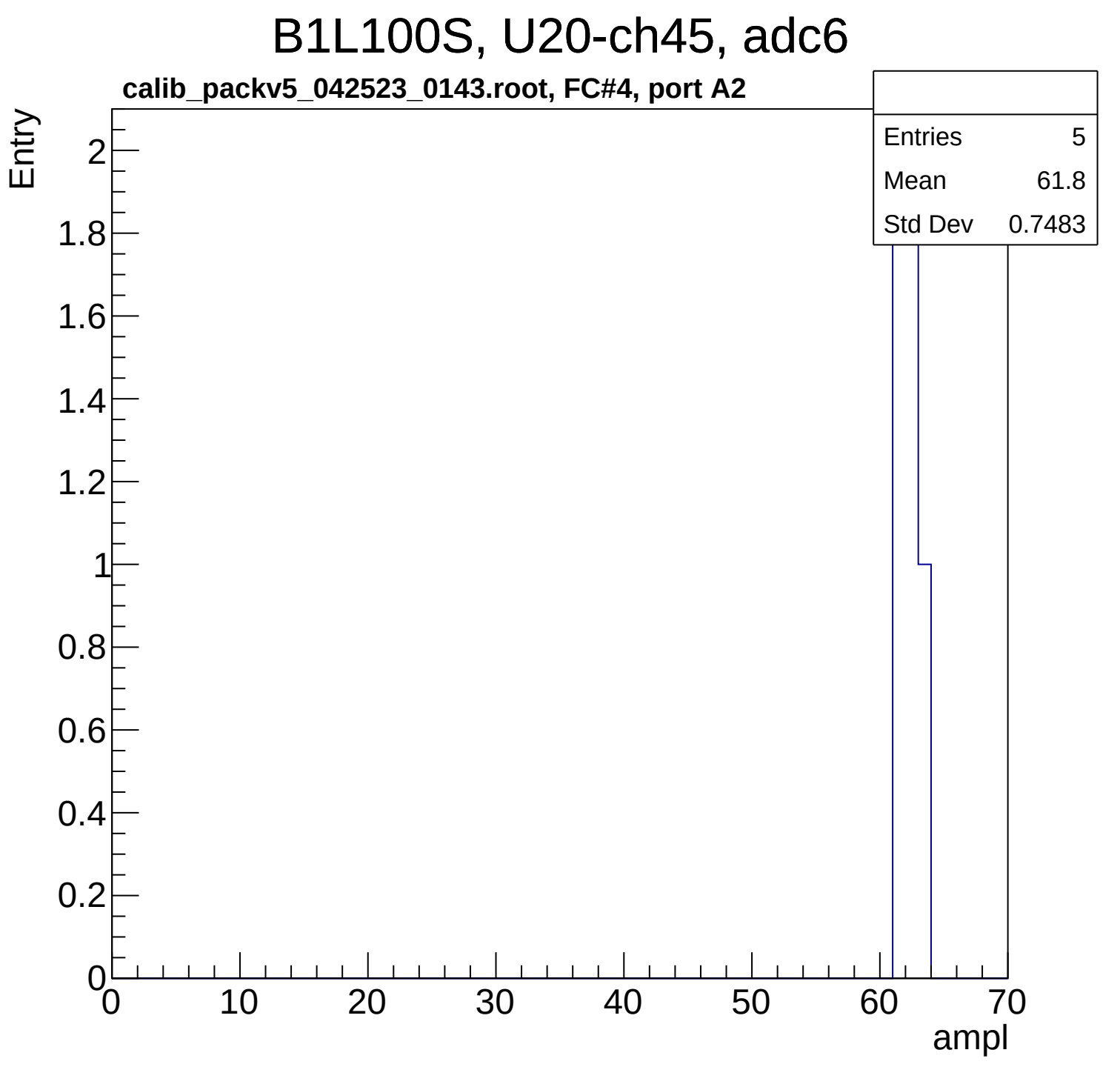
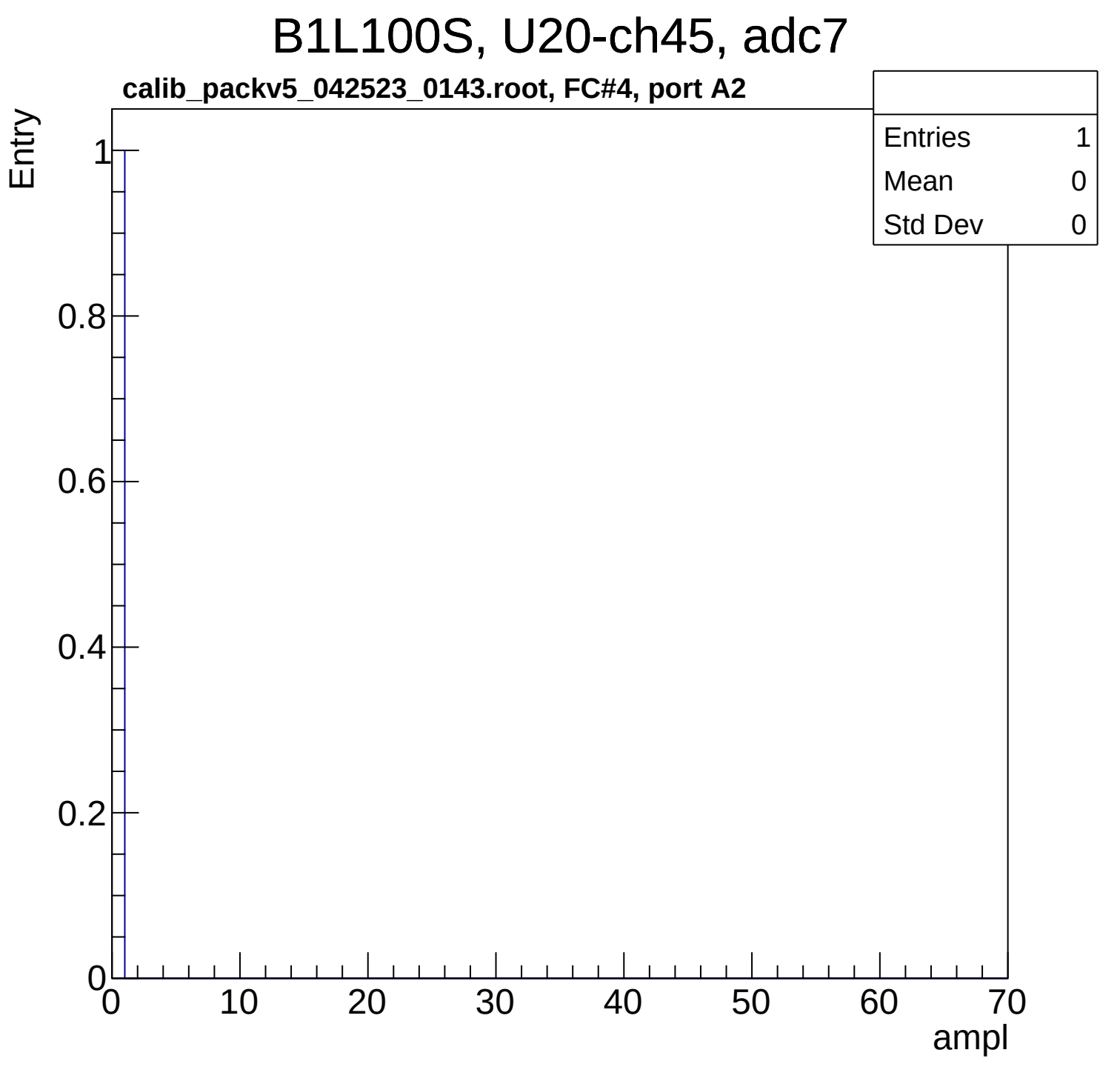
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch45, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch45, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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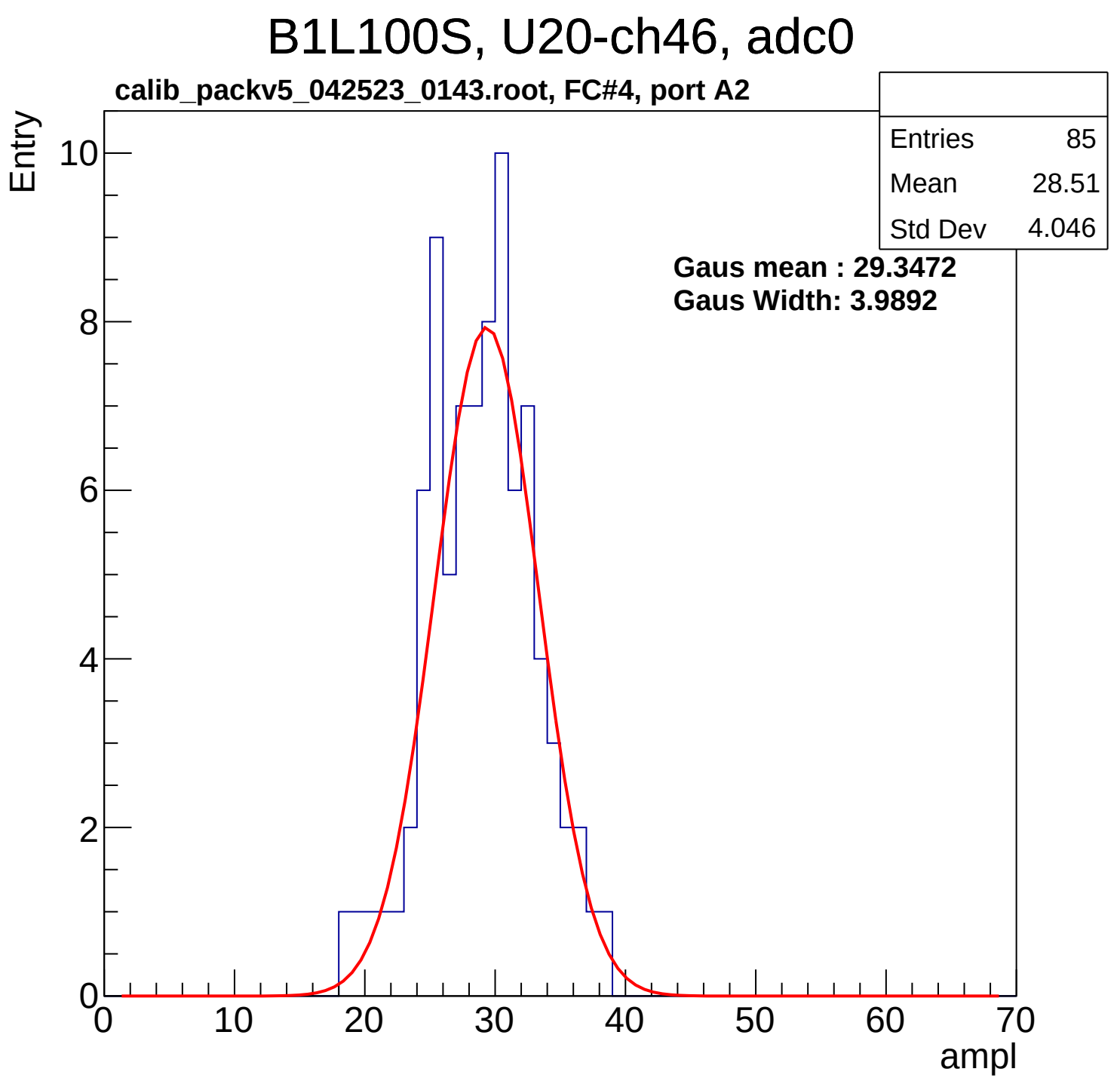
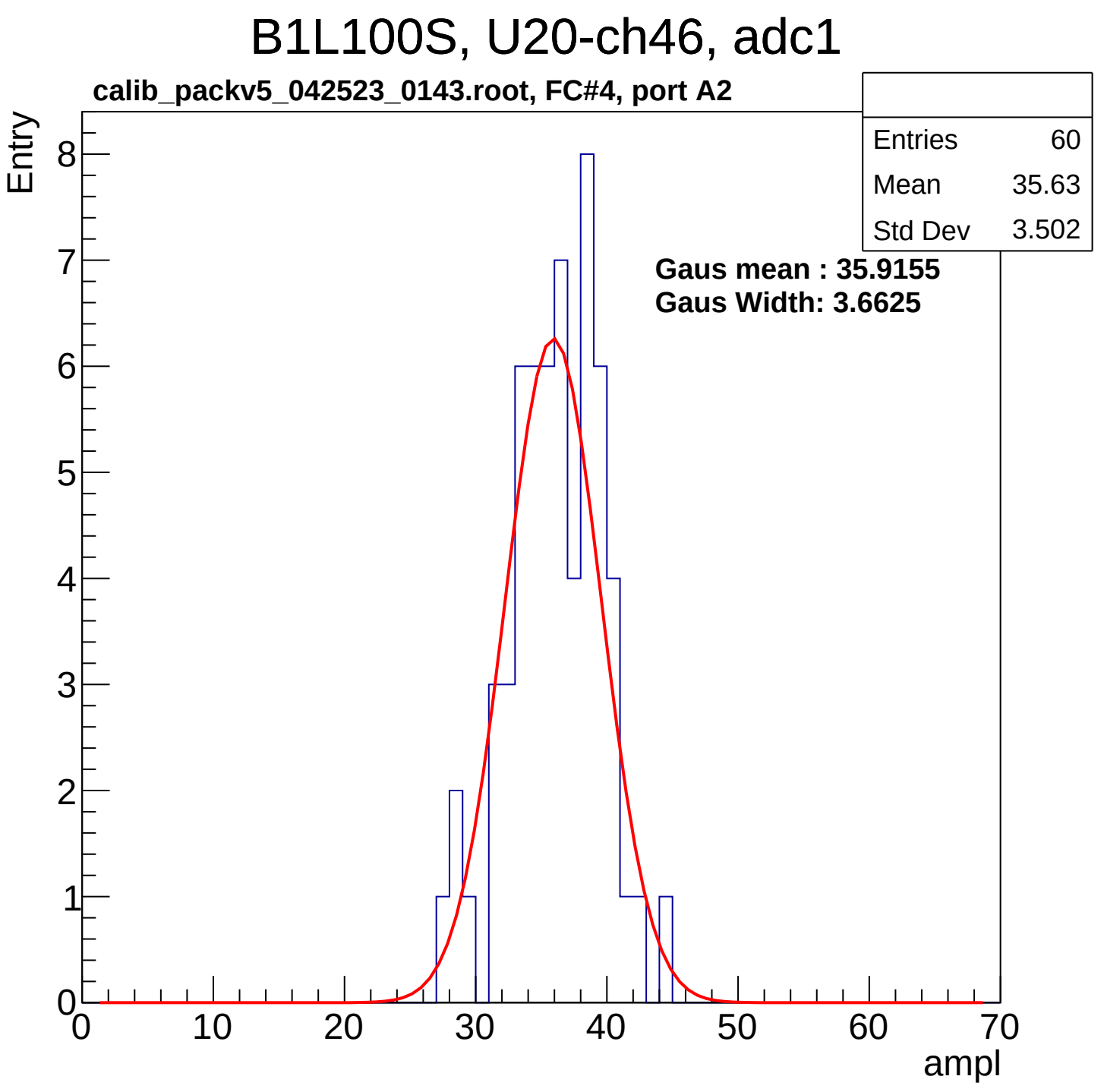
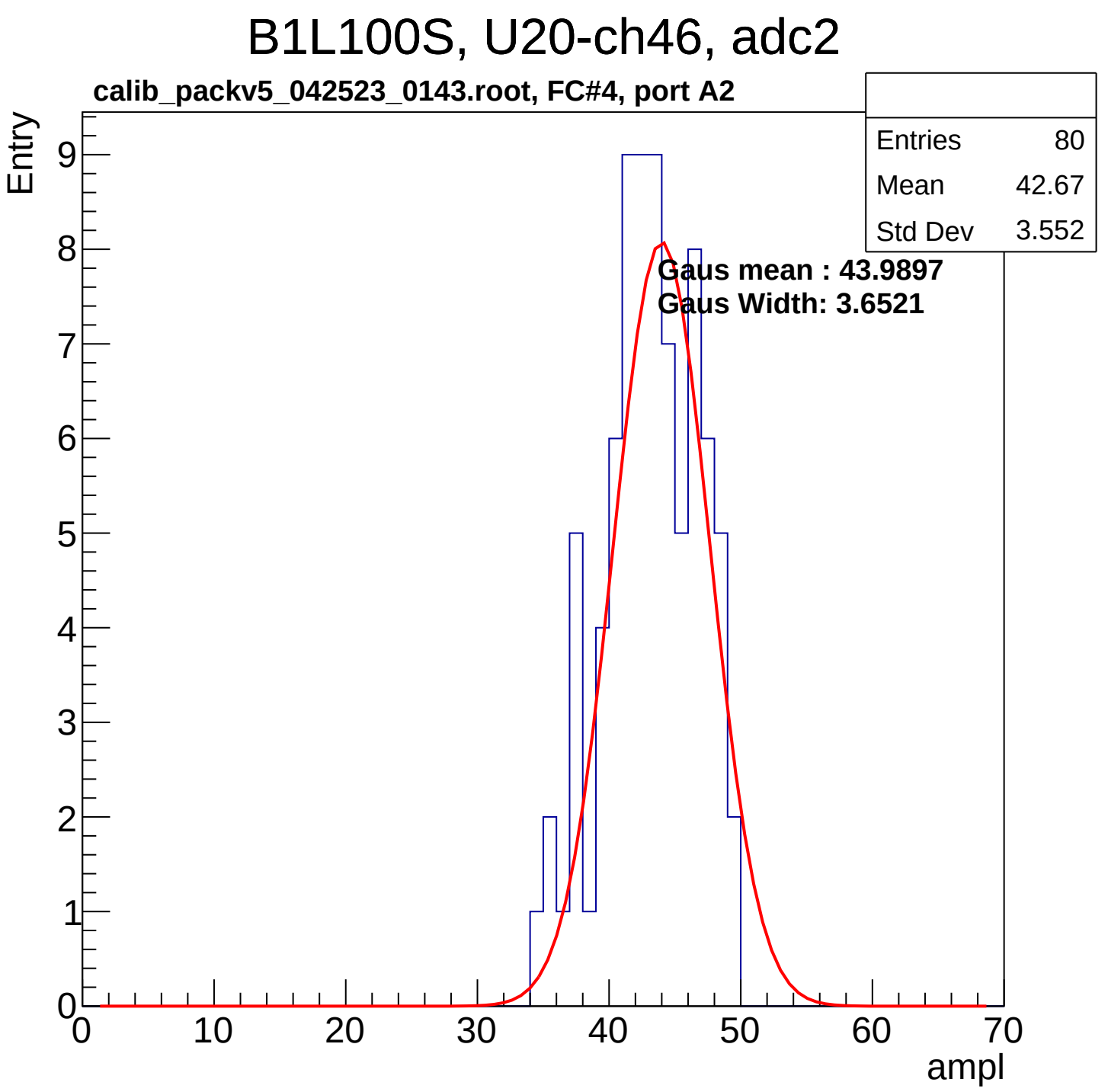
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 28.51
Std Dev 4.046
Gaus mean : 29.3472
Gaus Width: 3.9892
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.63
Std Dev 3.502
Gaus mean : 35.9155
Gaus Width: 3.6625
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 42.67
Std Dev 3.552
Gaus mean : 43.9897
Gaus Width: 3.6521
Entry
ampl
0
1
2
3
4
5
6
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8
9
0
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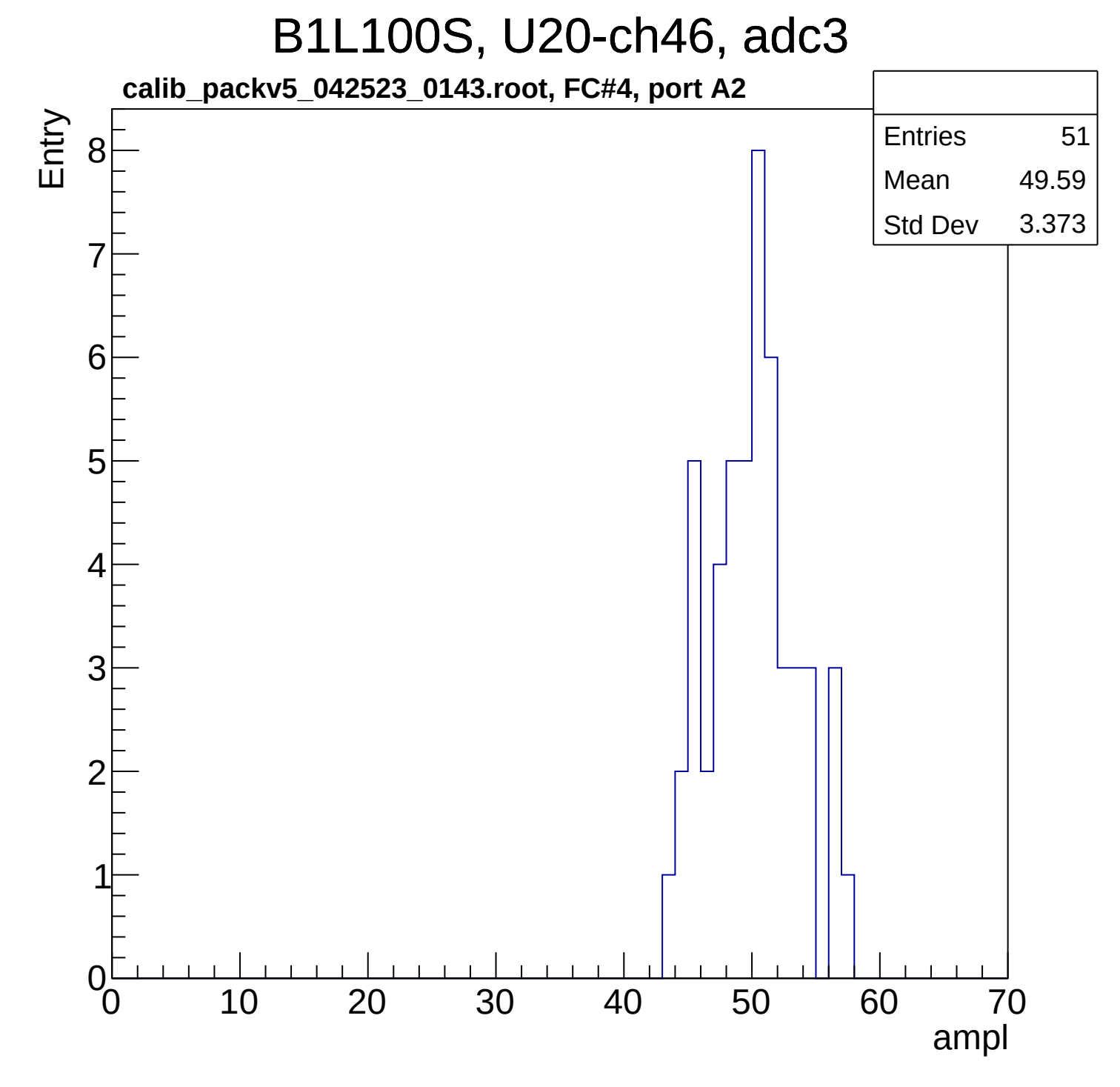
40
50
60
70


B1L100S, U20-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 49.59
Std Dev 3.373
Entry
ampl
0
1
2
3
4
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6
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0
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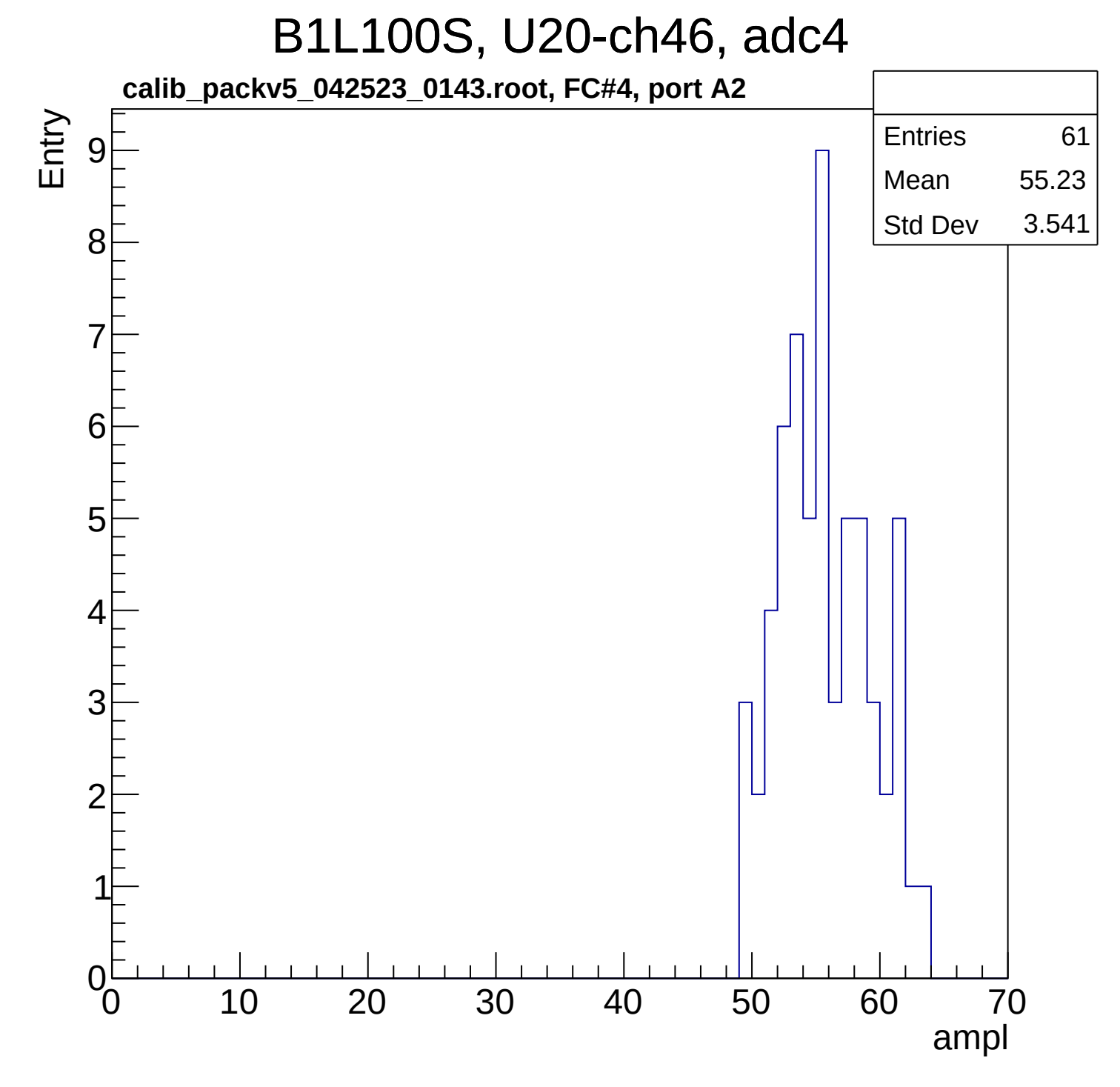
B1L100S, U20-ch46, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.23
Std Dev 3.541
Entry
ampl
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2
3
4
5
6
7
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0
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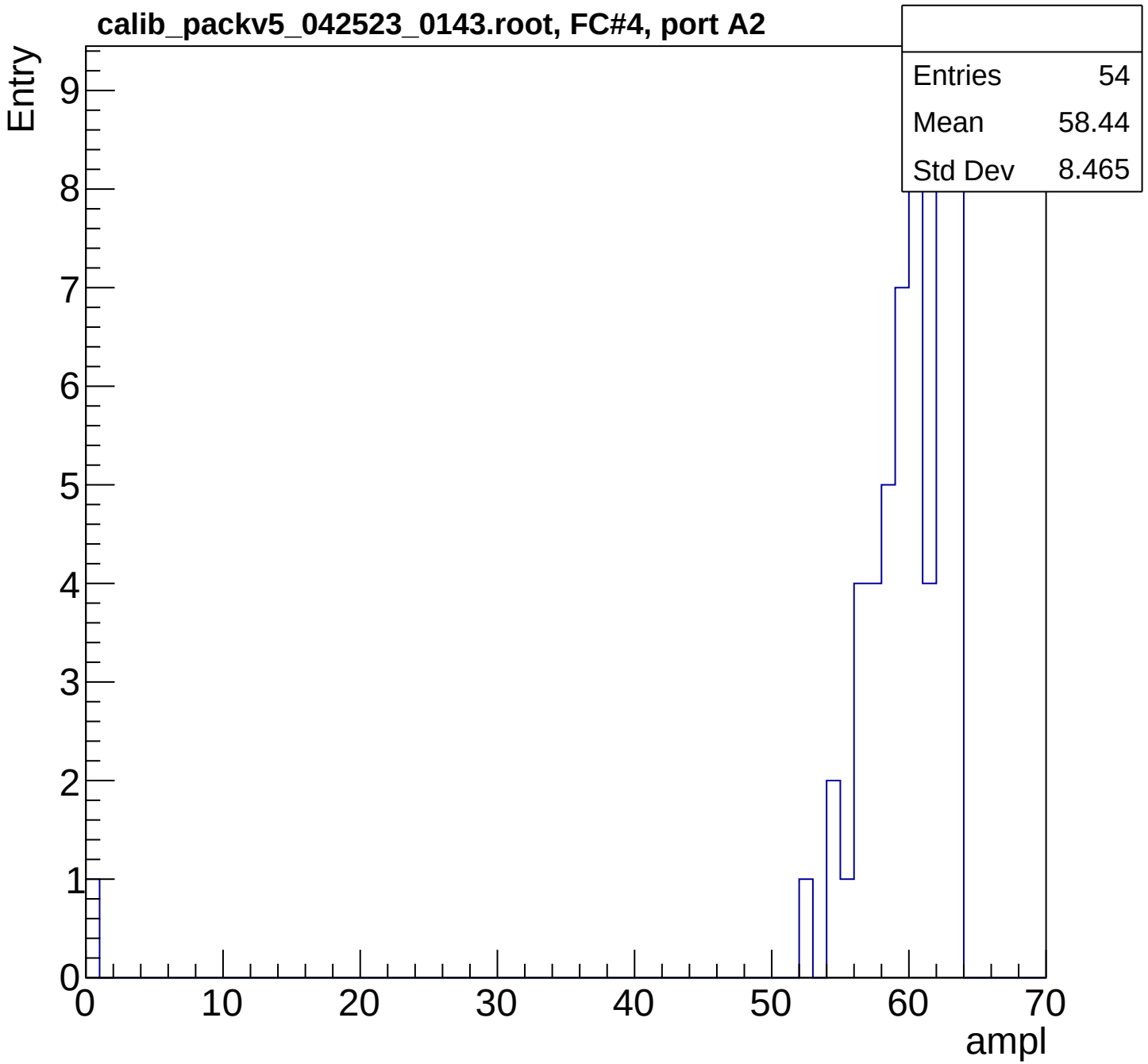
B1L100S, U20-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 58.44
Std Dev 8.465
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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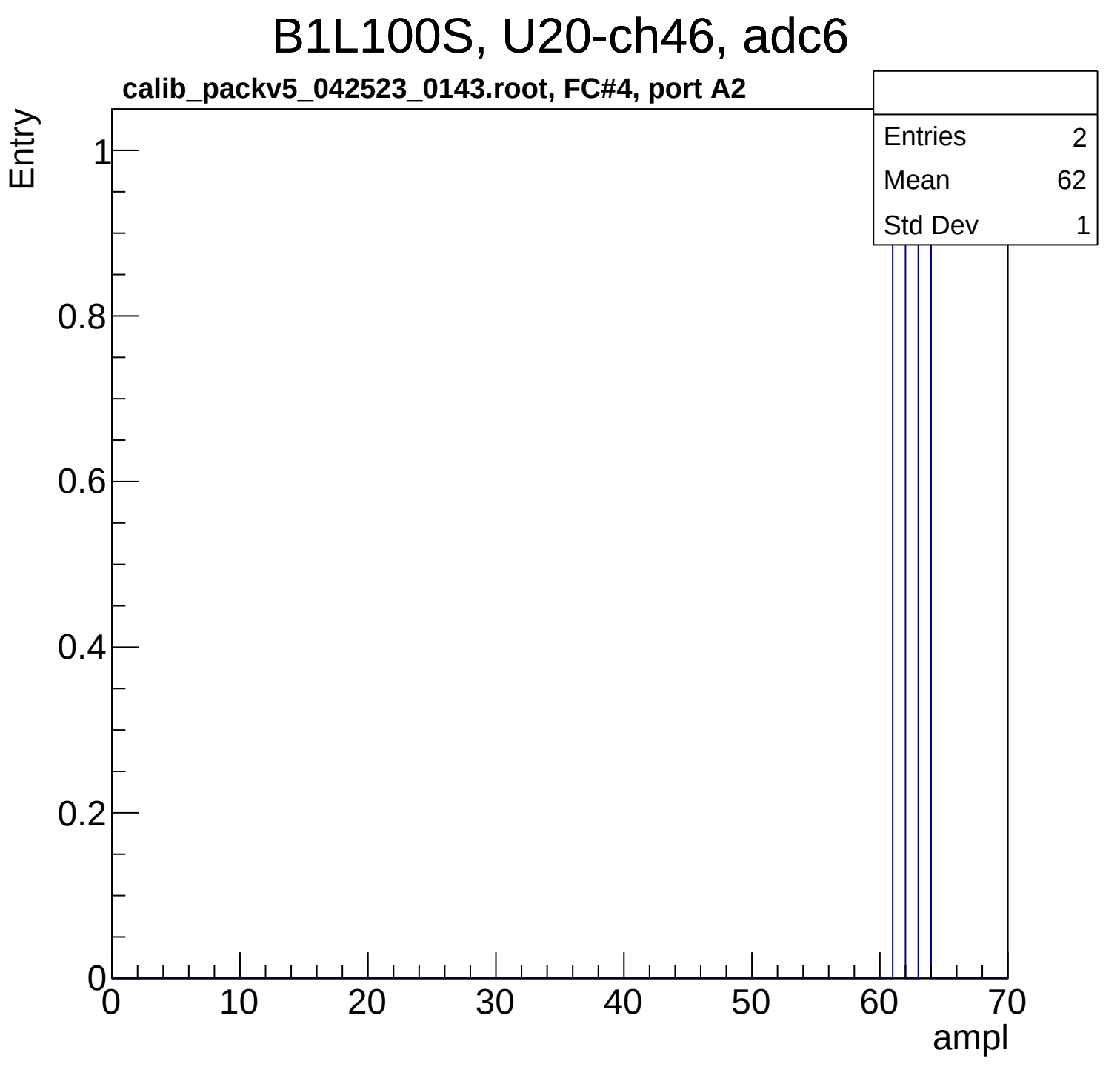
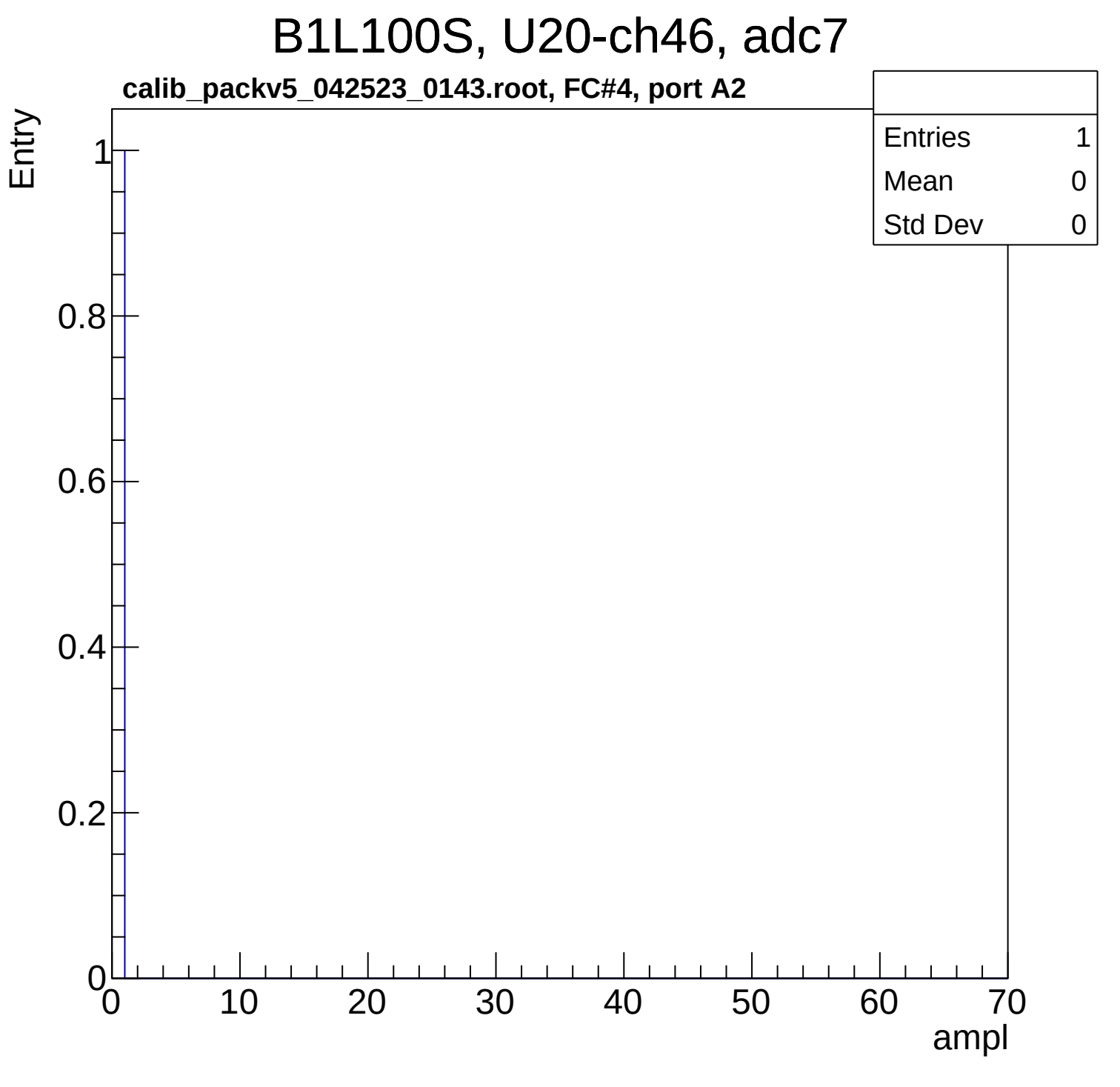
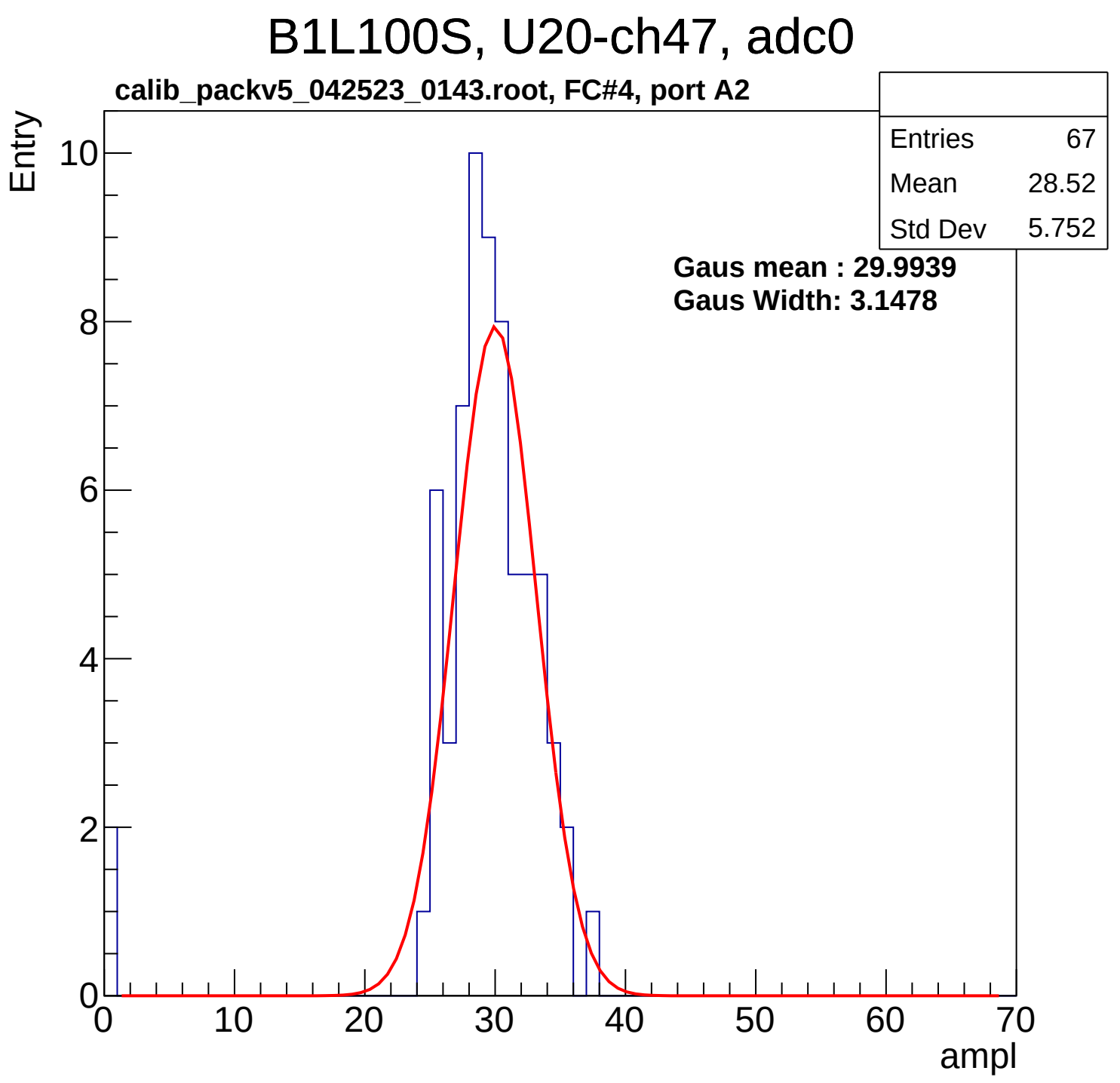
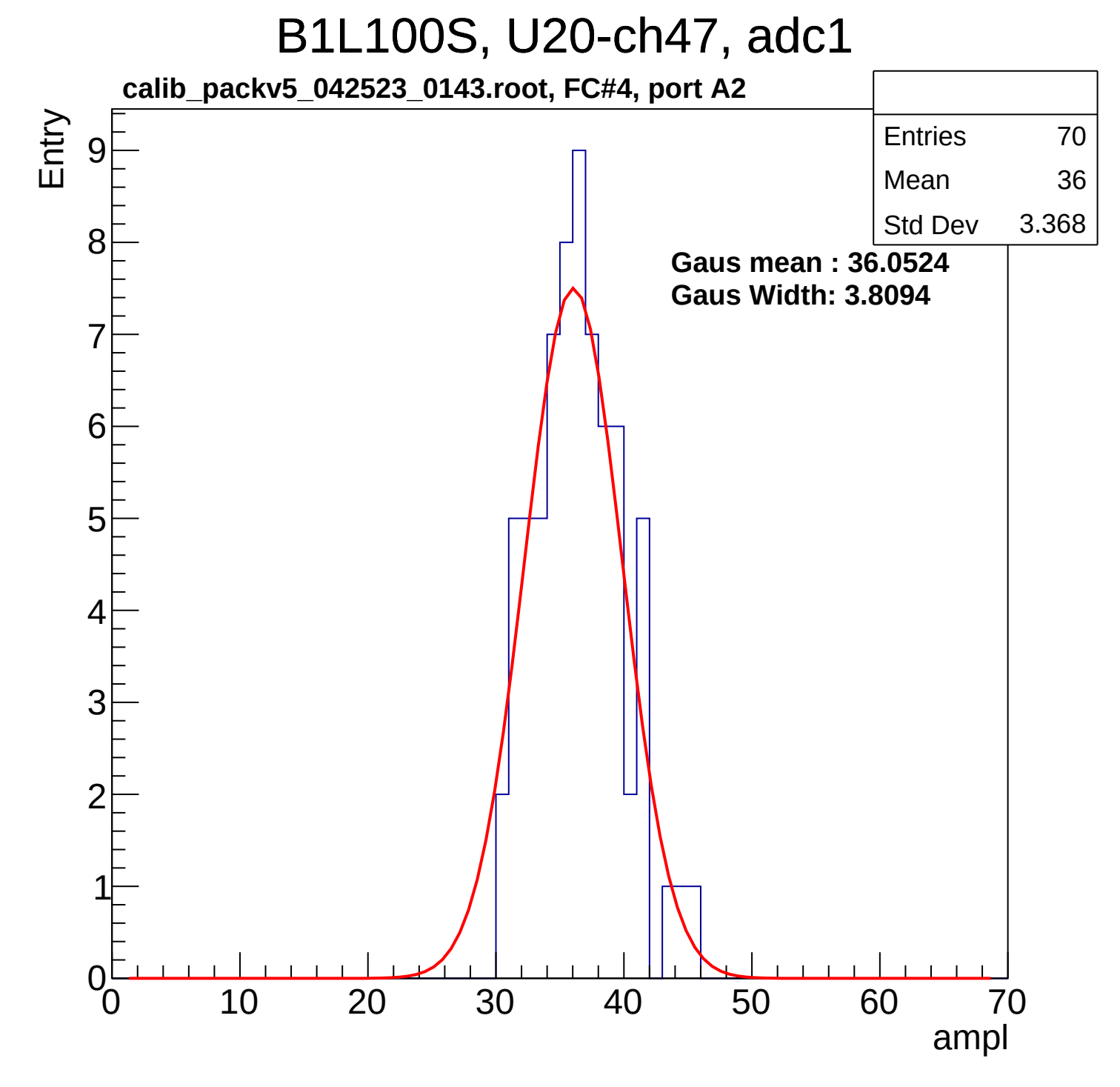

B1L100S, U20-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.52
Std Dev 5.752
Gaus mean : 29.9939
Gaus Width: 3.1478
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36
Std Dev 3.368
Gaus mean : 36.0524
Gaus Width: 3.8094
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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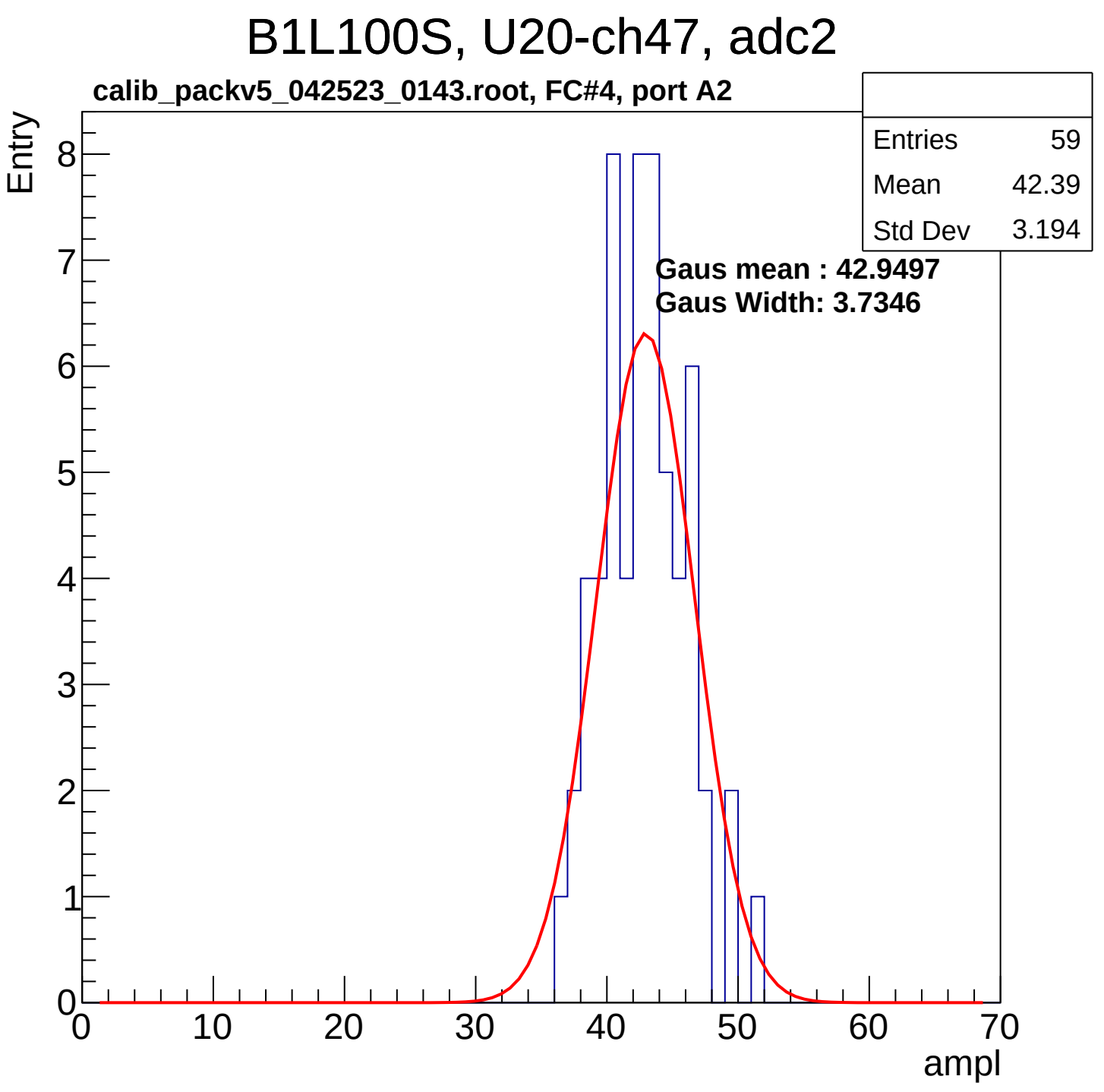
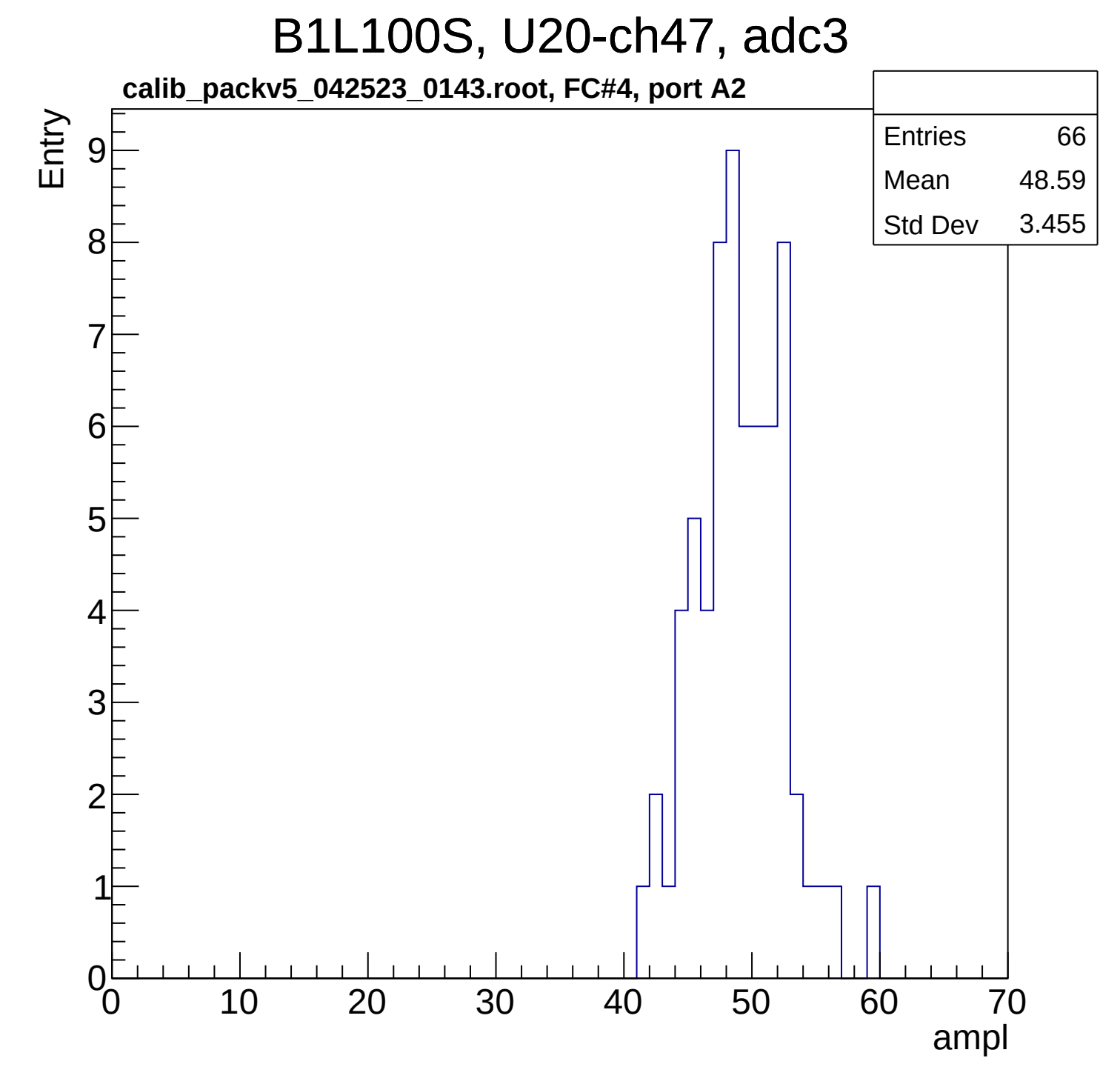
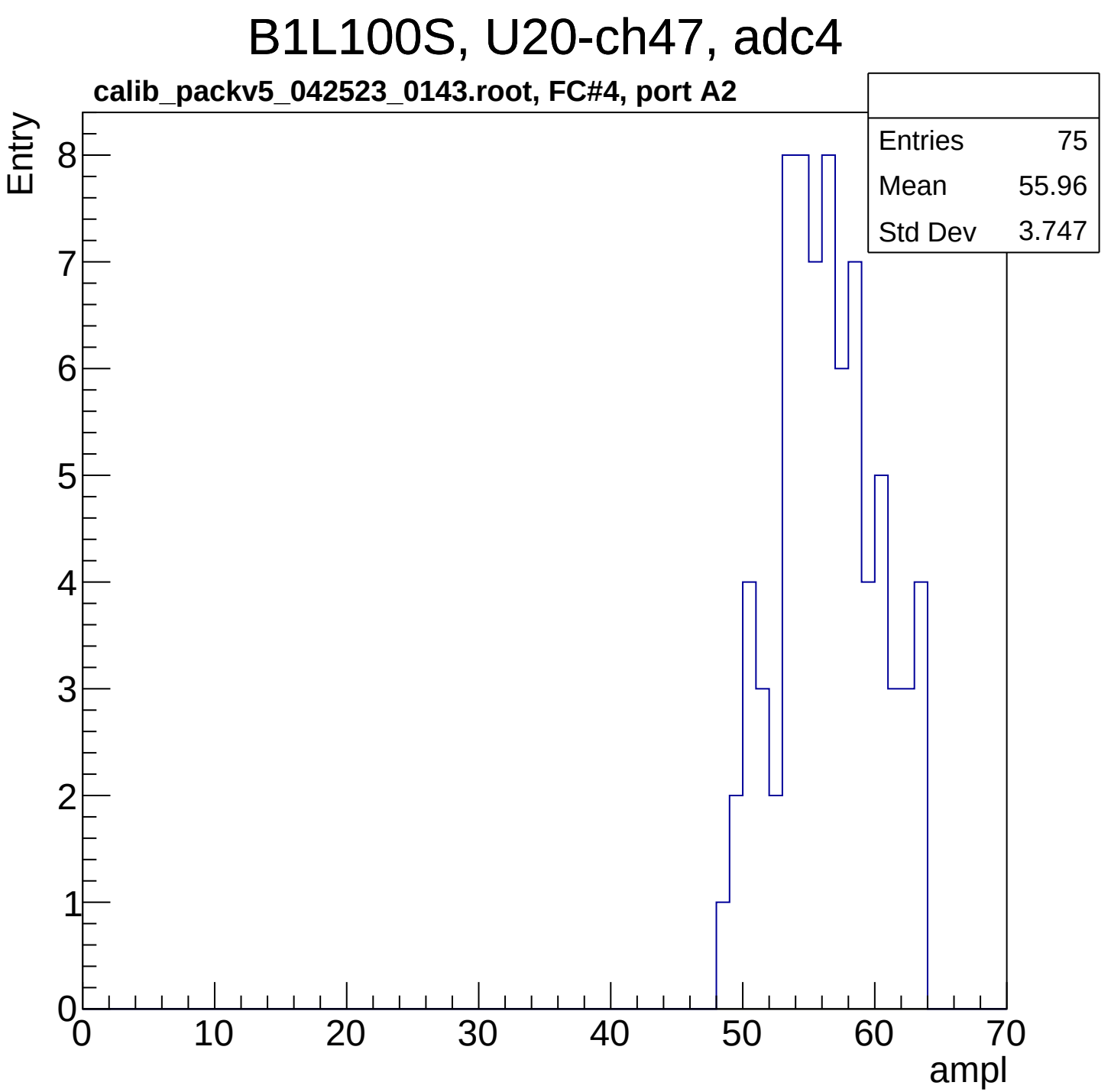
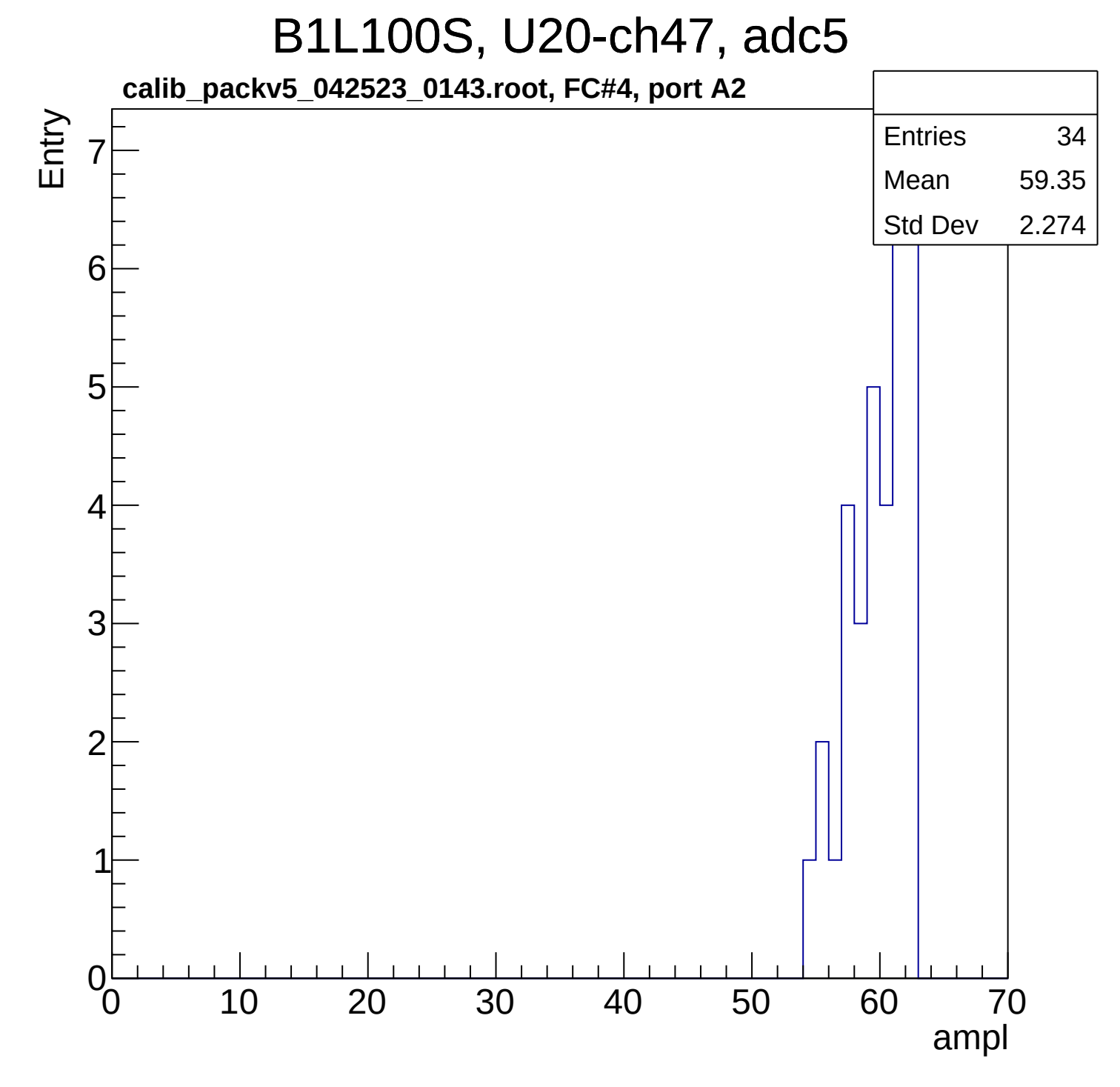

B1L100S, U20-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.39
Std Dev 3.194
Gaus mean : 42.9497
Gaus Width: 3.7346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 48.59
Std Dev 3.455
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 55.96
Std Dev 3.747
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59.35
Std Dev 2.274
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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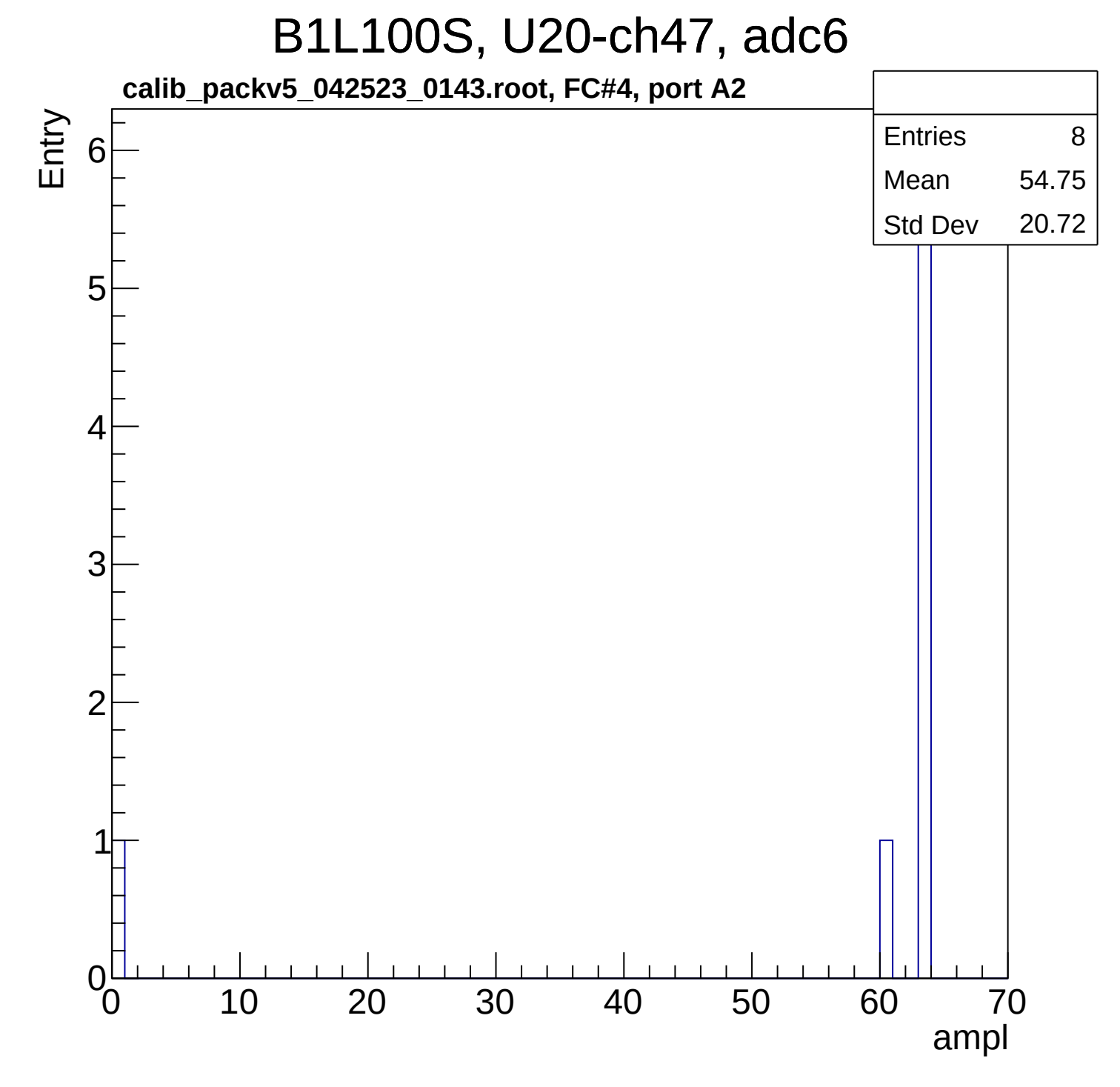
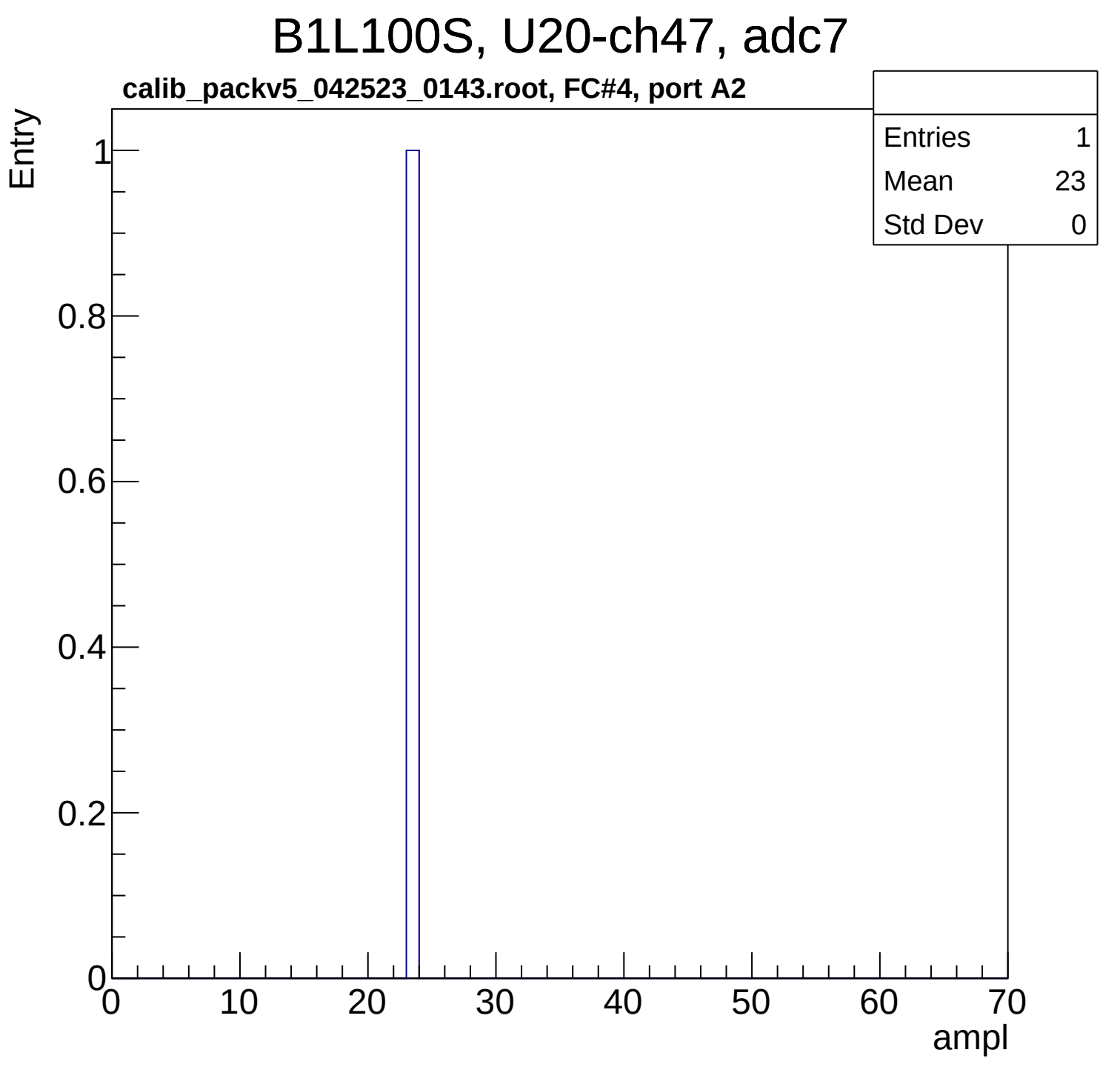
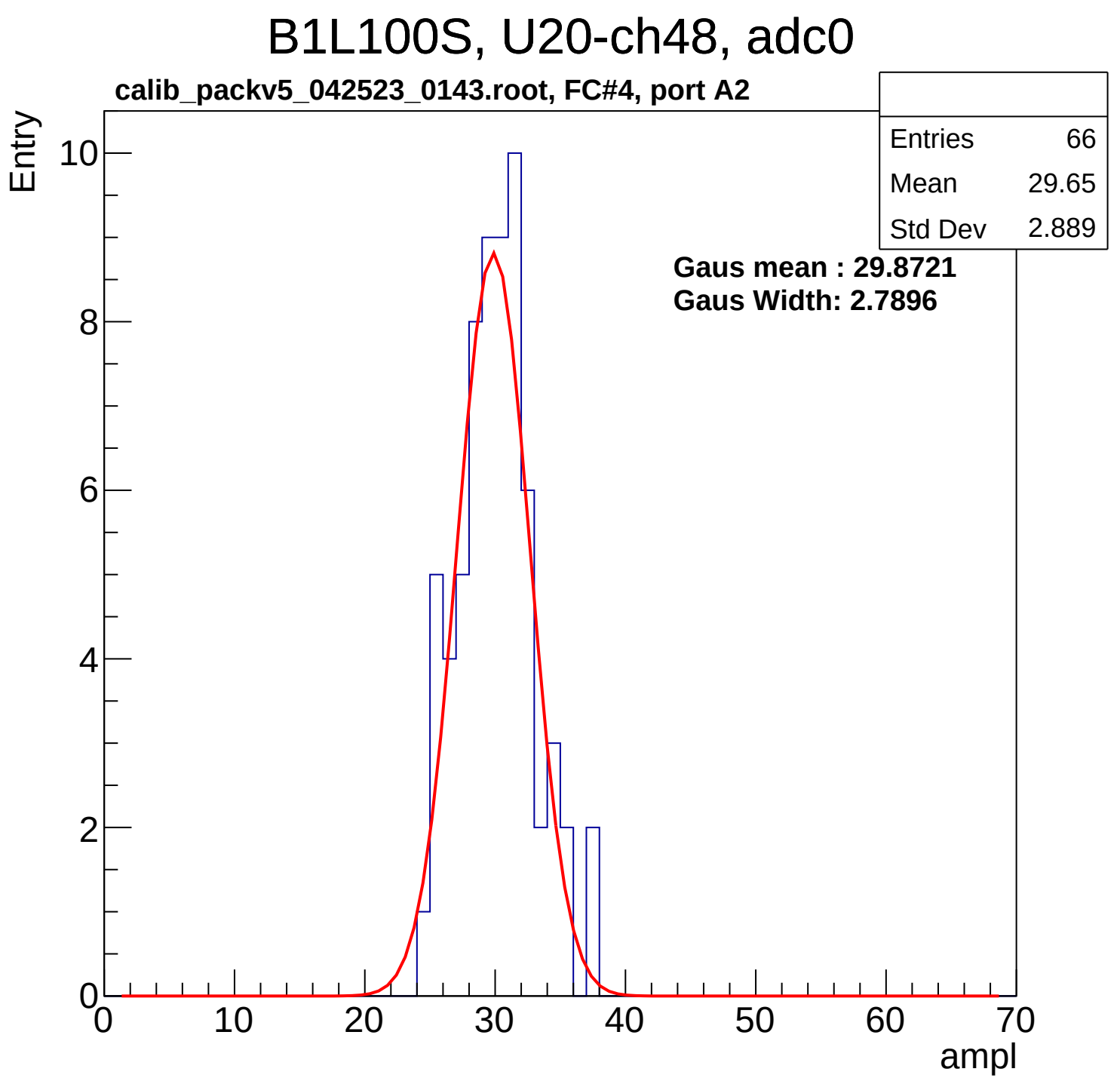
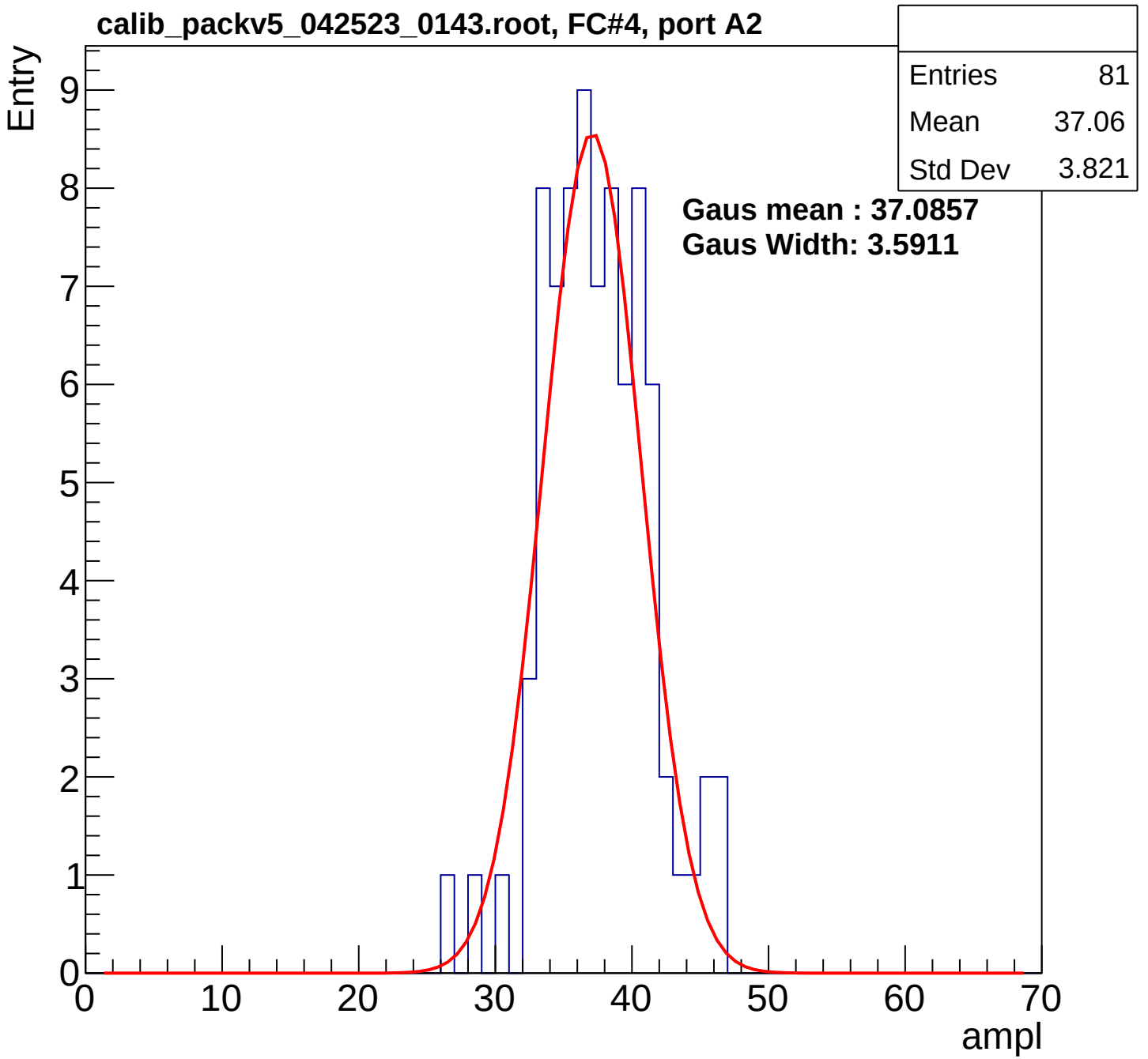
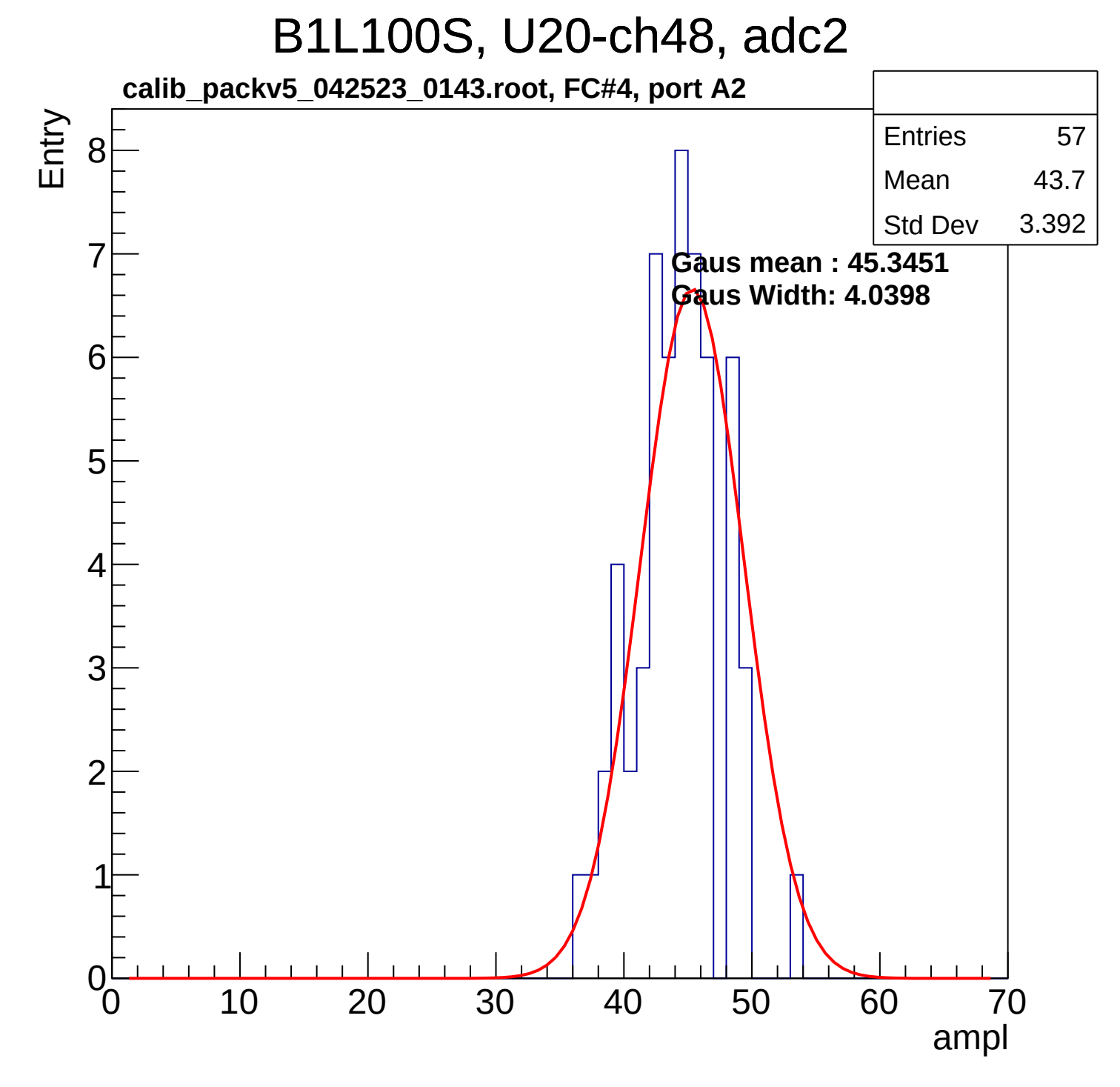
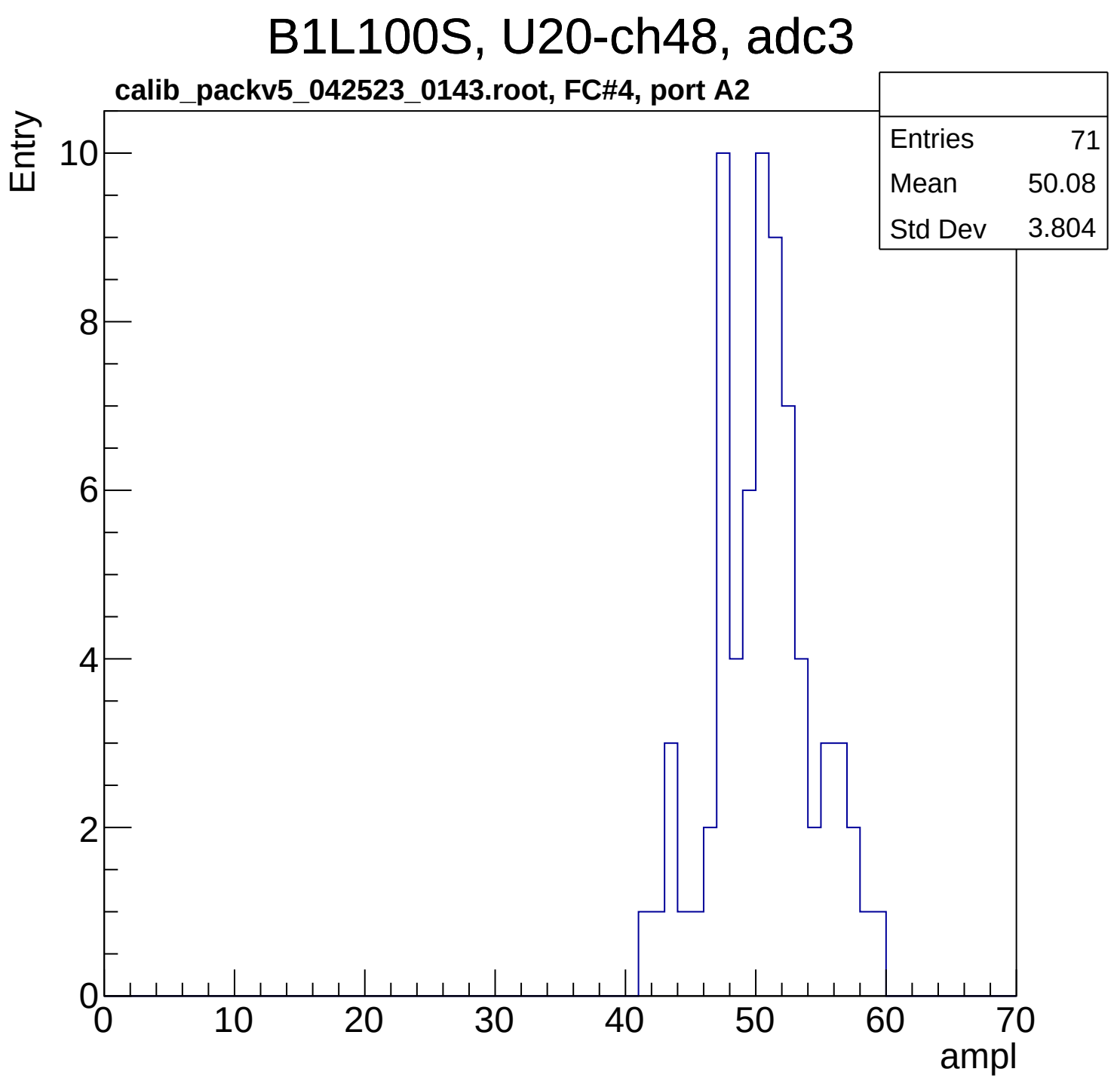

B1L100S, U20-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54.75
Std Dev 20.72
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

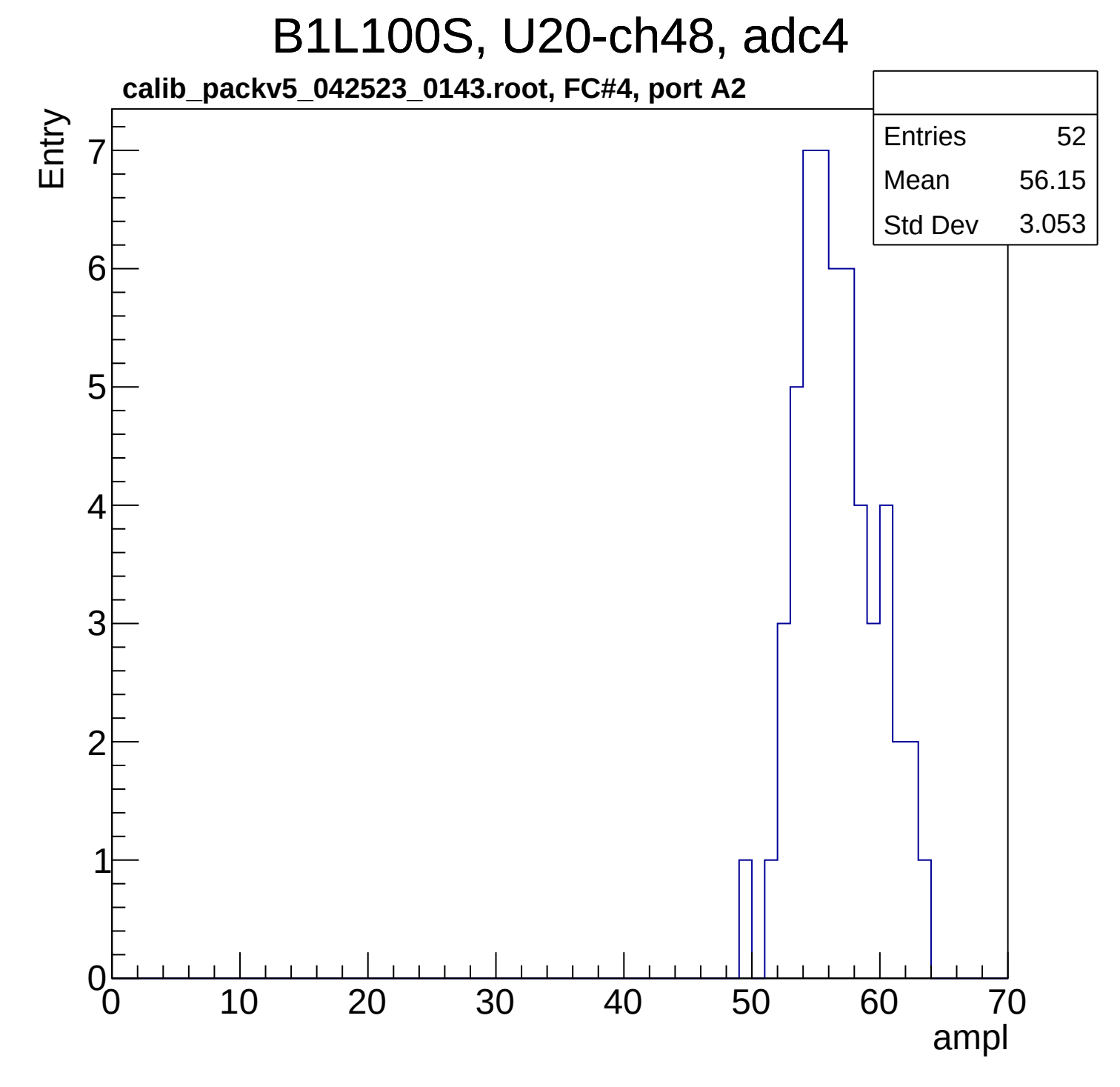

B1L100S, U20-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

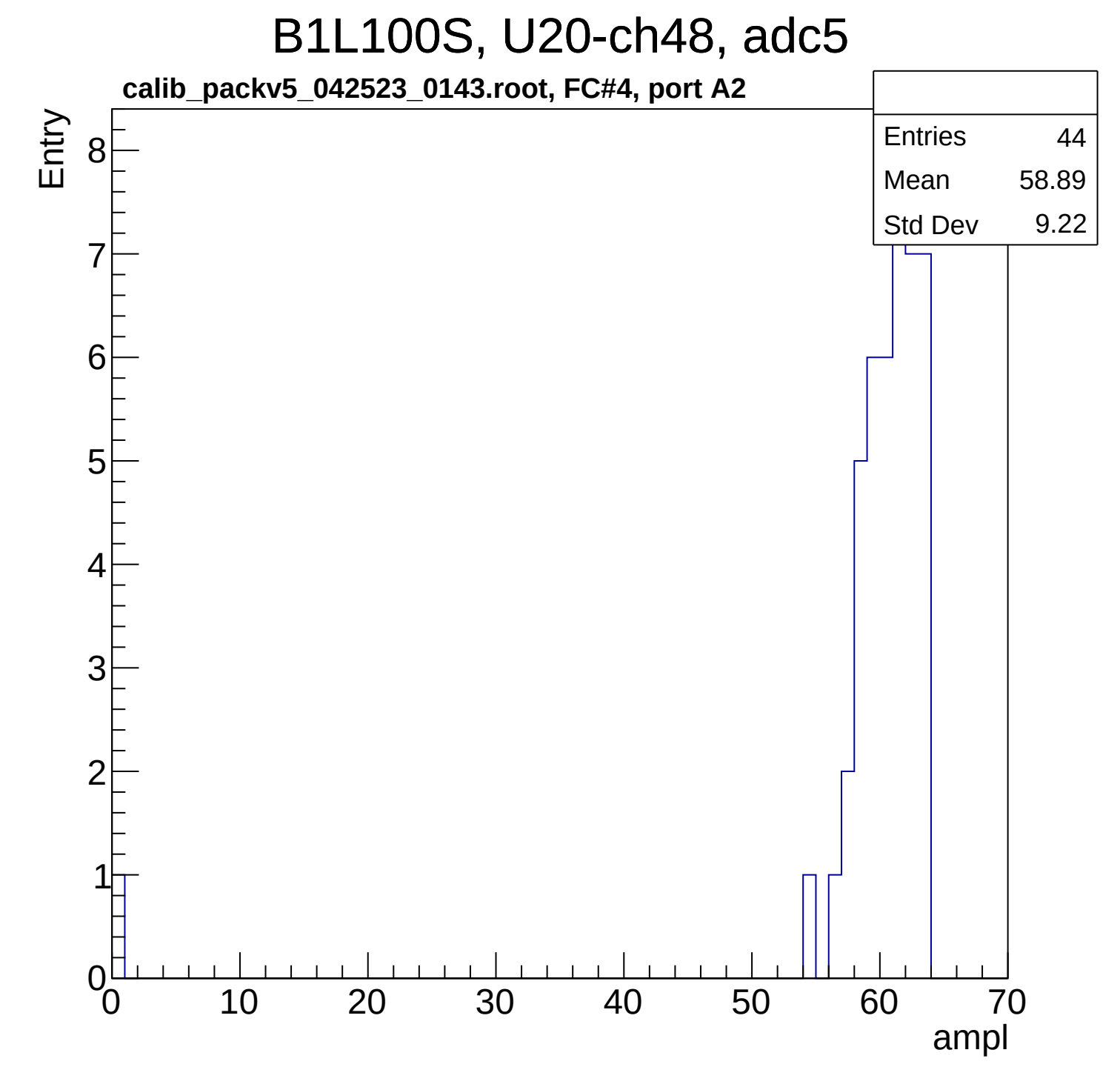

B1L100S, U20-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.65
Std Dev 2.889
Gaus mean : 29.8721
Gaus Width: 2.7896
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 37.06
Std Dev 3.821
Gaus mean : 37.0857
Gaus Width: 3.5911
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch48, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43.7
Std Dev 3.392
Gaus mean : 45.3451
Gaus Width: 4.0398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.08
Std Dev 3.804
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.15
Std Dev 3.053
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.89
Std Dev 9.22
Entry
ampl
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60
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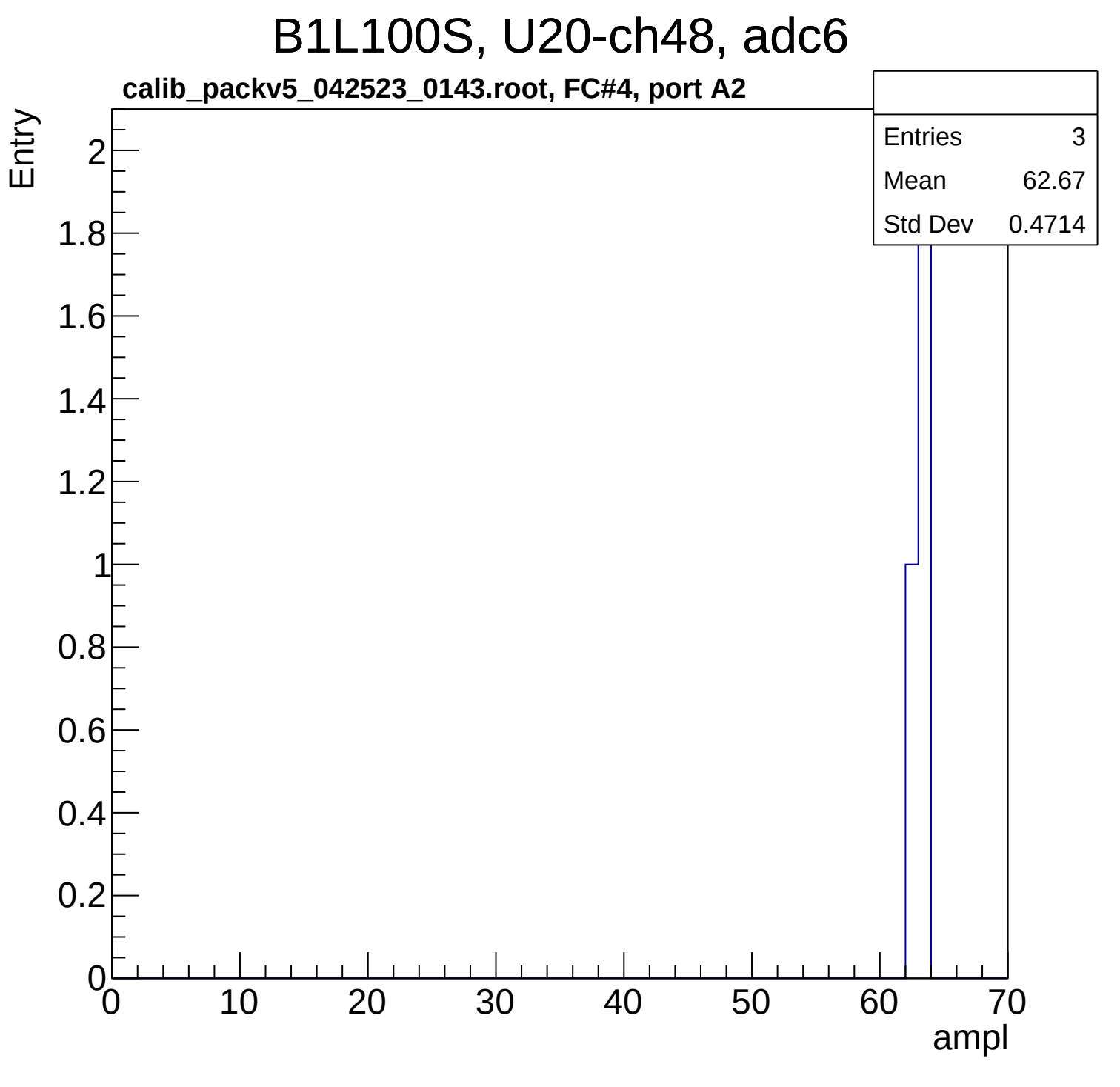
B1L100S, U20-ch48, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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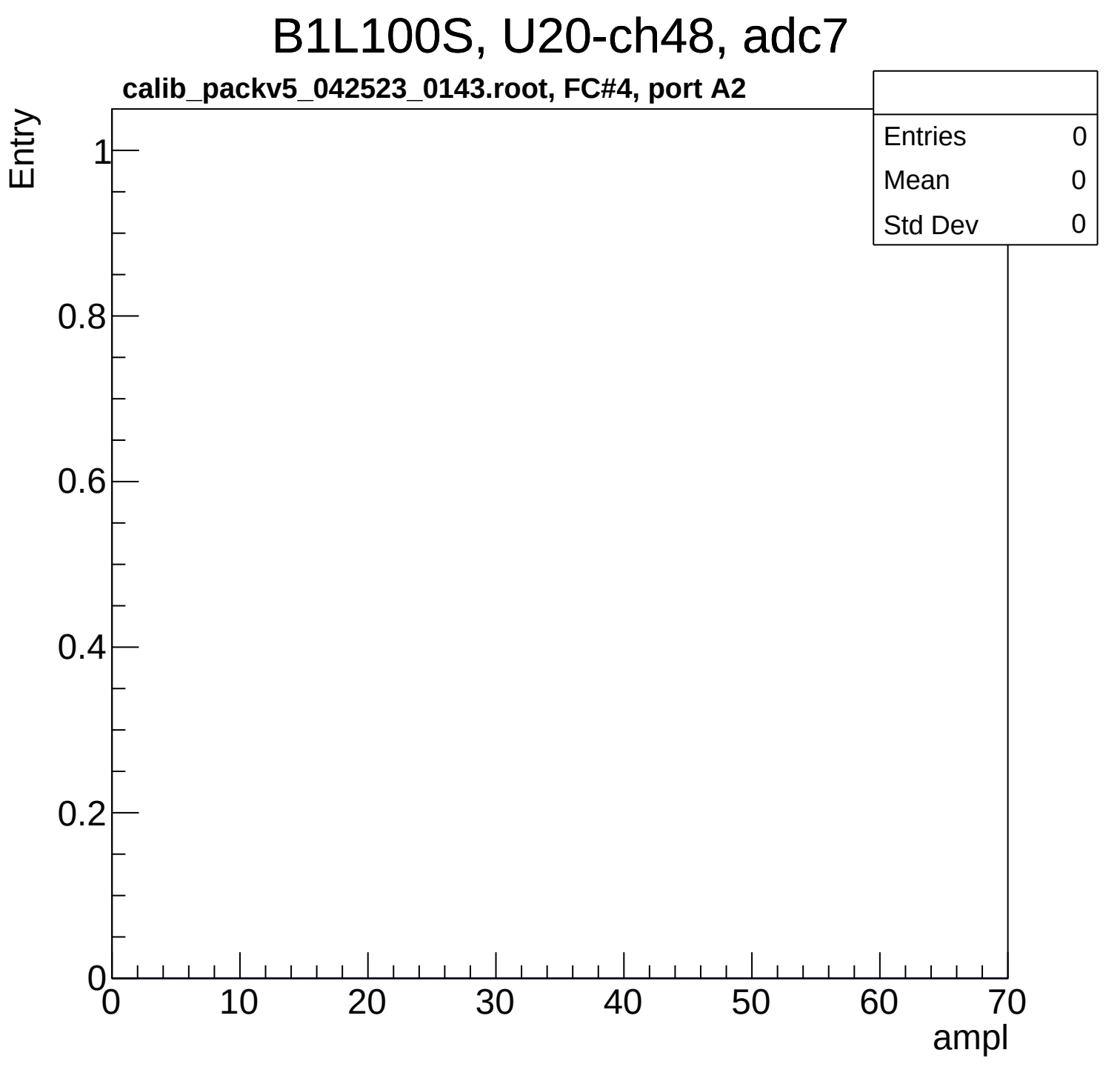
B1L100S, U20-ch48, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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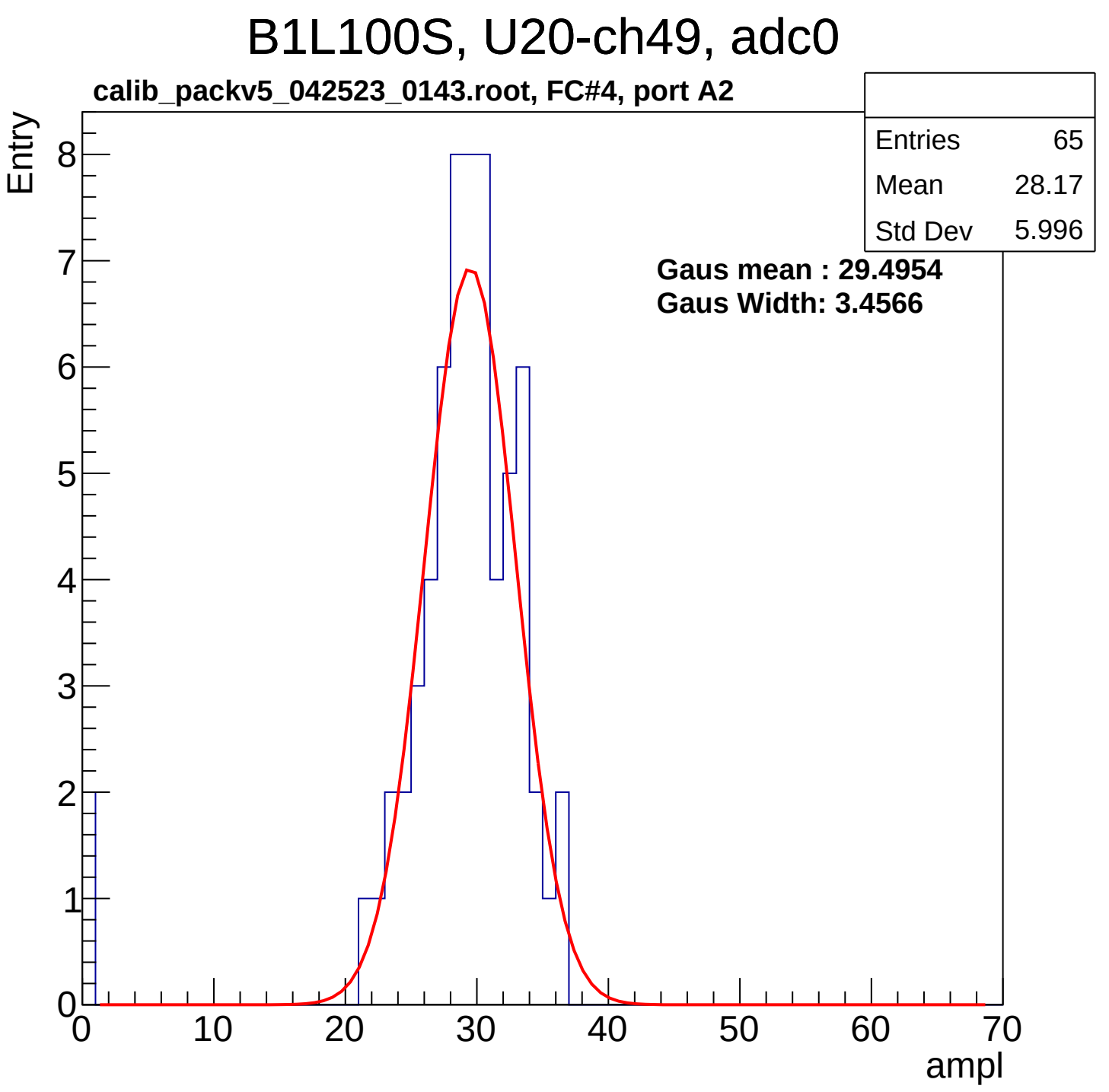
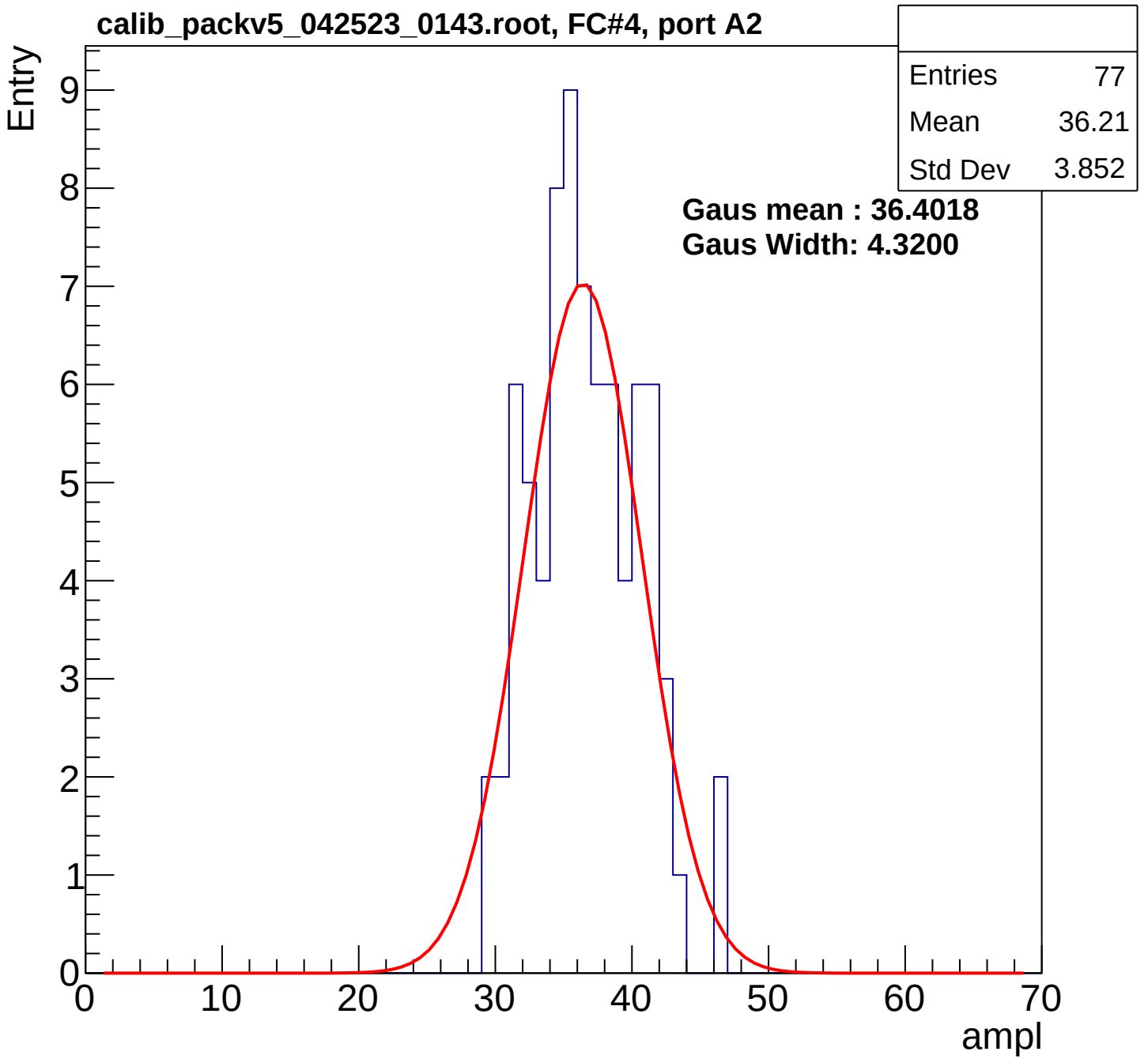
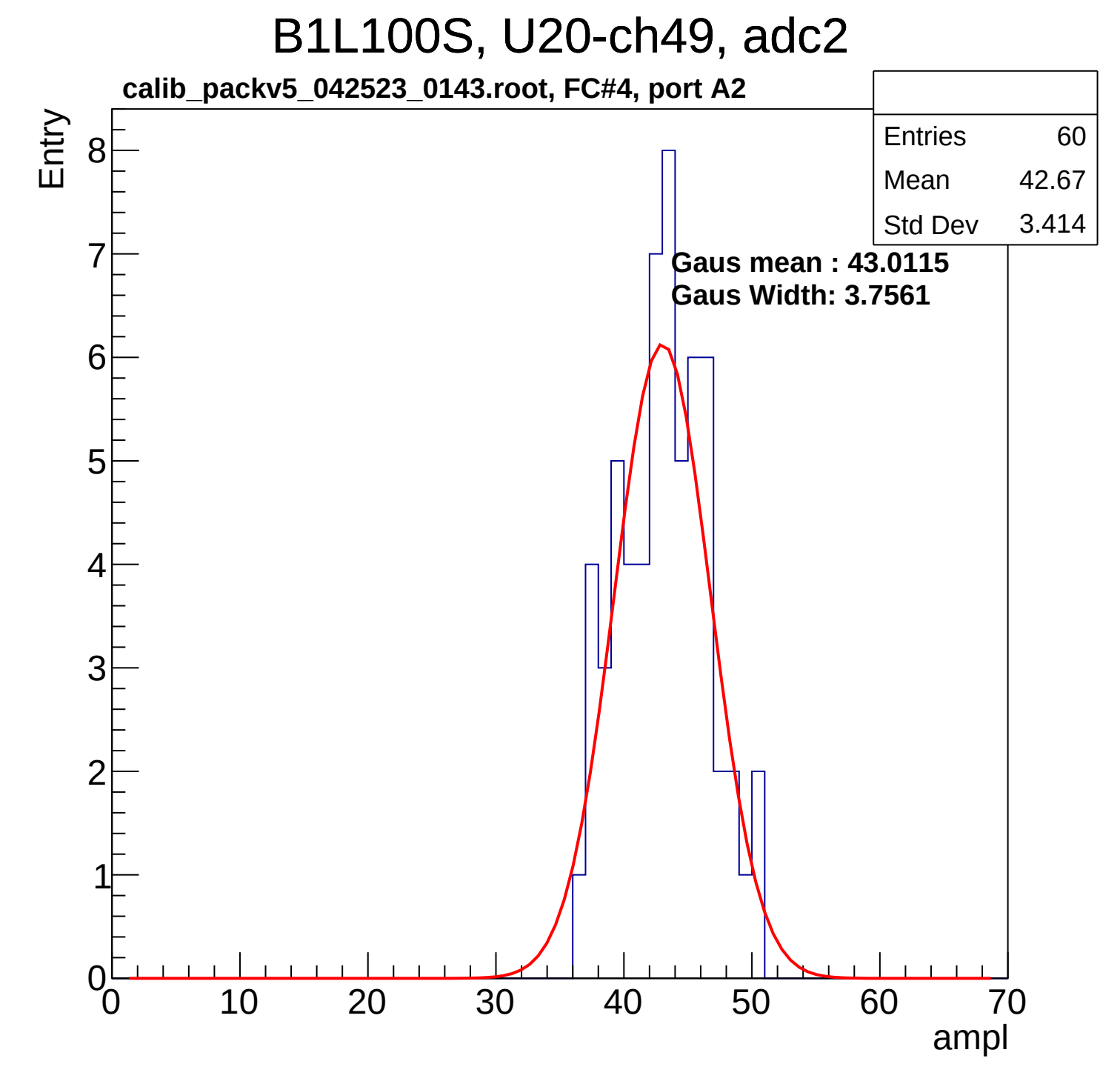
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch49, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.17
Std Dev 5.996
Gaus mean : 29.4954
Gaus Width: 3.4566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.21
Std Dev 3.852
Gaus mean : 36.4018
Gaus Width: 4.3200
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.67
Std Dev 3.414
Gaus mean : 43.0115
Gaus Width: 3.7561
Entry
ampl
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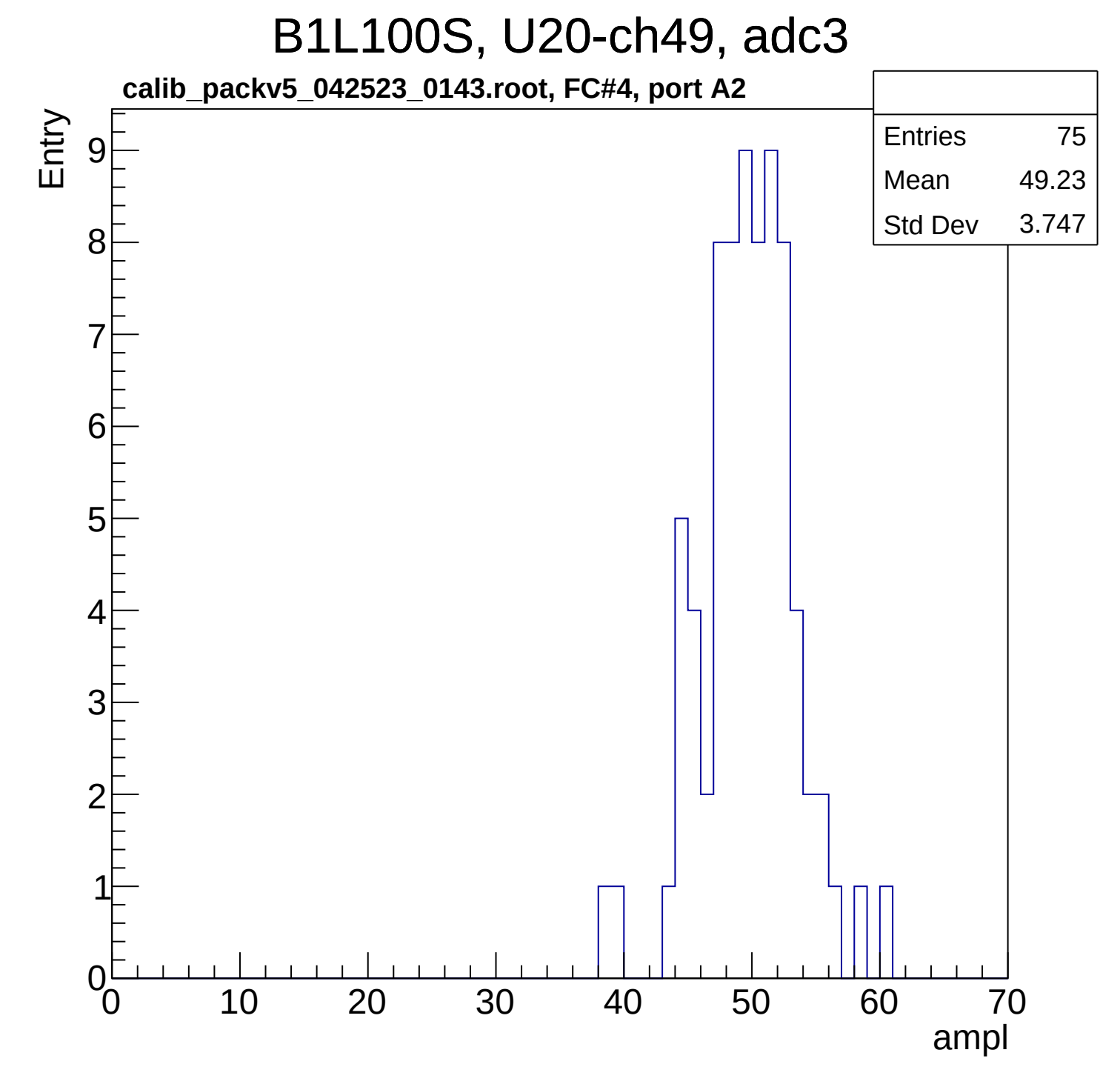
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 49.23
Std Dev 3.747
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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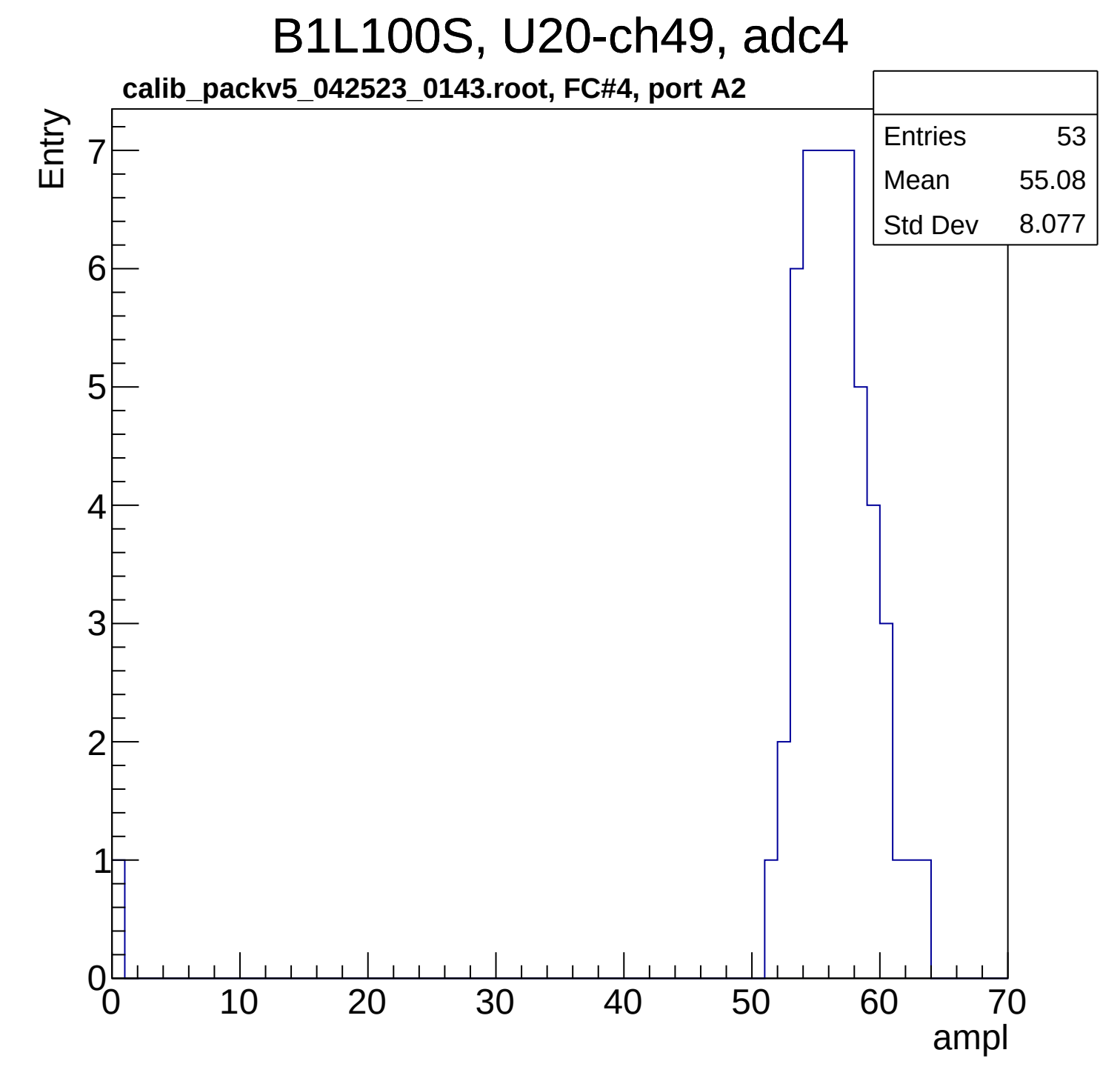
40
50
60
70


B1L100S, U20-ch49, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.08
Std Dev 8.077
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
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60
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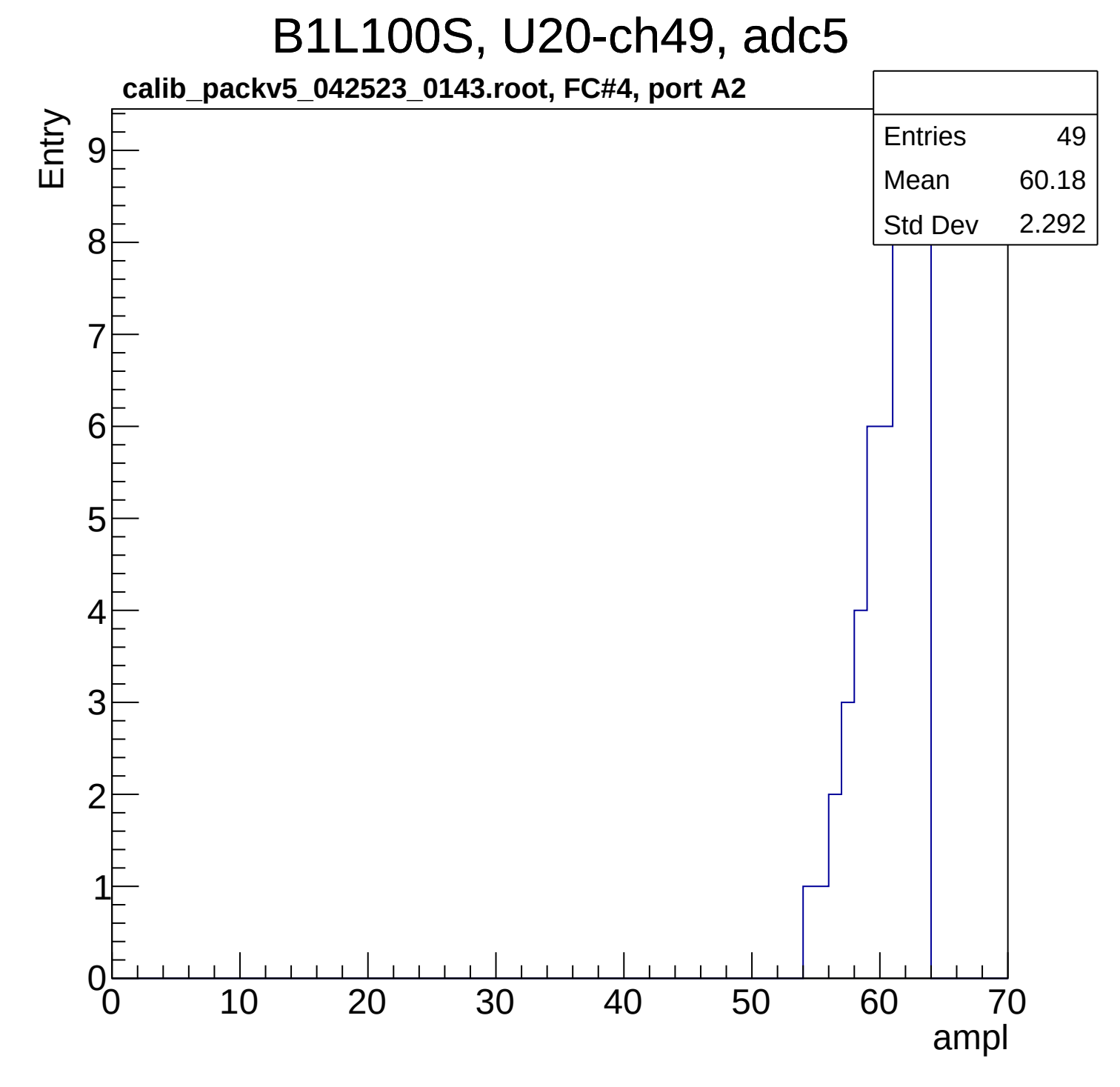
B1L100S, U20-ch49, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 60.18
Std Dev 2.292
Entry
ampl
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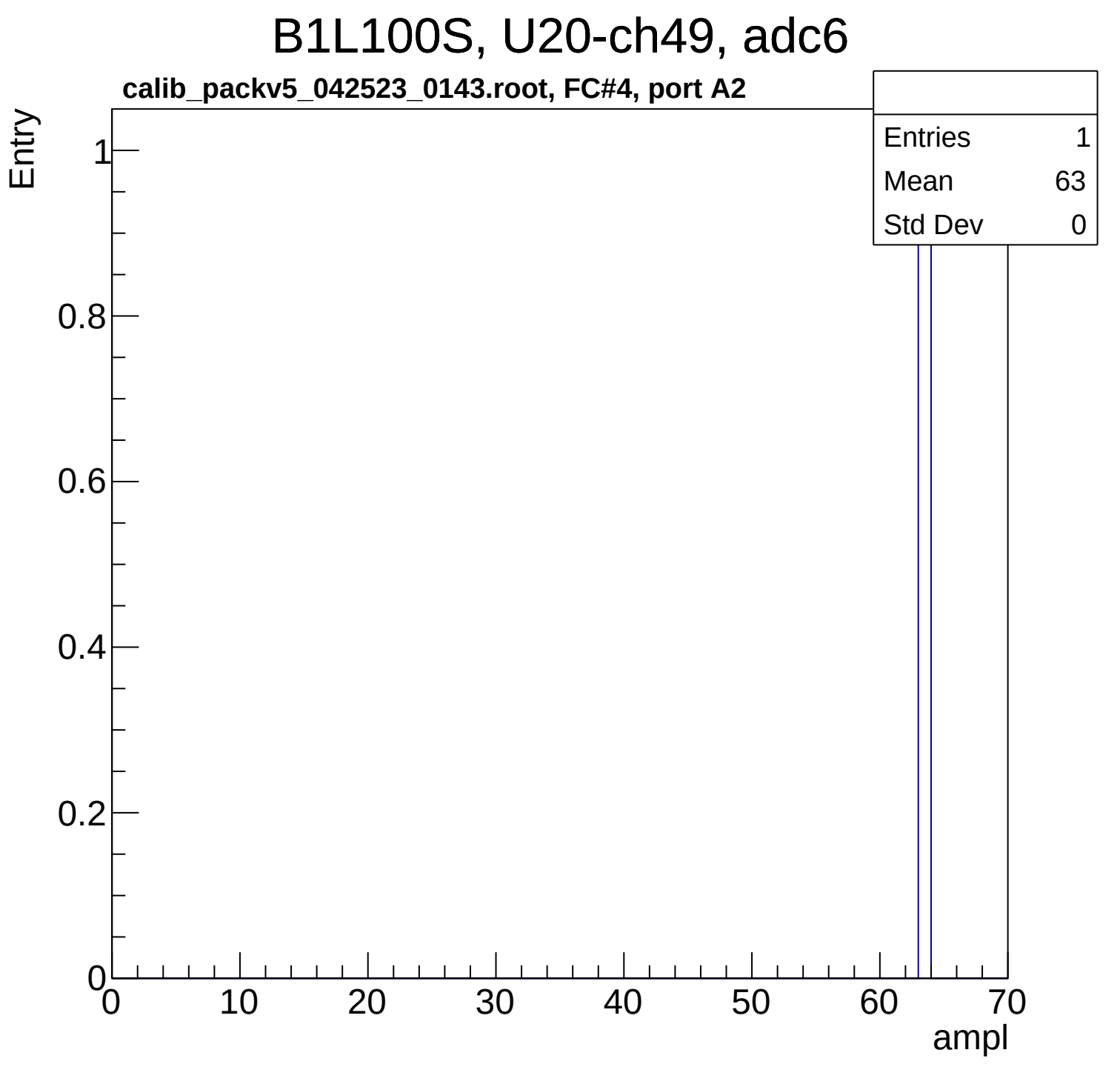
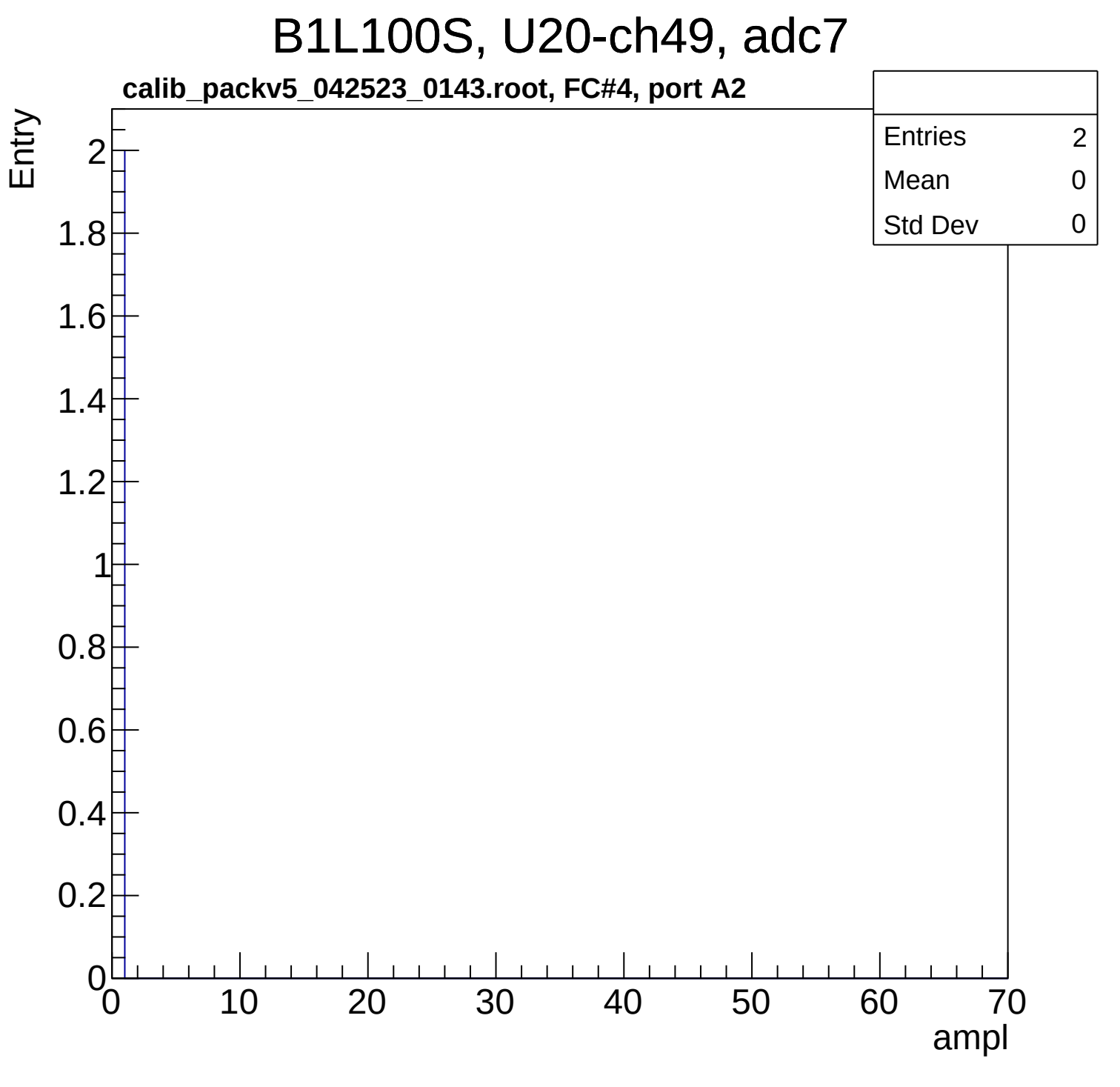
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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10
20
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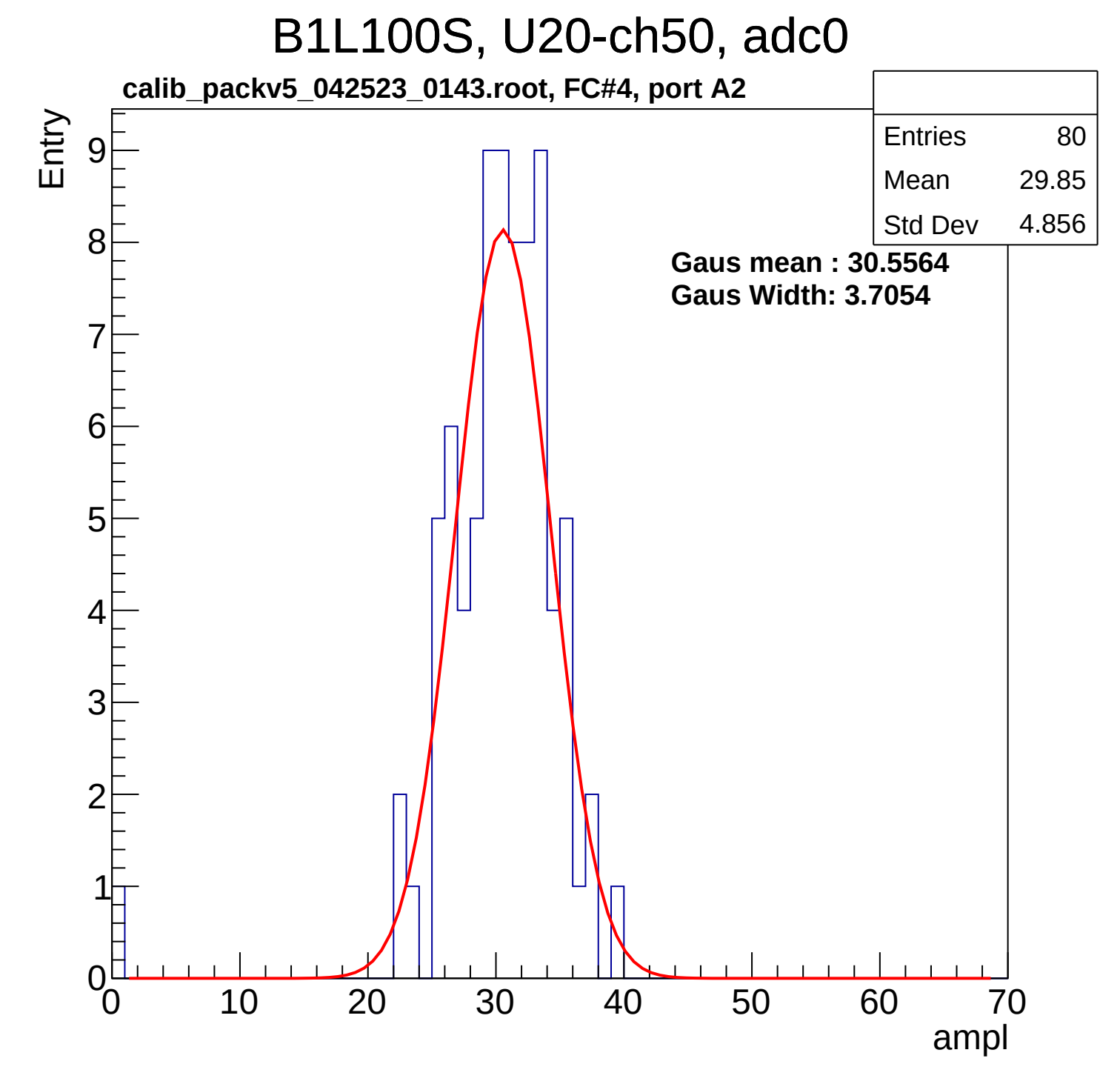
50
60
70
ampl


B1L100S, U20-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 29.85
Std Dev 4.856
Gaus mean : 30.5564
Gaus Width: 3.7054
Entry
ampl
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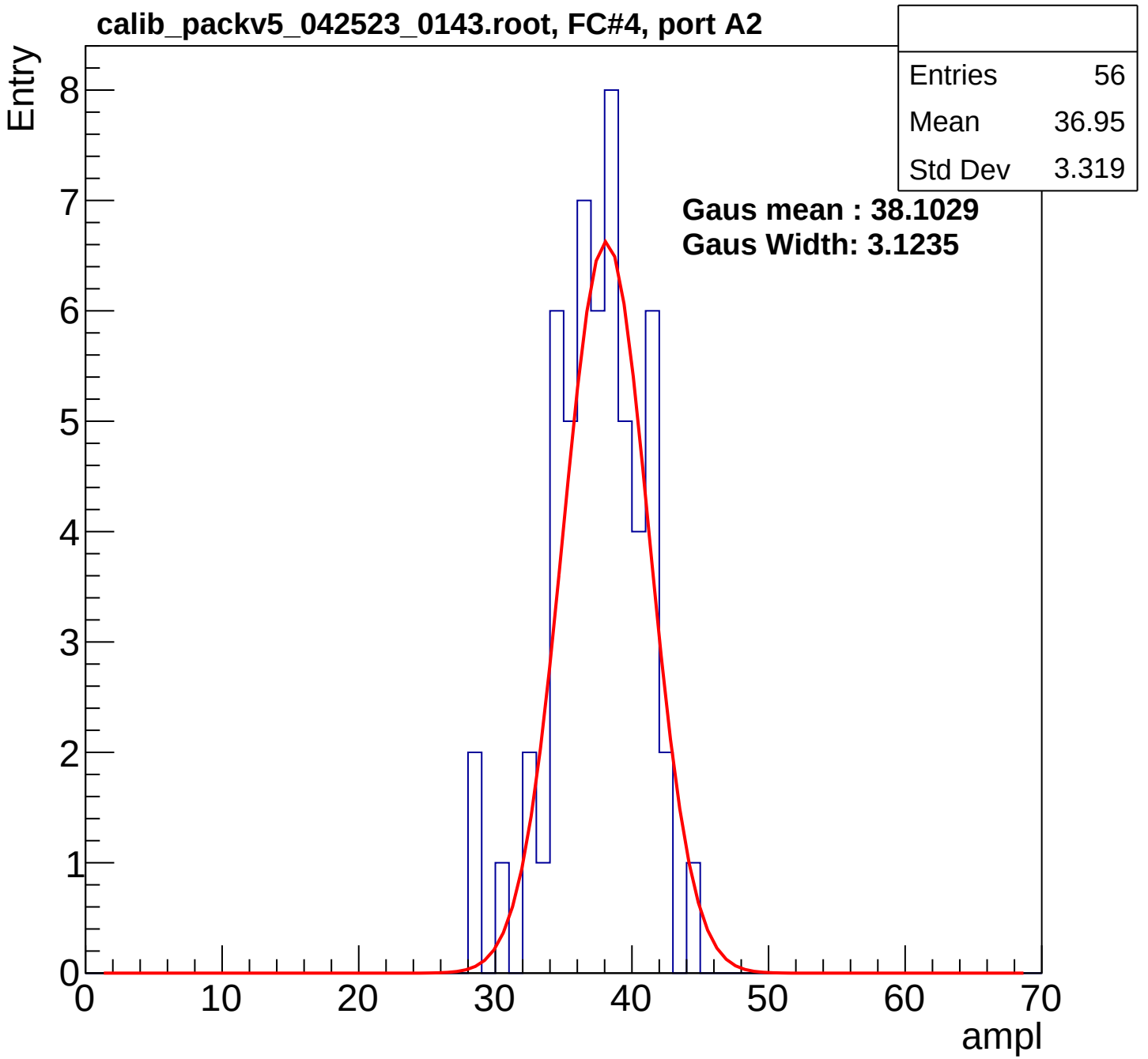
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 36.95
Std Dev 3.319
Gaus mean : 38.1029
Gaus Width: 3.1235
Entry
ampl
0
1
2
3
4
5
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7
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0
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60
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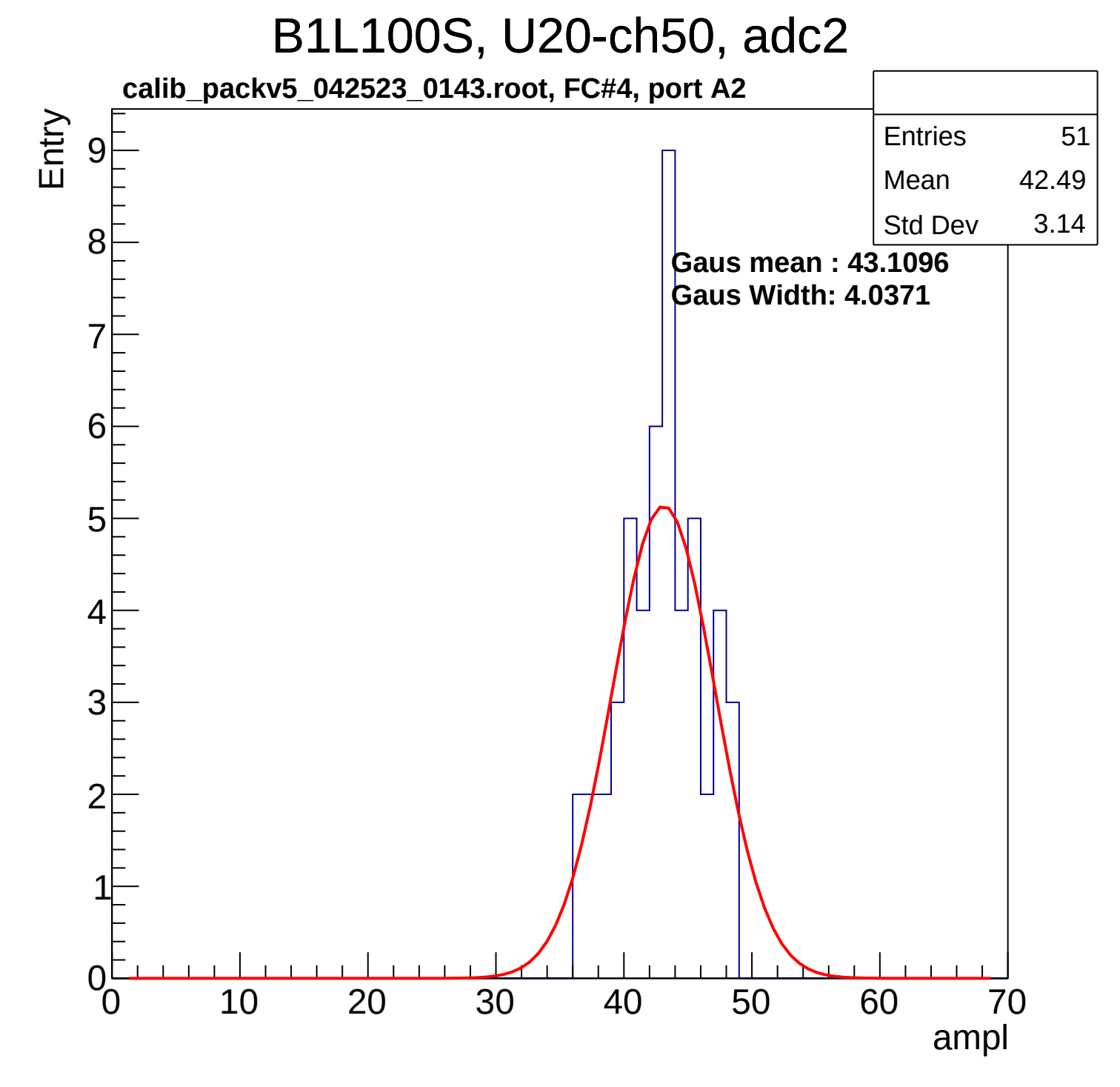
B1L100S, U20-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 42.49
Std Dev 3.14
Gaus mean : 43.1096
Gaus Width: 4.0371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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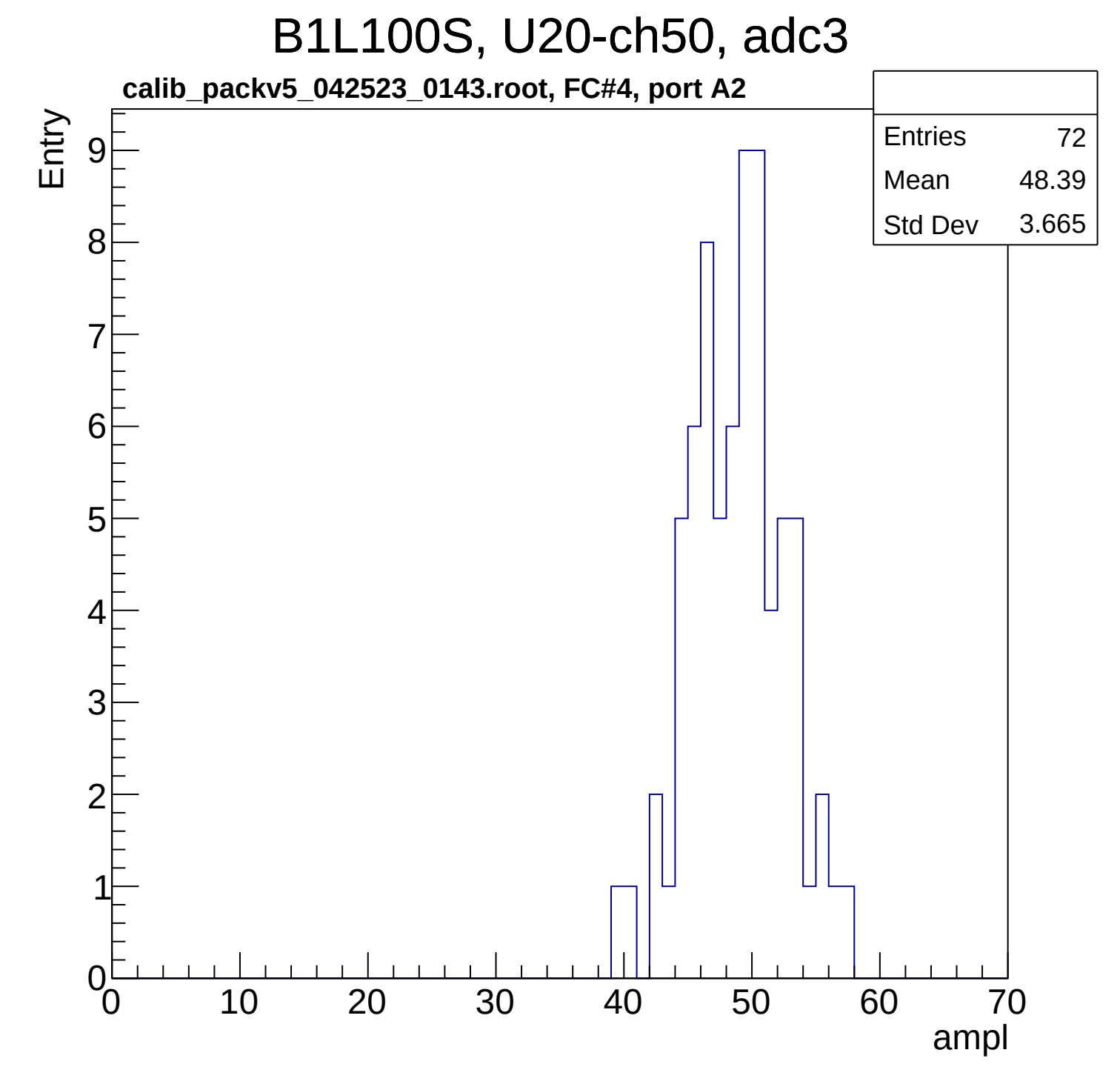
B1L100S, U20-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 48.39
Std Dev 3.665
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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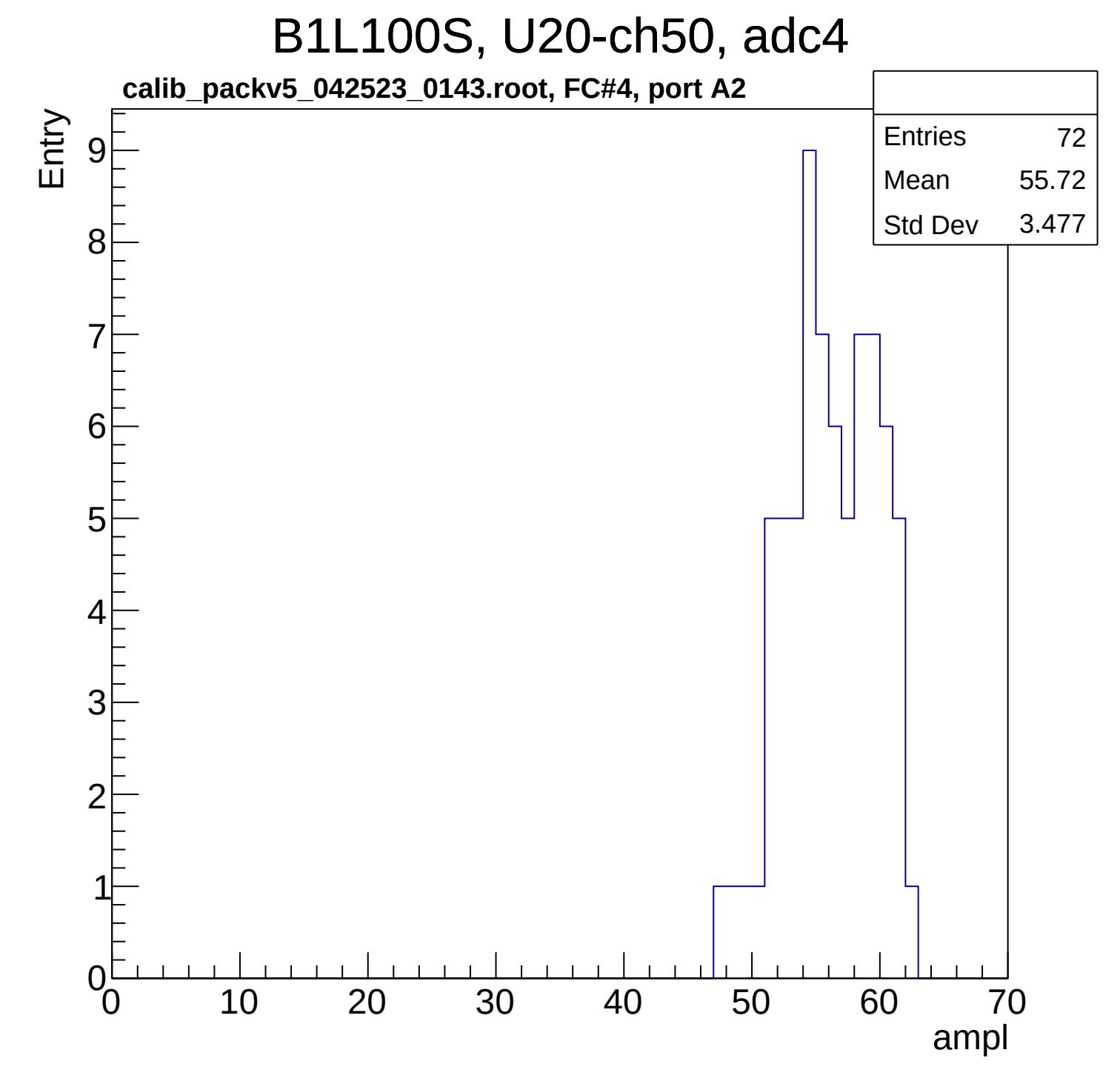
B1L100S, U20-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 55.72
Std Dev 3.477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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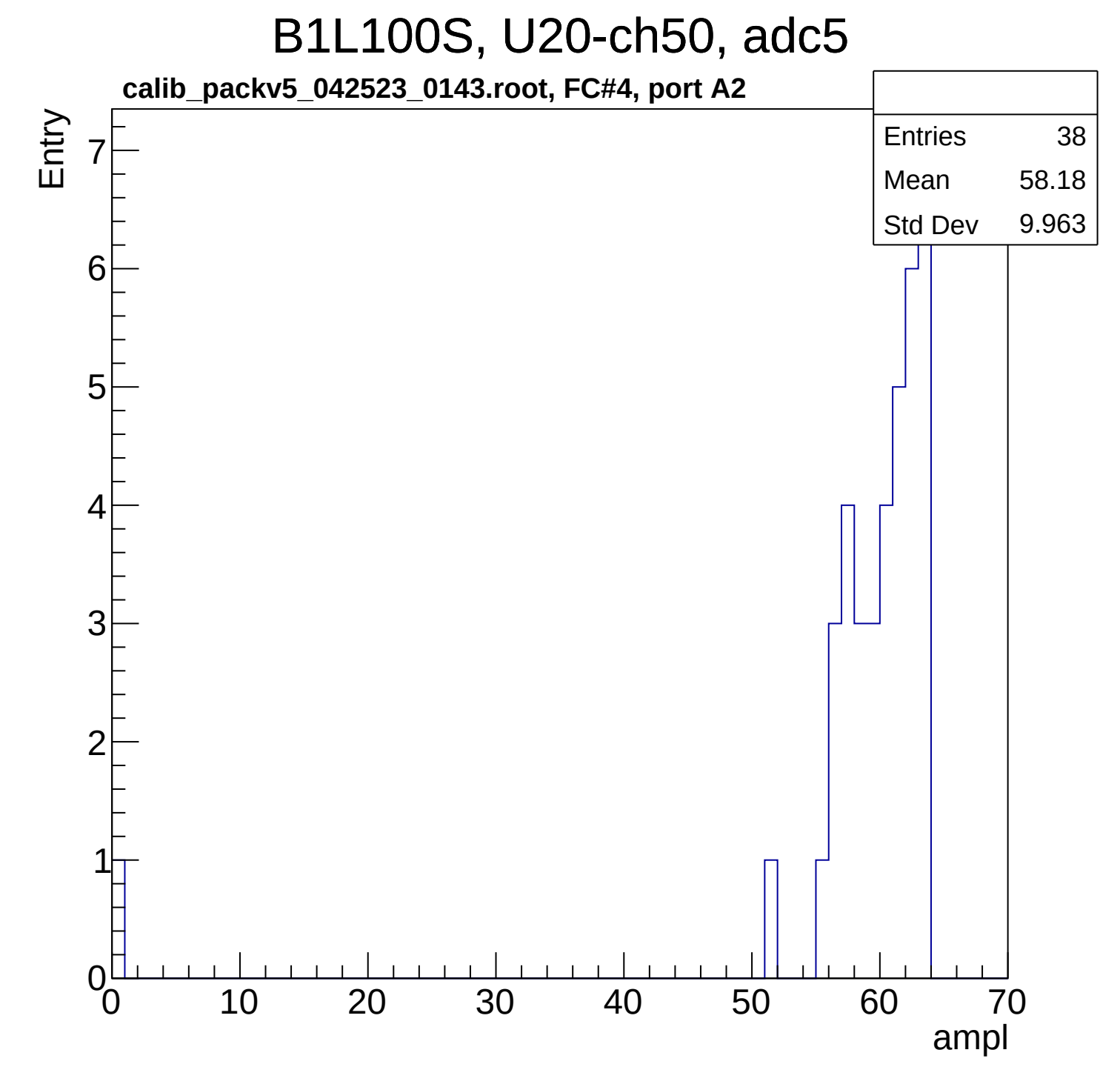
B1L100S, U20-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.18
Std Dev 9.963
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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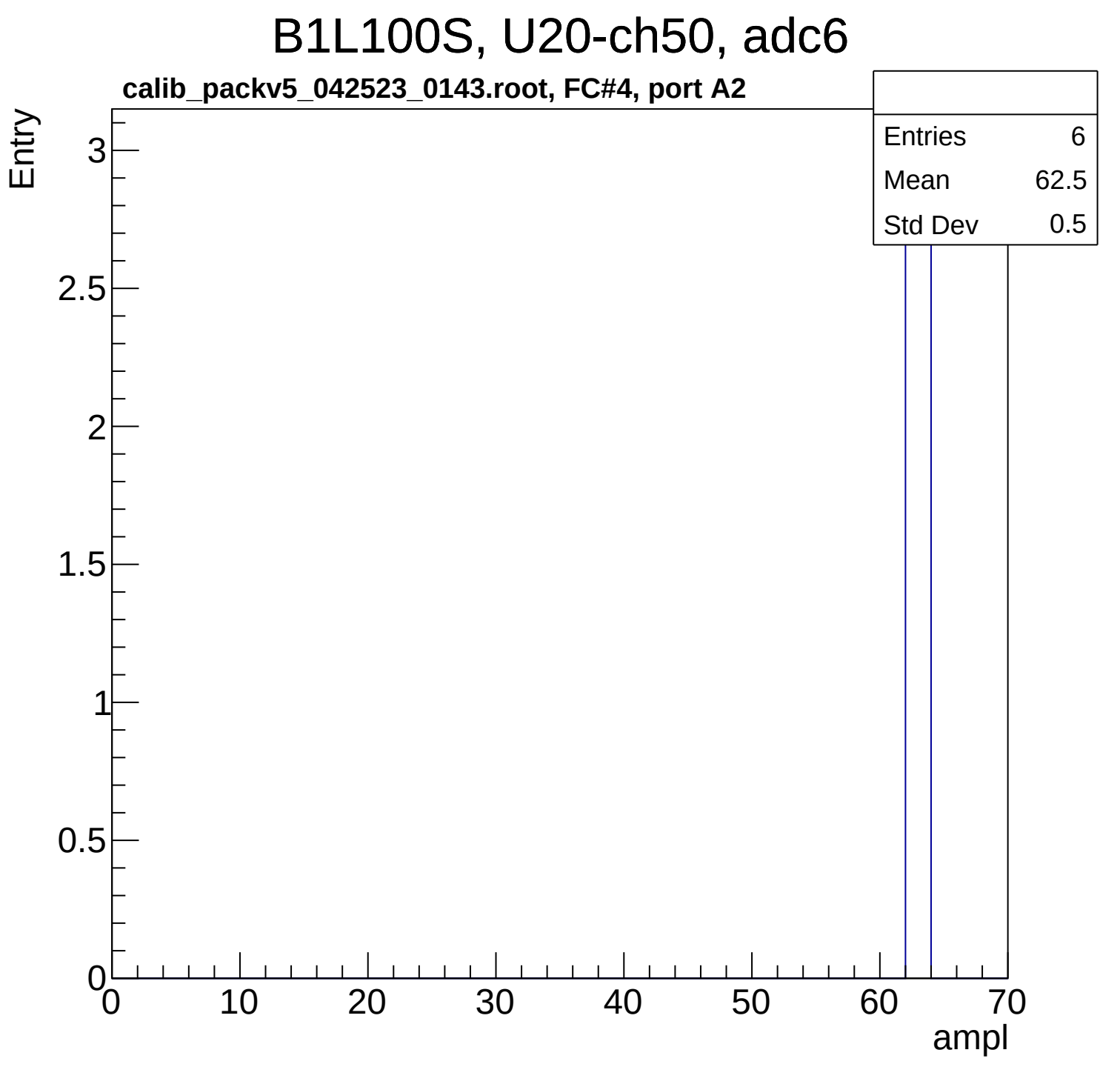
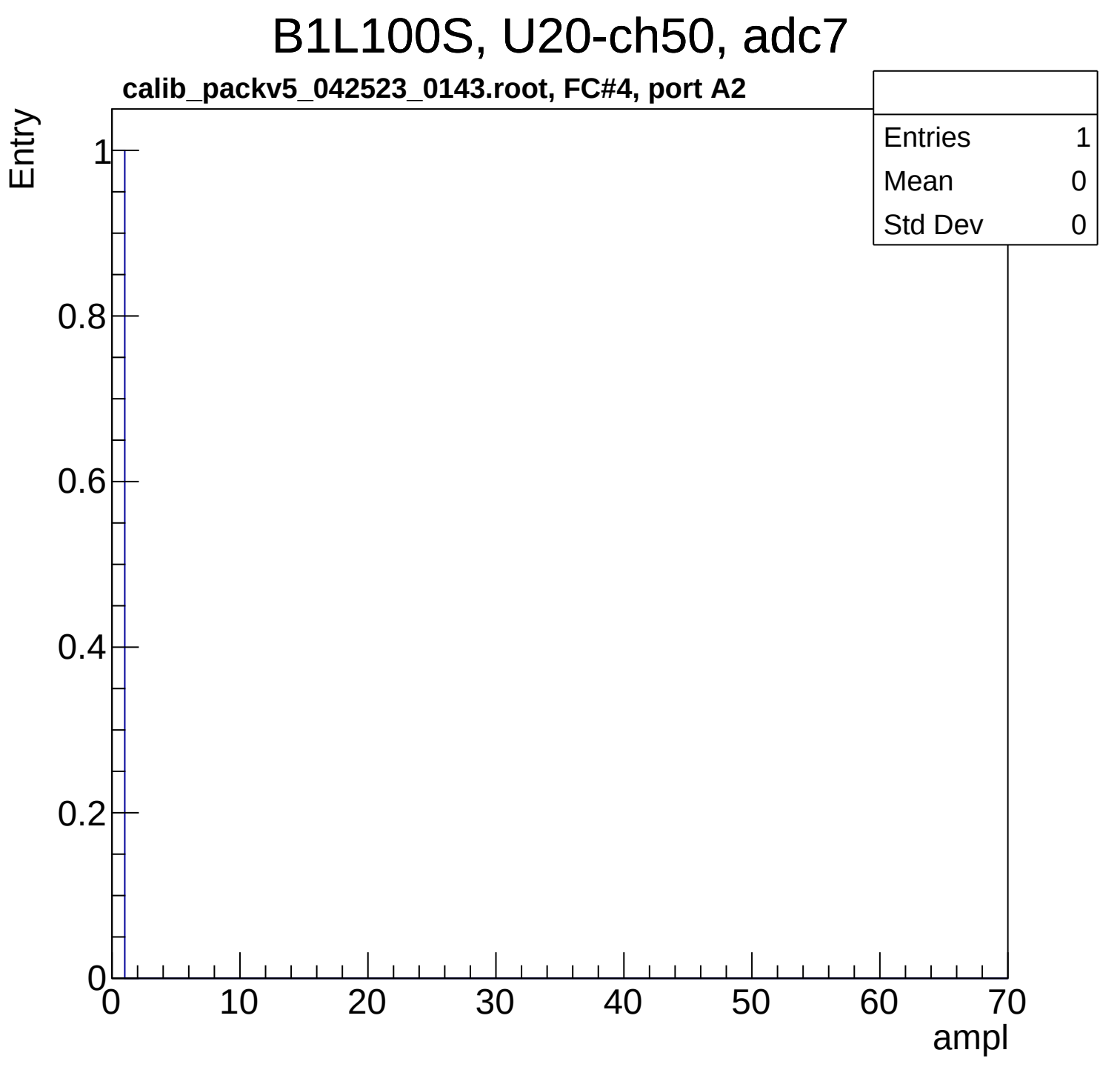
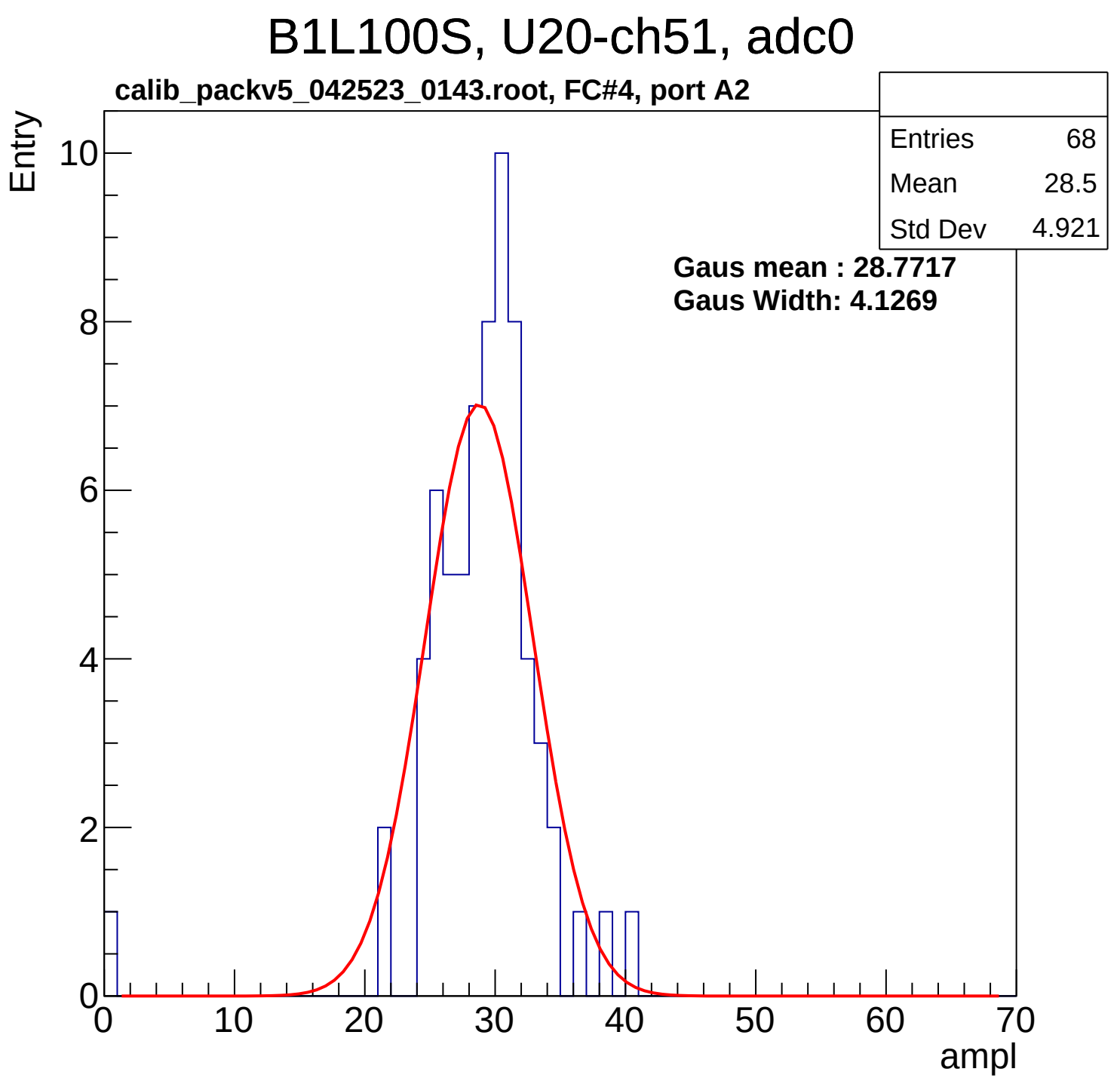
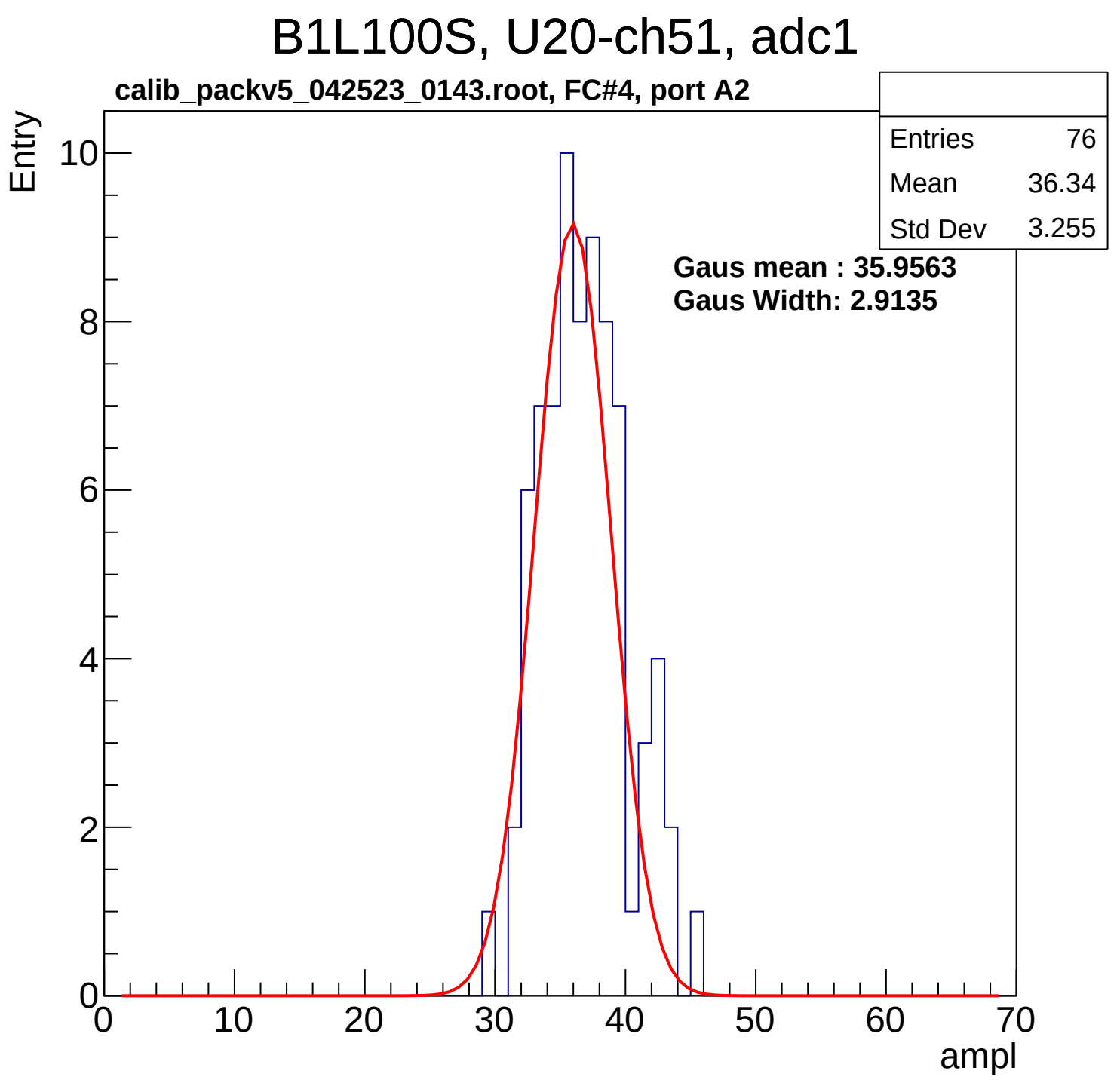
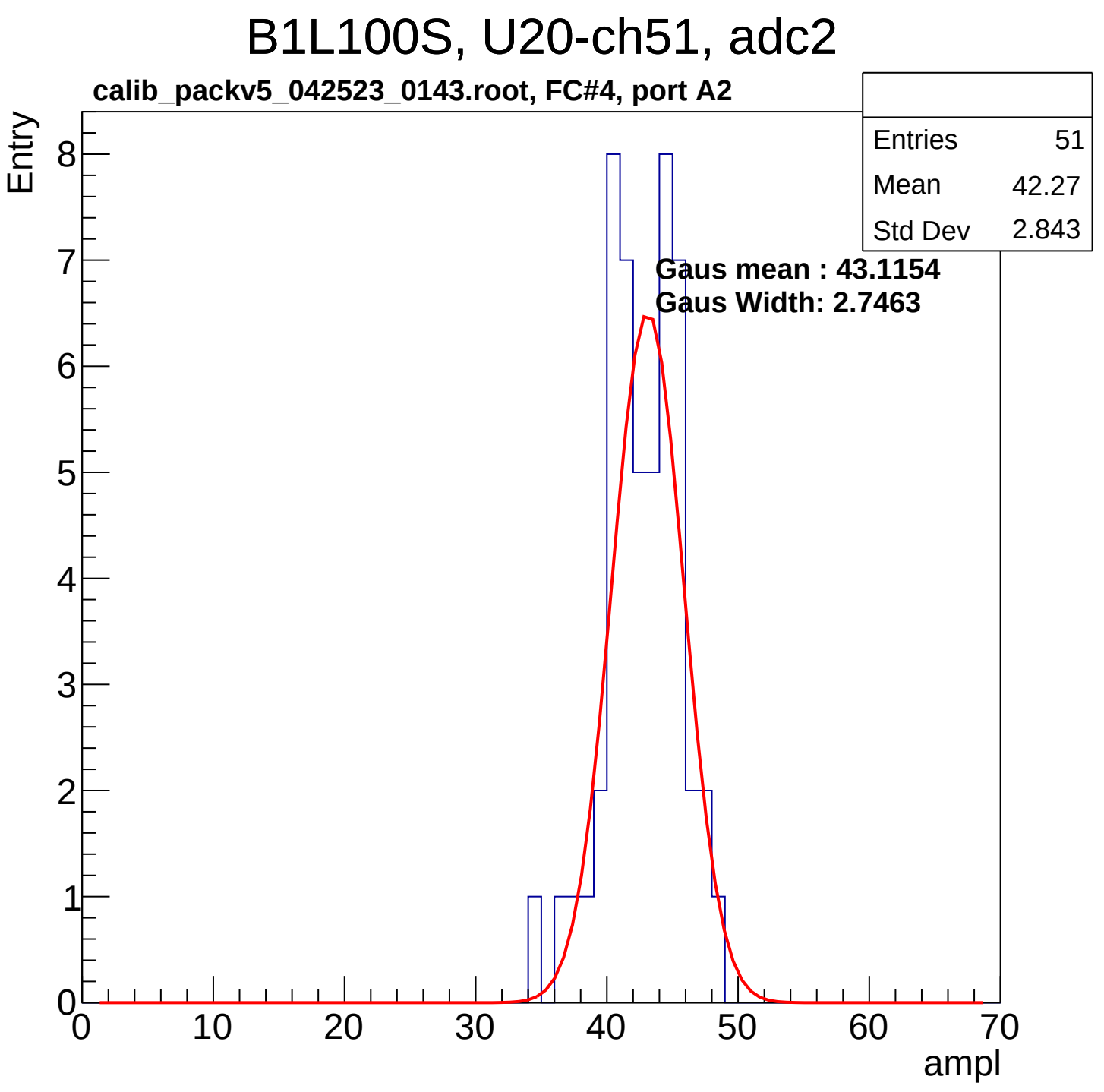
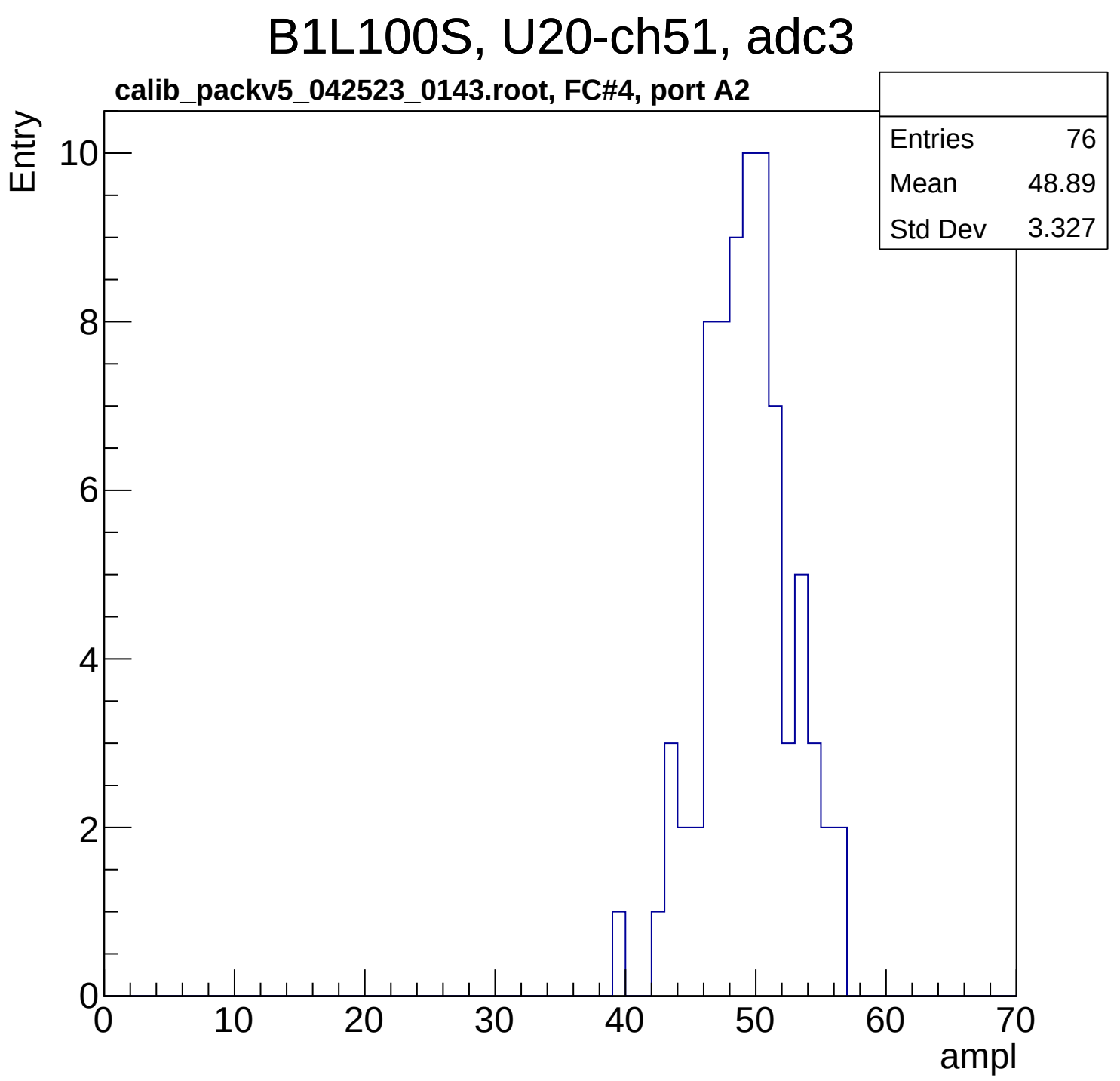

B1L100S, U20-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

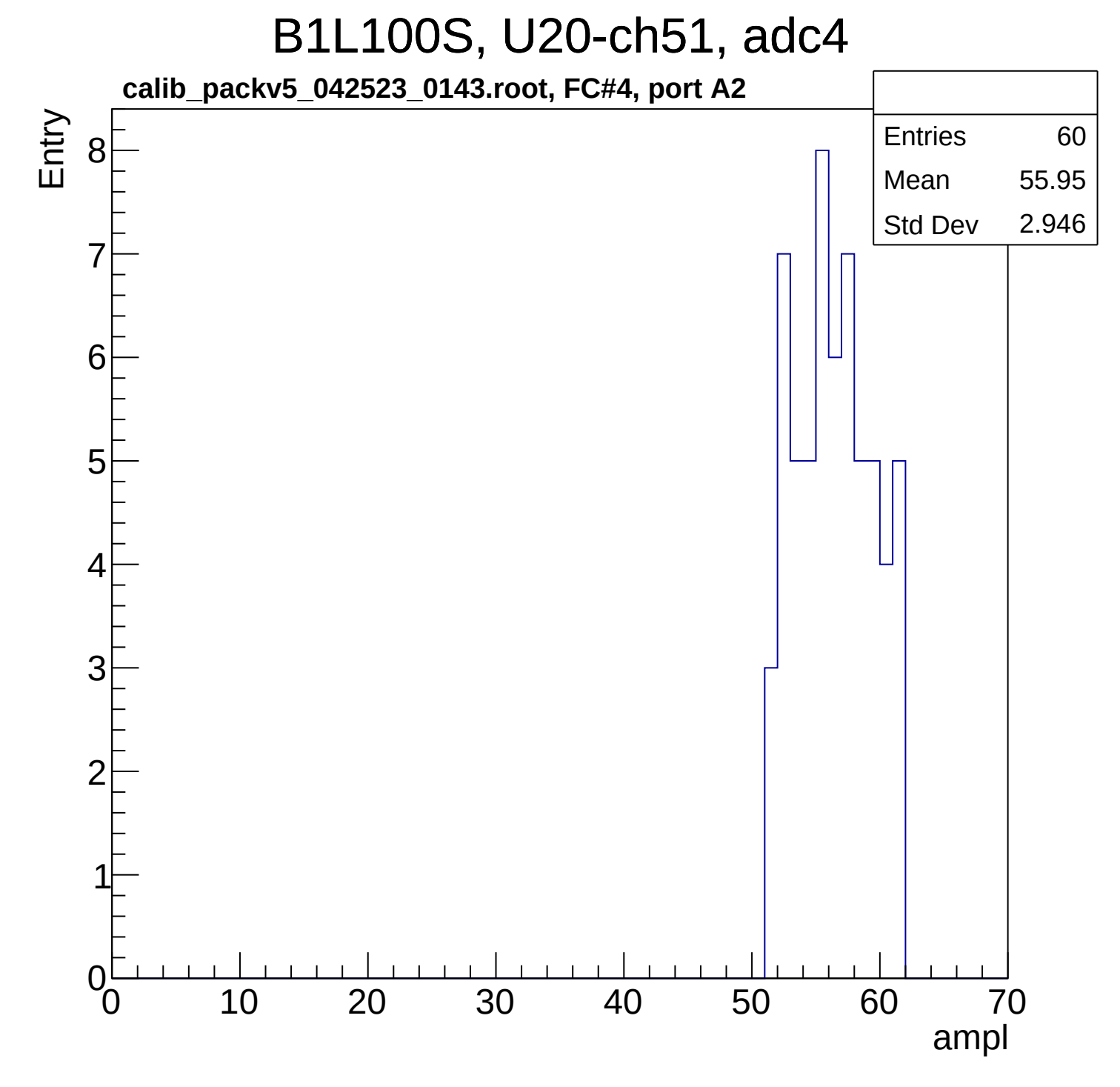

B1L100S, U20-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

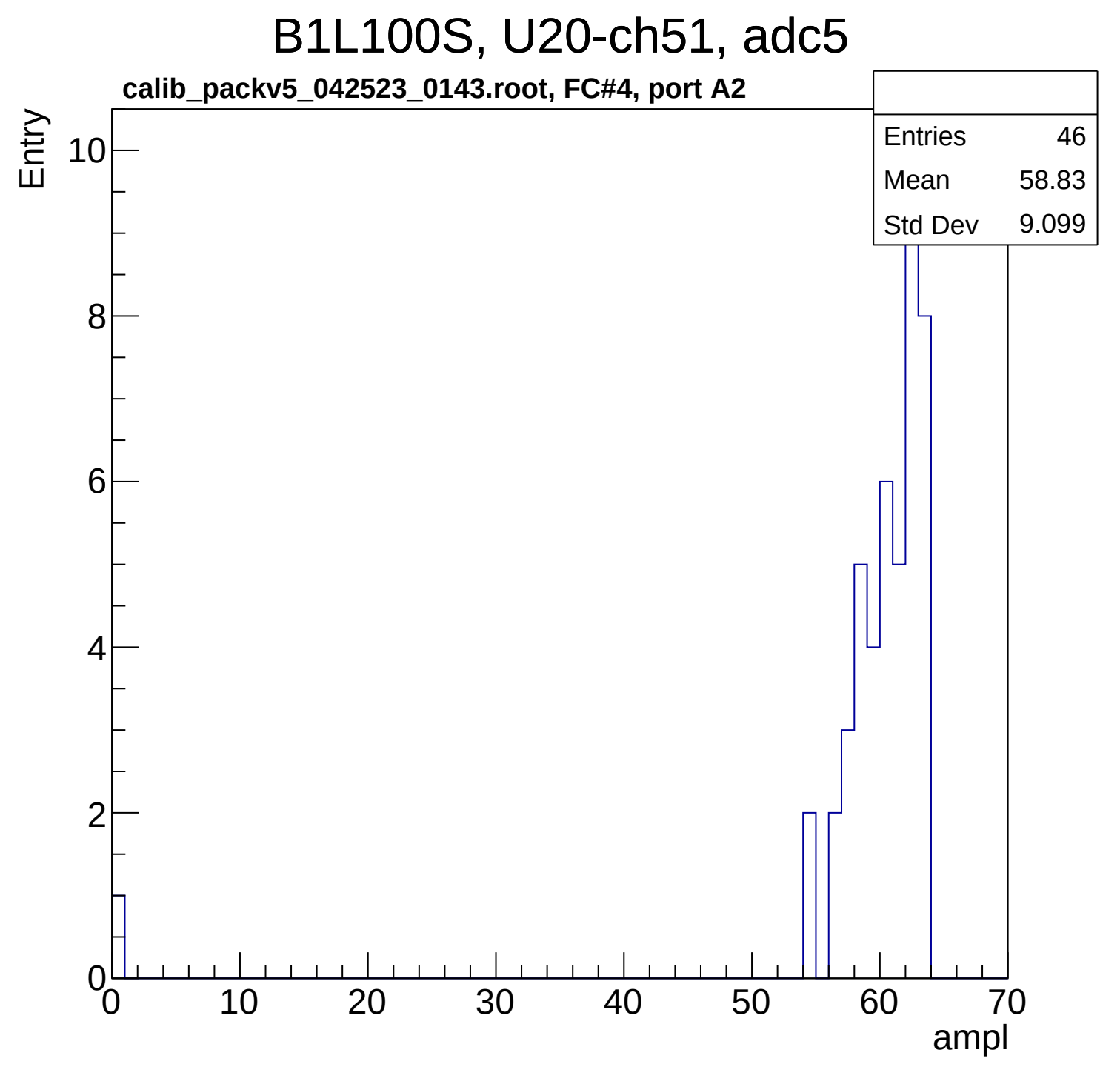

B1L100S, U20-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 28.5
Std Dev 4.921
Gaus mean : 28.7717
Gaus Width: 4.1269
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.34
Std Dev 3.255
Gaus mean : 35.9563
Gaus Width: 2.9135
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 42.27
Std Dev 2.843
Gaus mean : 43.1154
Gaus Width: 2.7463
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch51, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 48.89
Std Dev 3.327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch51, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.95
Std Dev 2.946
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.83
Std Dev 9.099
Entry
ampl
0
2
4
6
8
10
0
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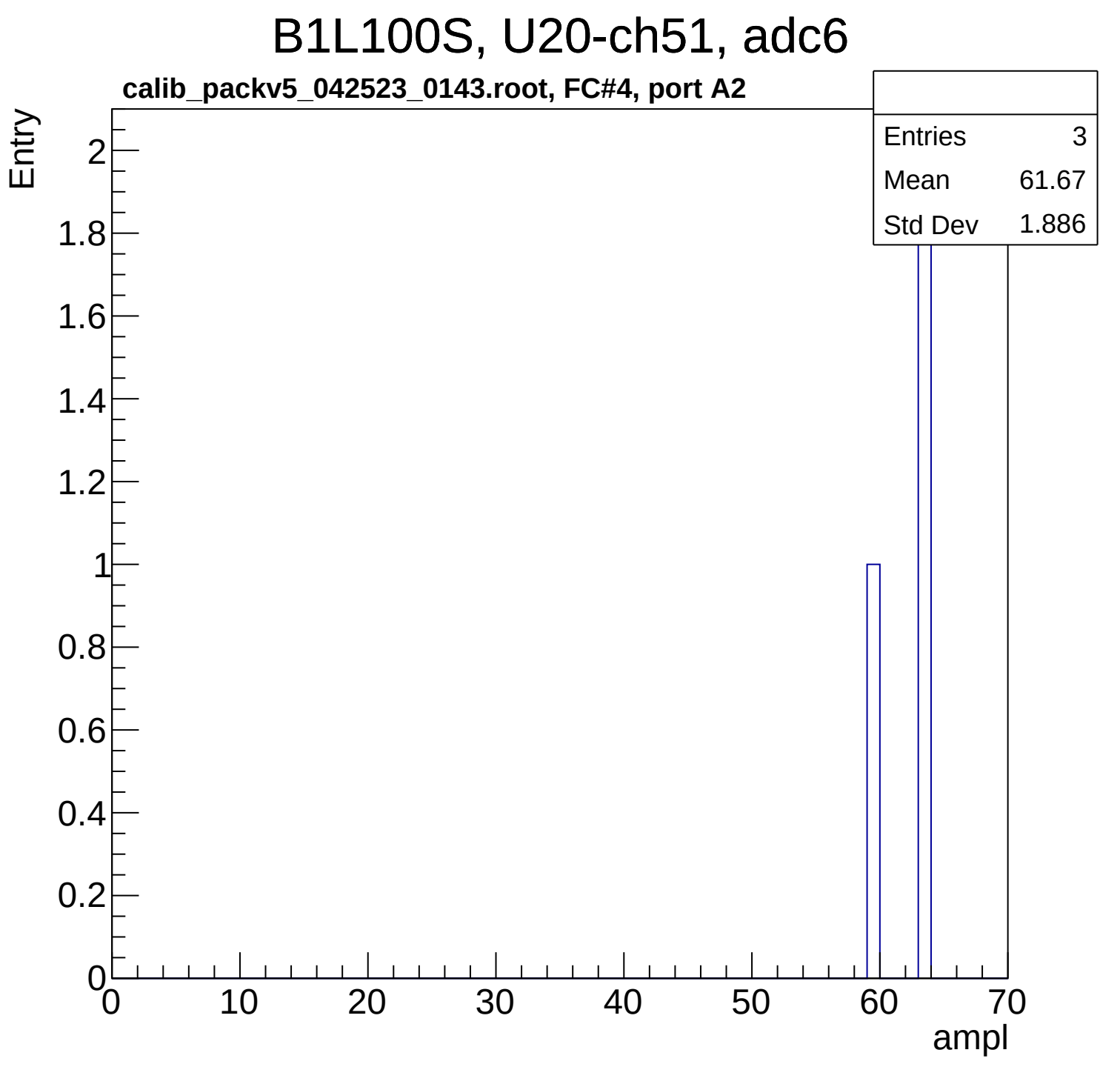
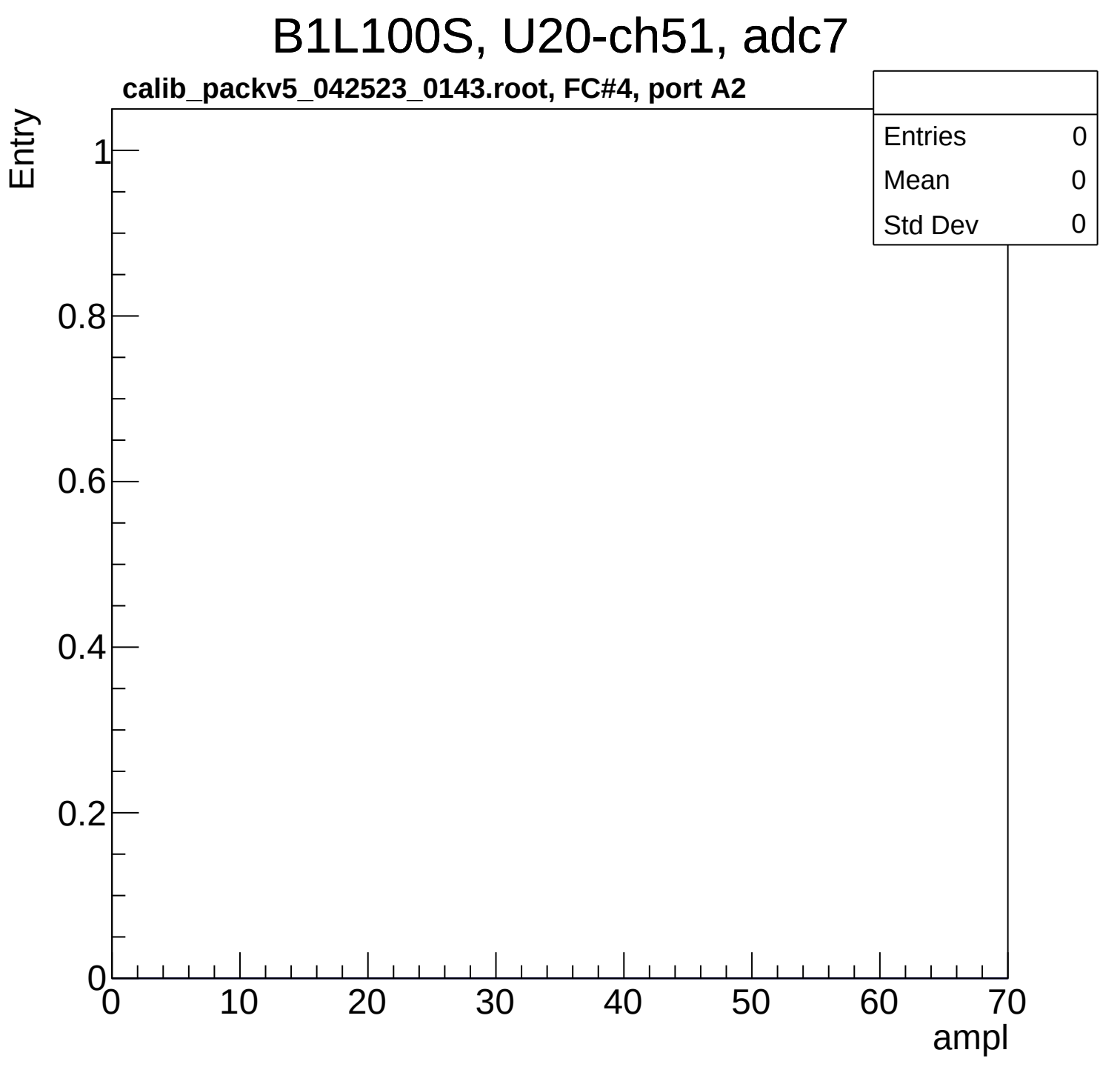
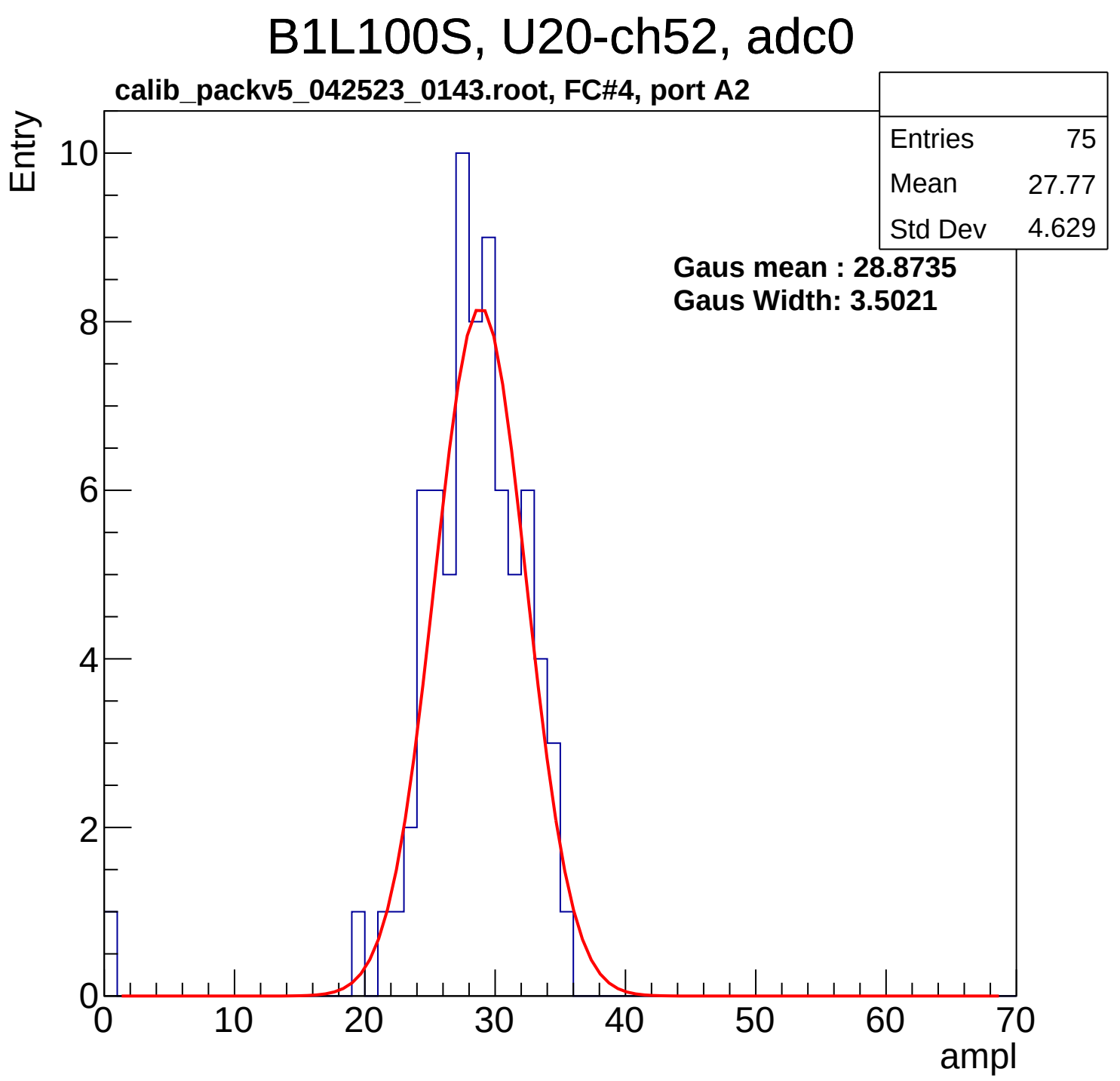
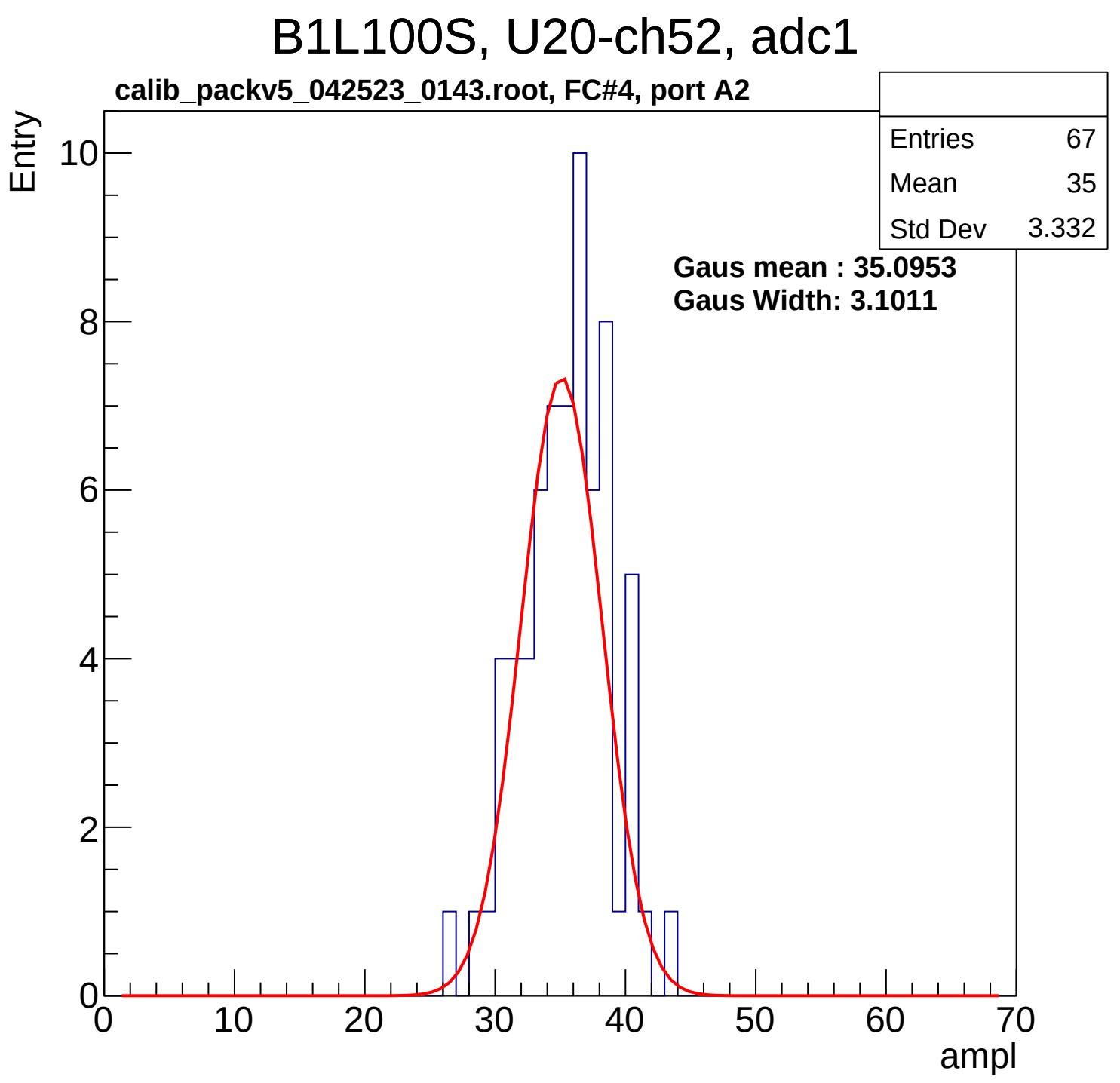
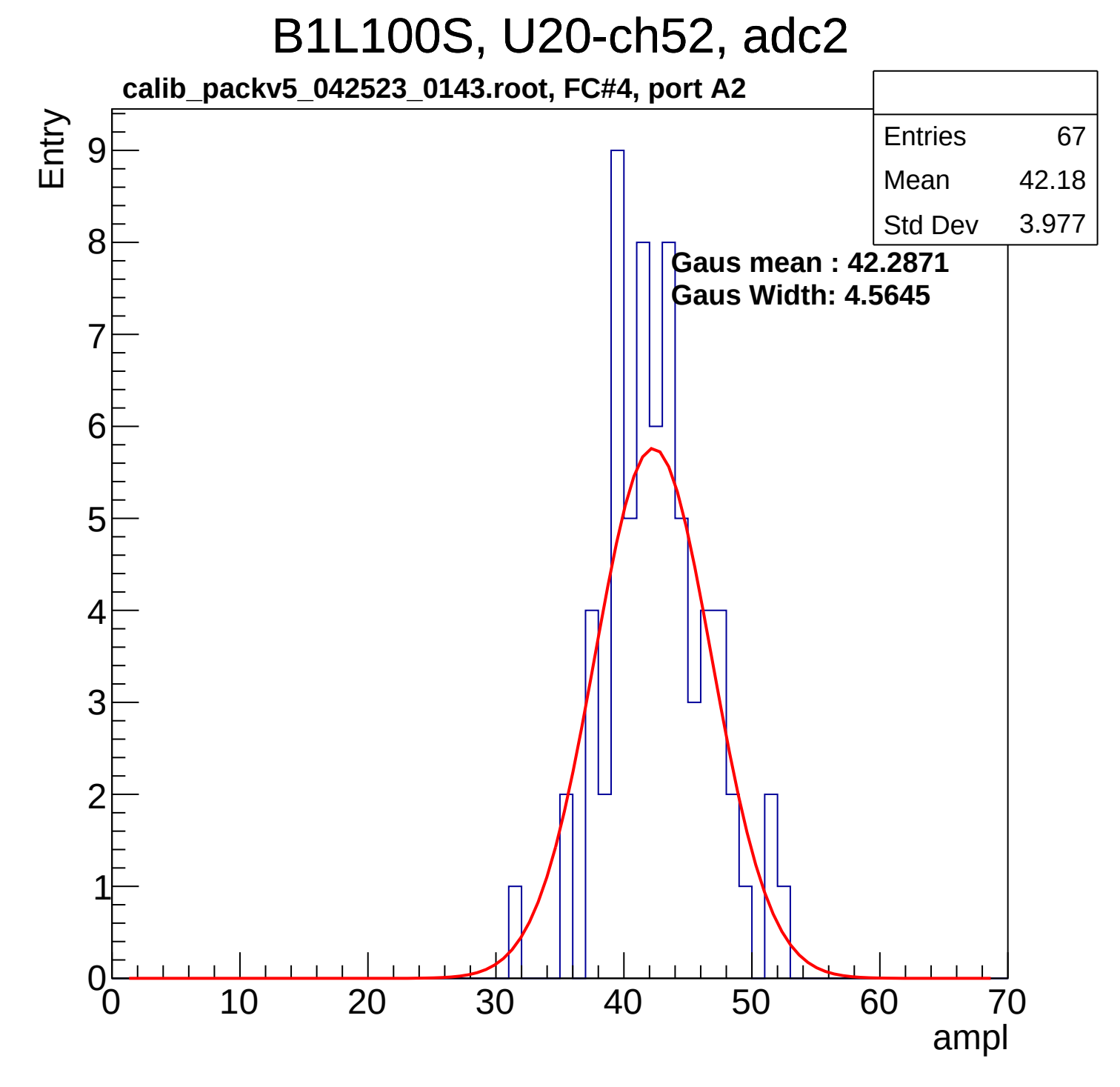
40
50
60
70


B1L100S, U20-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 27.77
Std Dev 4.629
Gaus mean : 28.8735
Gaus Width: 3.5021
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 35
Std Dev 3.332
Gaus mean : 35.0953
Gaus Width: 3.1011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 42.18
Std Dev 3.977
Gaus mean : 42.2871
Gaus Width: 4.5645
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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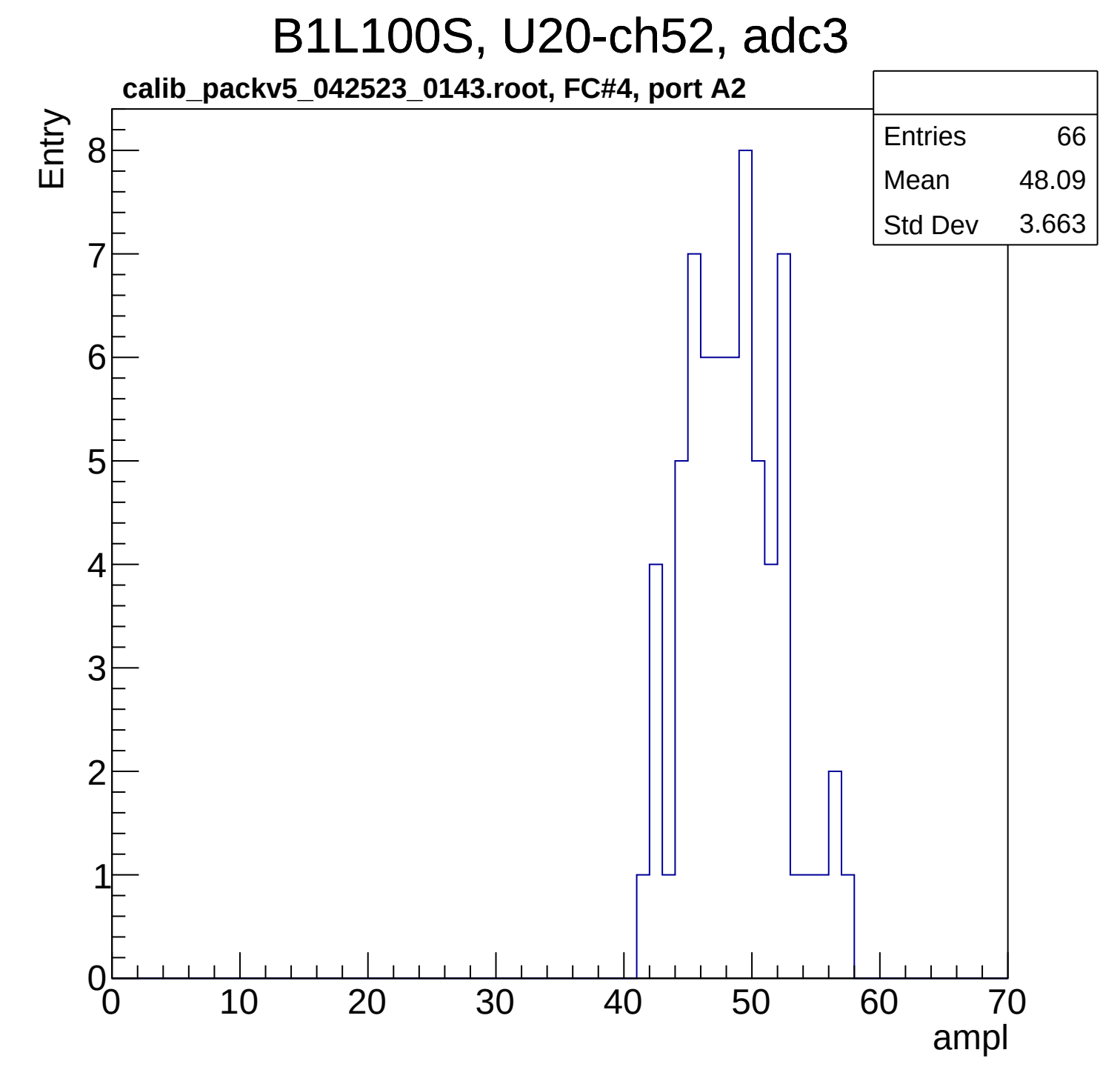
B1L100S, U20-ch52, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 48.09
Std Dev 3.663
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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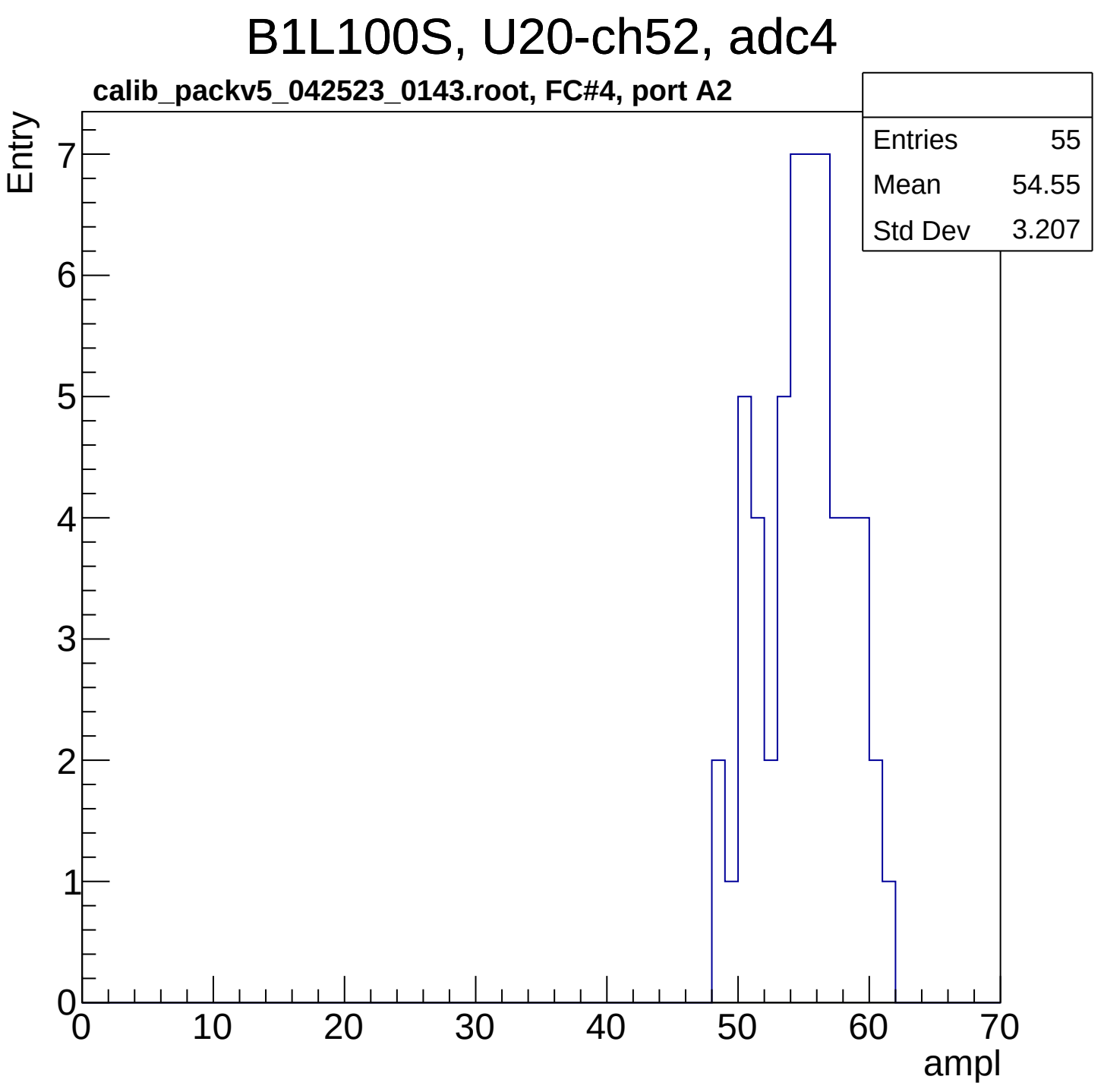
60
70


B1L100S, U20-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 54.55
Std Dev 3.207
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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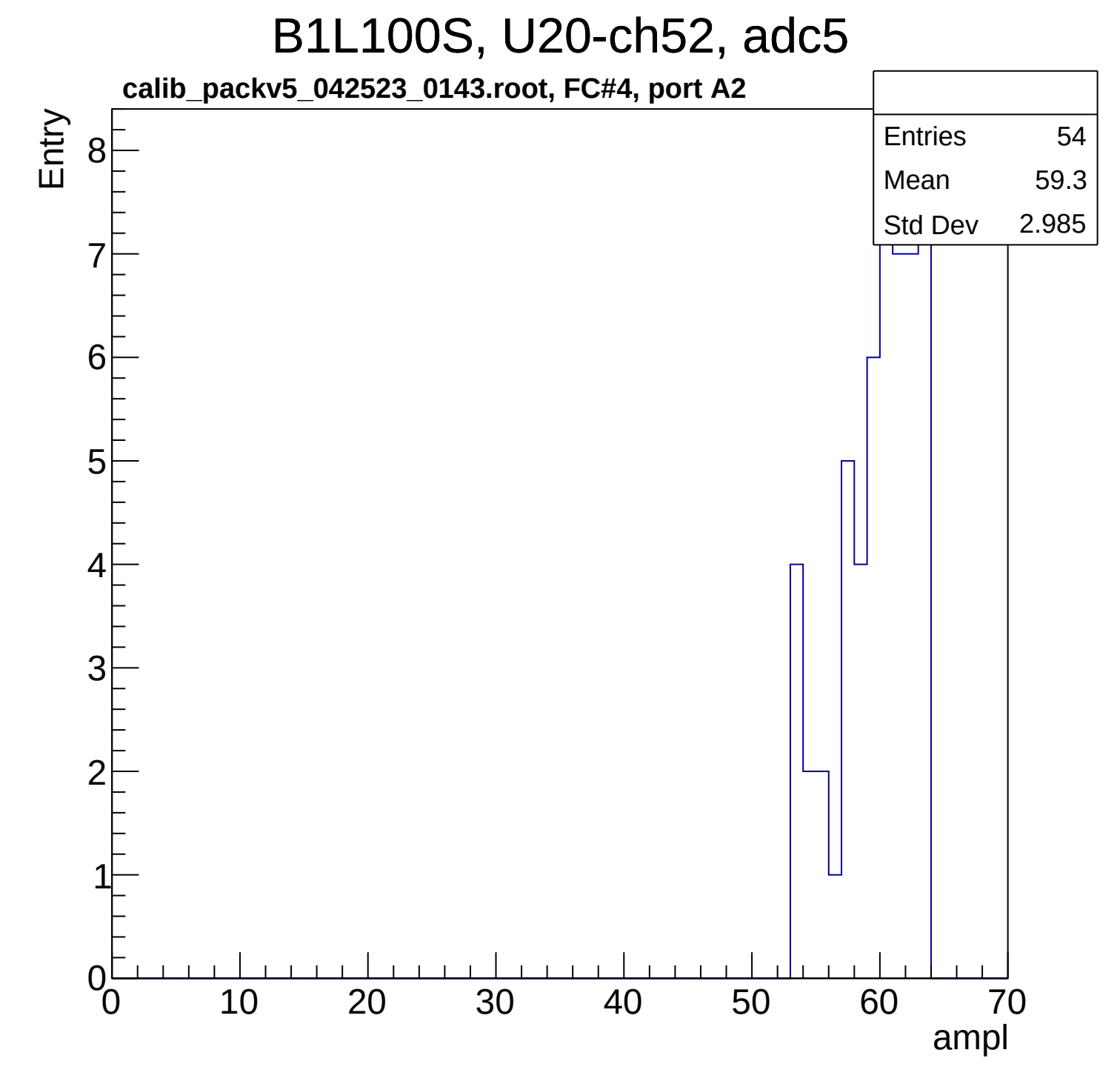
B1L100S, U20-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 59.3
Std Dev 2.985
Entry
ampl
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1
2
3
4
5
6
7
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0
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20
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40
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60
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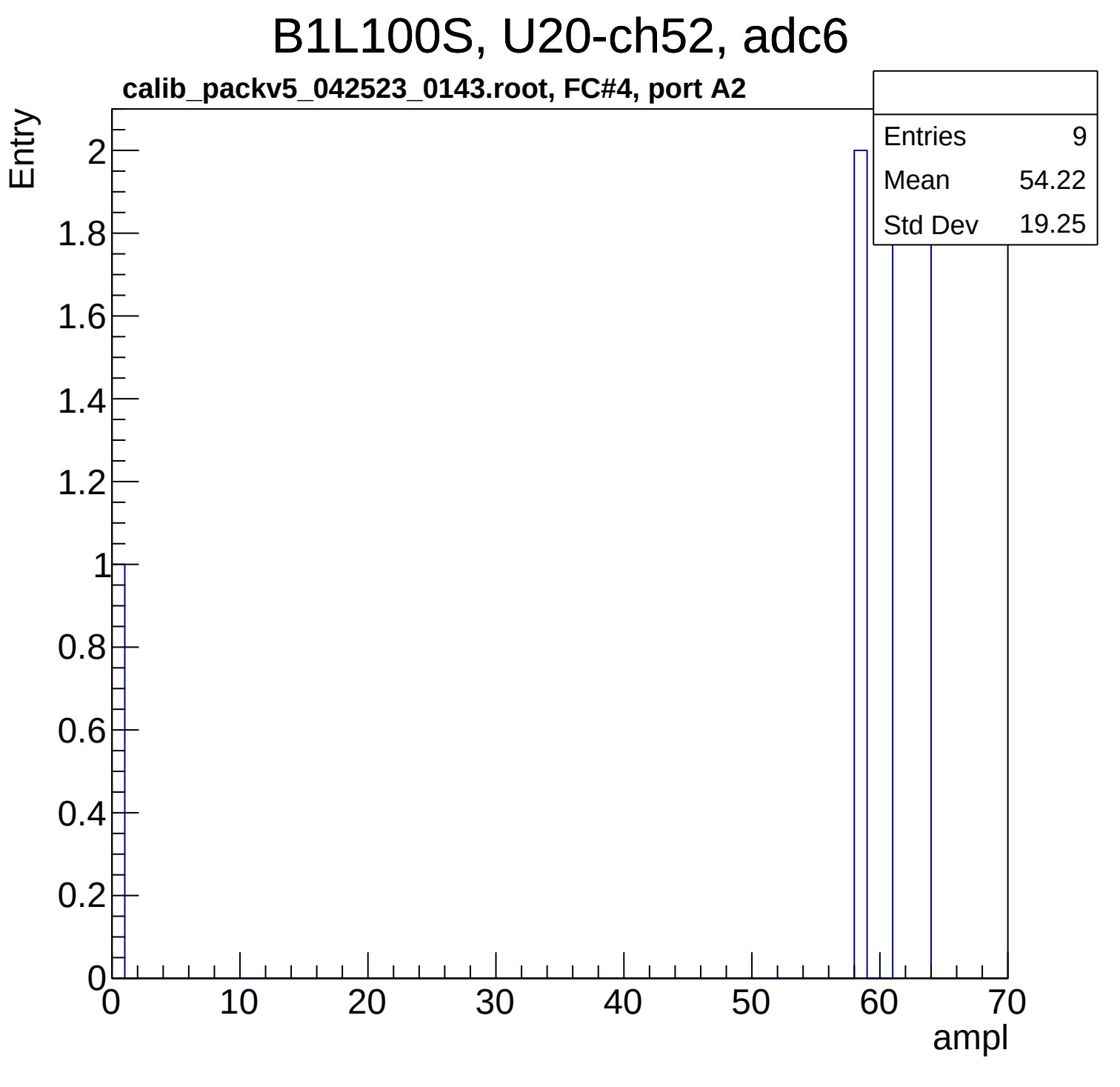
B1L100S, U20-ch52, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 54.22
Std Dev 19.25
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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40
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60
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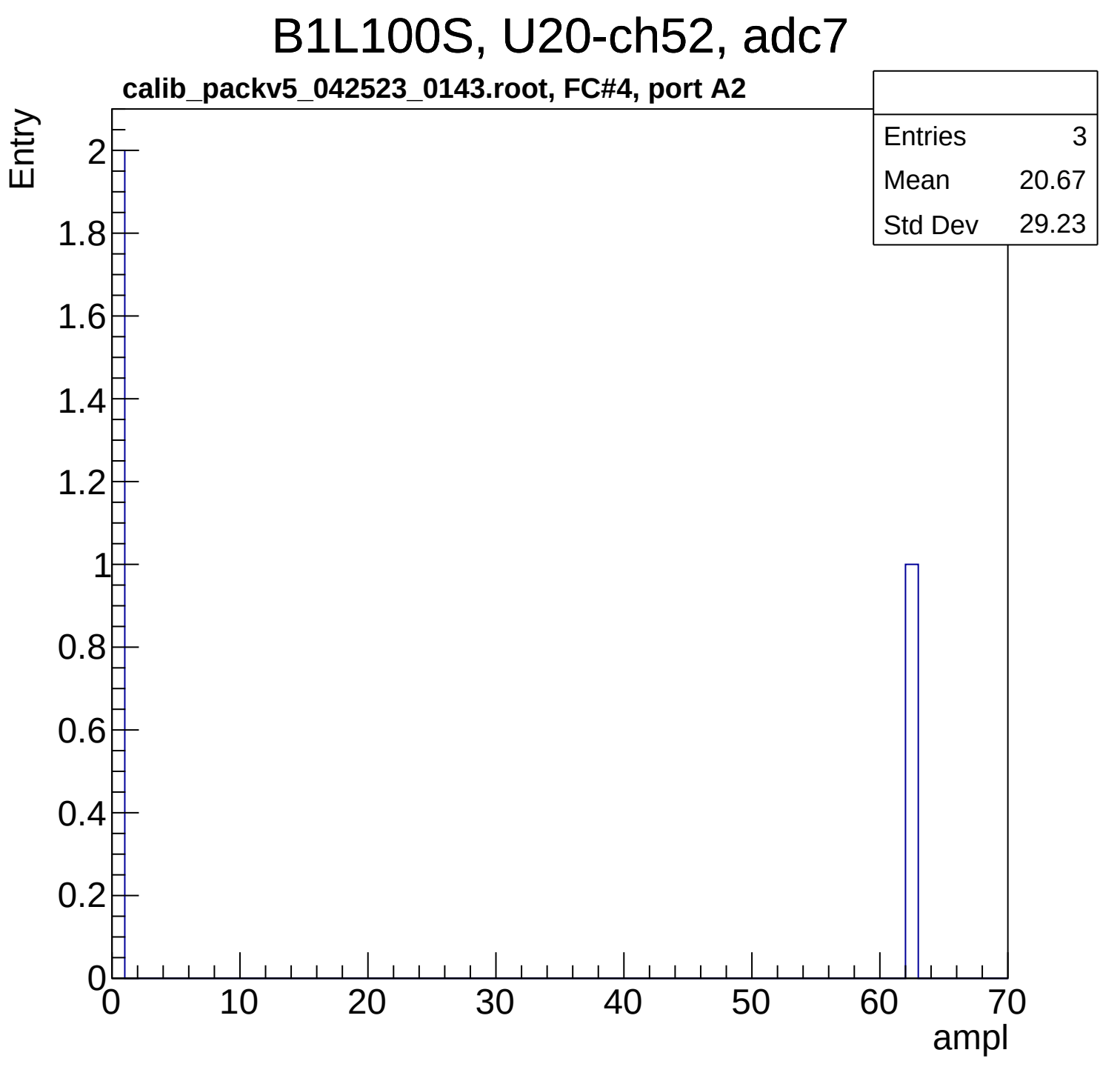
B1L100S, U20-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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60
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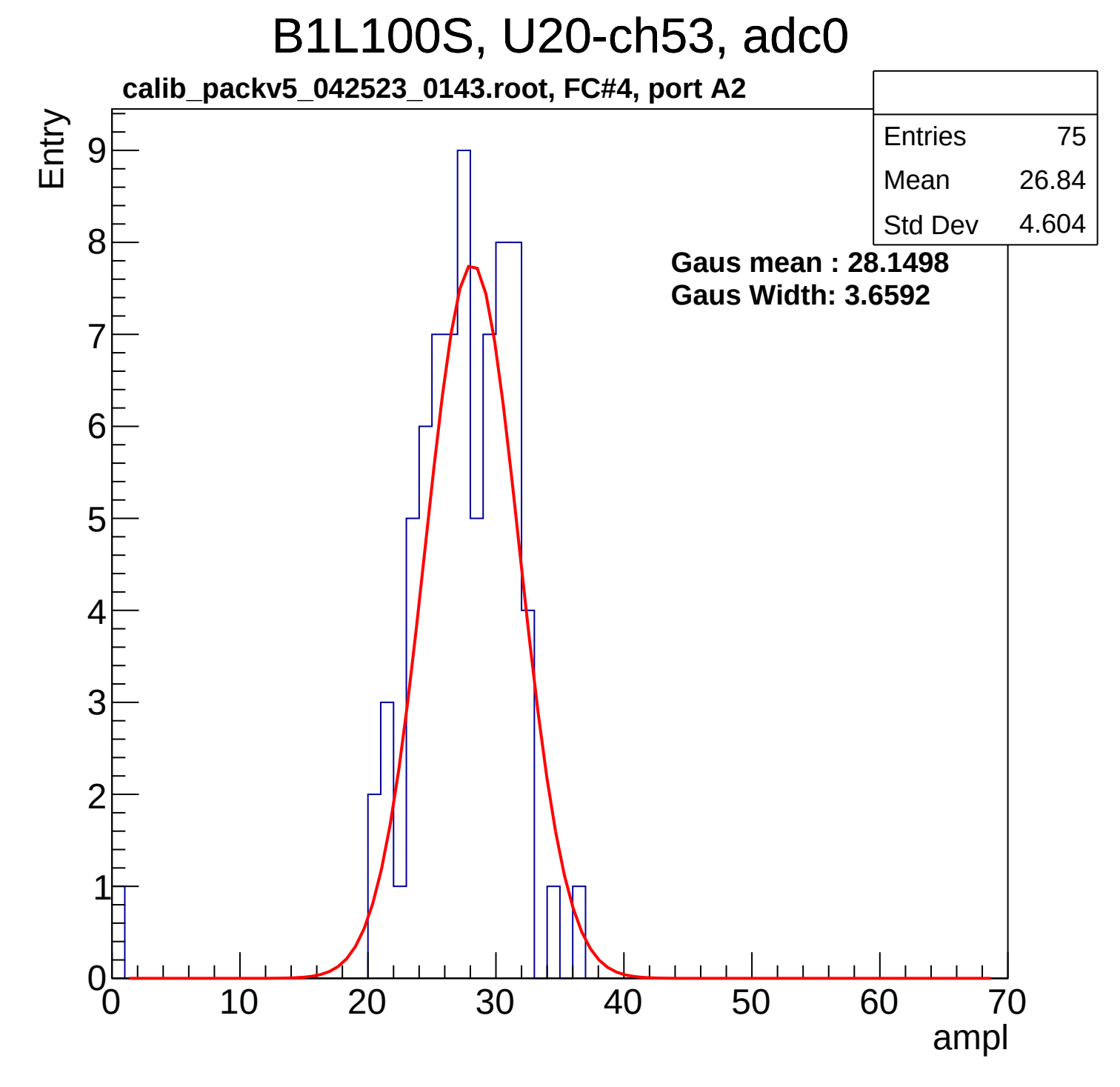
B1L100S, U20-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 26.84
Std Dev 4.604
Gaus mean : 28.1498
Gaus Width: 3.6592
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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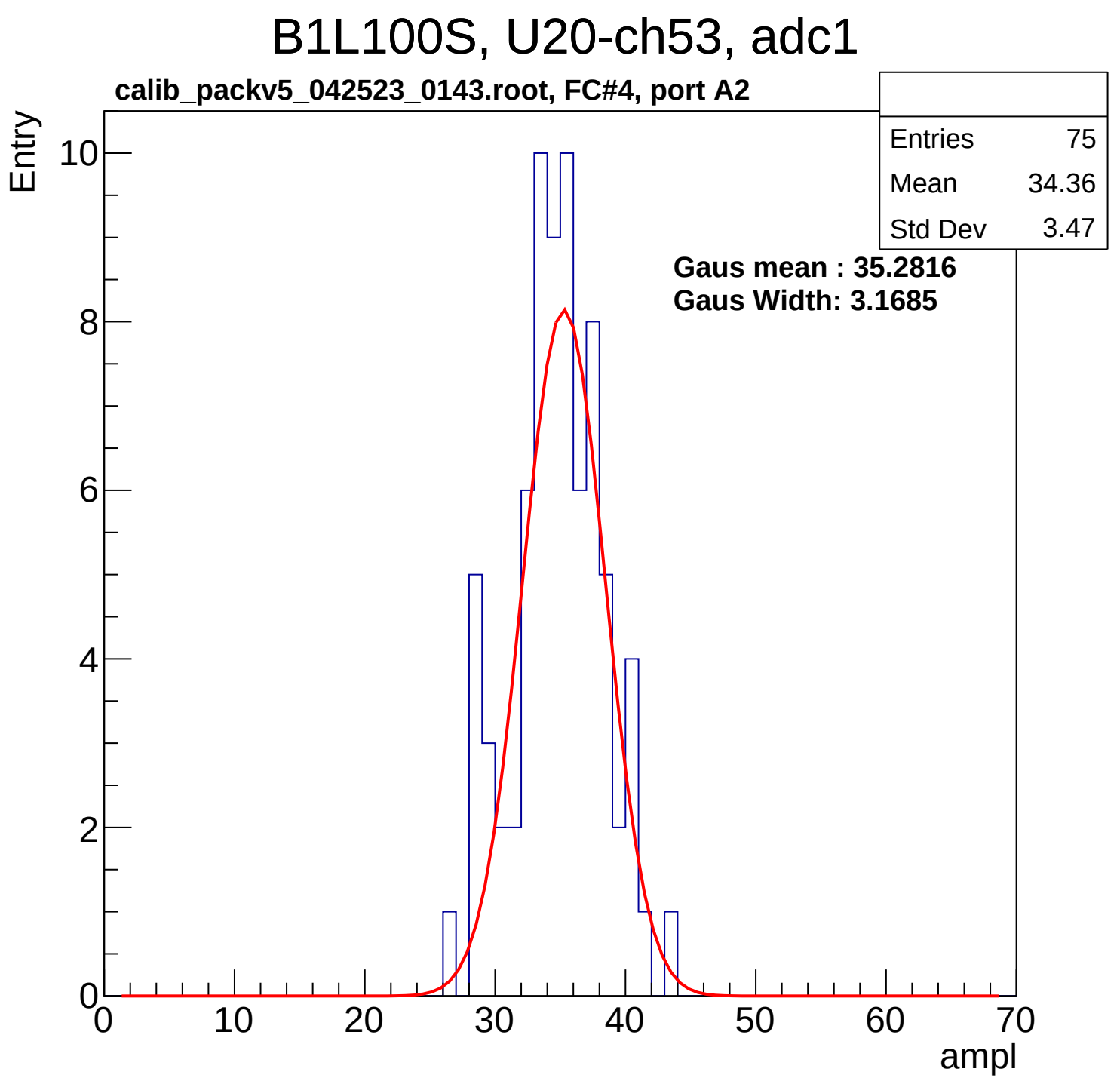
40
50
60
70


B1L100S, U20-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 34.36
Std Dev 3.47
Gaus mean : 35.2816
Gaus Width: 3.1685
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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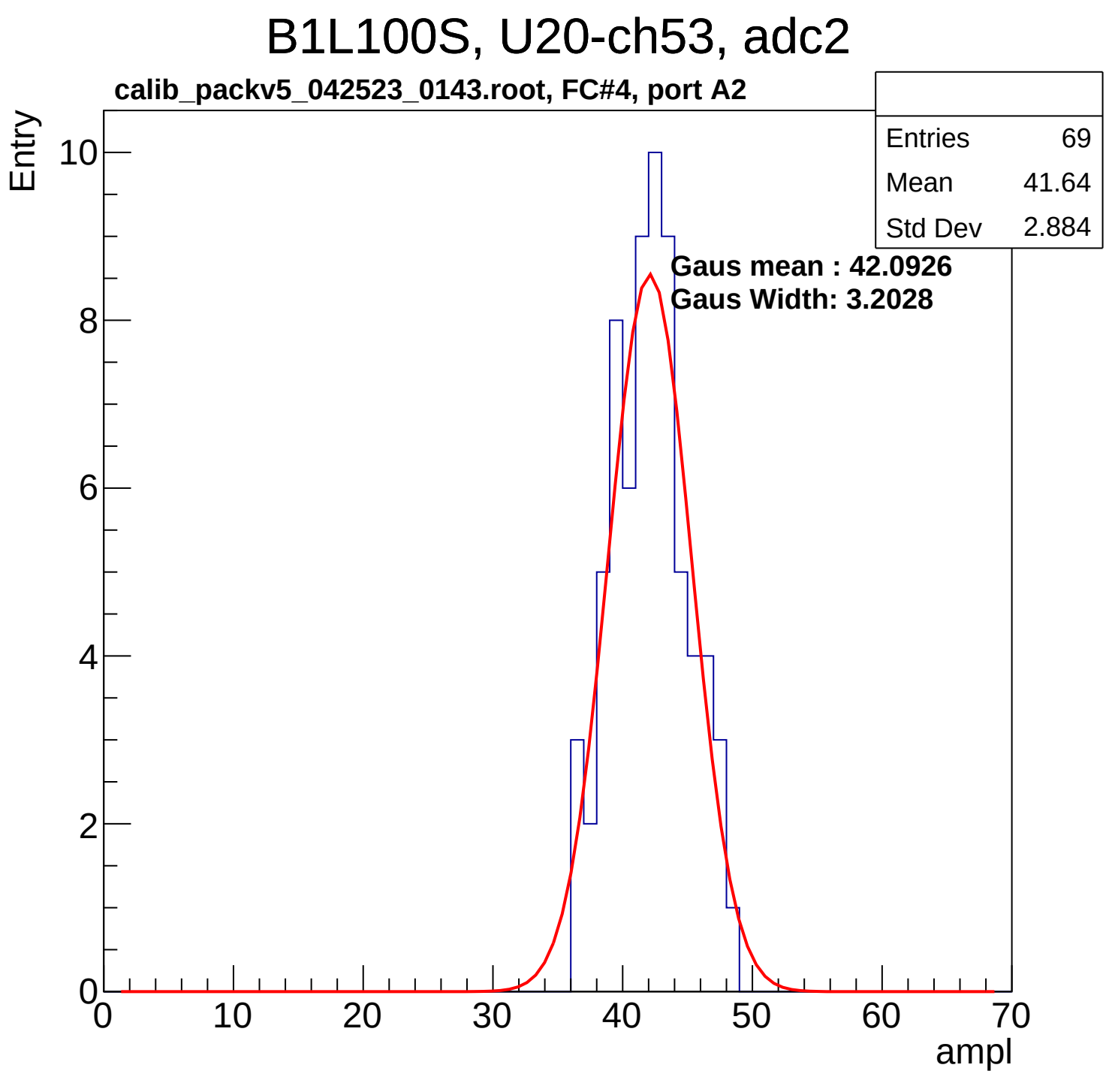
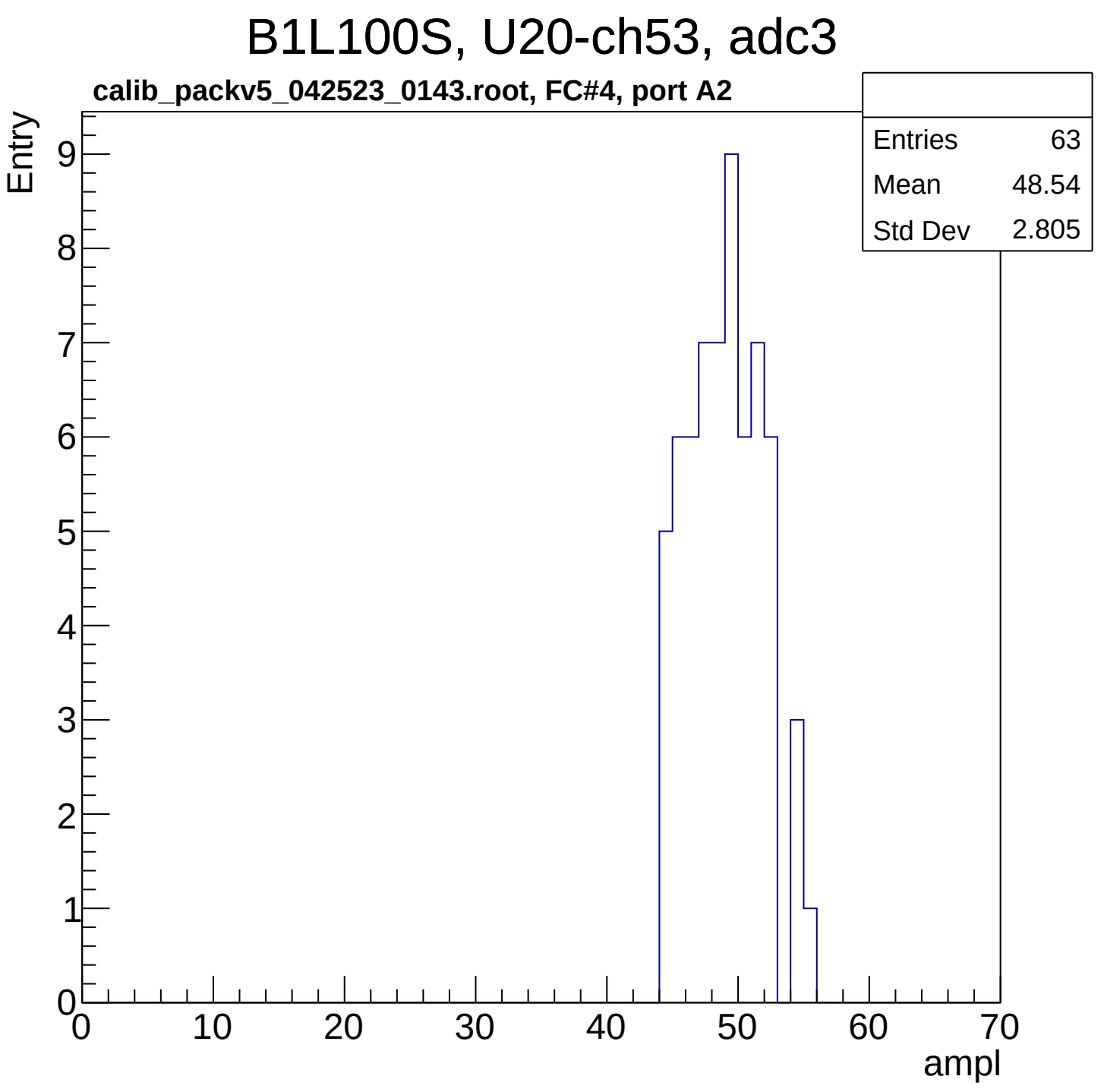

B1L100S, U20-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 41.64
Std Dev 2.884
Gaus mean : 42.0926
Gaus Width: 3.2028
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.54
Std Dev 2.805
Entry
ampl
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2
3
4
5
6
7
8
9
0
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50
60
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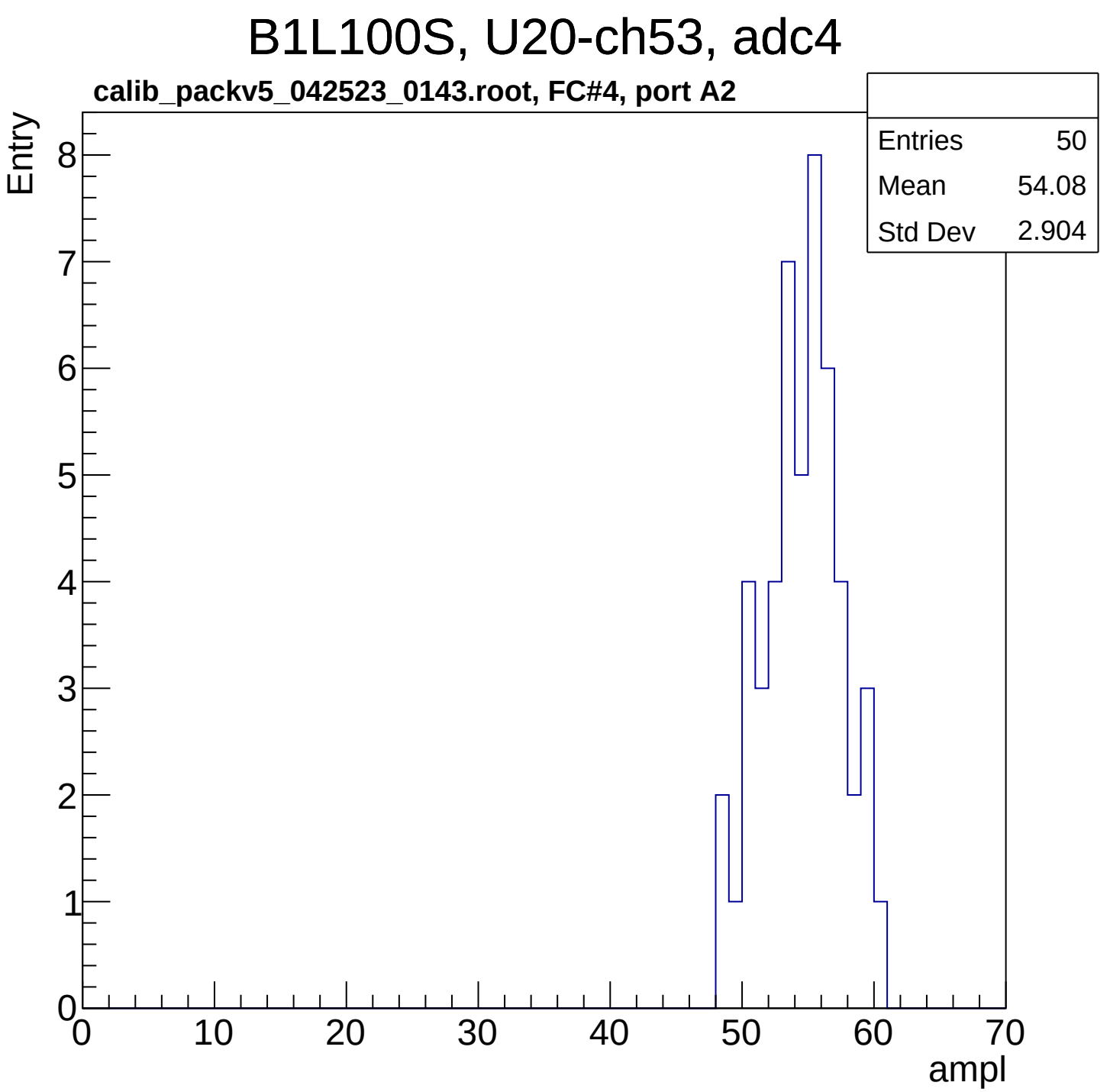
B1L100S, U20-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.08
Std Dev 2.904
Entry
ampl
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3
4
5
6
7
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0
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20
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40
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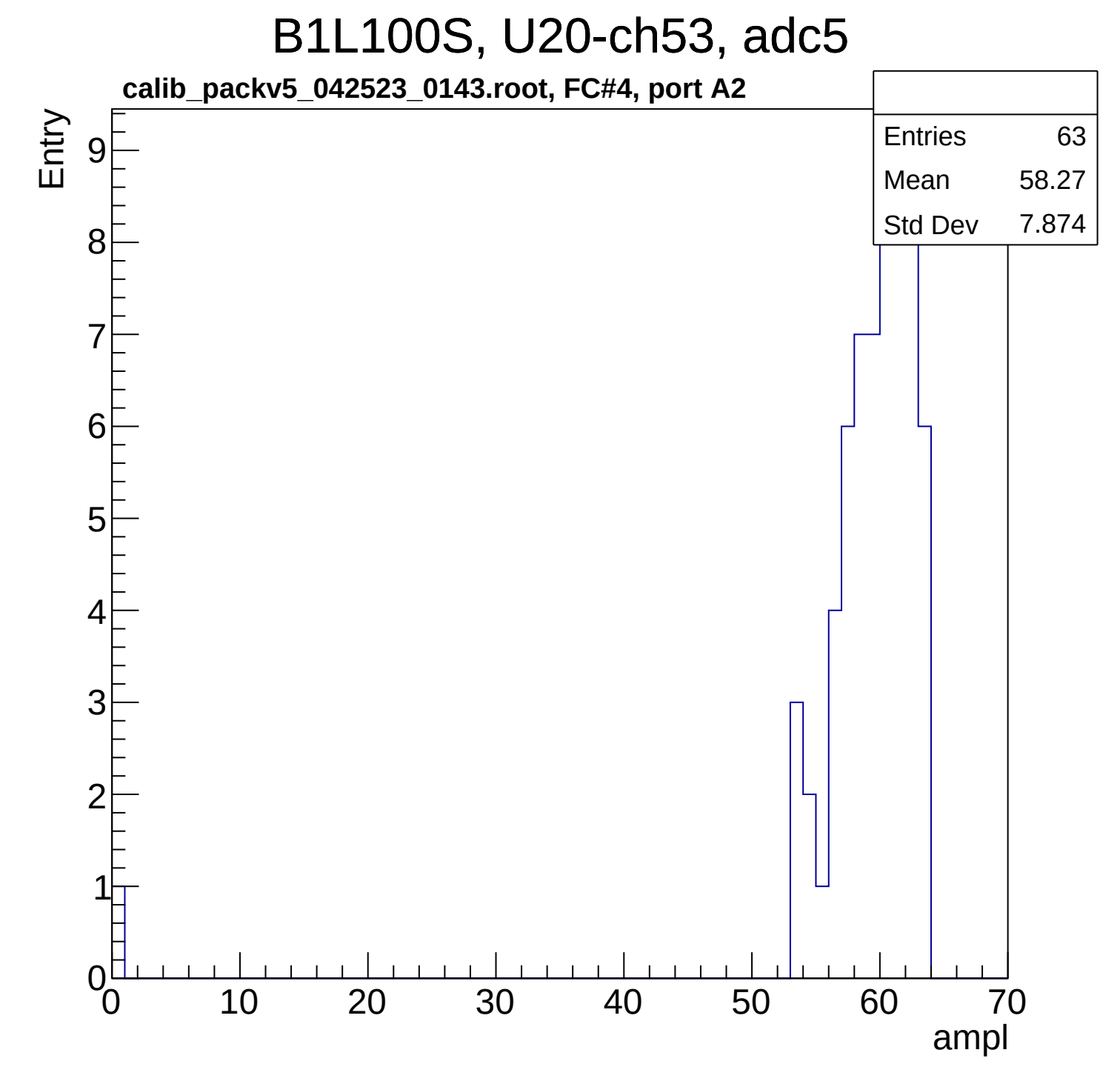
60
70


B1L100S, U20-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 58.27
Std Dev 7.874
Entry
ampl
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2
3
4
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6
7
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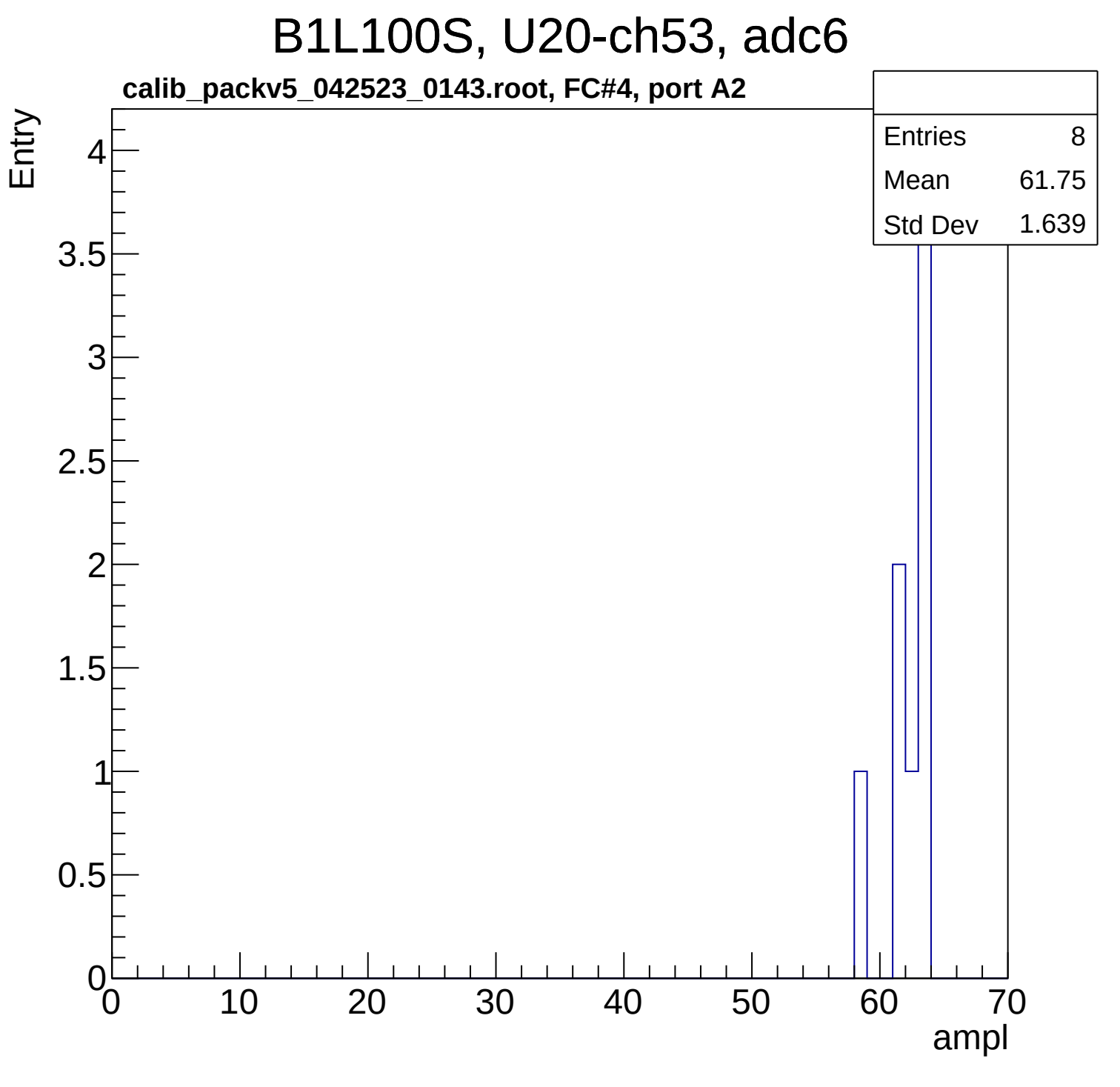
B1L100S, U20-ch53, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.75
Std Dev 1.639
Entry
ampl
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0.5
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1.5
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2.5
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3.5
4
0
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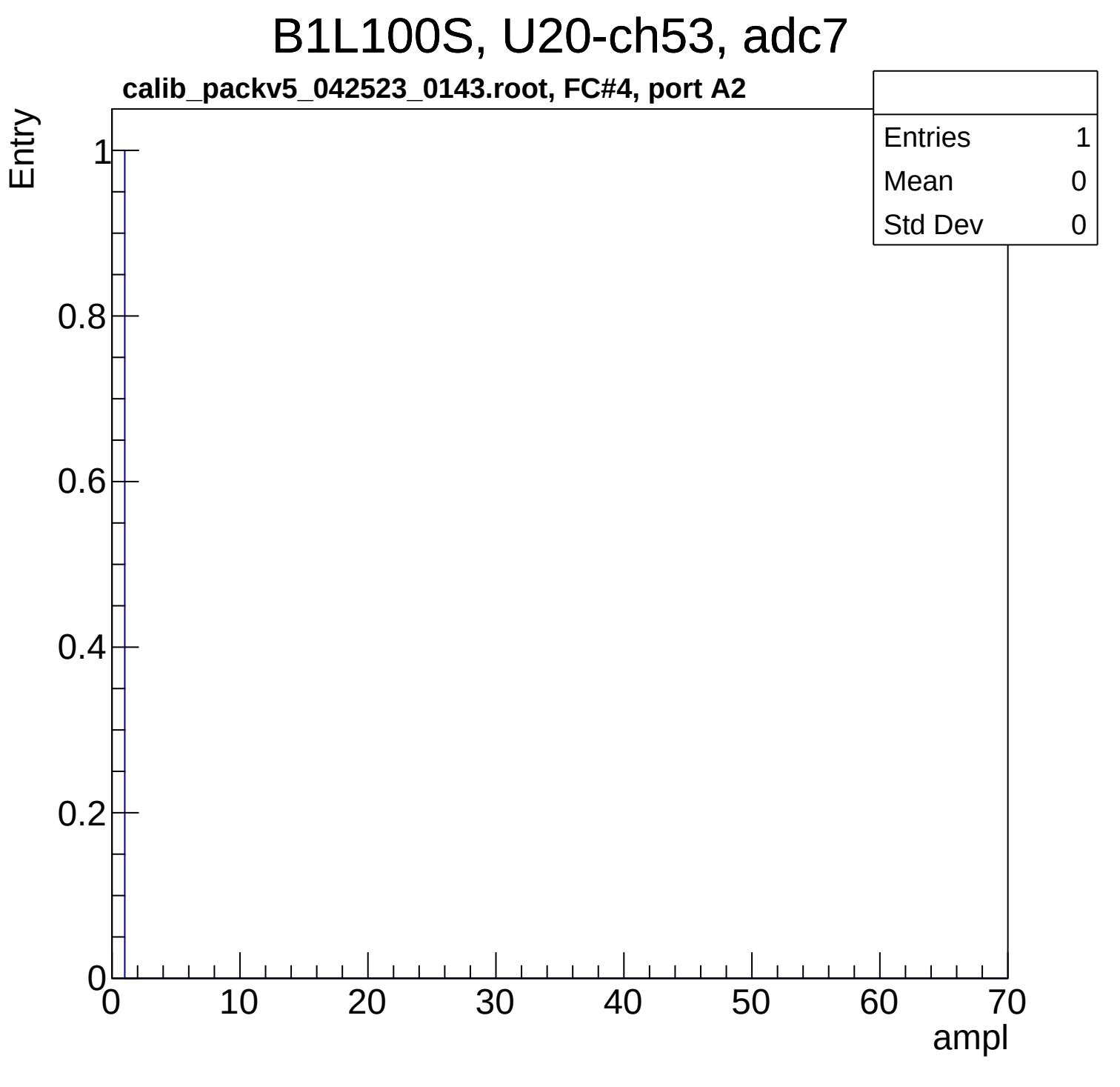
B1L100S, U20-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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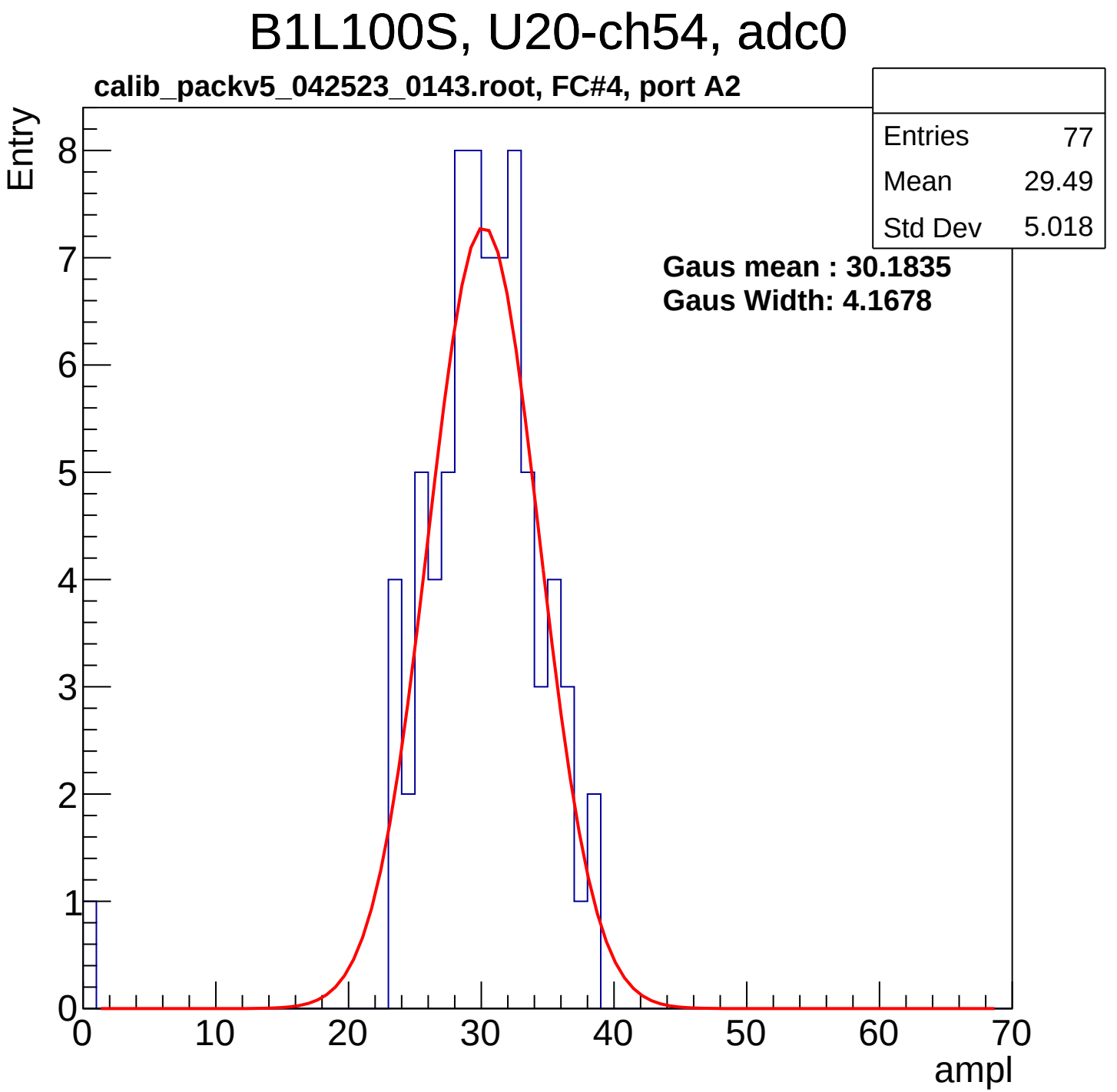
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.49
Std Dev 5.018
Gaus mean : 30.1835
Gaus Width: 4.1678
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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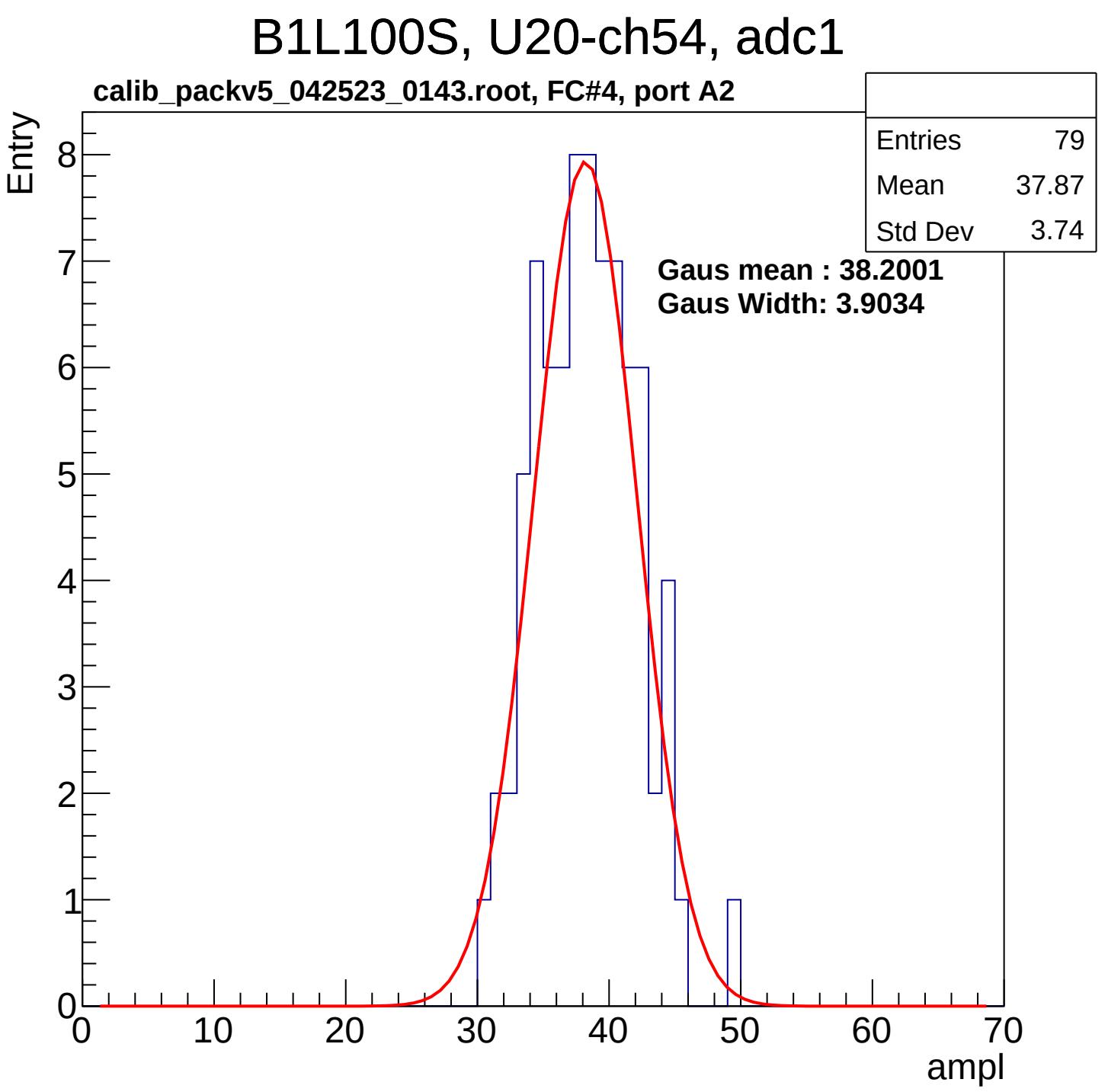
60
70


B1L100S, U20-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 37.87
Std Dev 3.74
Gaus mean : 38.2001
Gaus Width: 3.9034
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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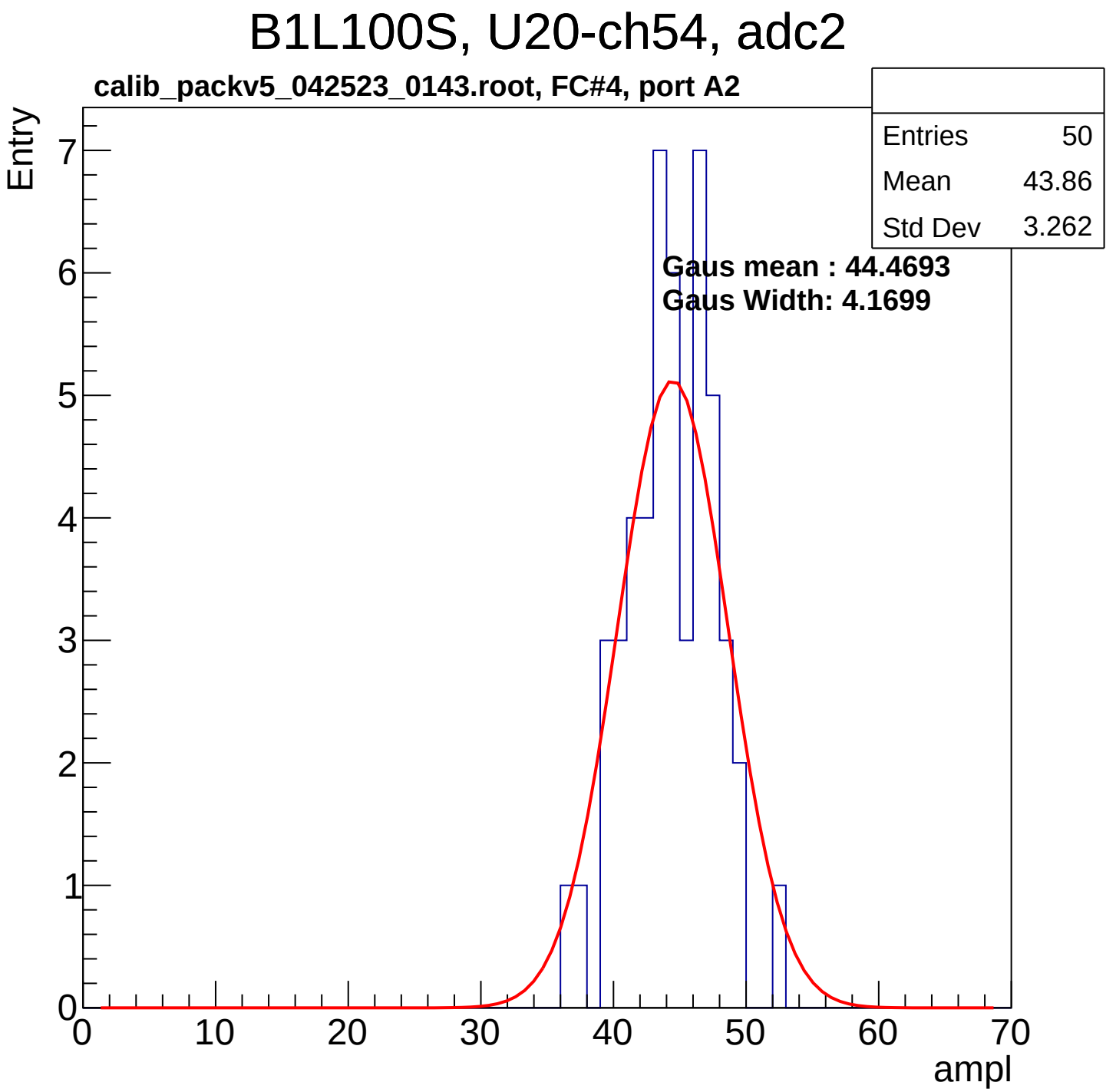
B1L100S, U20-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 43.86
Std Dev 3.262
Gaus mean : 44.4693
Gaus Width: 4.1699
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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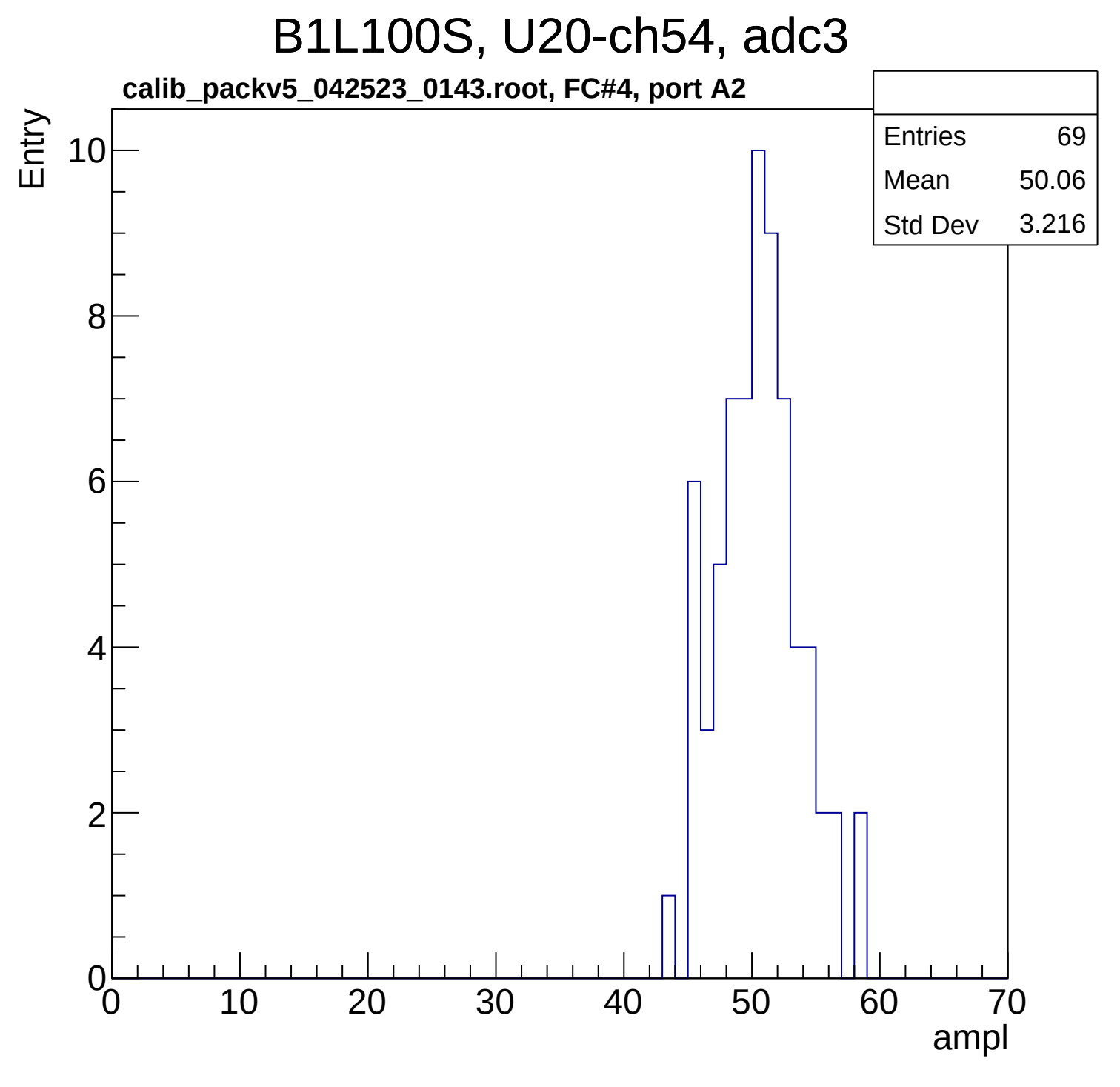
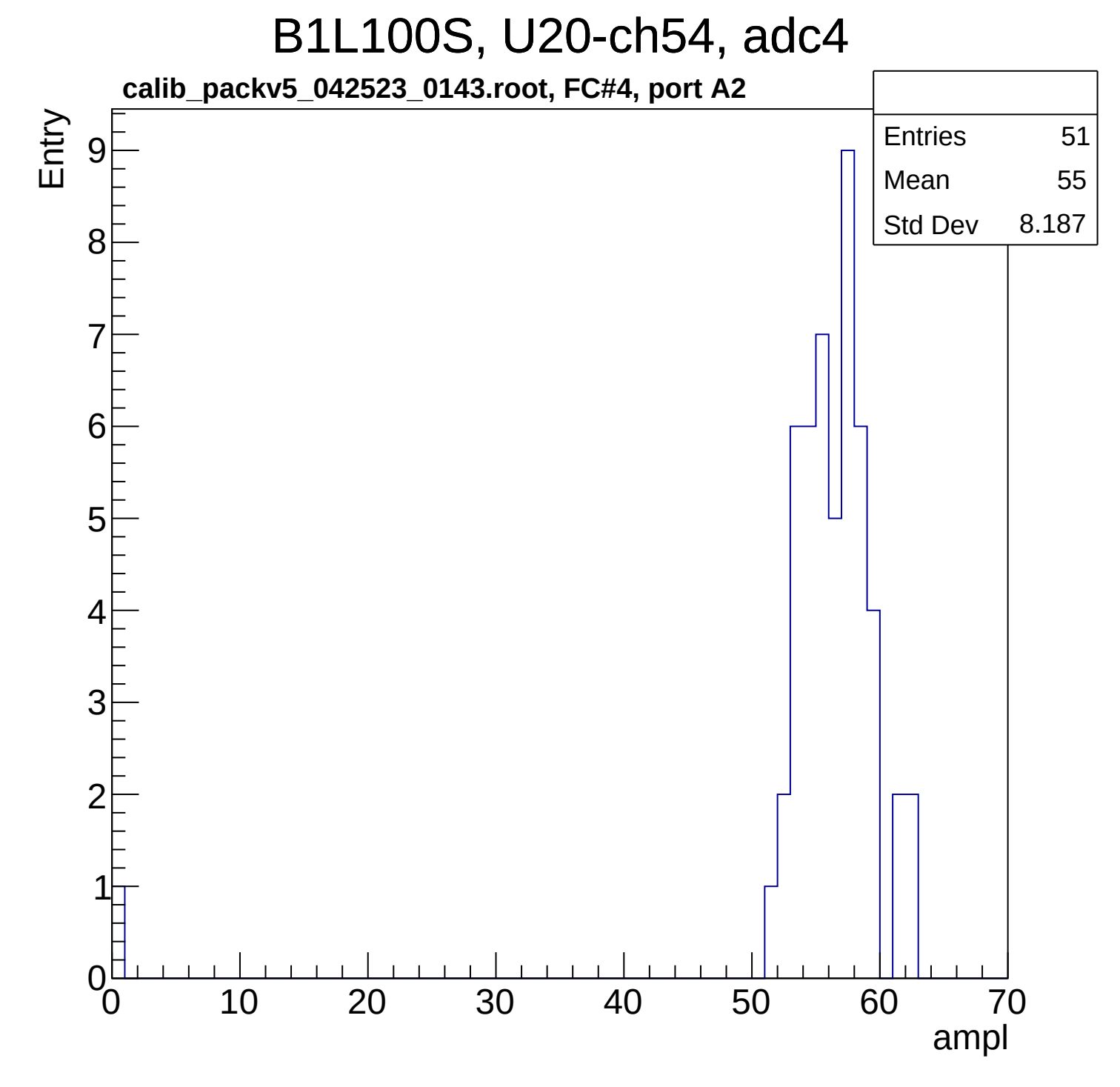

B1L100S, U20-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 50.06
Std Dev 3.216
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch54, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55
Std Dev 8.187
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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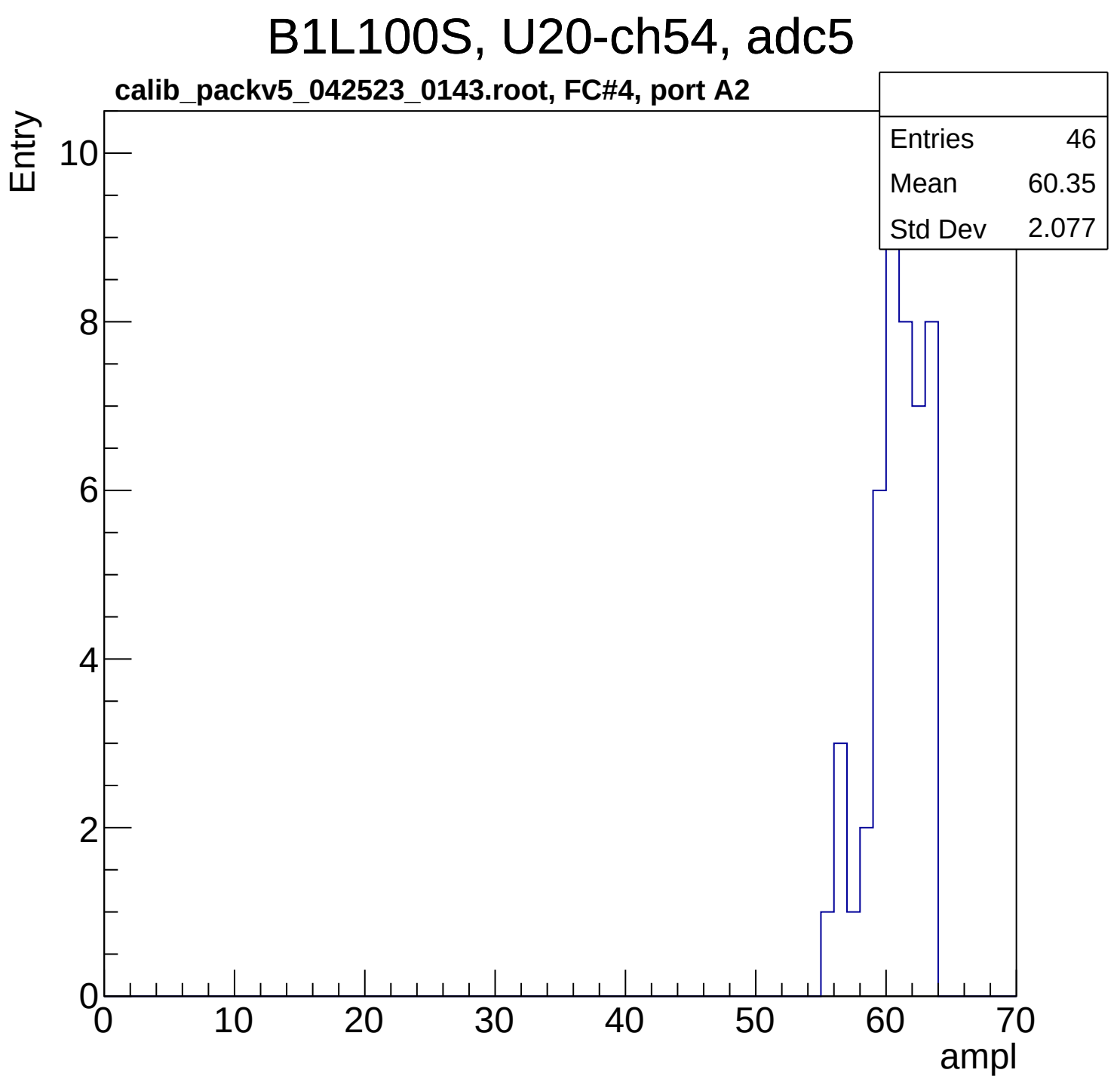
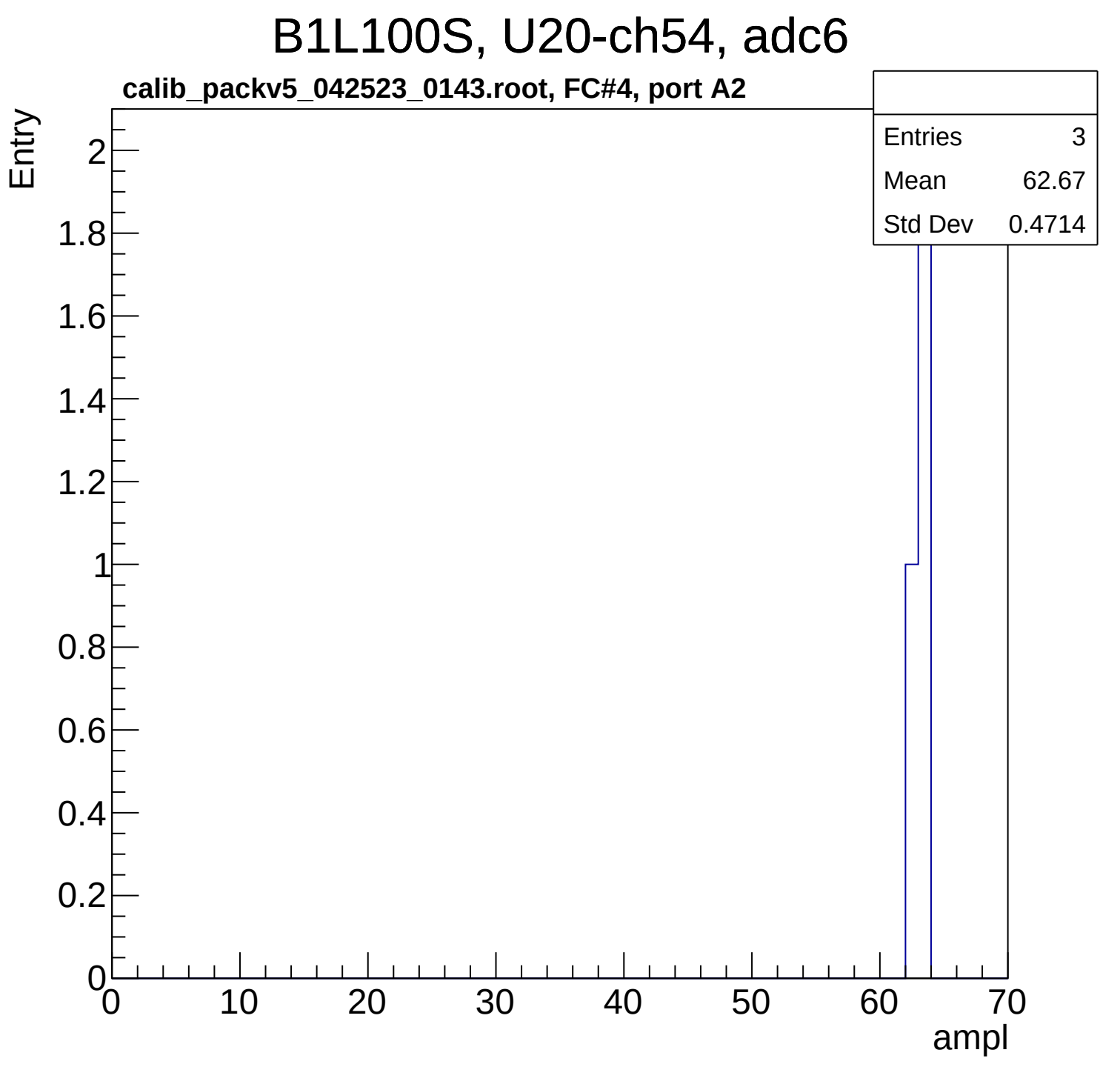

B1L100S, U20-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 60.35
Std Dev 2.077
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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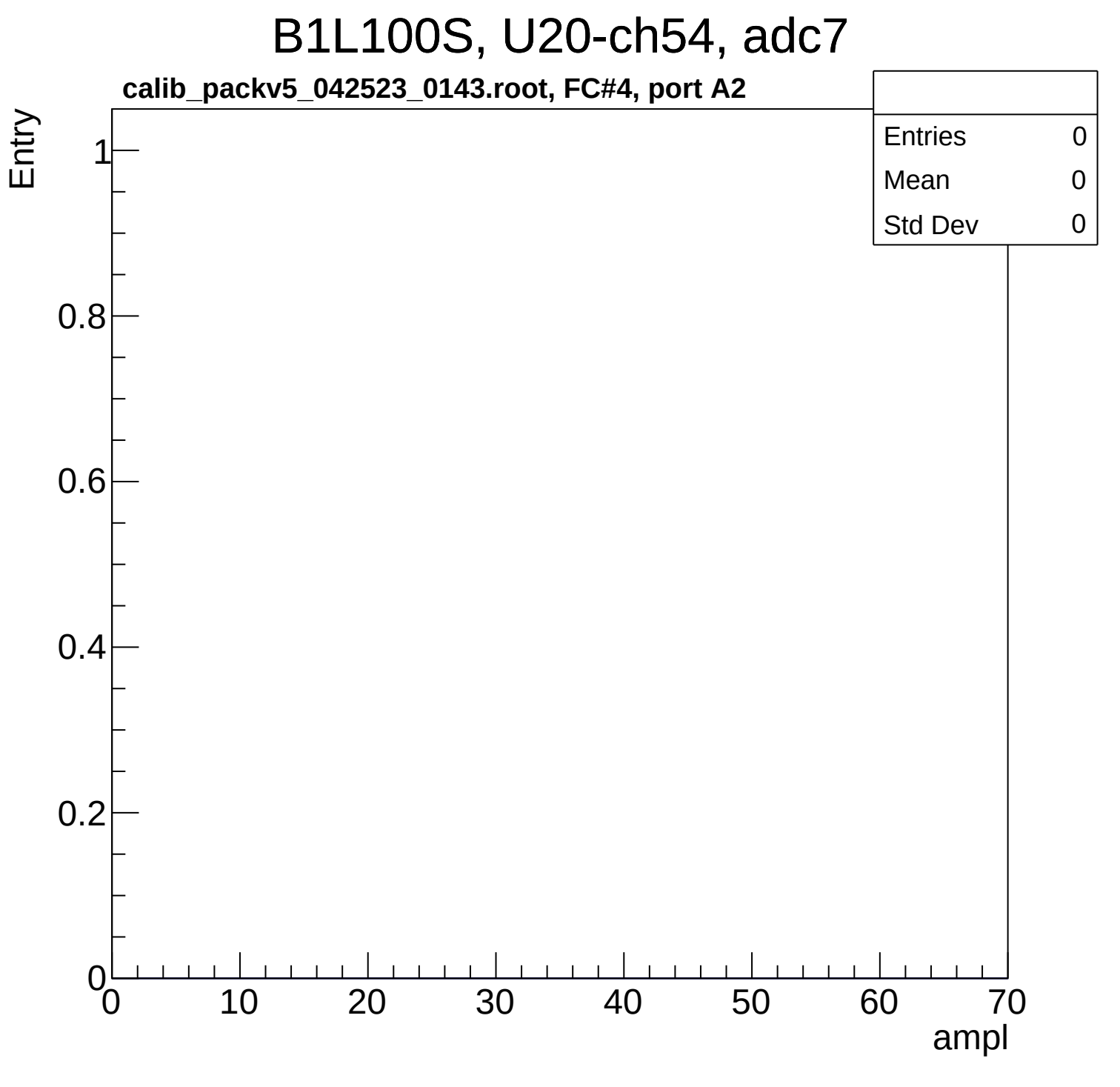
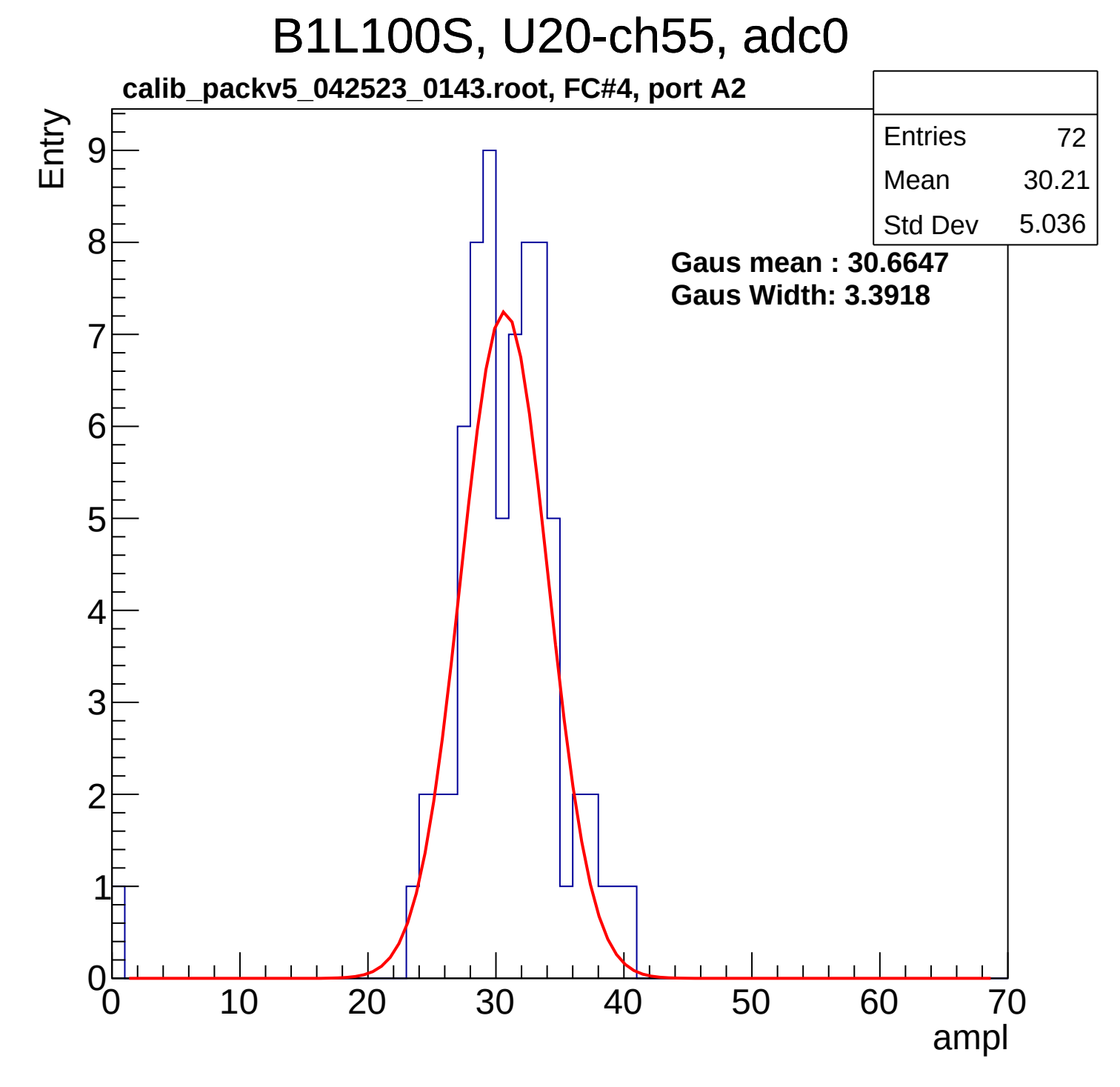

B1L100S, U20-ch54, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.21
Std Dev 5.036
Gaus mean : 30.6647
Gaus Width: 3.3918
Entry
ampl
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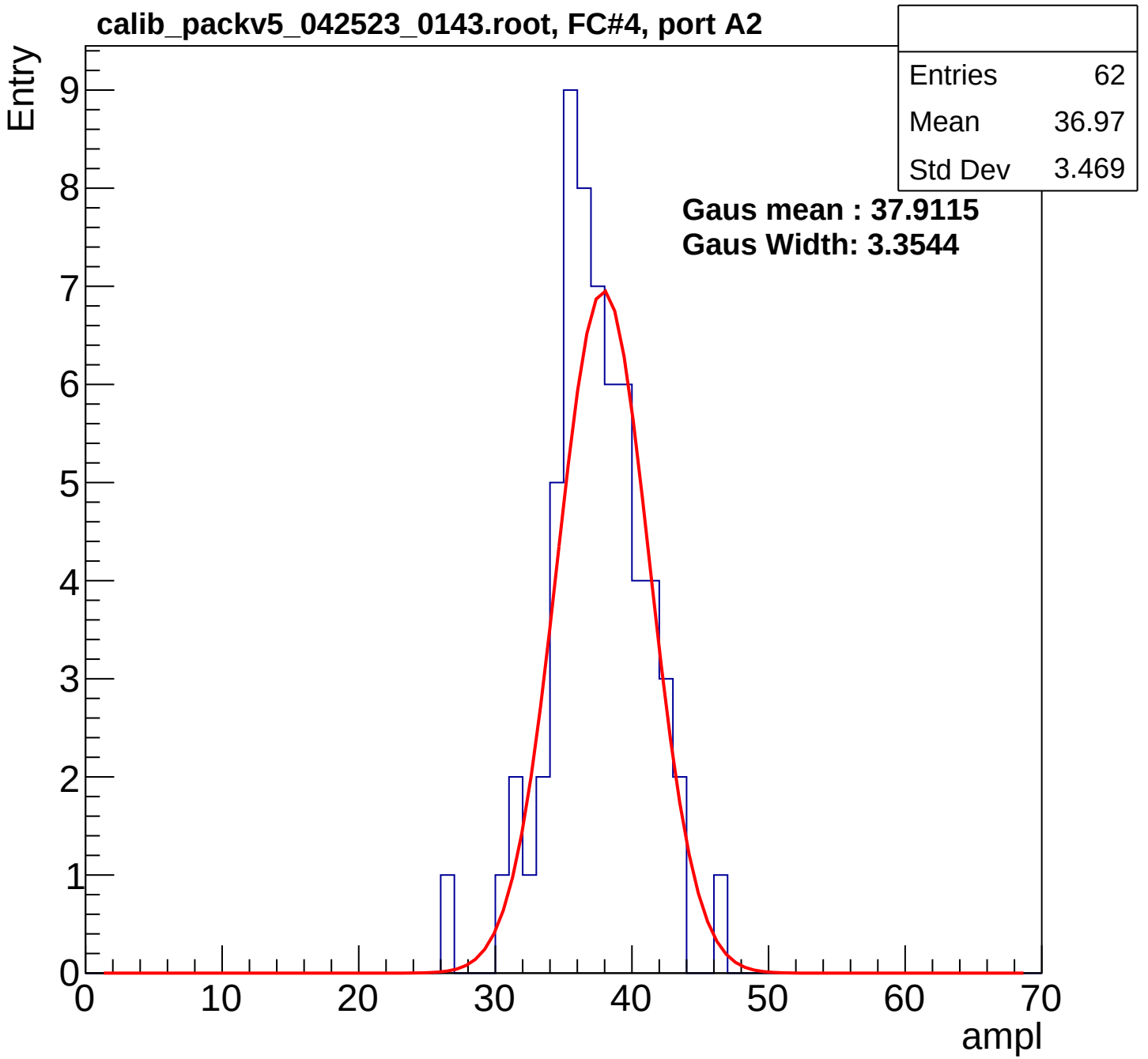
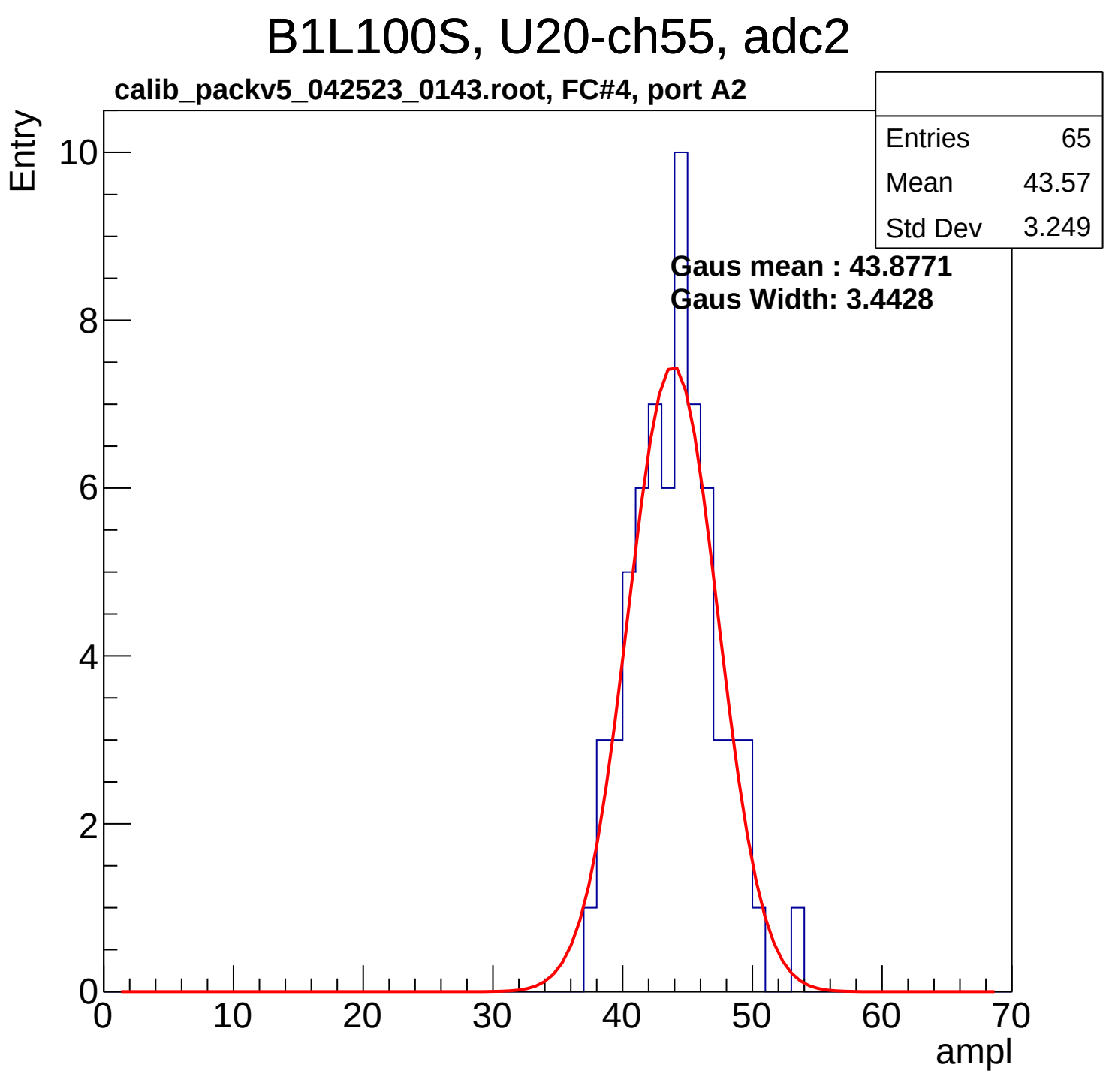
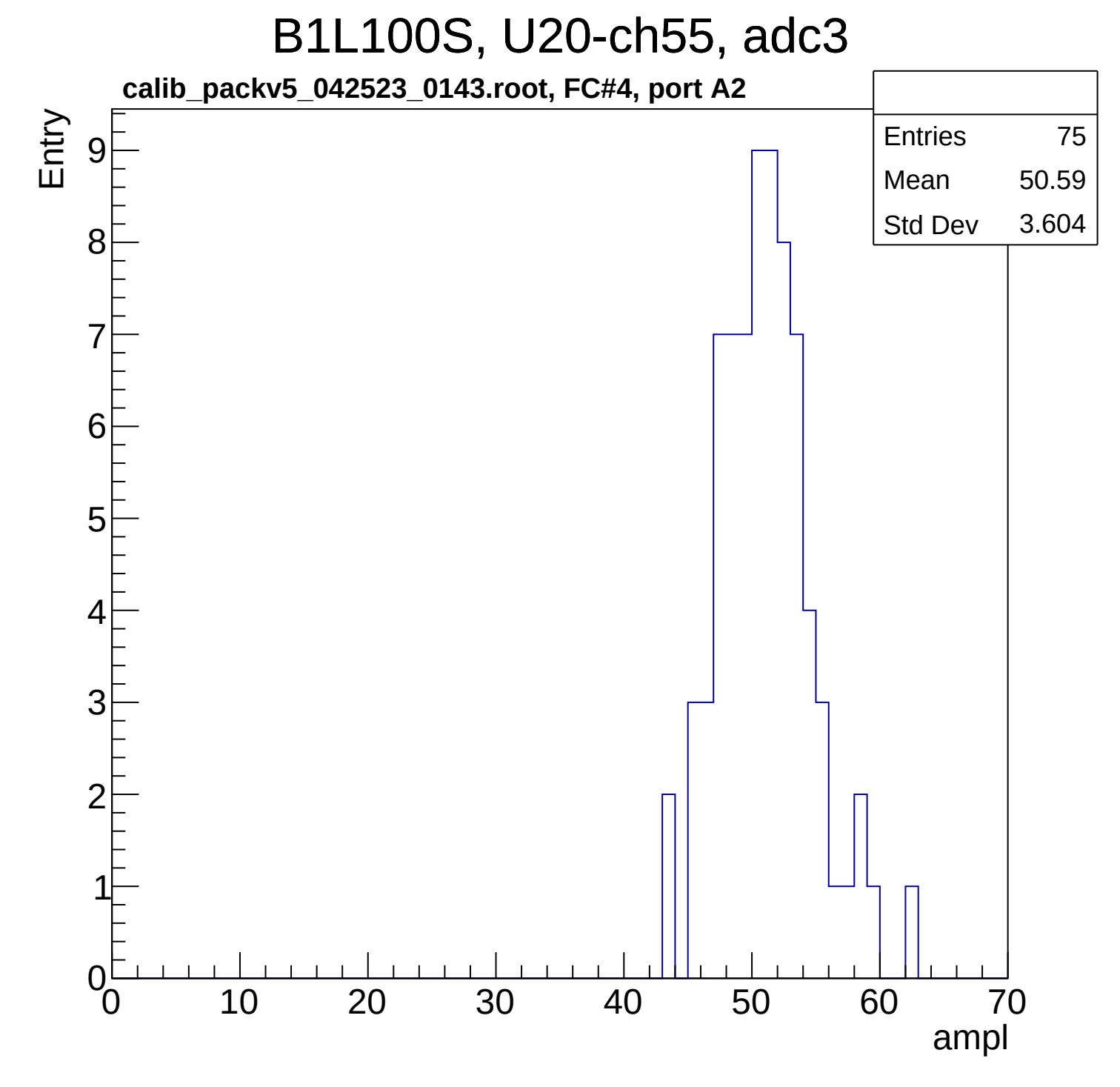
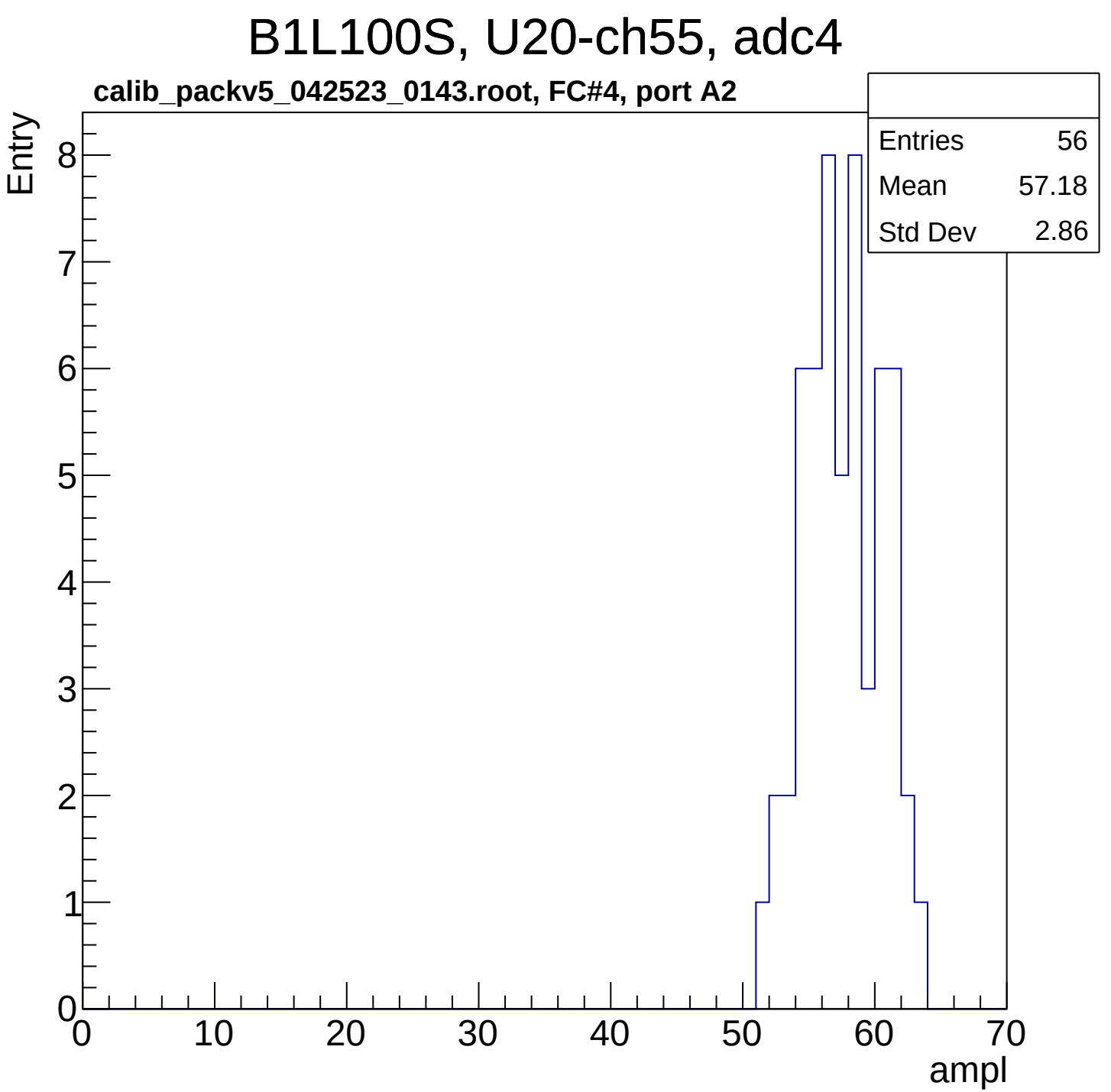
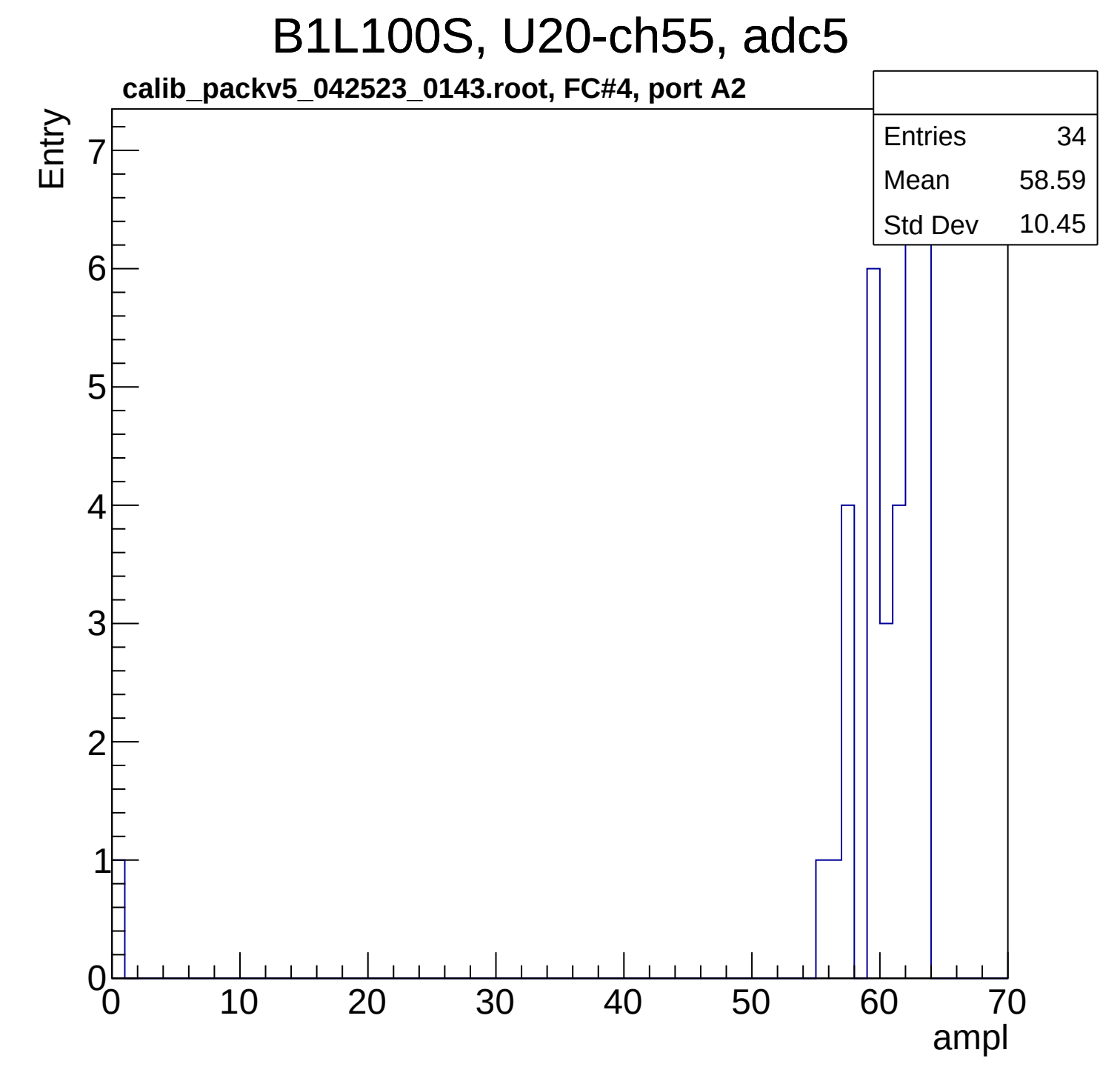
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

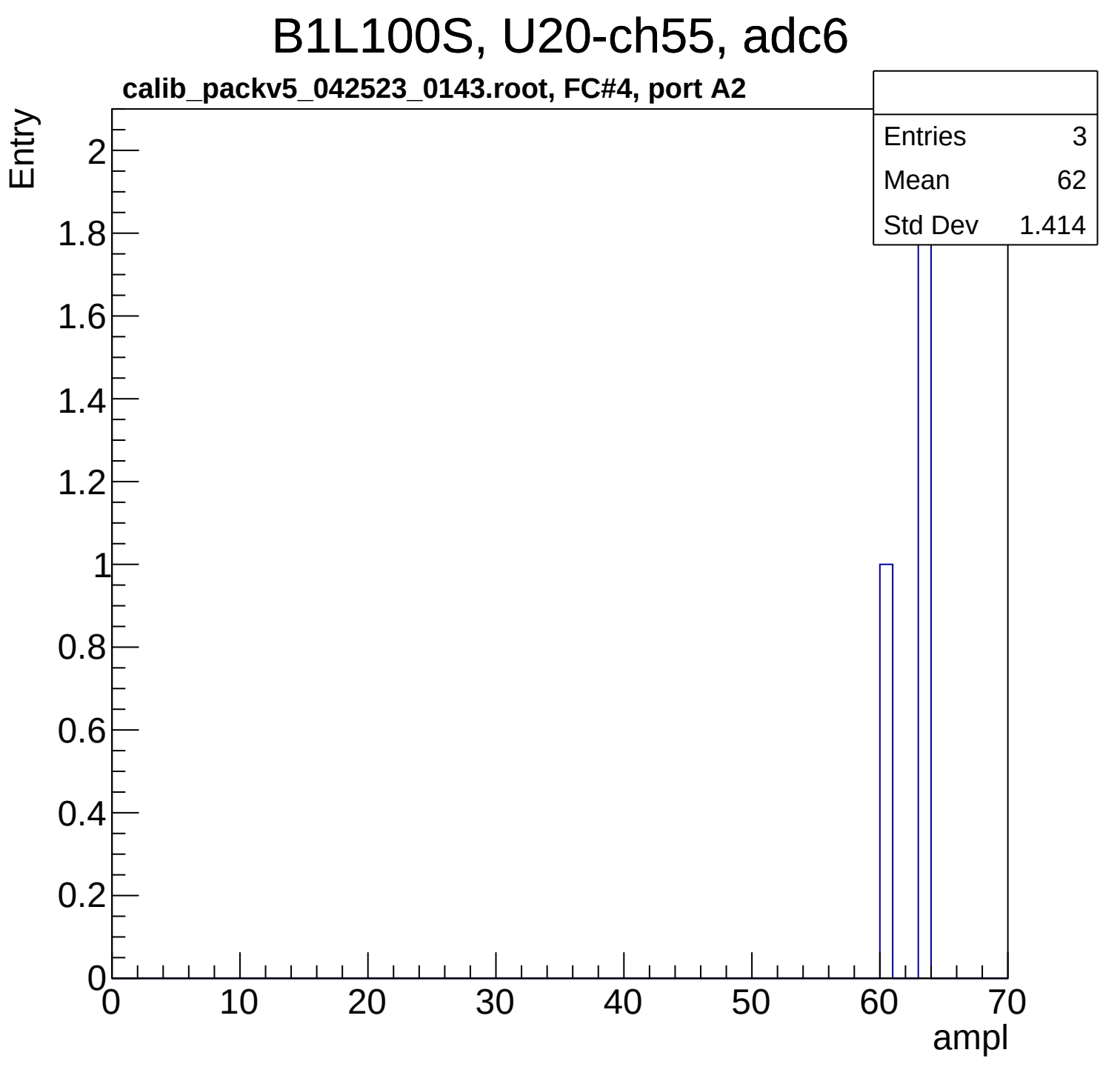

B1L100S, U20-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.97
Std Dev 3.469
Gaus mean : 37.9115
Gaus Width: 3.3544
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

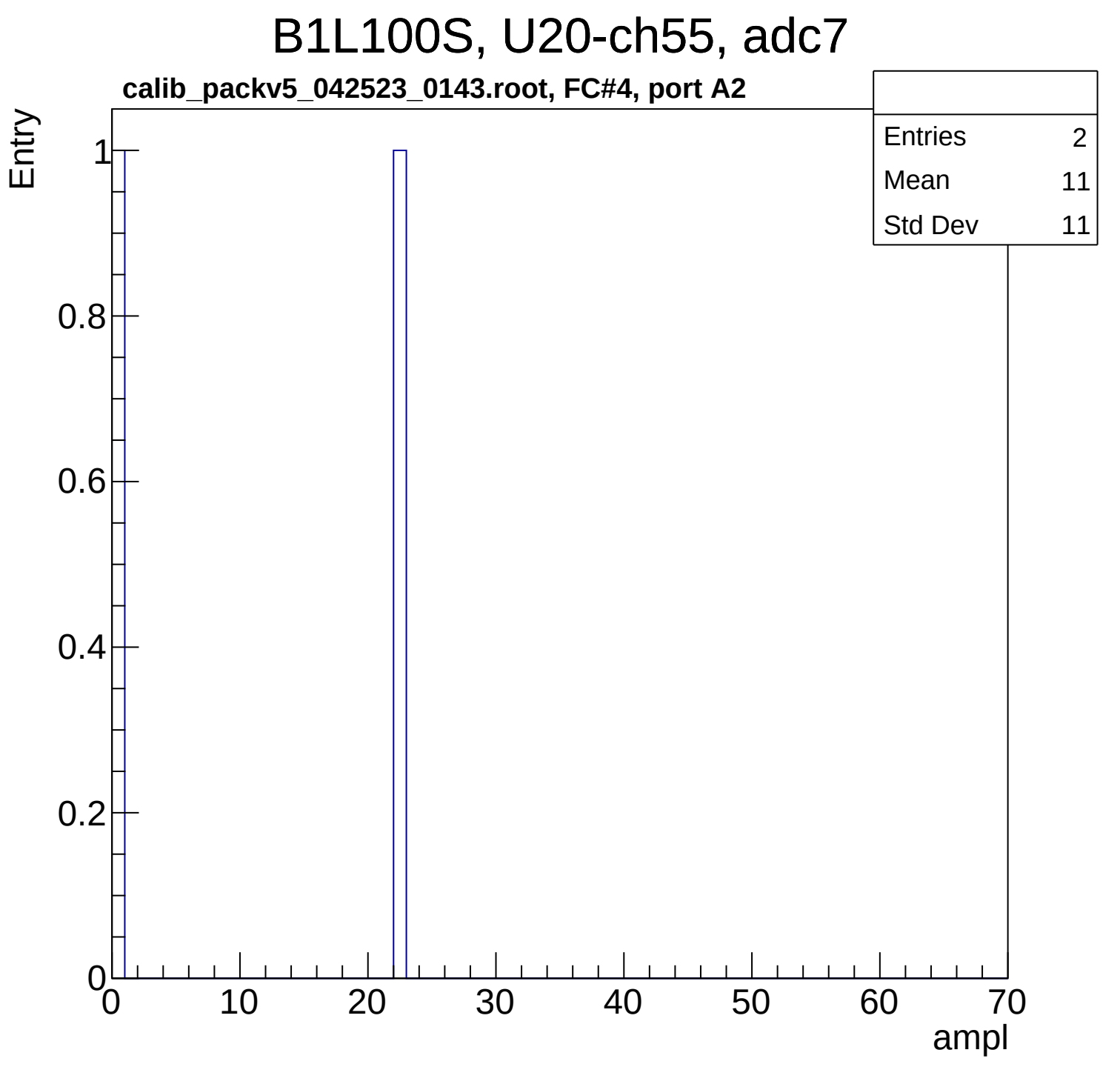

B1L100S, U20-ch55, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.57
Std Dev 3.249
Gaus mean : 43.8771
Gaus Width: 3.4428
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch55, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.59
Std Dev 3.604
Entry
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 57.18
Std Dev 2.86
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.59
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch55, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
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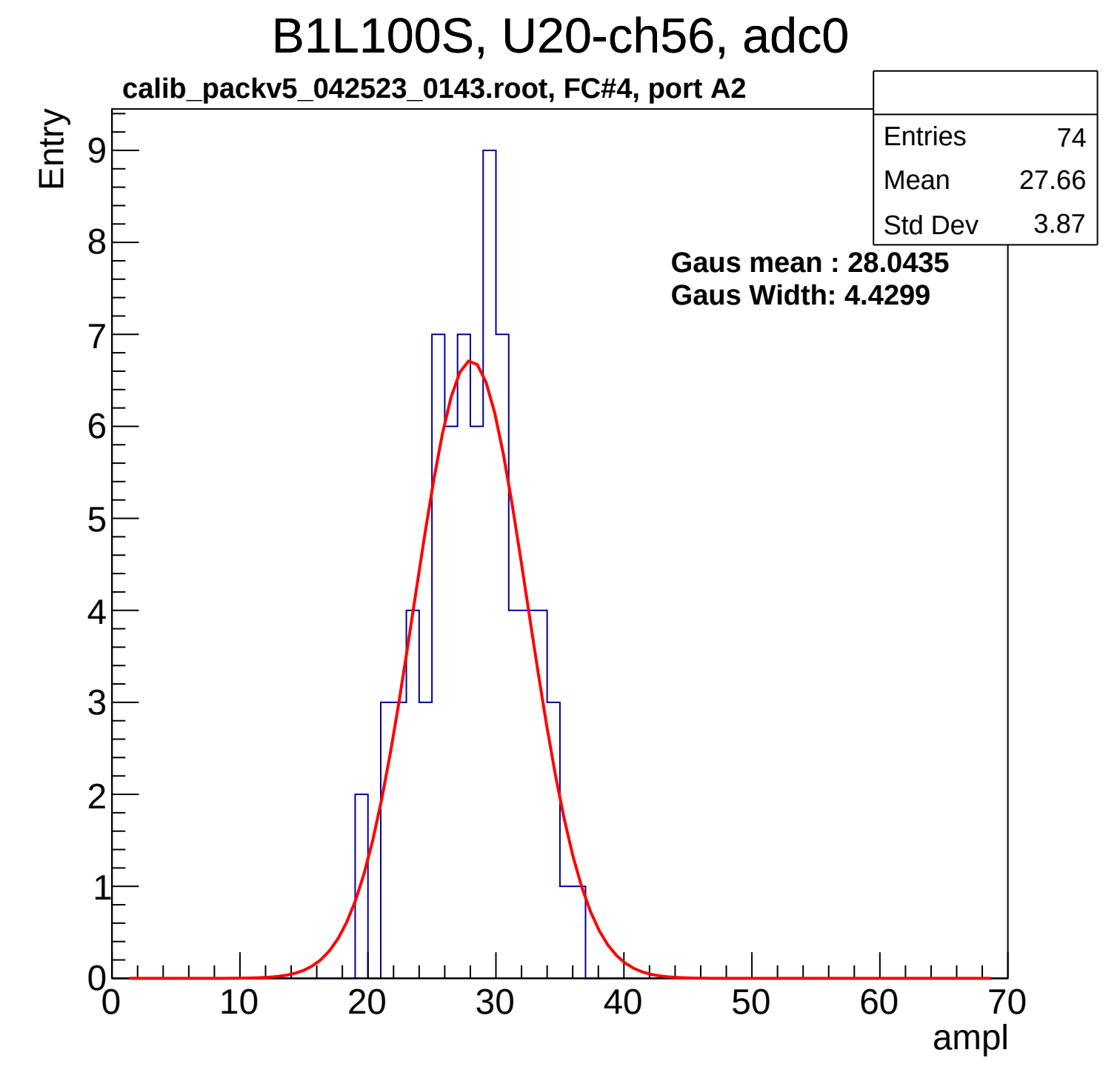
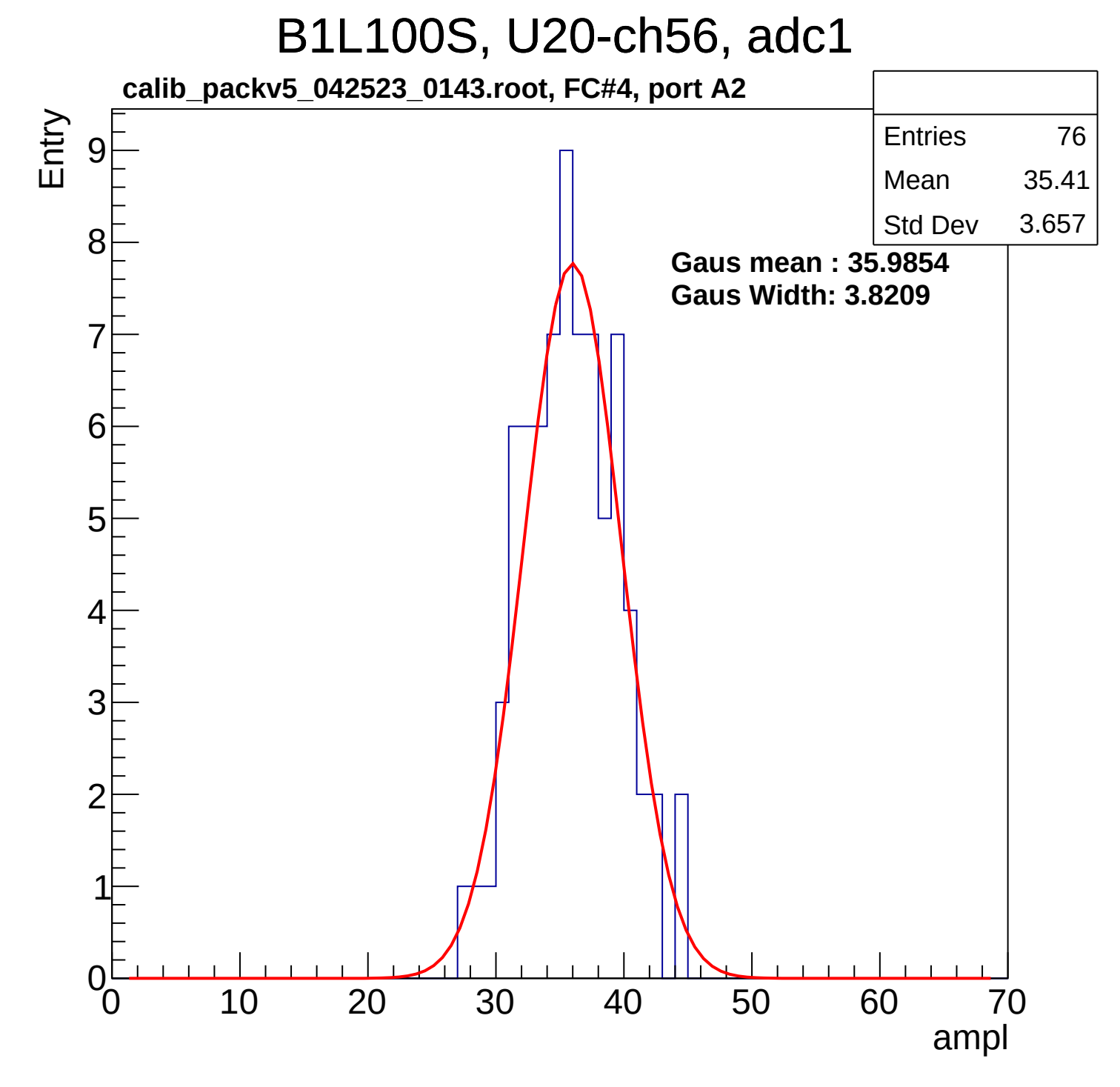
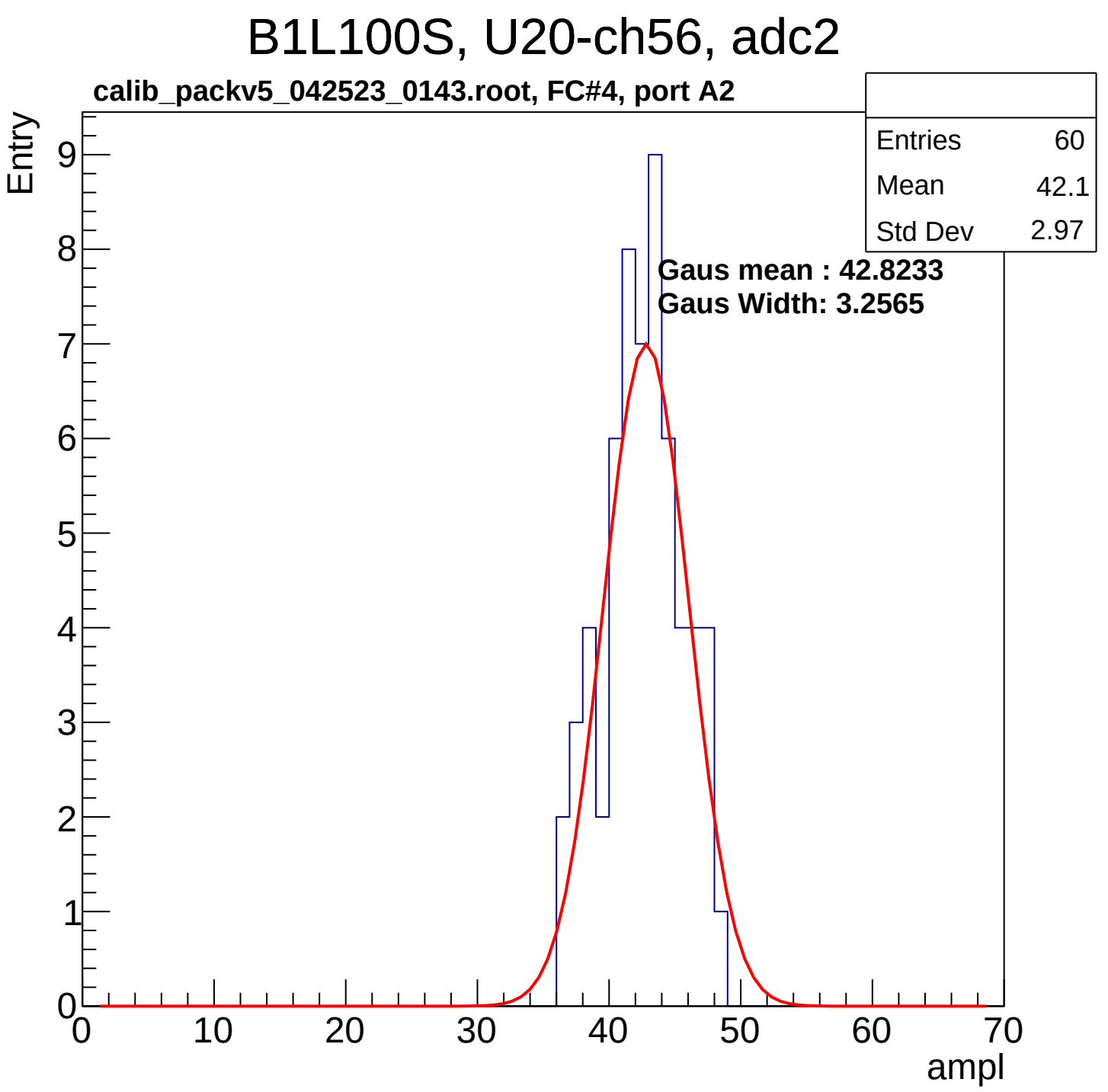
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 27.66
Std Dev 3.87
Gaus mean : 28.0435
Gaus Width: 4.4299
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.41
Std Dev 3.657
Gaus mean : 35.9854
Gaus Width: 3.8209
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.1
Std Dev 2.97
Gaus mean : 42.8233
Gaus Width: 3.2565
Entry
ampl
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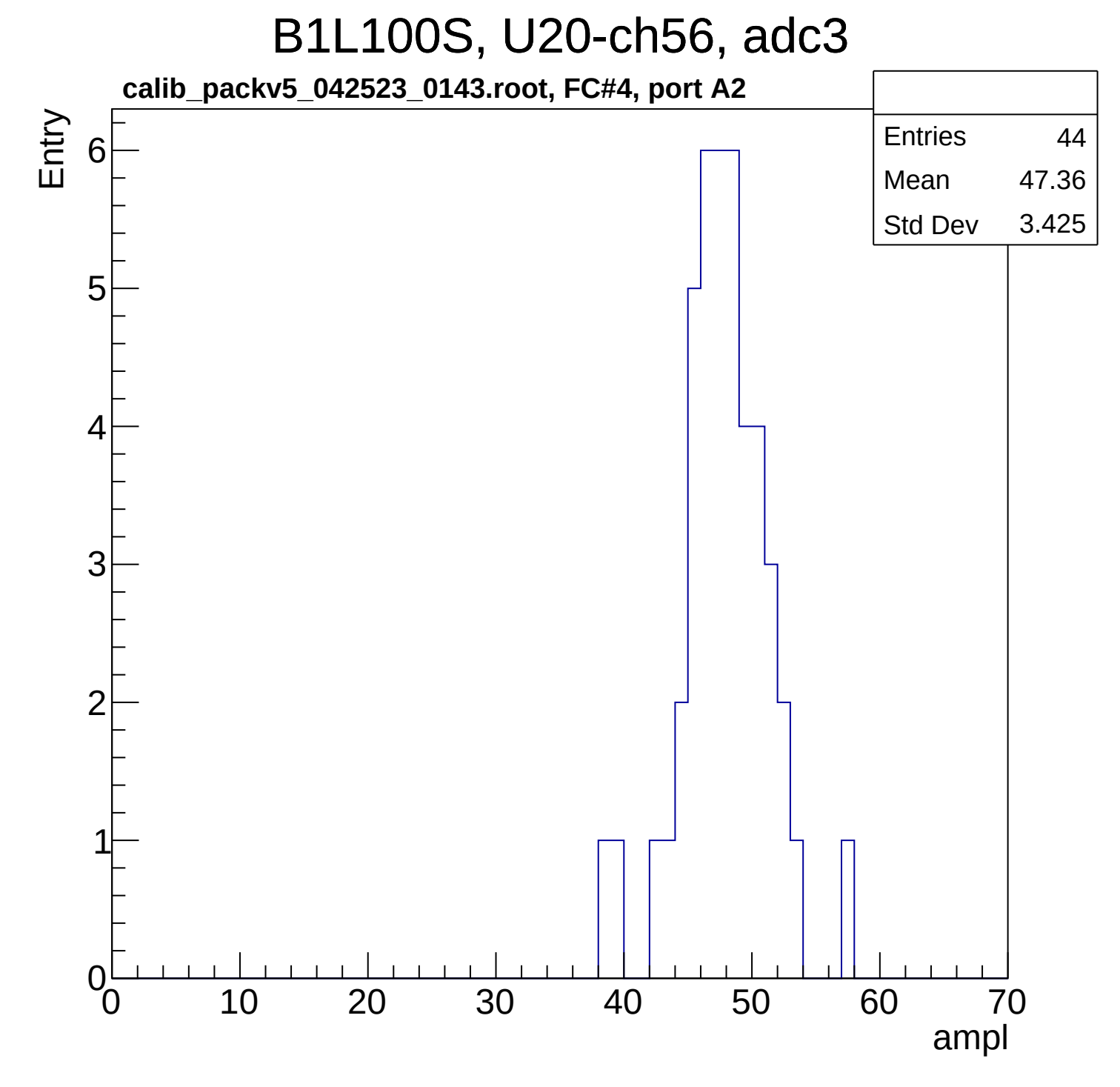
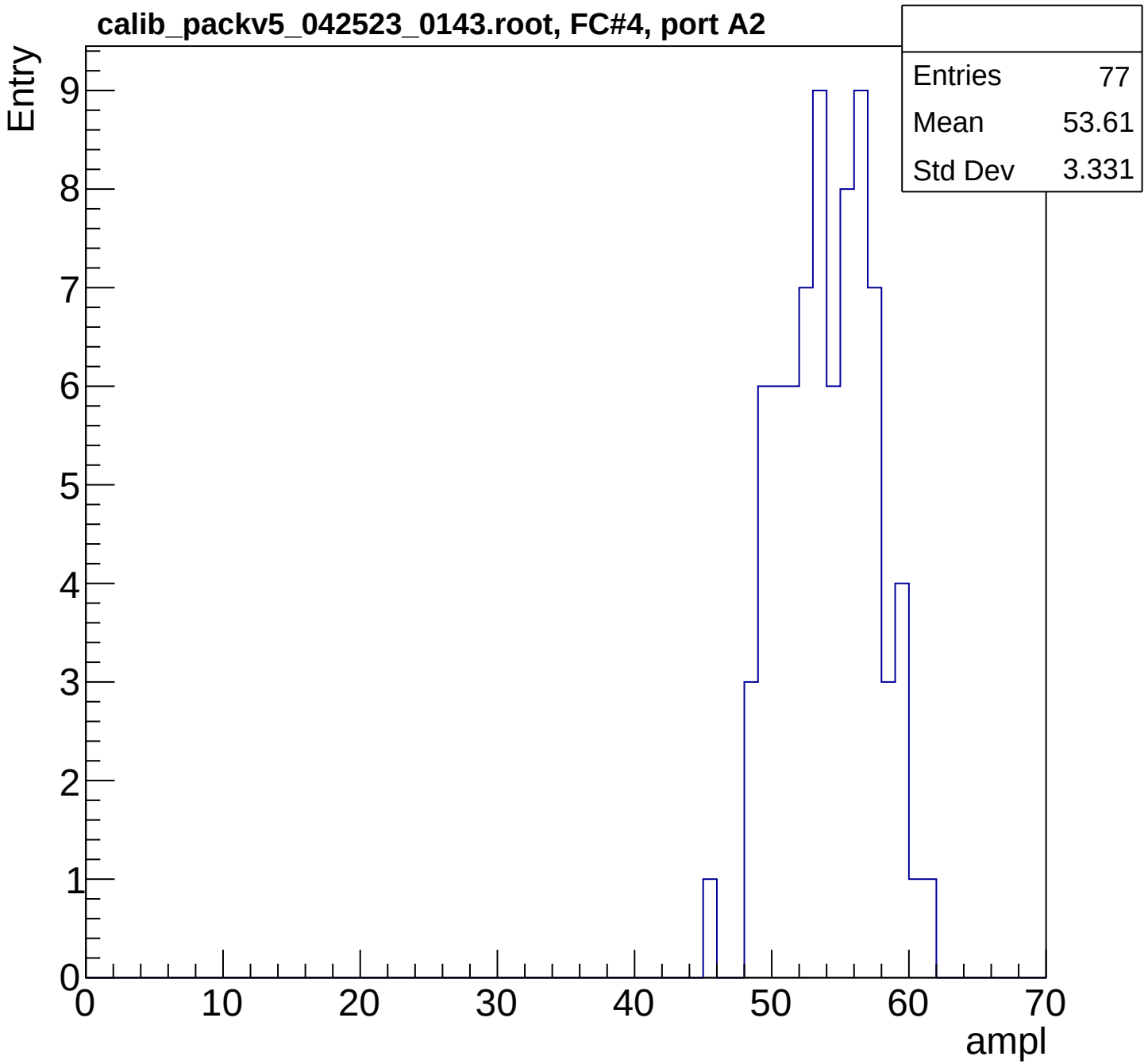
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 47.36
Std Dev 3.425
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch56, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 53.61
Std Dev 3.331
Entry
ampl
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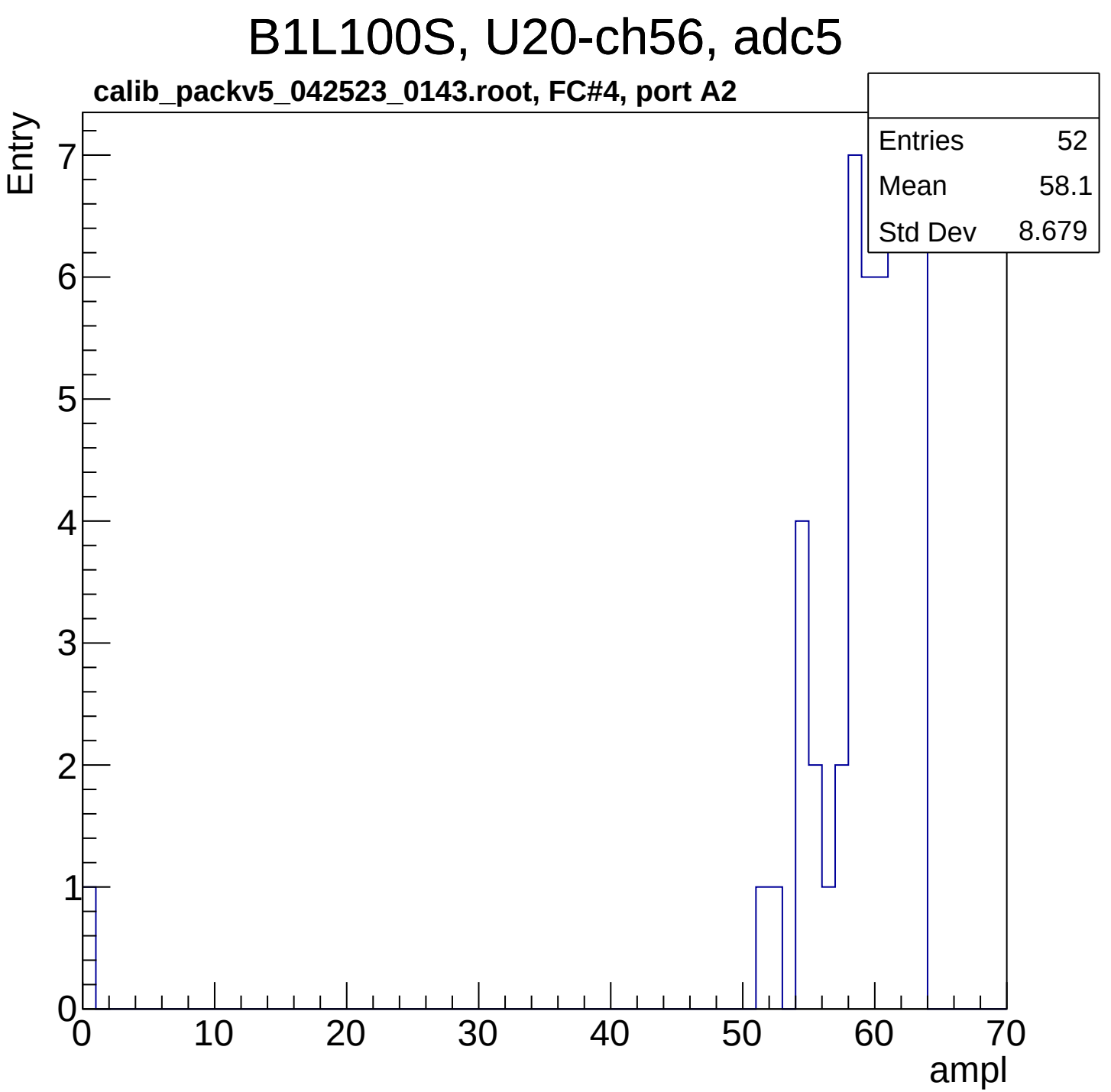
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.1
Std Dev 8.679
Entry
ampl
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1
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3
4
5
6
7
0
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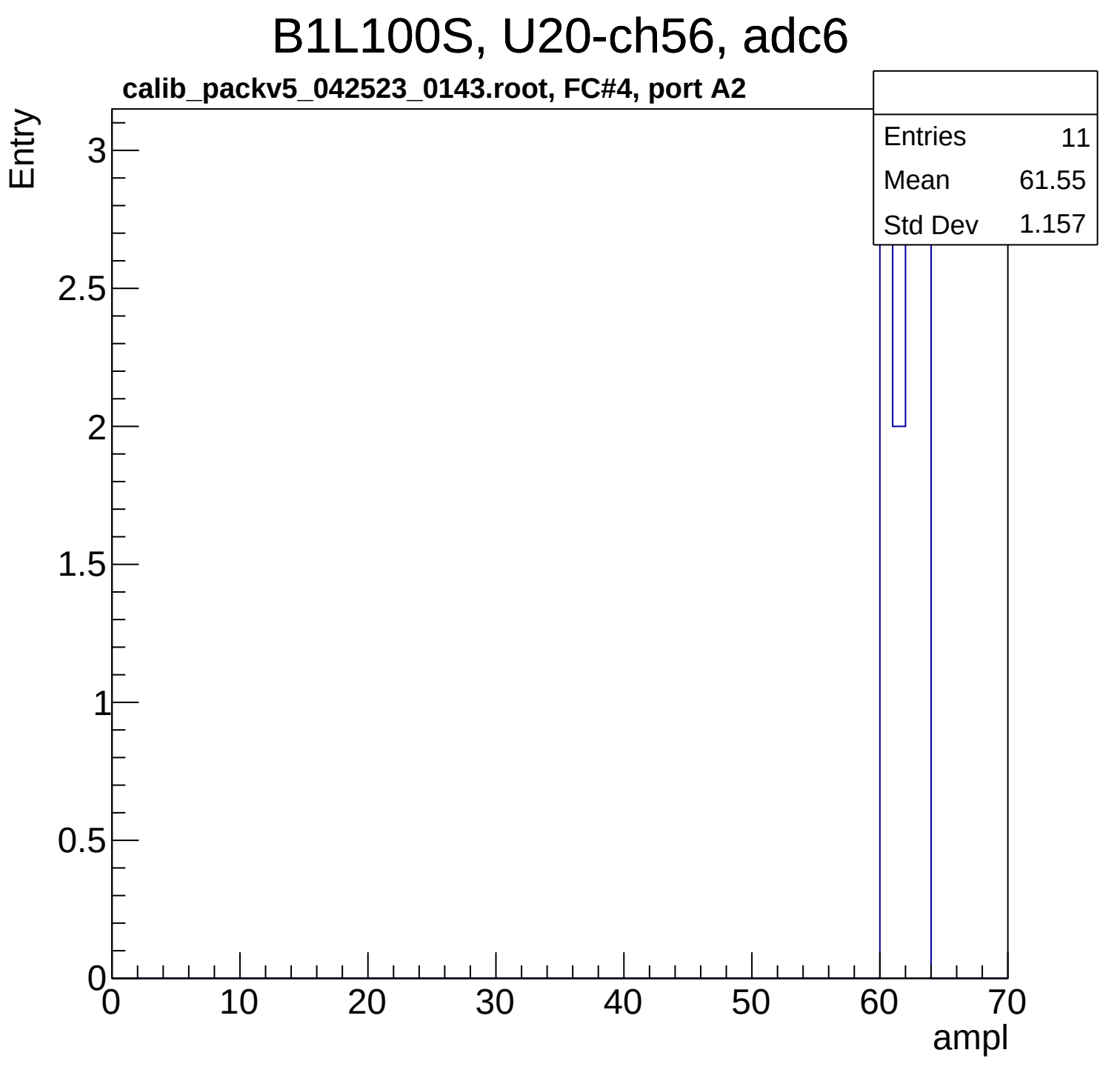
20
30
40
50
60
70


B1L100S, U20-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.55
Std Dev 1.157
Entry
ampl
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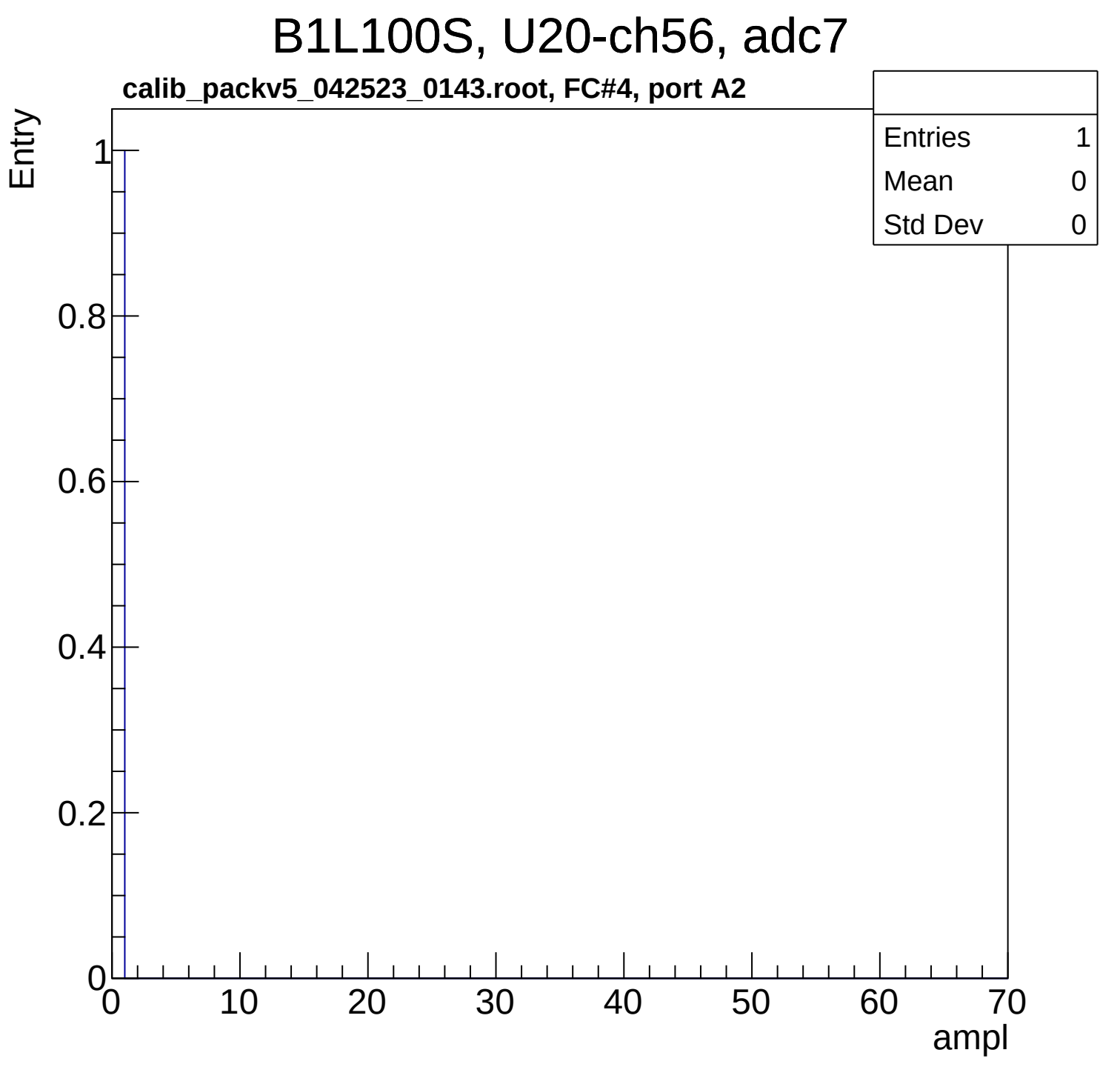
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
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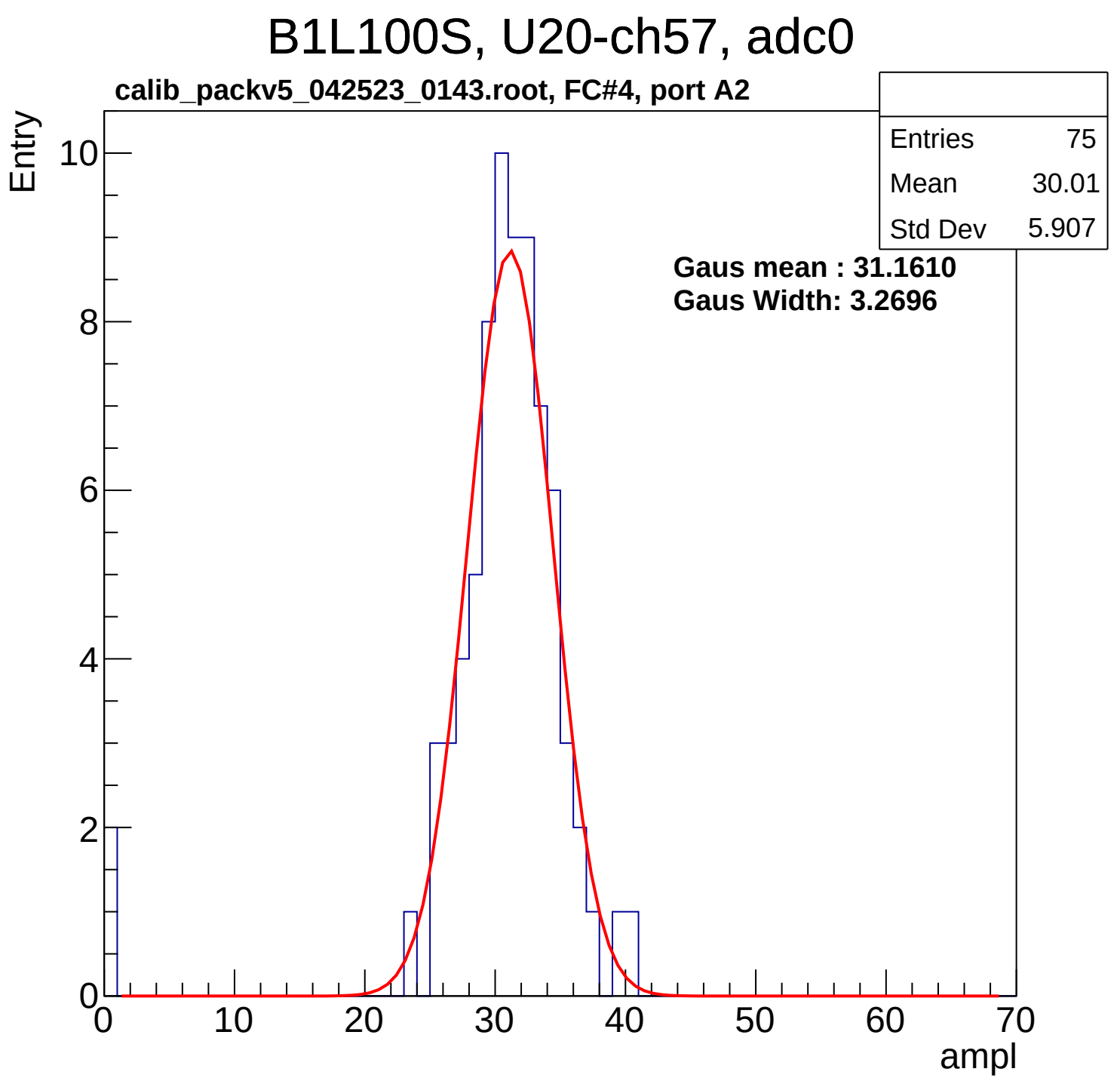
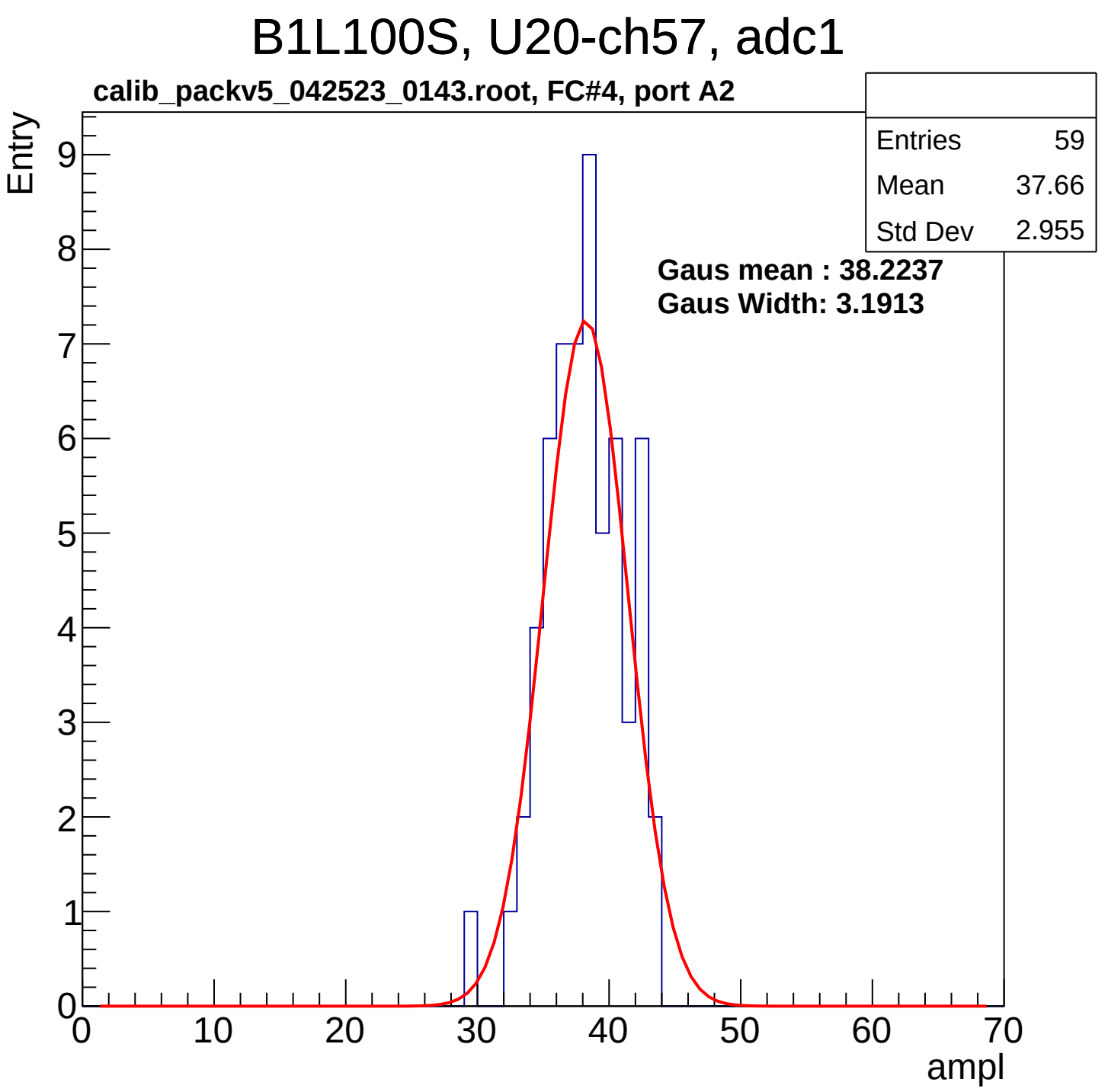
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.01
Std Dev 5.907
Gaus mean : 31.1610
Gaus Width: 3.2696
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 37.66
Std Dev 2.955
Gaus mean : 38.2237
Gaus Width: 3.1913
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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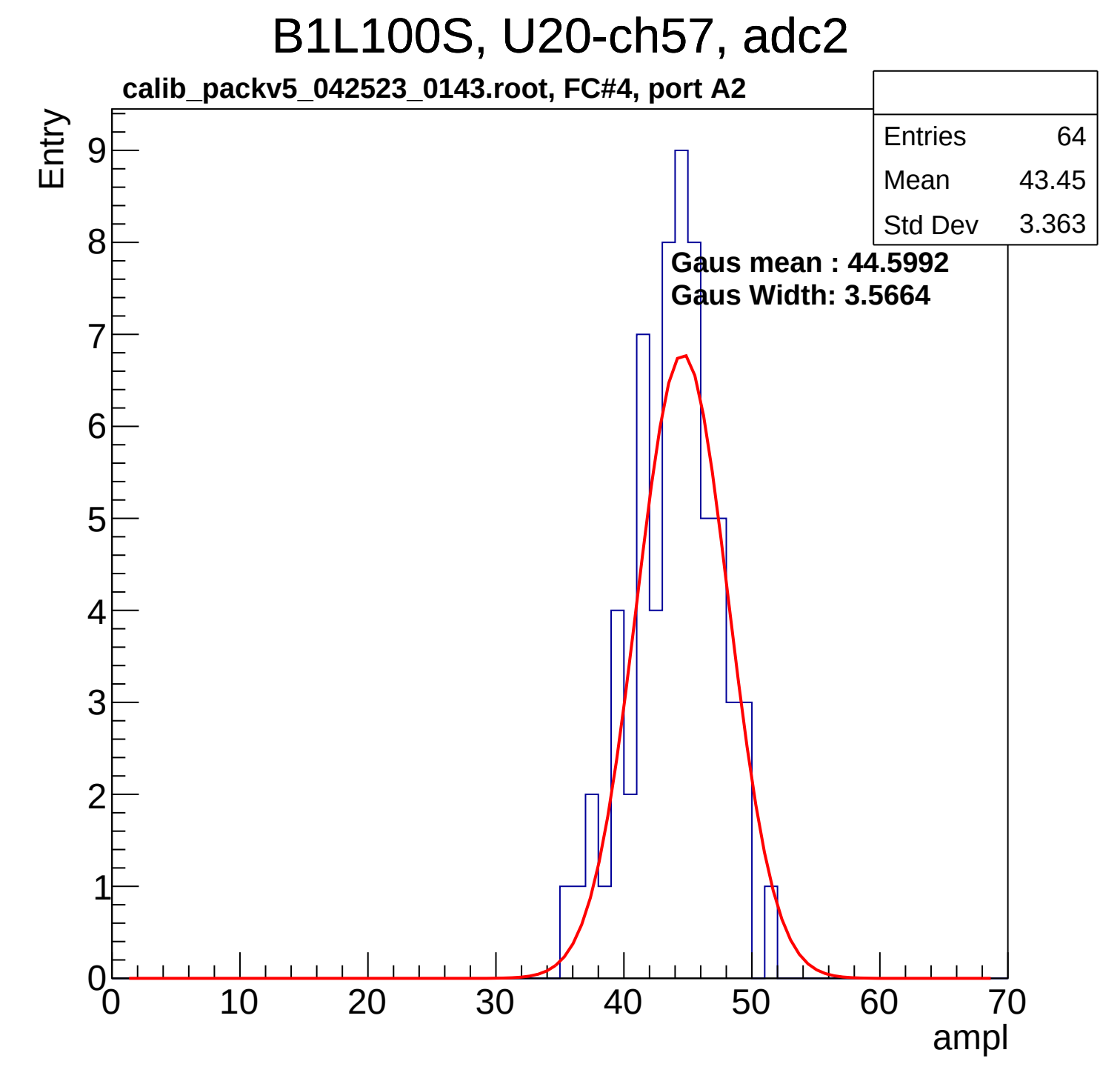
B1L100S, U20-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.45
Std Dev 3.363
Gaus mean : 44.5992
Gaus Width: 3.5664
Entry
ampl
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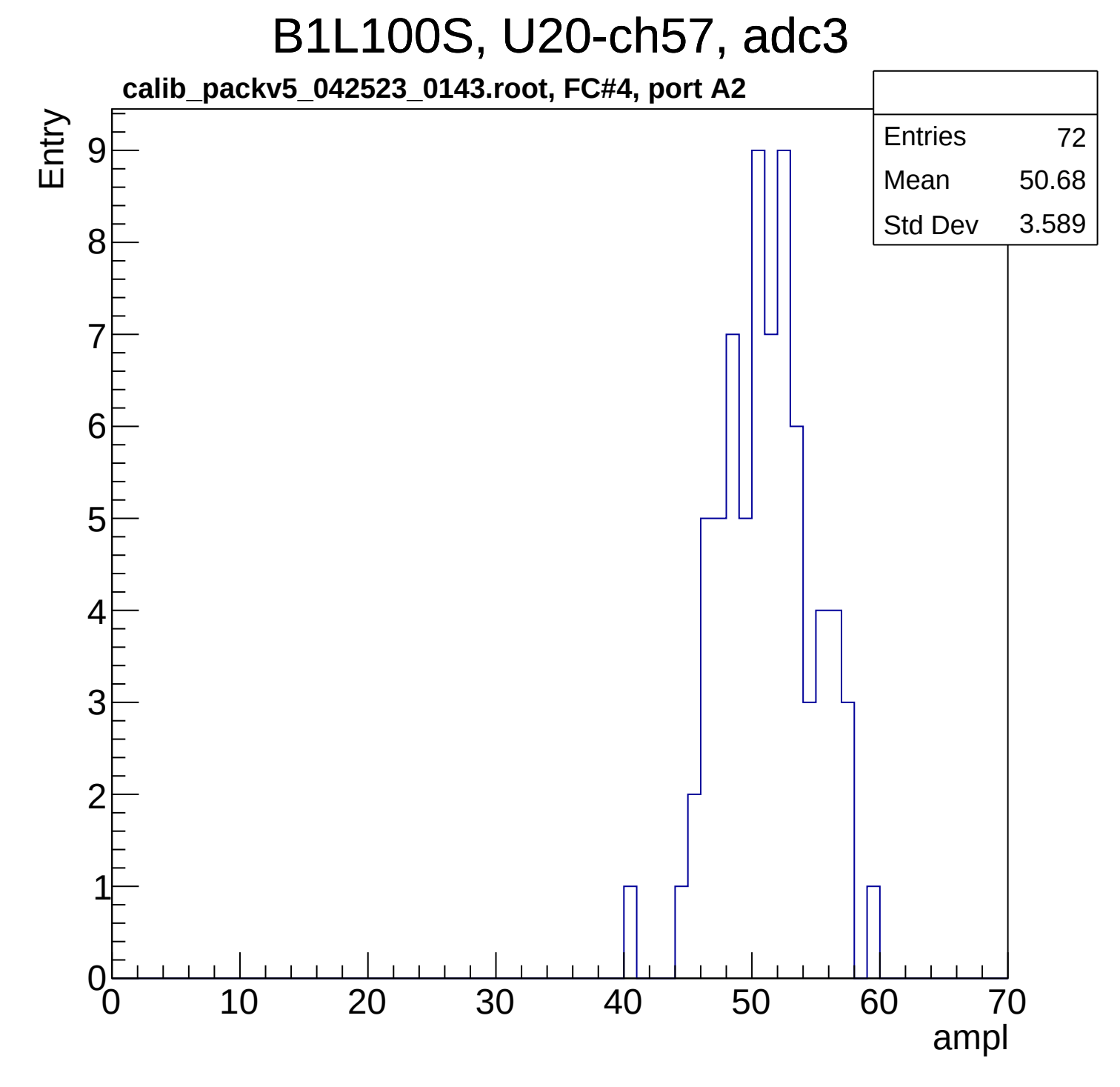
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U20-ch57, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.68
Std Dev 3.589
Entry
ampl
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2
3
4
5
6
7
8
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0
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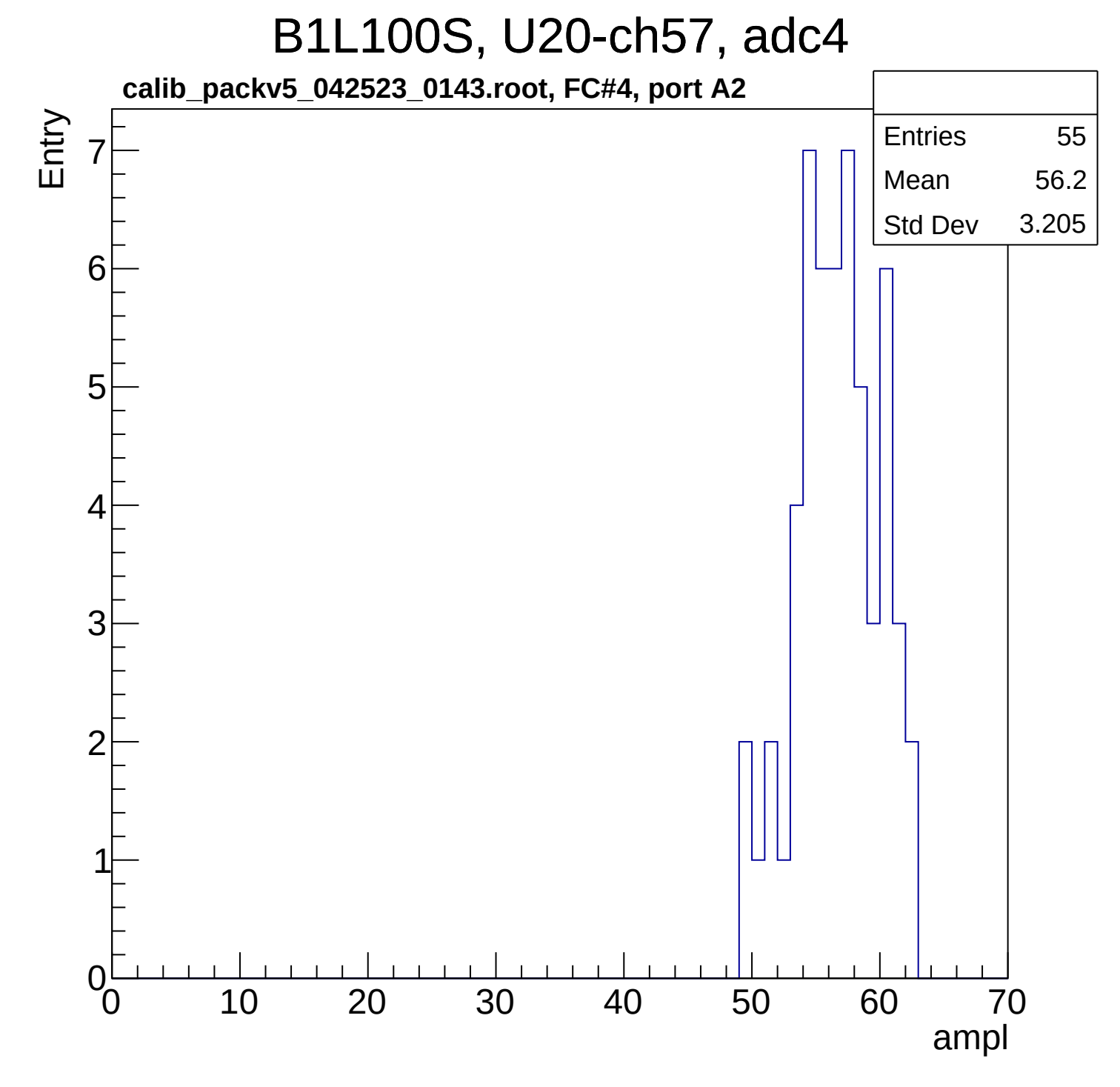
B1L100S, U20-ch57, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.2
Std Dev 3.205
Entry
ampl
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60
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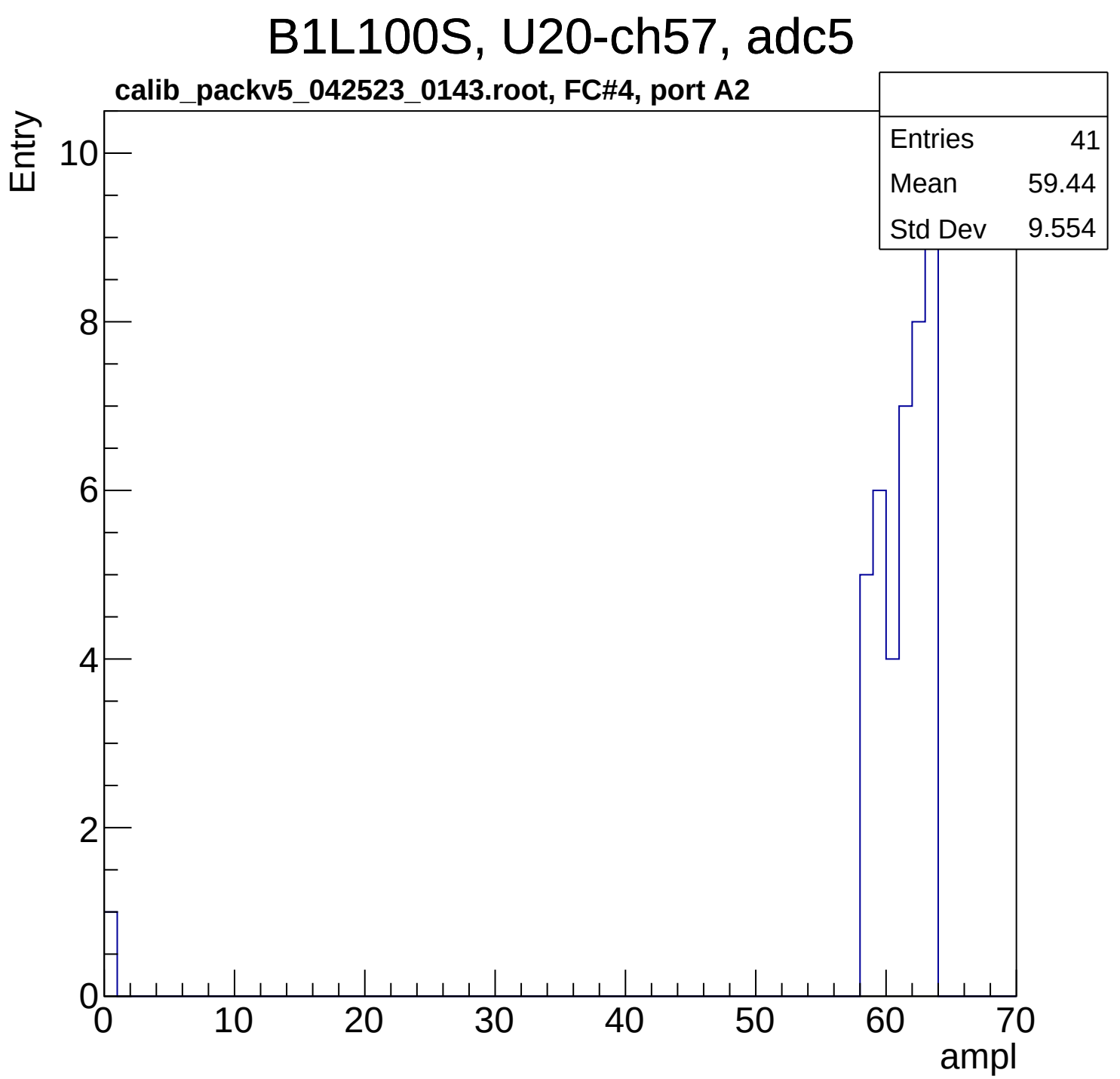
B1L100S, U20-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.44
Std Dev 9.554
Entry
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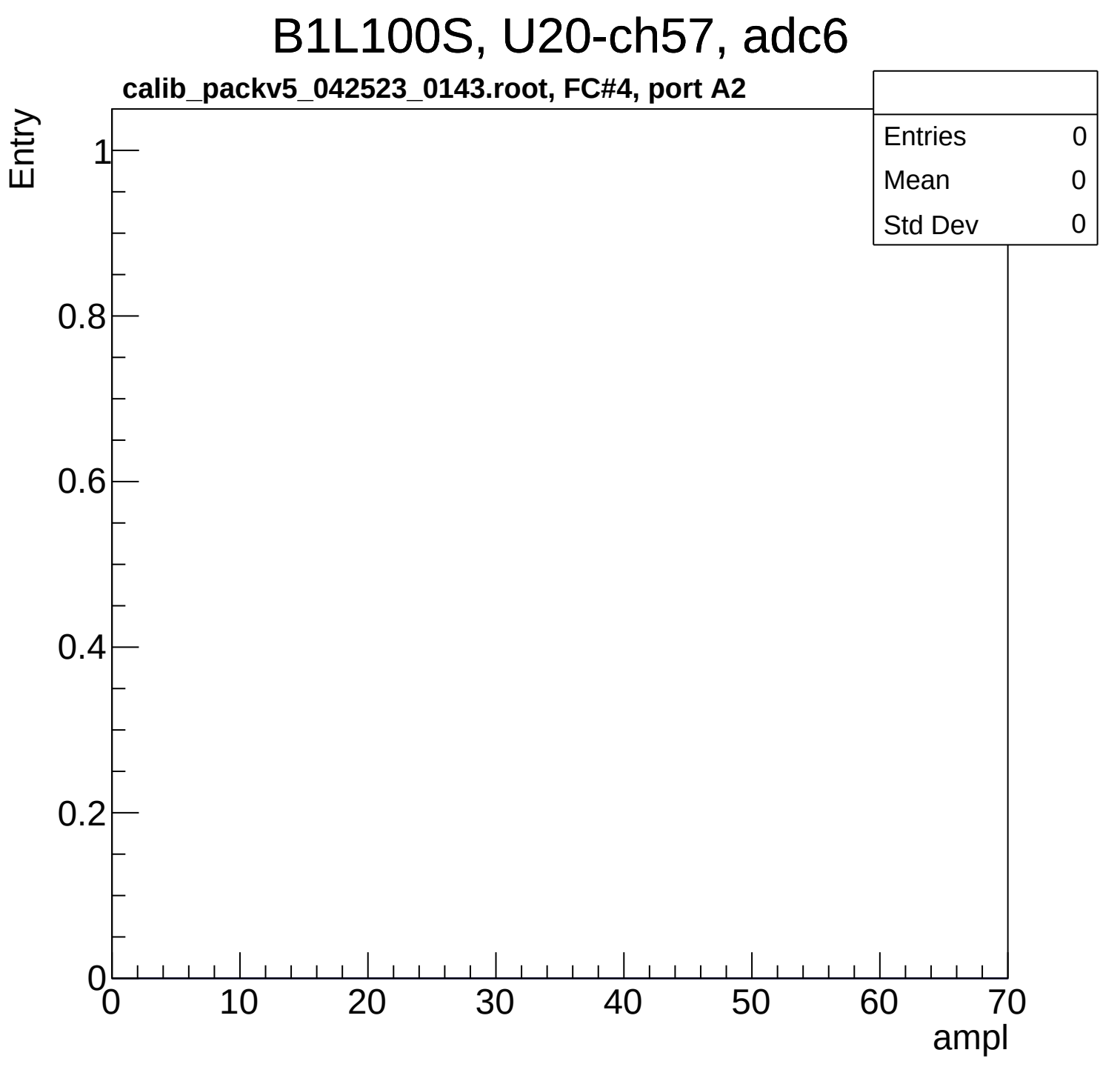
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch57, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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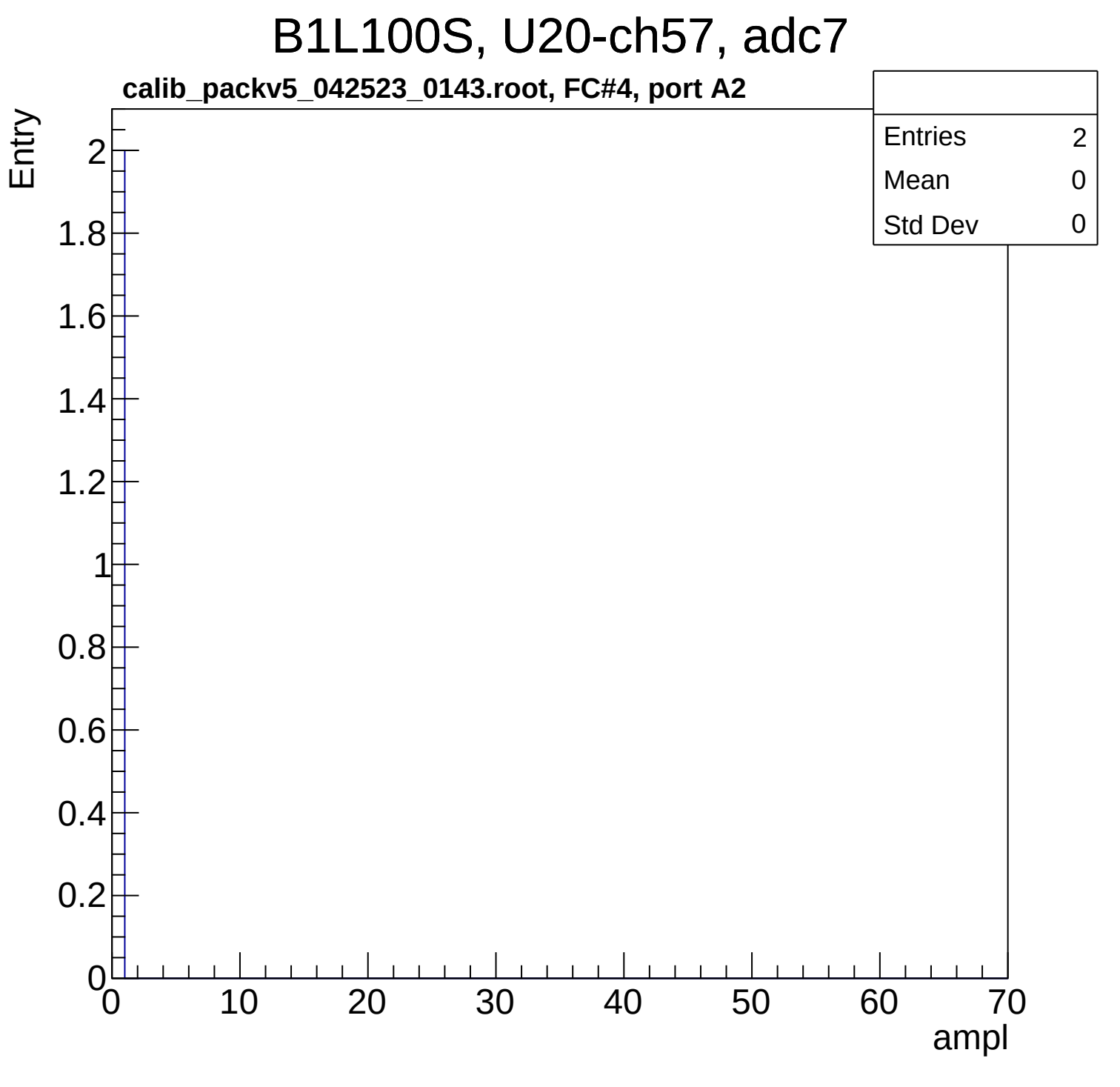
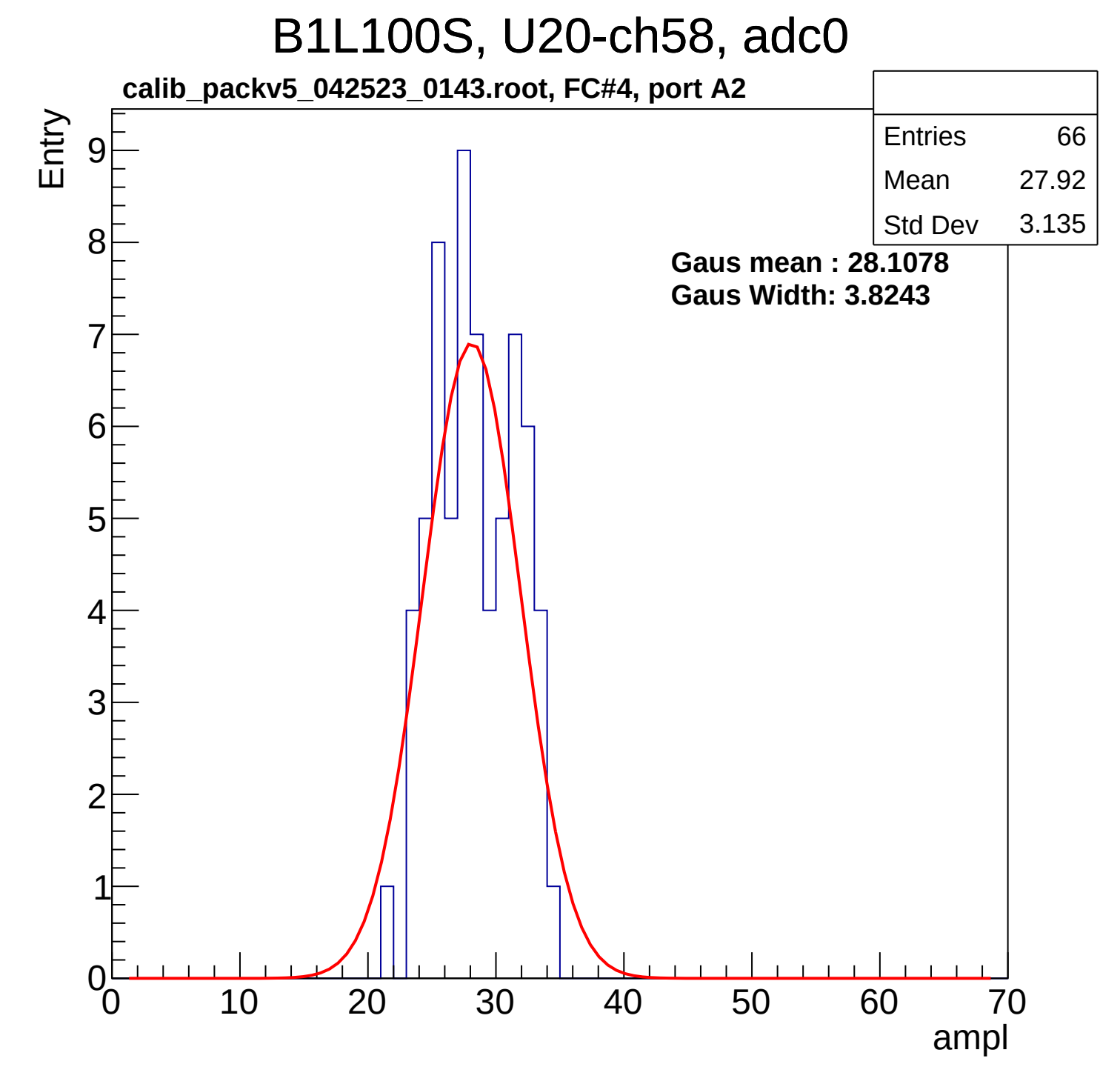
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 27.92
Std Dev 3.135
Gaus mean : 28.1078
Gaus Width: 3.8243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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40
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60
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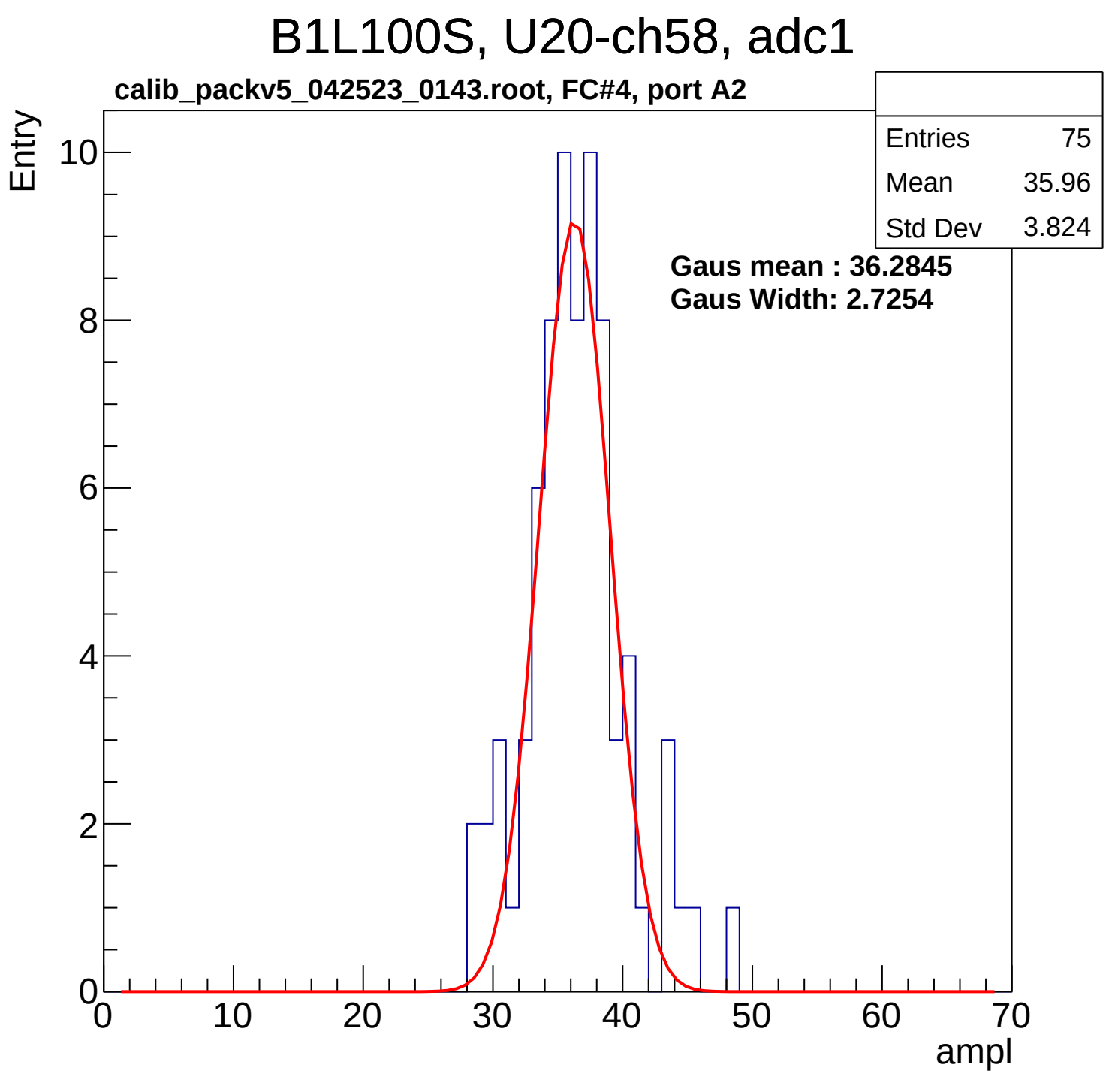
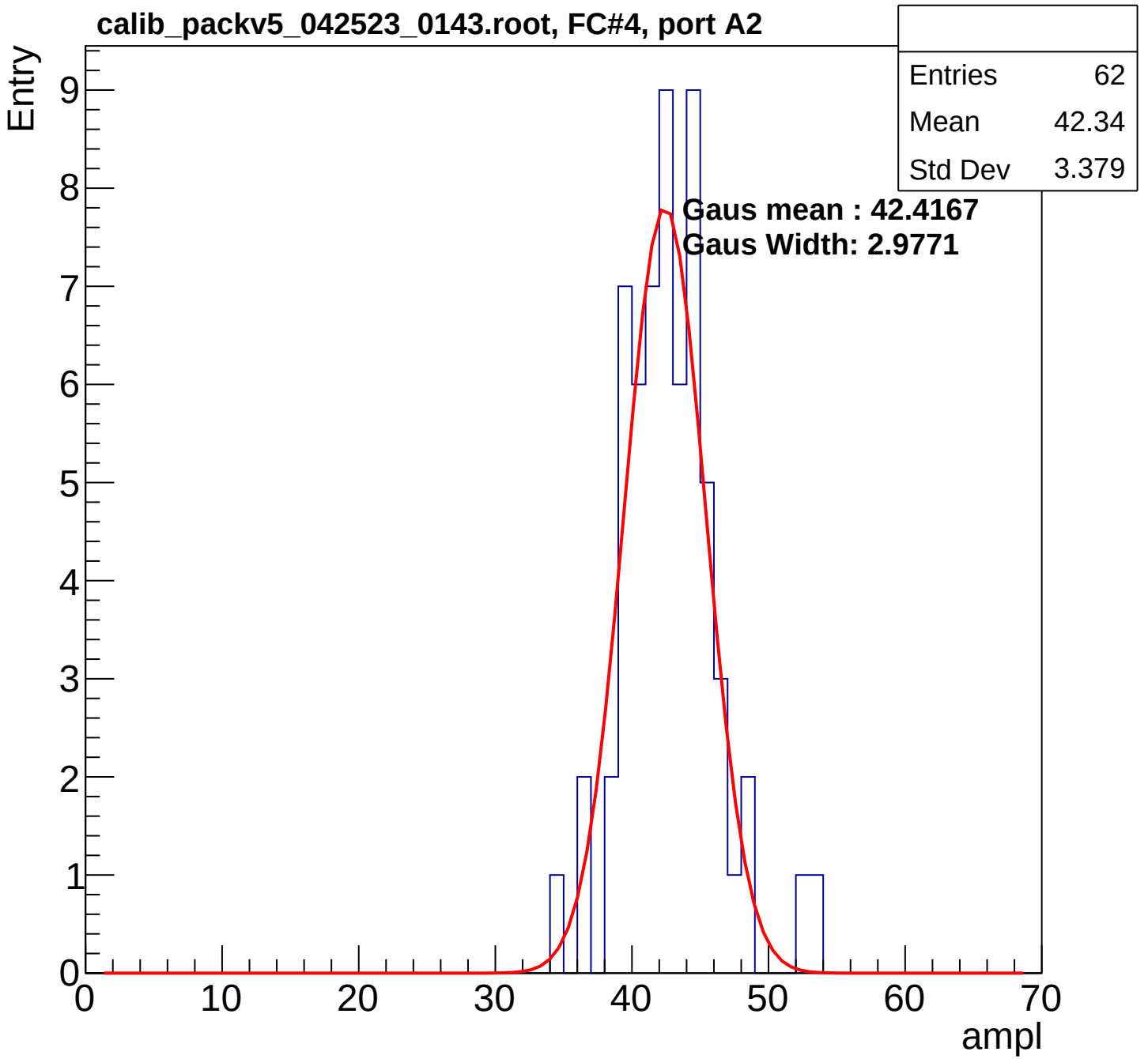

B1L100S, U20-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 35.96
Std Dev 3.824
Gaus mean : 36.2845
Gaus Width: 2.7254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.34
Std Dev 3.379
Gaus mean : 42.4167
Gaus Width: 2.9771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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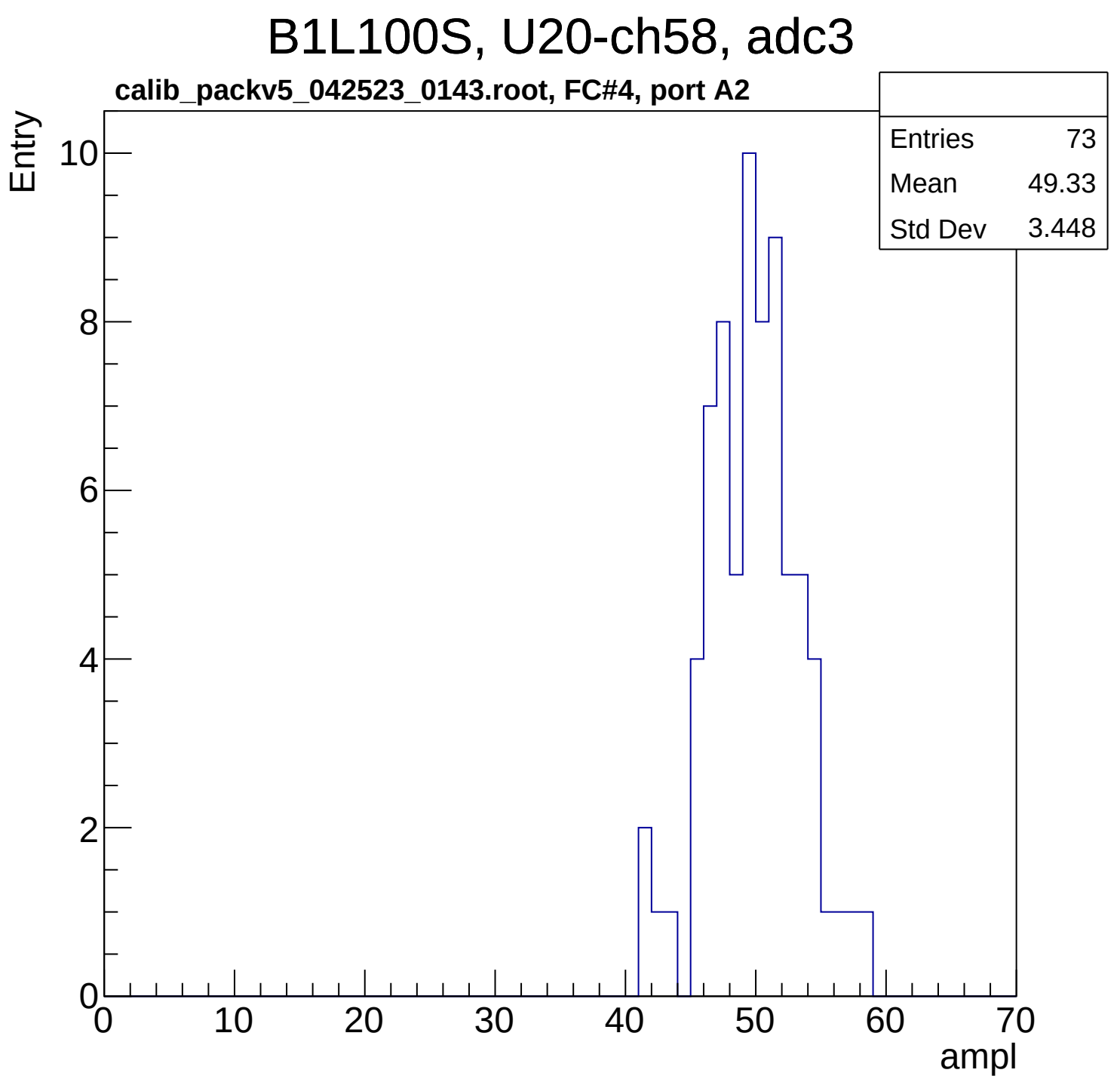
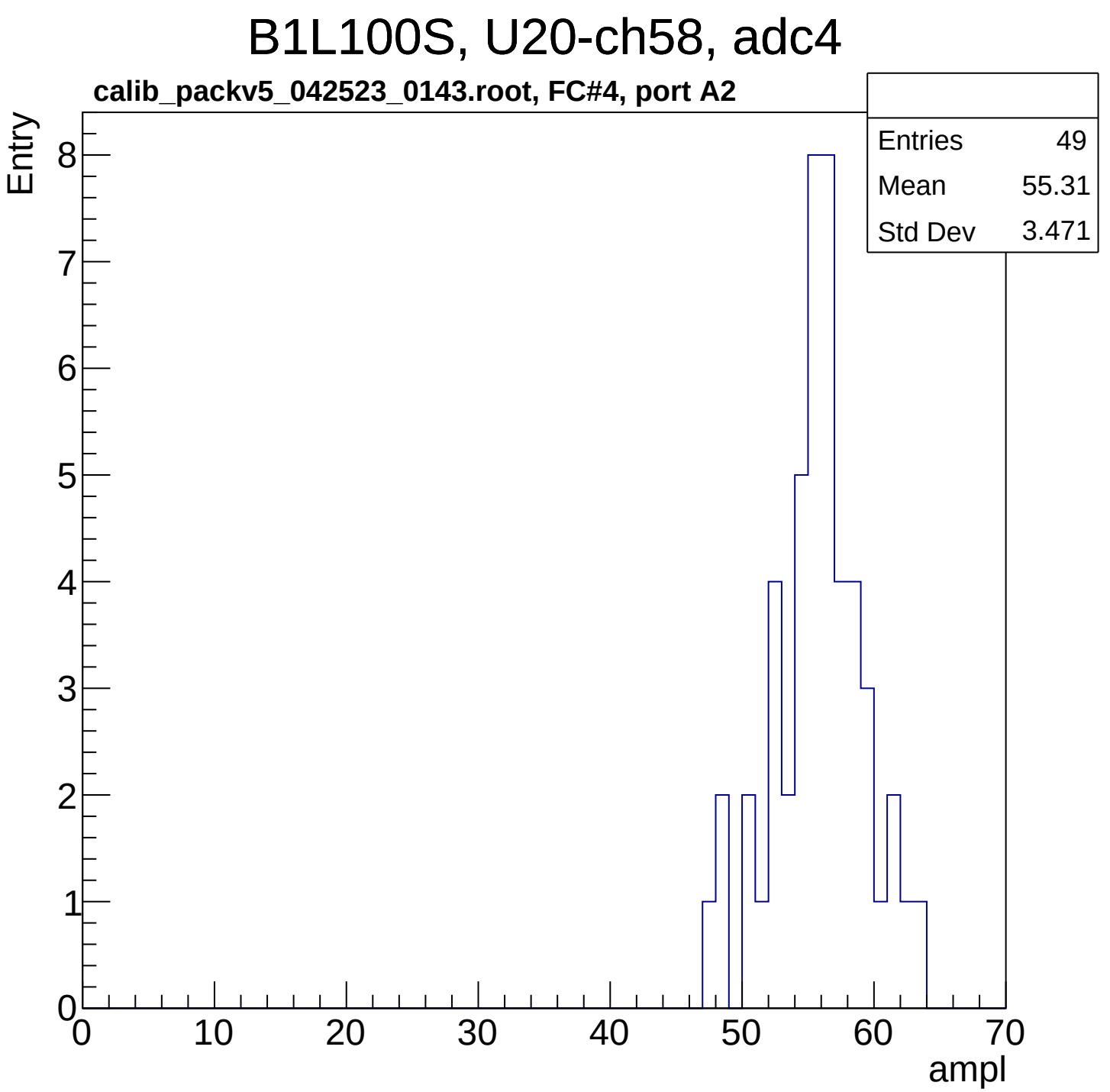

B1L100S, U20-ch58, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 49.33
Std Dev 3.448
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.31
Std Dev 3.471
Entry
ampl
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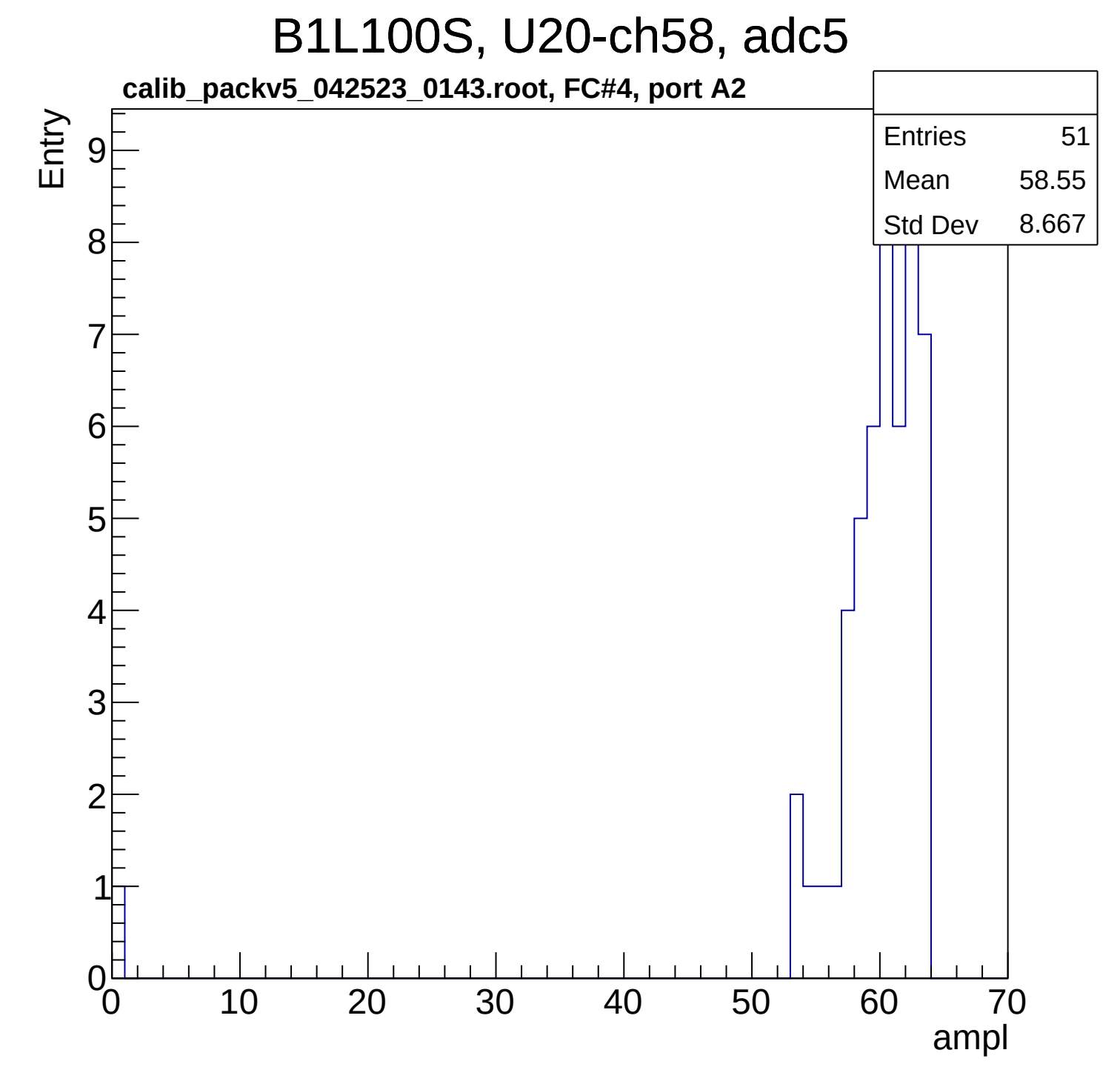
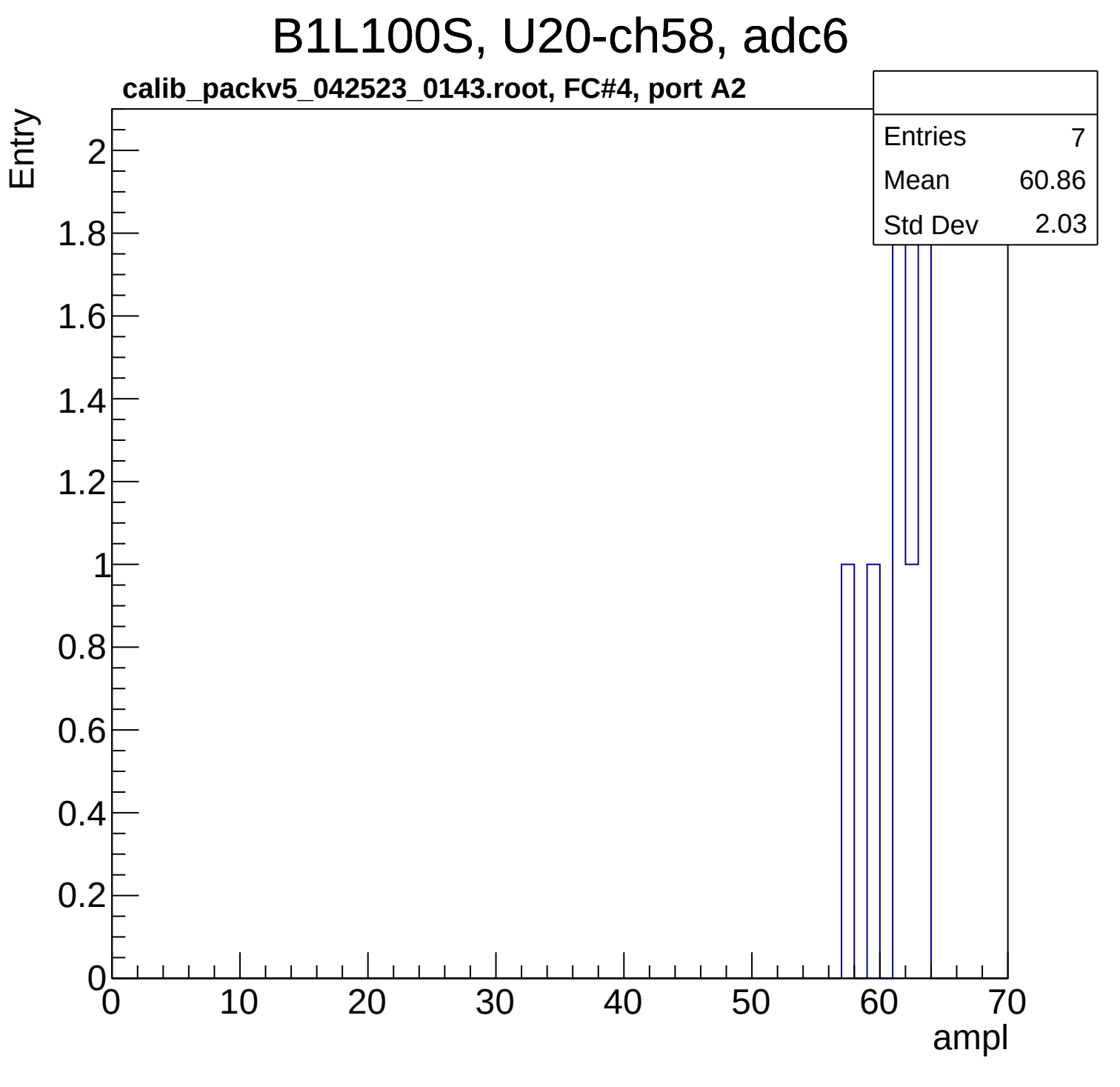
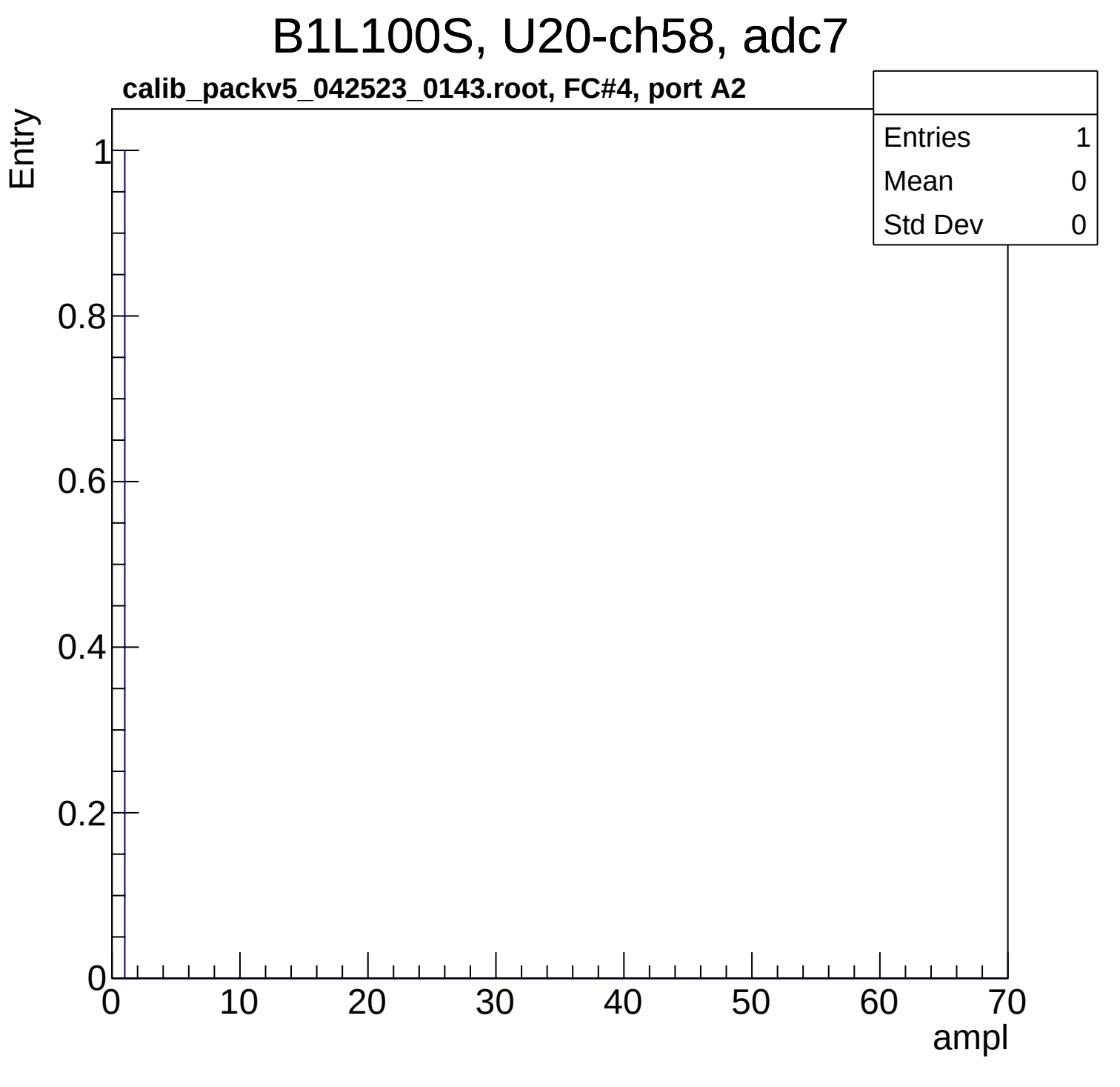
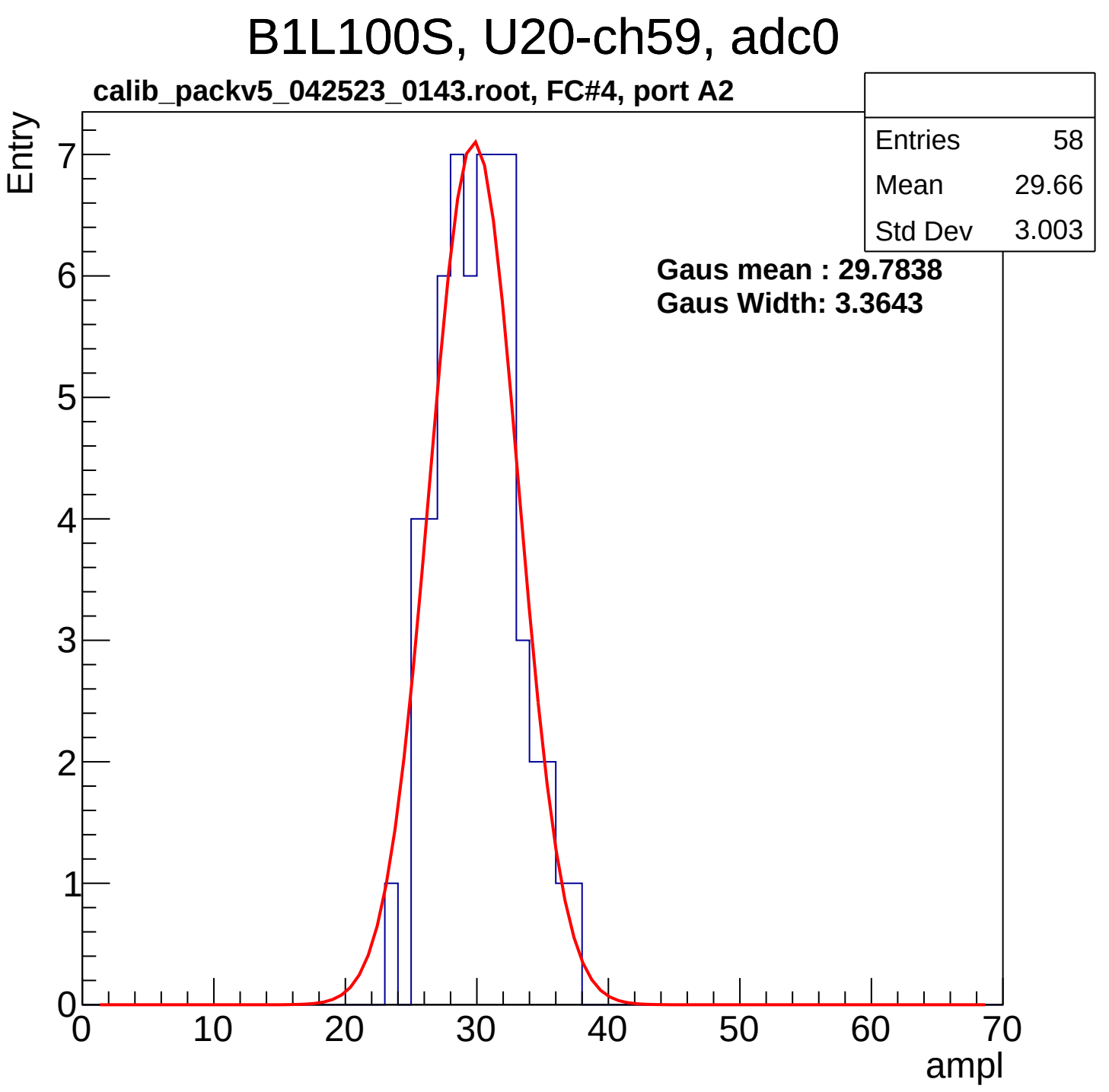
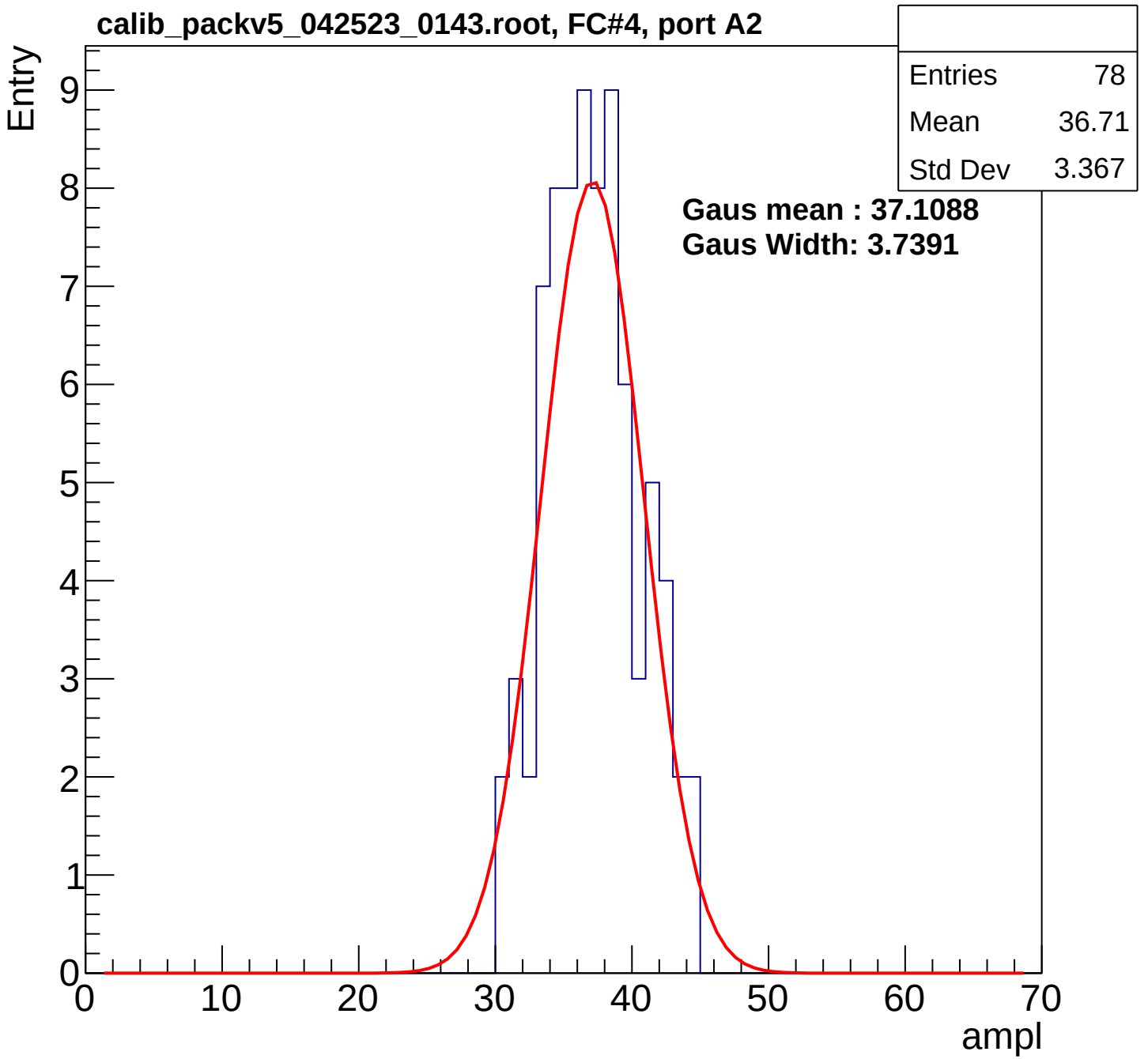
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

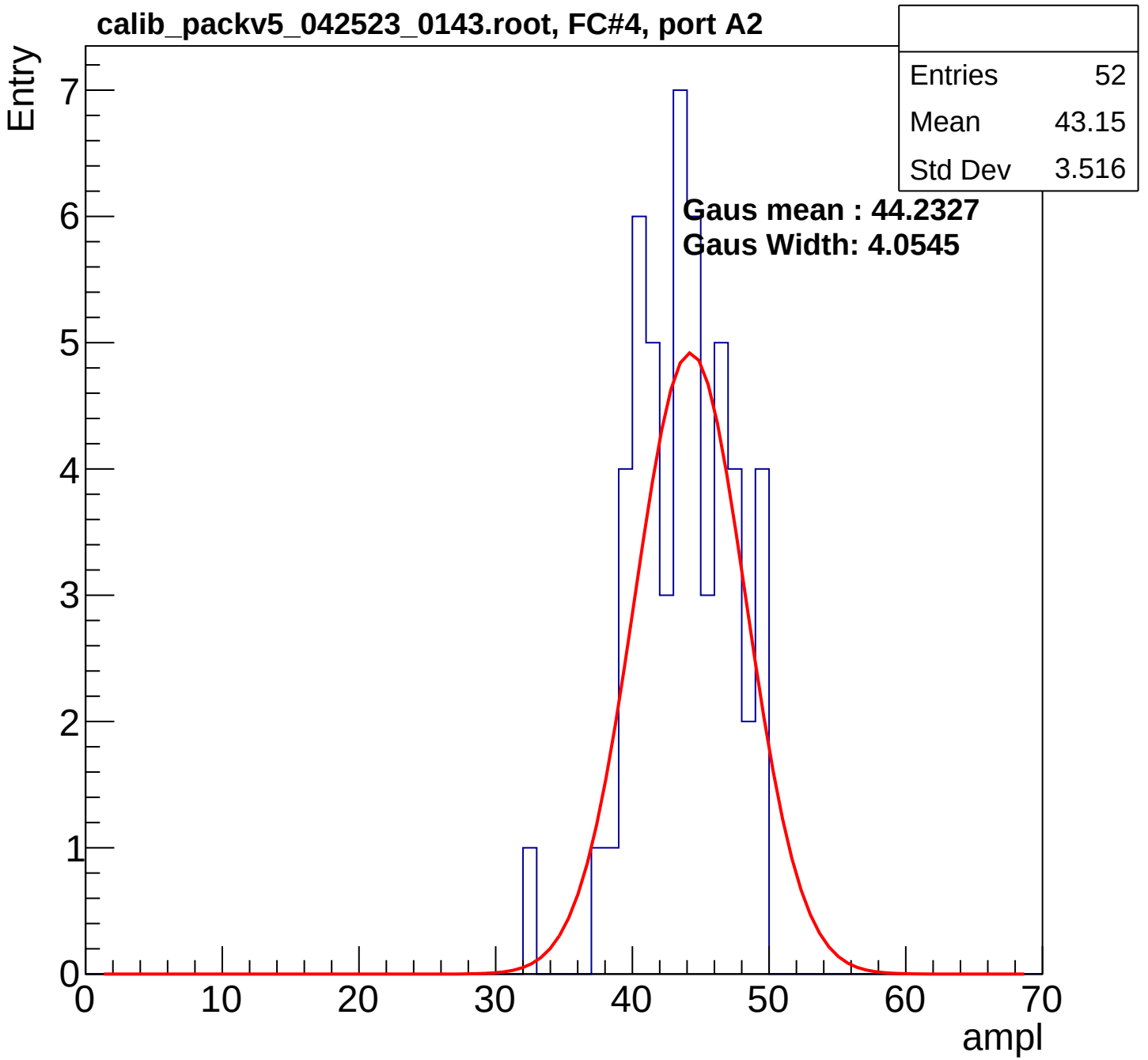

B1L100S, U20-ch58, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.55
Std Dev 8.667
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 60.86
Std Dev 2.03
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch58, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 29.66
Std Dev 3.003
Gaus mean : 29.7838
Gaus Width: 3.3643
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.71
Std Dev 3.367
Gaus mean : 37.1088
Gaus Width: 3.7391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.15
Std Dev 3.516
Gaus mean : 44.2327
Gaus Width: 4.0545
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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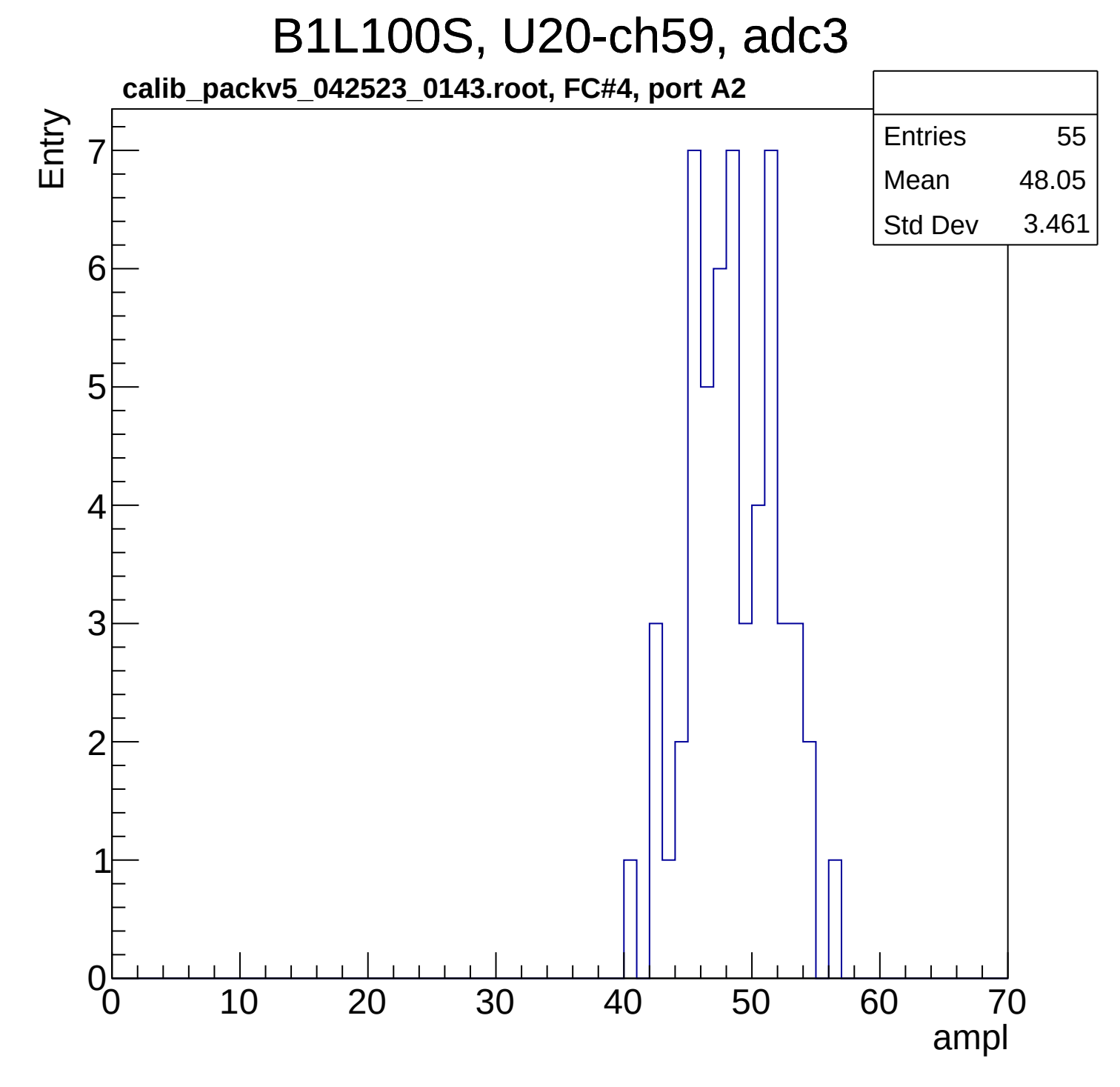
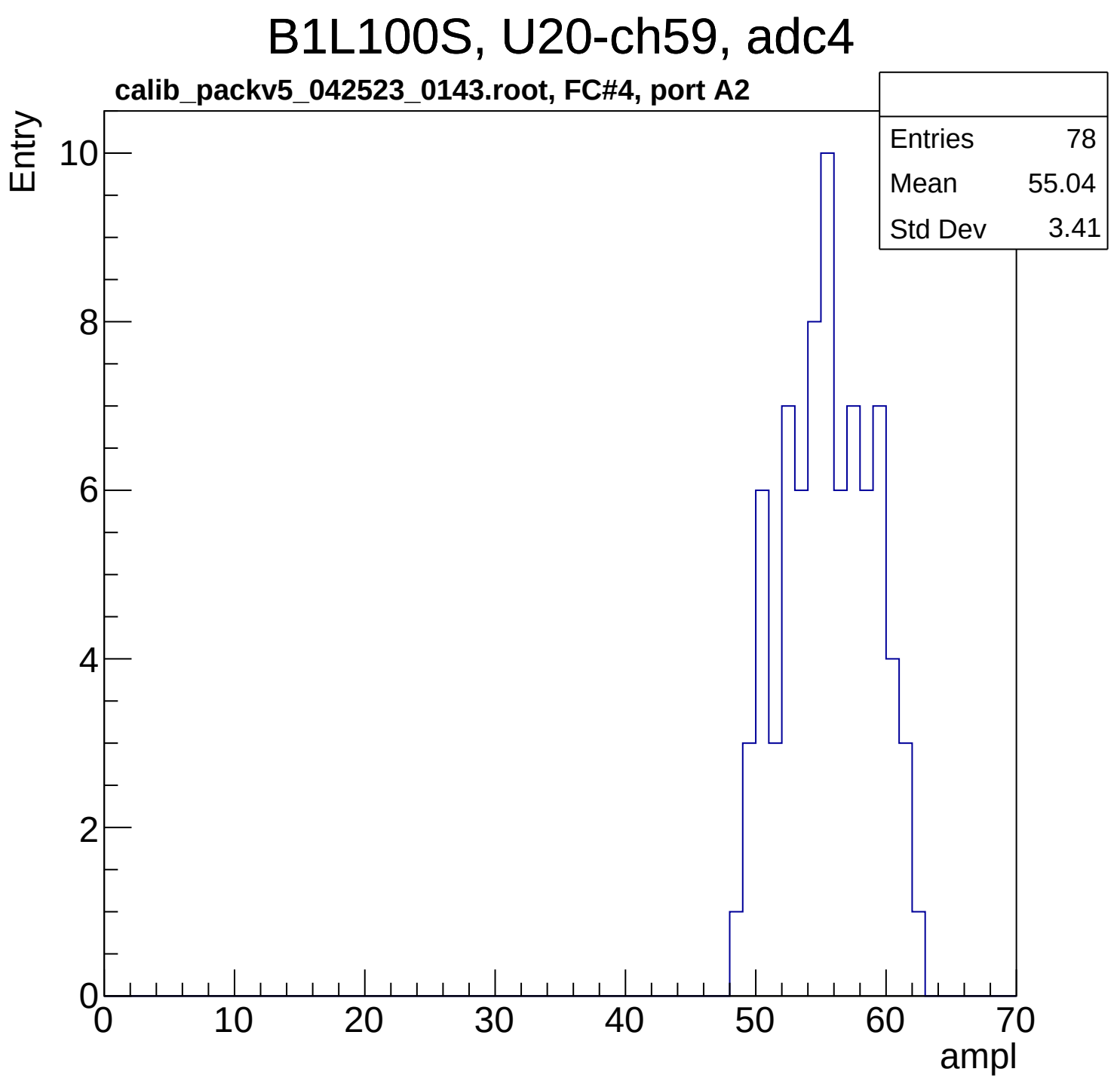
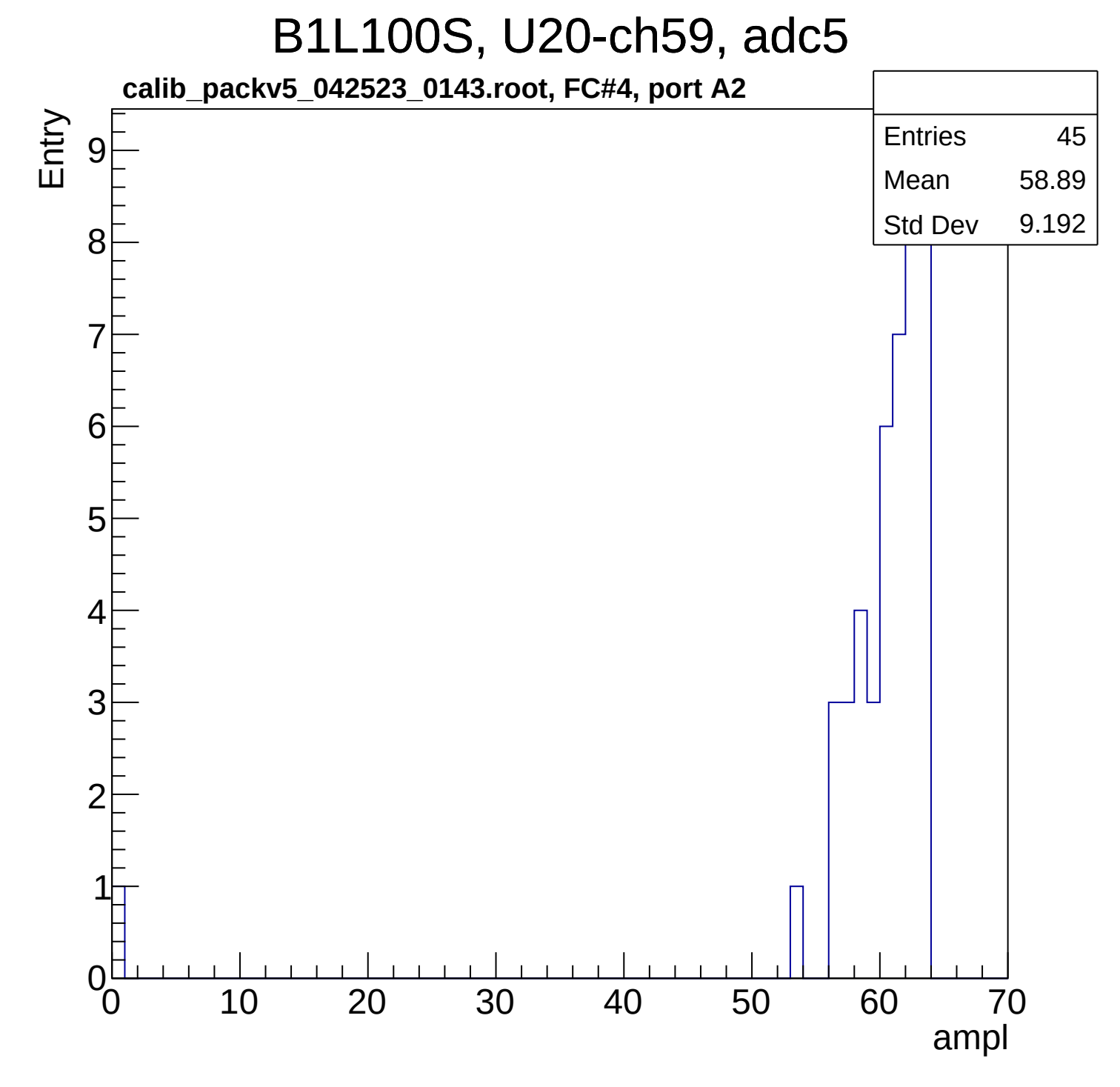
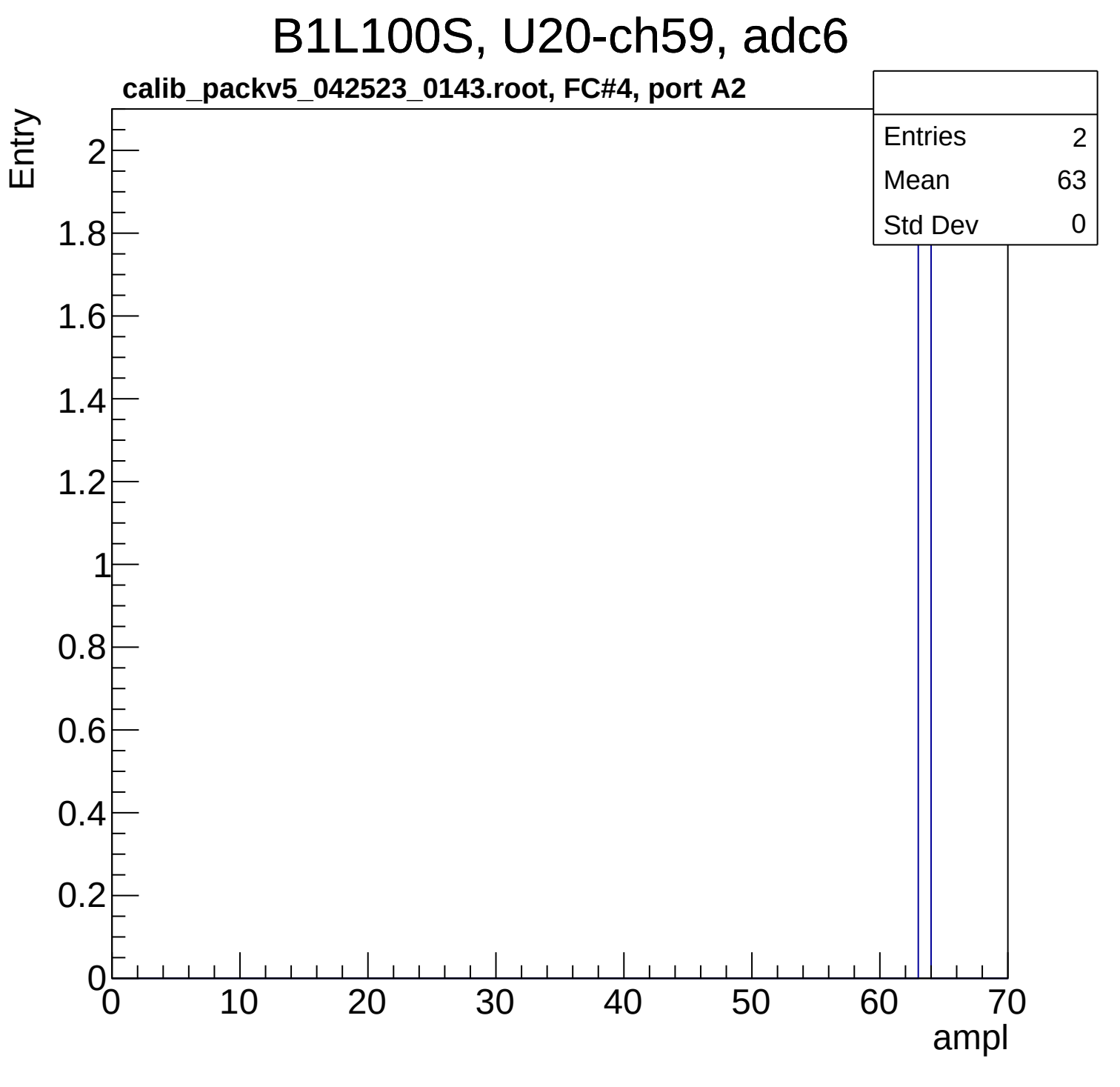
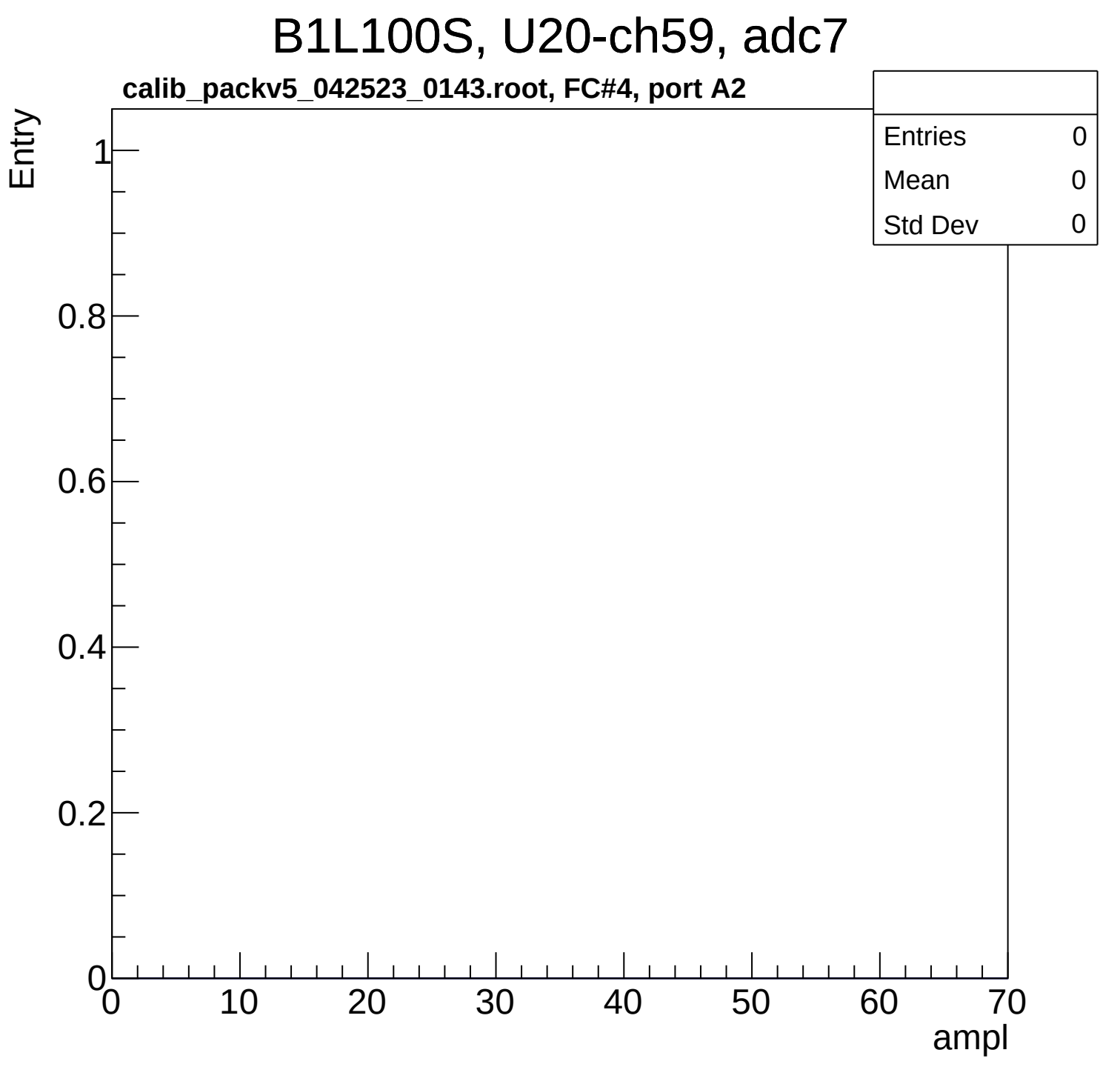
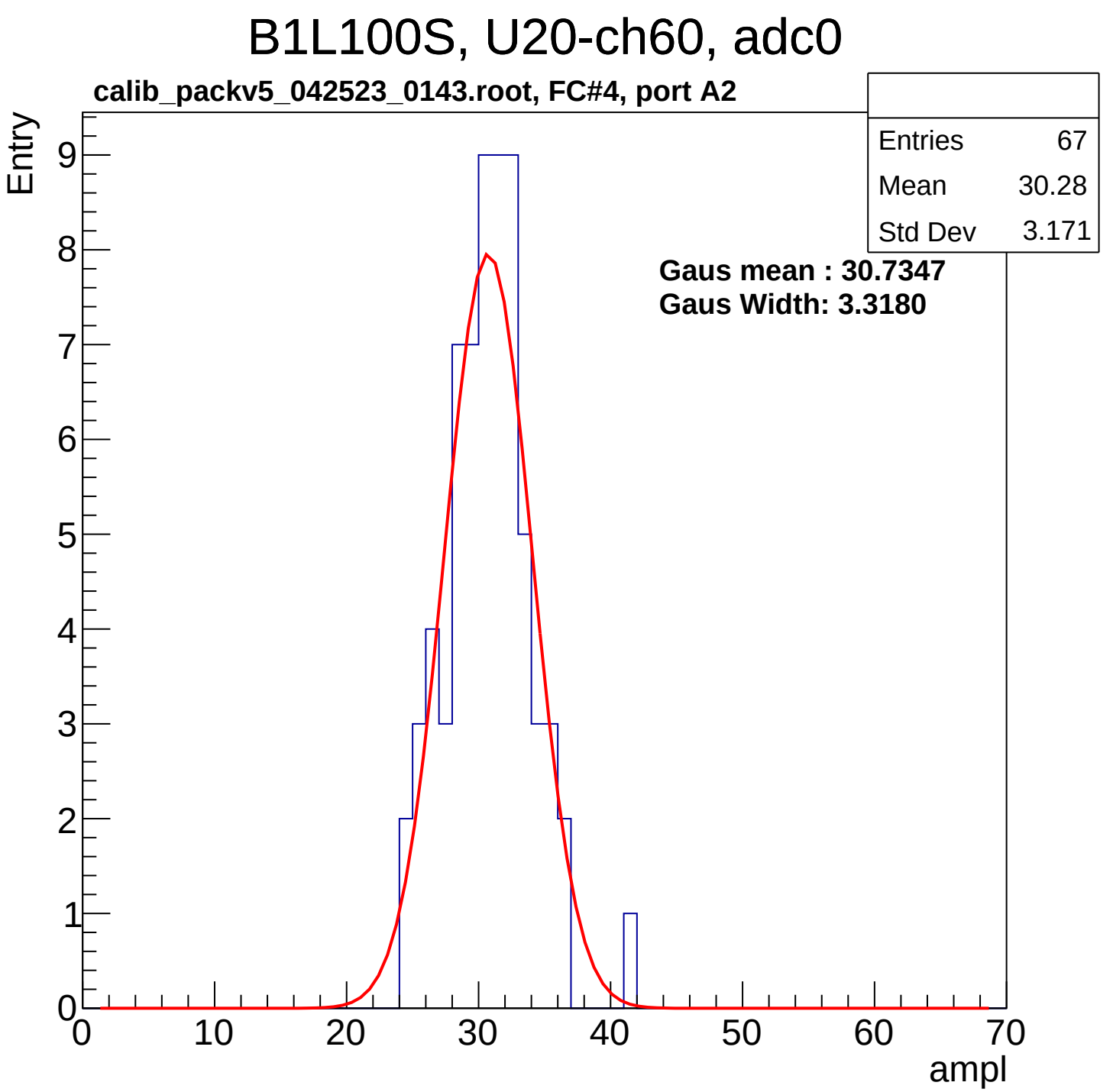

B1L100S, U20-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 48.05
Std Dev 3.461
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

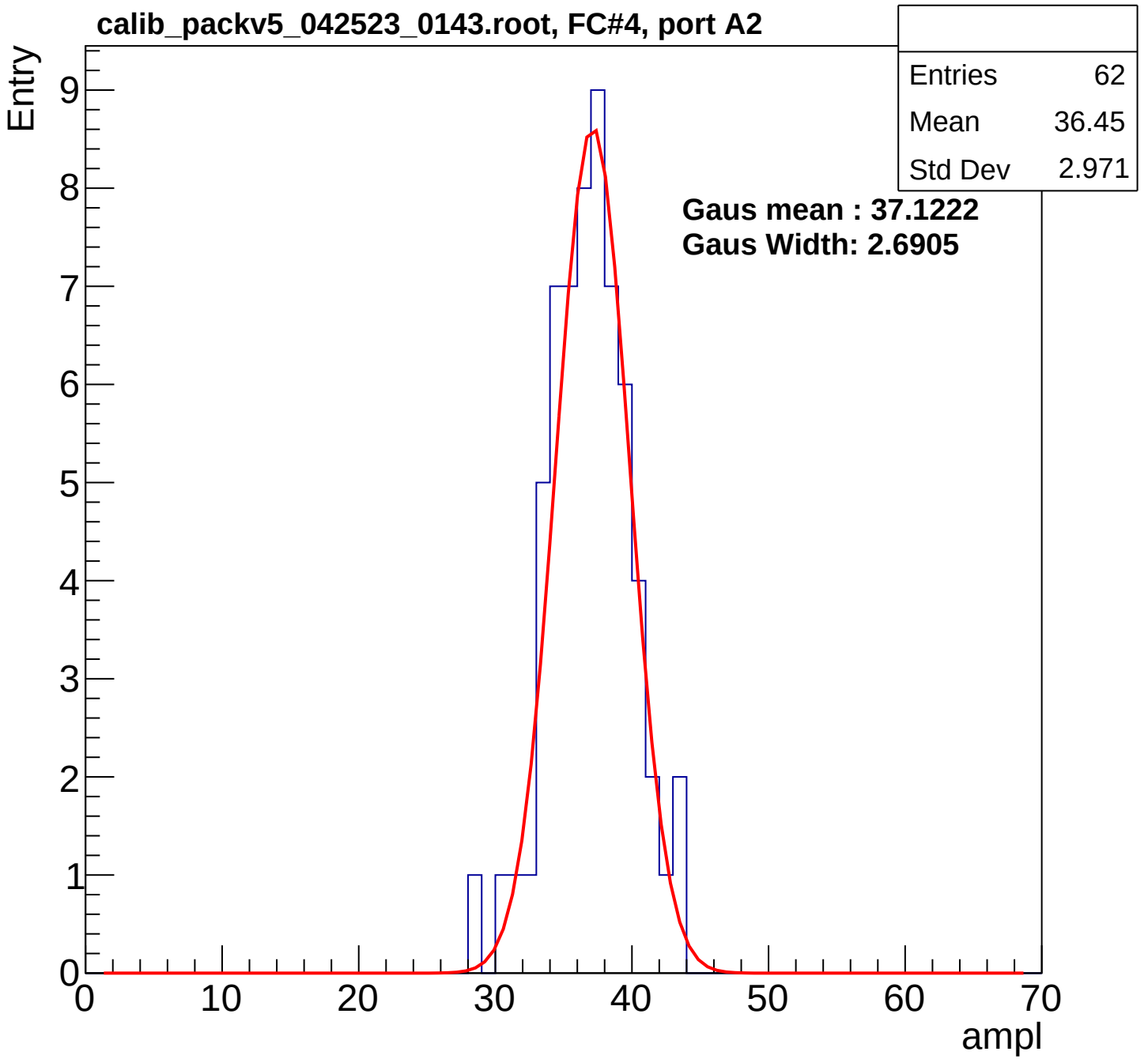

B1L100S, U20-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 55.04
Std Dev 3.41
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

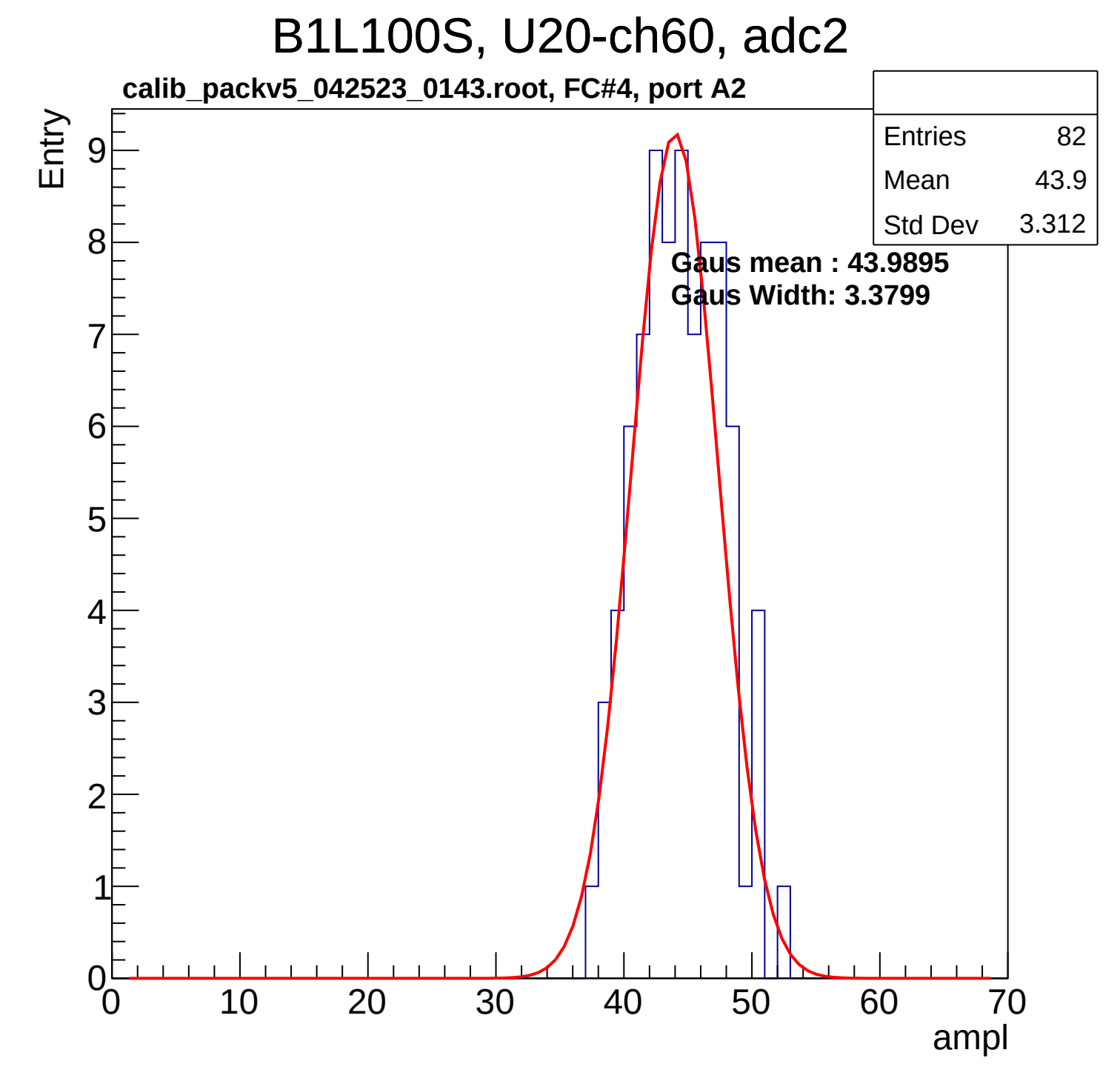

B1L100S, U20-ch59, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.89
Std Dev 9.192
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch59, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.28
Std Dev 3.171
Gaus mean : 30.7347
Gaus Width: 3.3180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.45
Std Dev 2.971
Gaus mean : 37.1222
Gaus Width: 2.6905
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U20-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 43.9
Std Dev 3.312
Gaus mean : 43.9895
Gaus Width: 3.3799
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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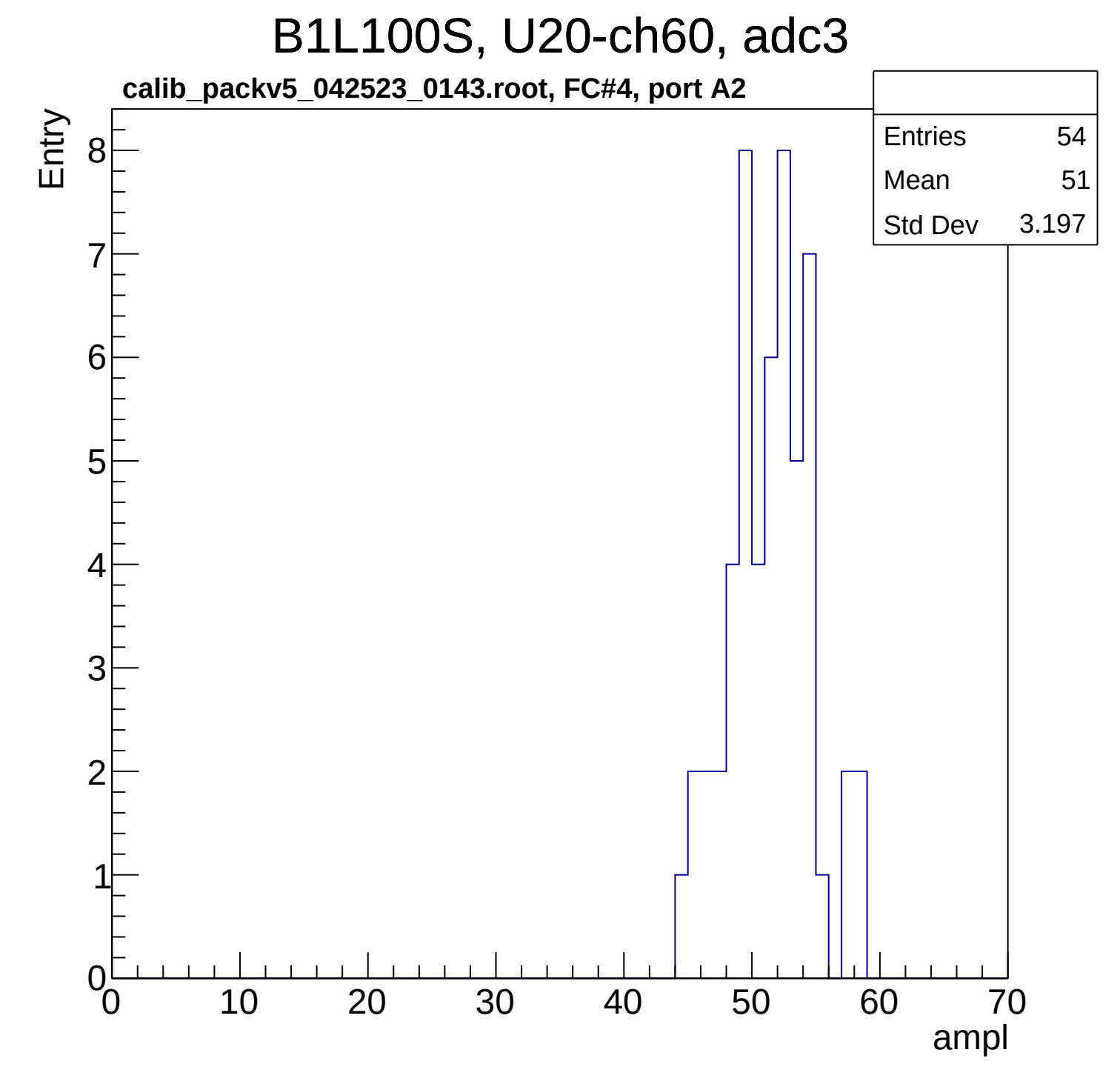
B1L100S, U20-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 51
Std Dev 3.197
Entry
ampl
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2
3
4
5
6
7
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0
10
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50
60
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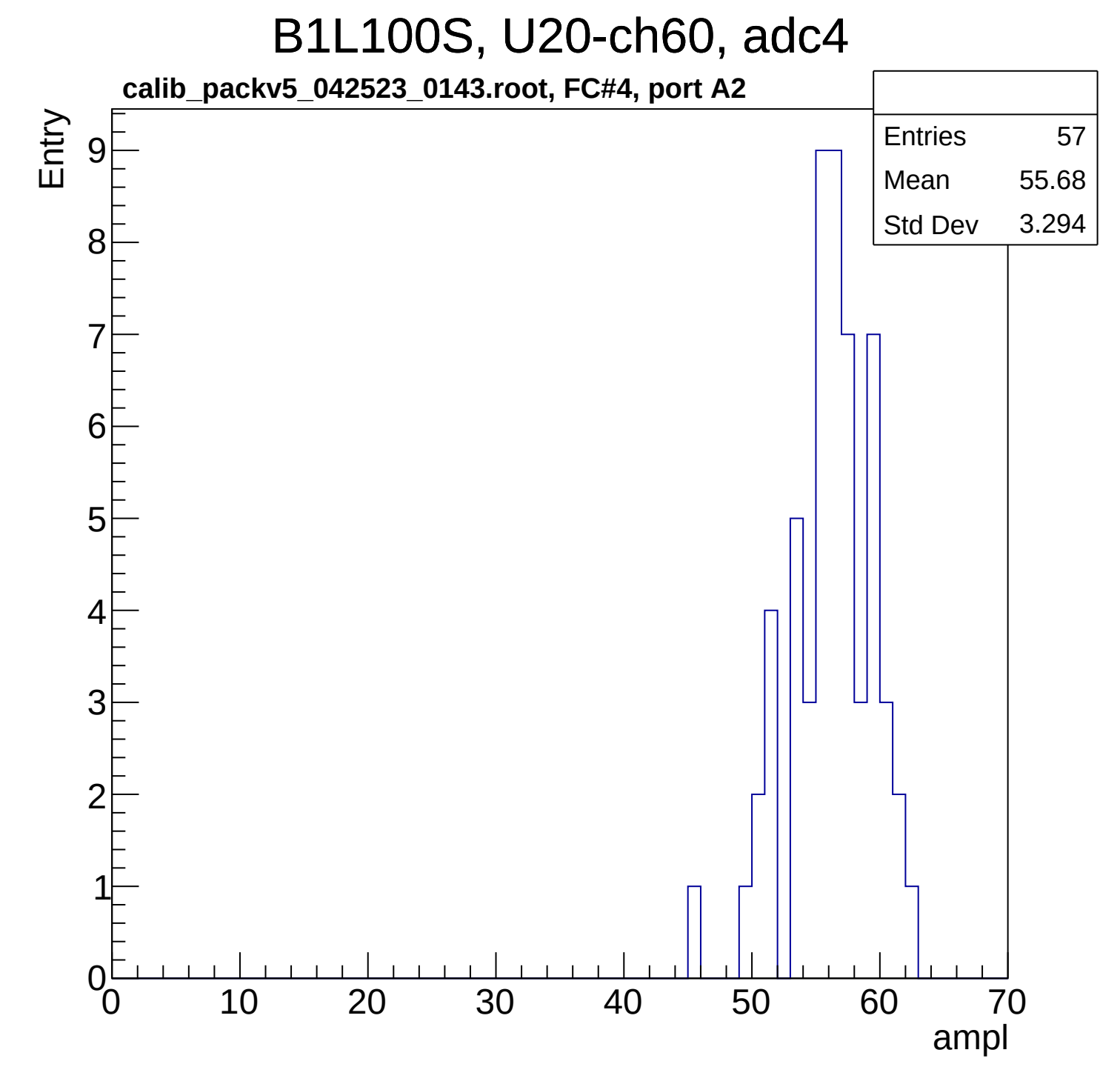
B1L100S, U20-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 55.68
Std Dev 3.294
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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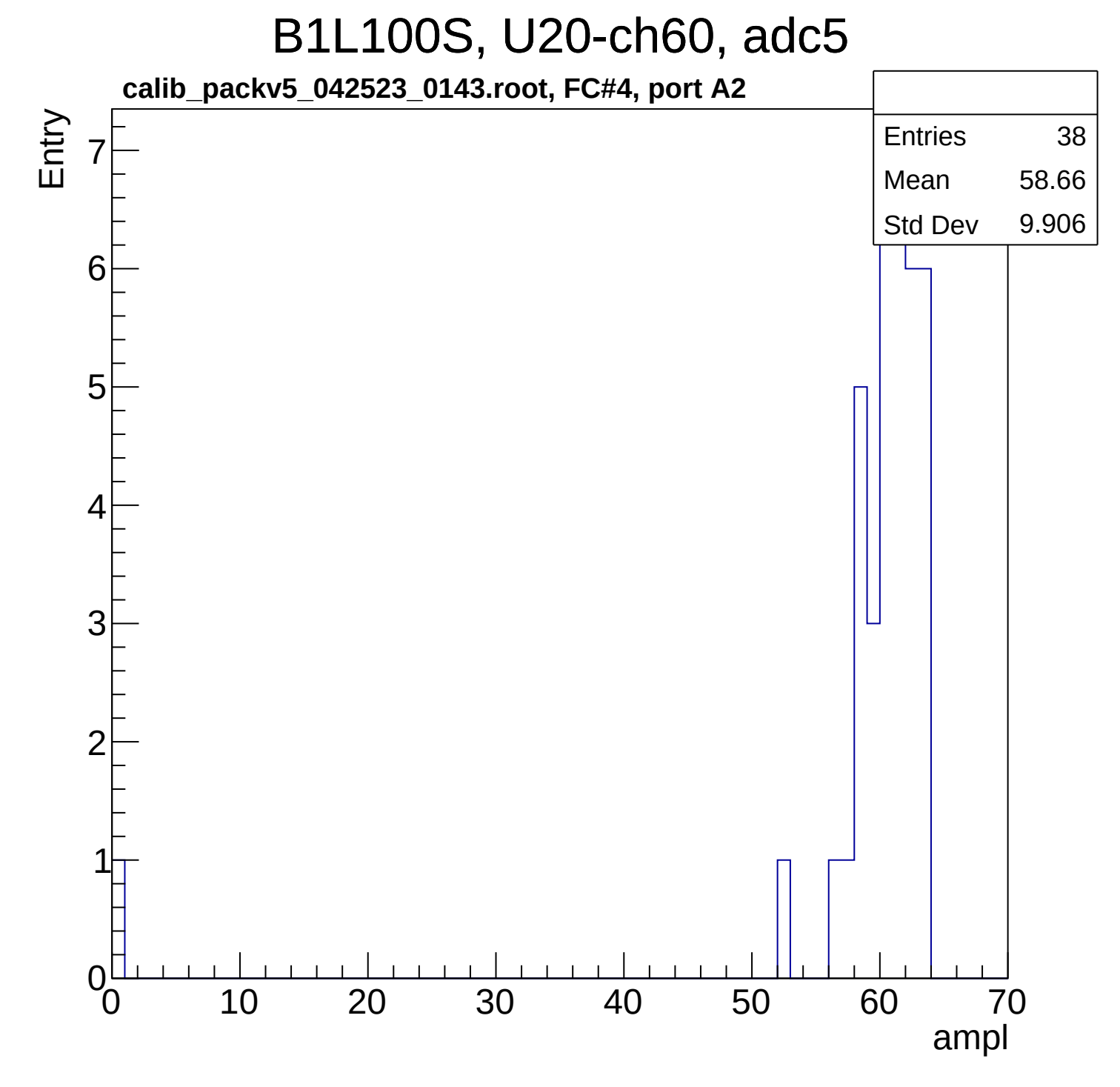
40
50
60
70


B1L100S, U20-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.66
Std Dev 9.906
Entry
ampl
0
1
2
3
4
5
6
7
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20
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40
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60
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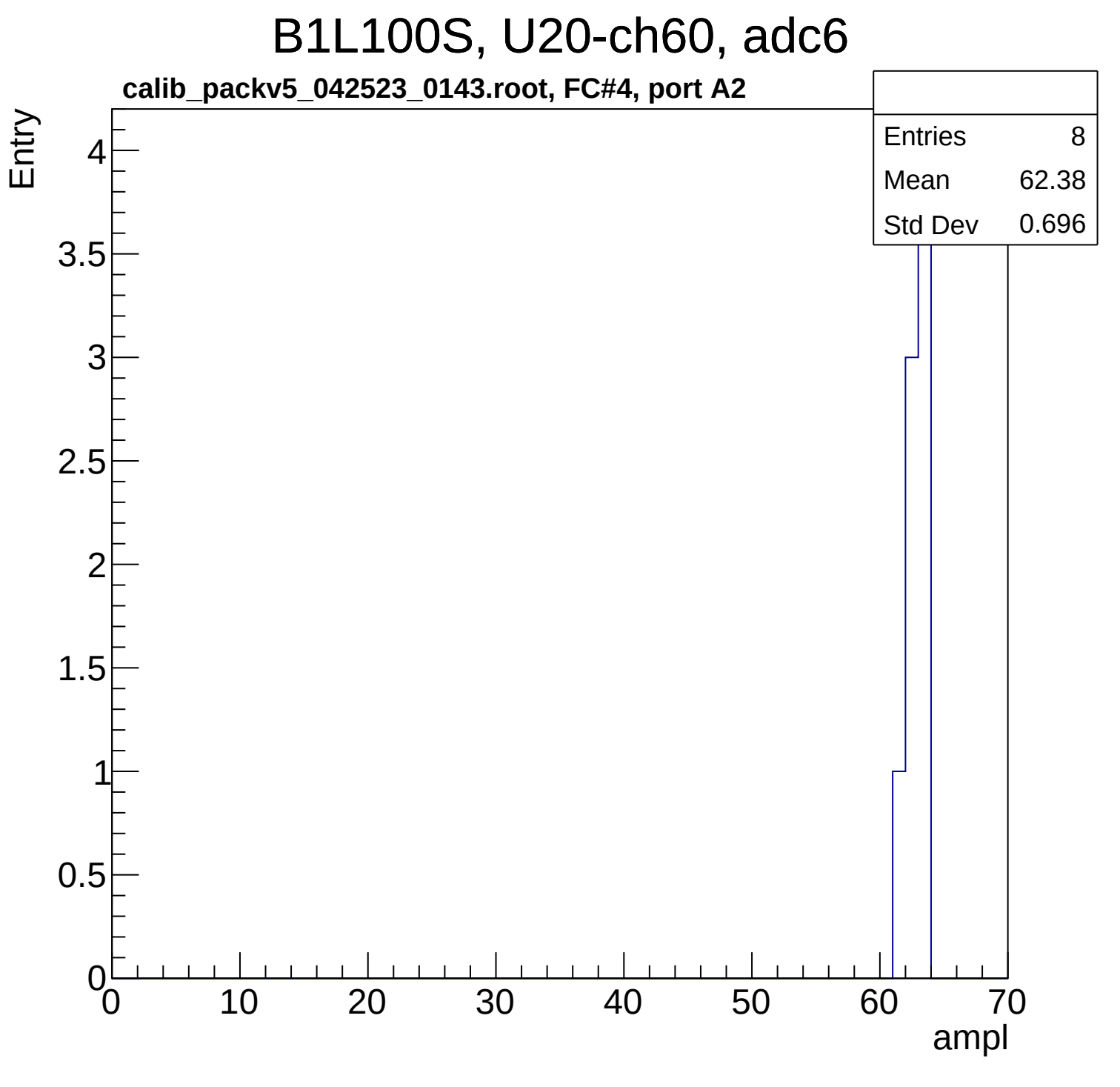
B1L100S, U20-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.38
Std Dev 0.696
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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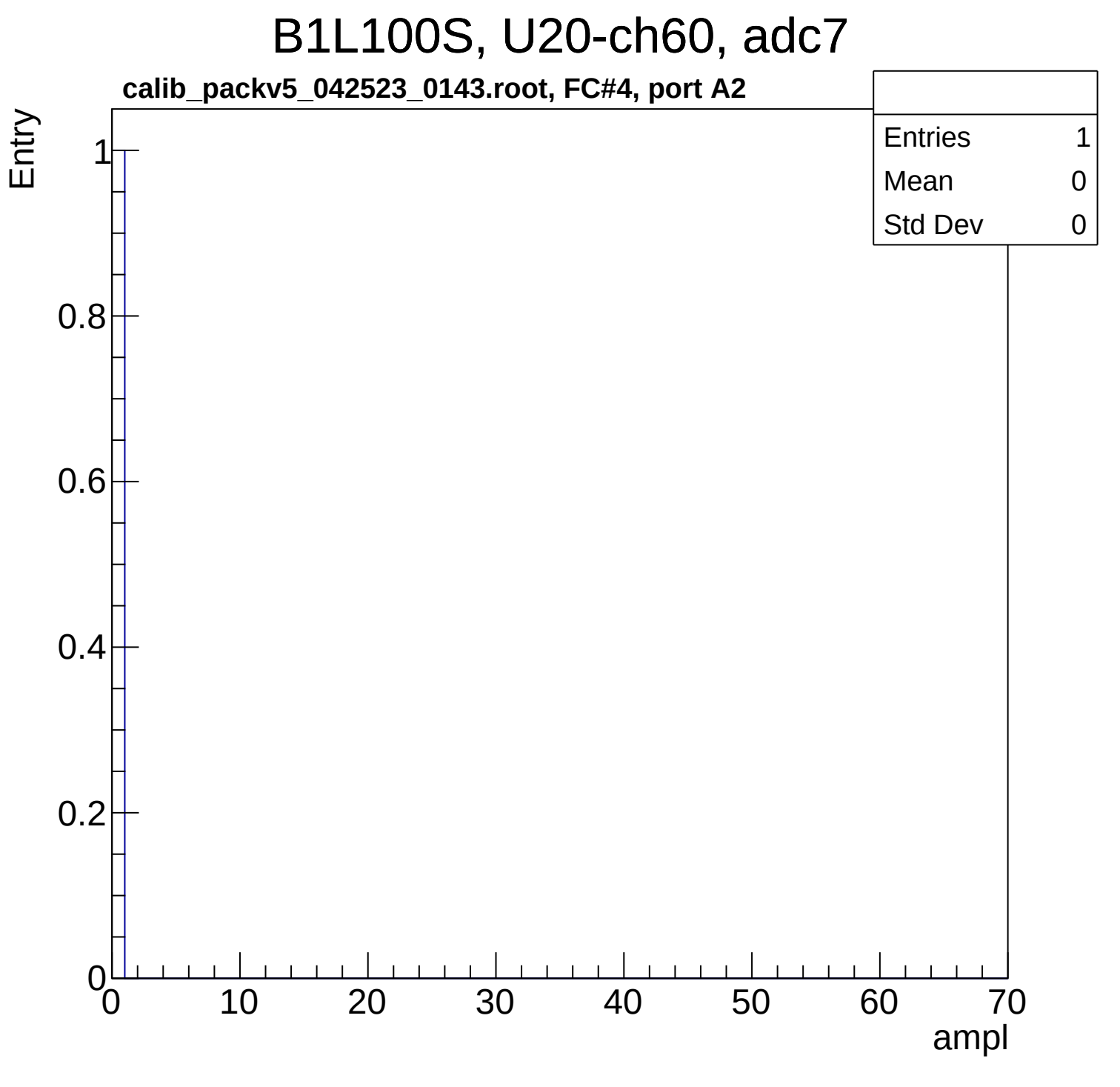
50
60
70


B1L100S, U20-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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10
20
30
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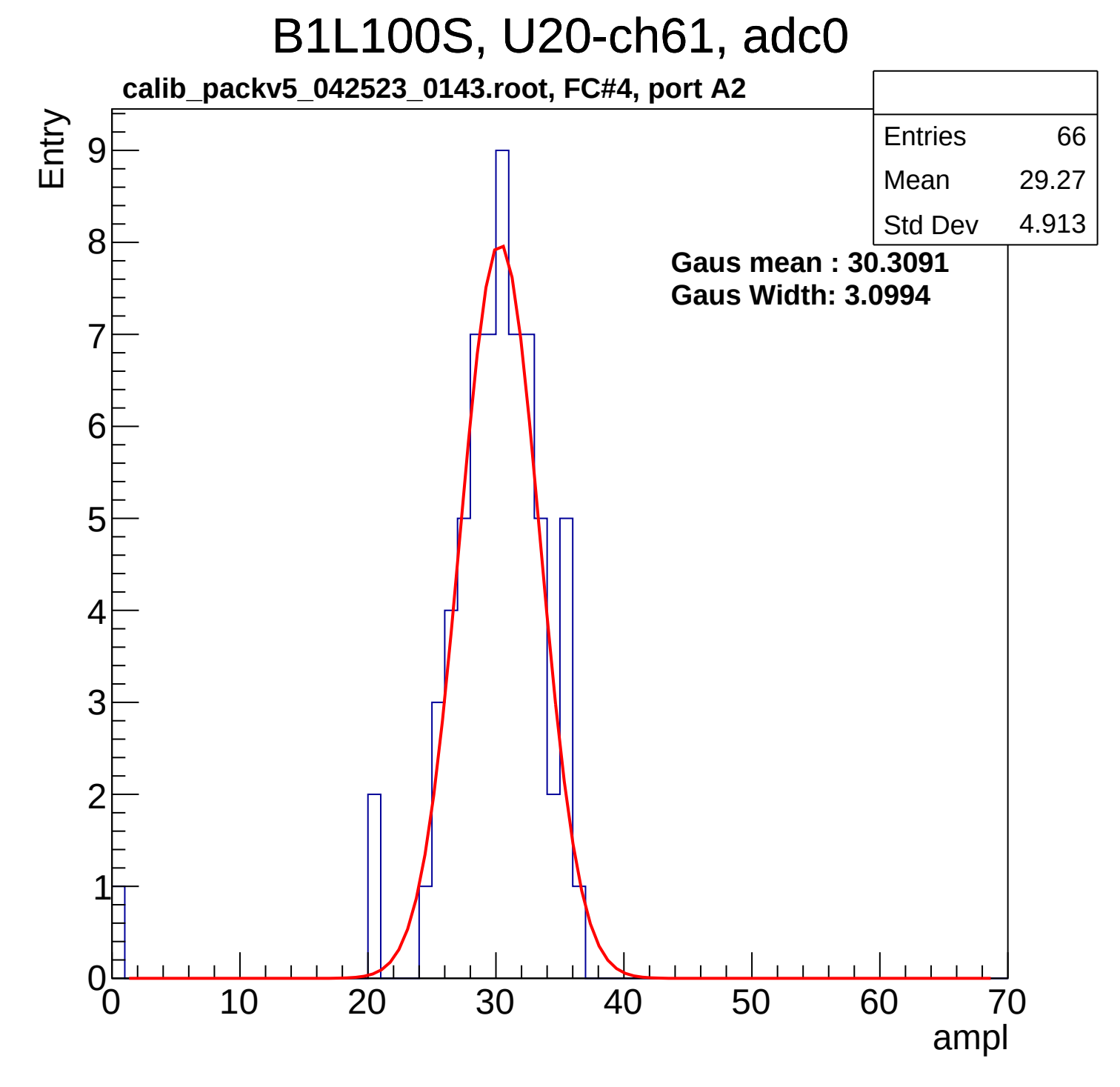
50
60
70
ampl


B1L100S, U20-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.27
Std Dev 4.913
Gaus mean : 30.3091
Gaus Width: 3.0994
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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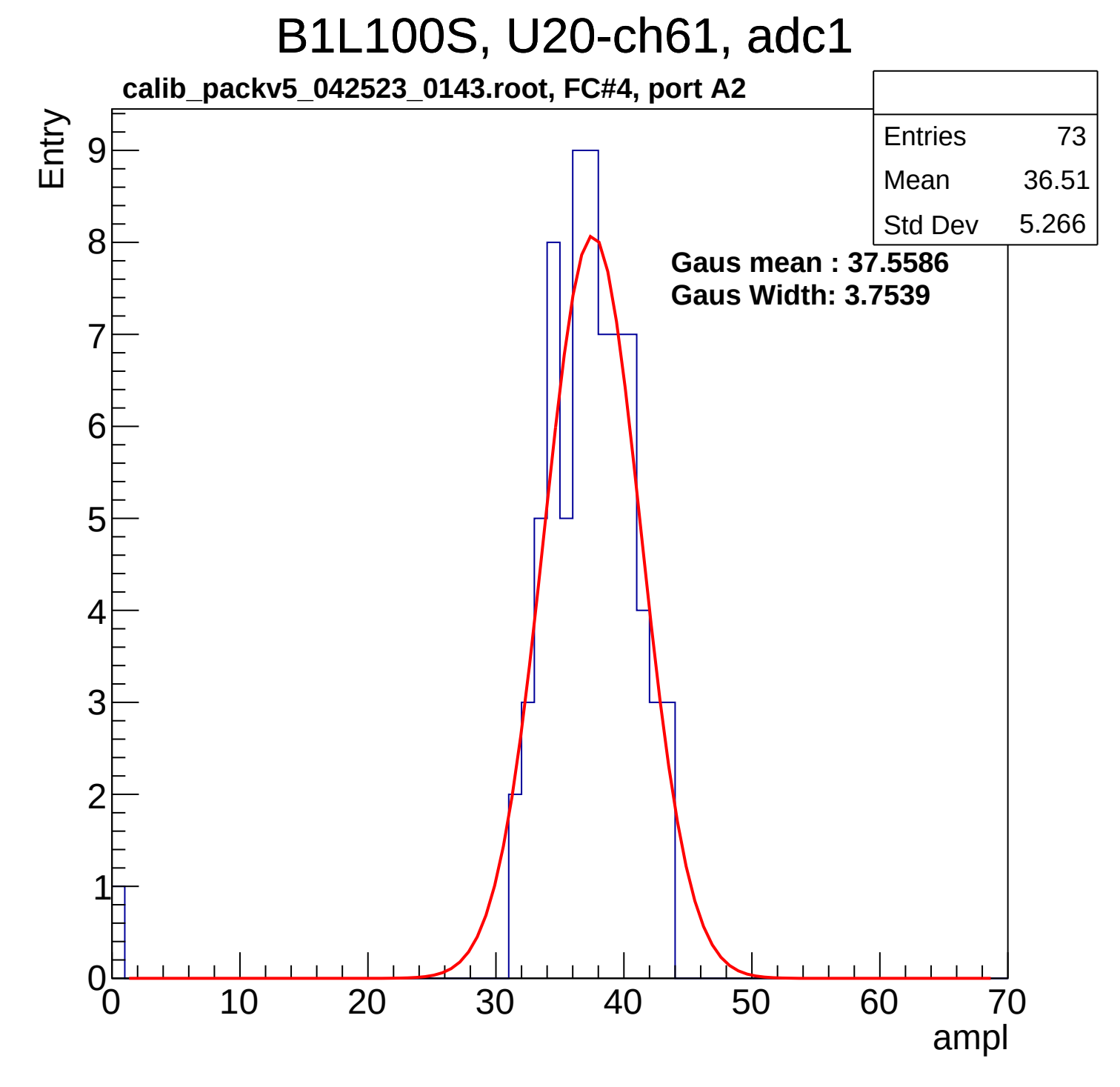
40
50
60
70


B1L100S, U20-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.51
Std Dev 5.266
Gaus mean : 37.5586
Gaus Width: 3.7539
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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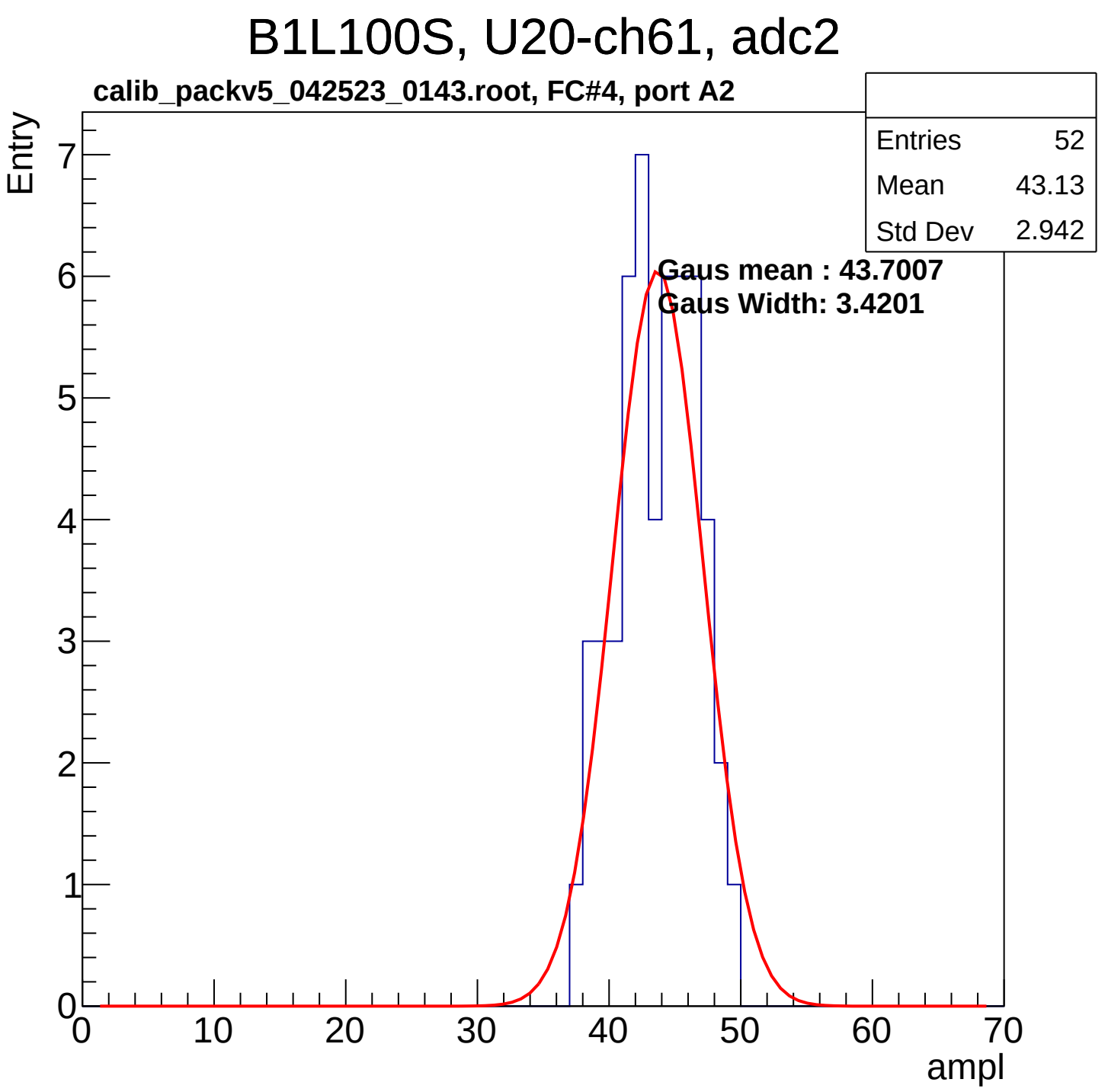
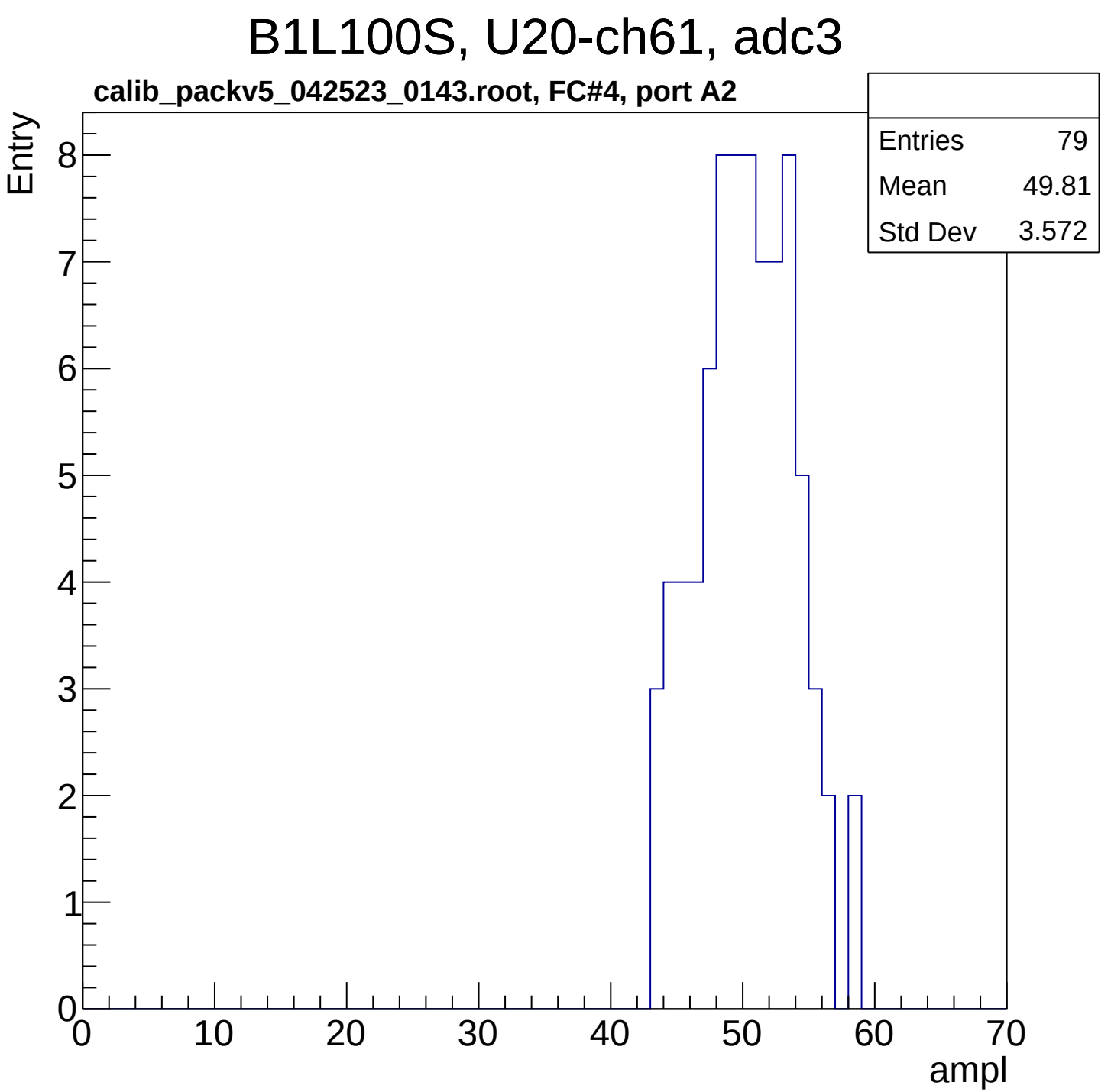

B1L100S, U20-ch61, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 43.13
Std Dev 2.942
Gaus mean : 43.7007
Gaus Width: 3.4201
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch61, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 49.81
Std Dev 3.572
Entry
ampl
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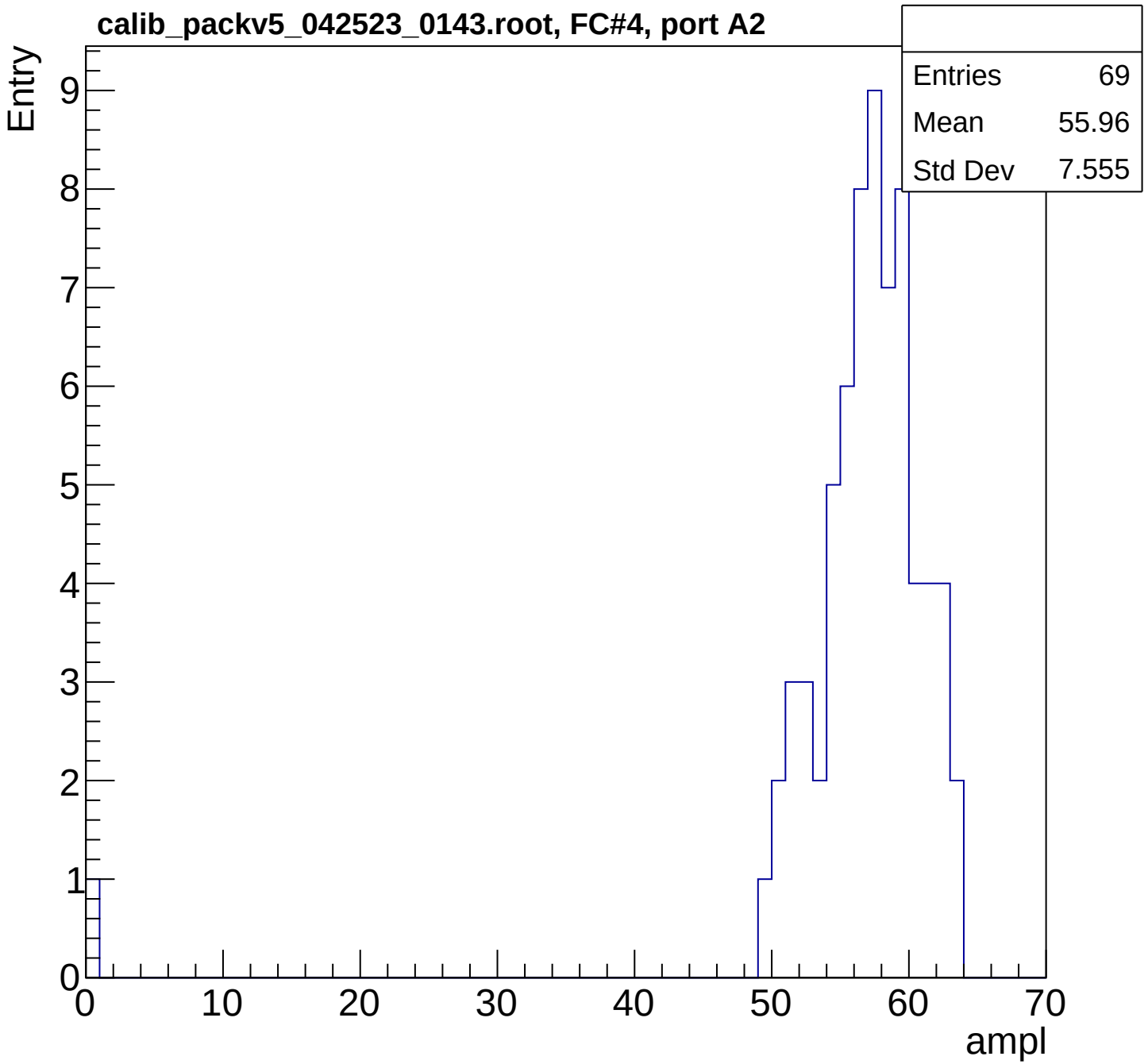
B1L100S, U20-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.96
Std Dev 7.555
Entry
ampl
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2
3
4
5
6
7
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0
10
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60
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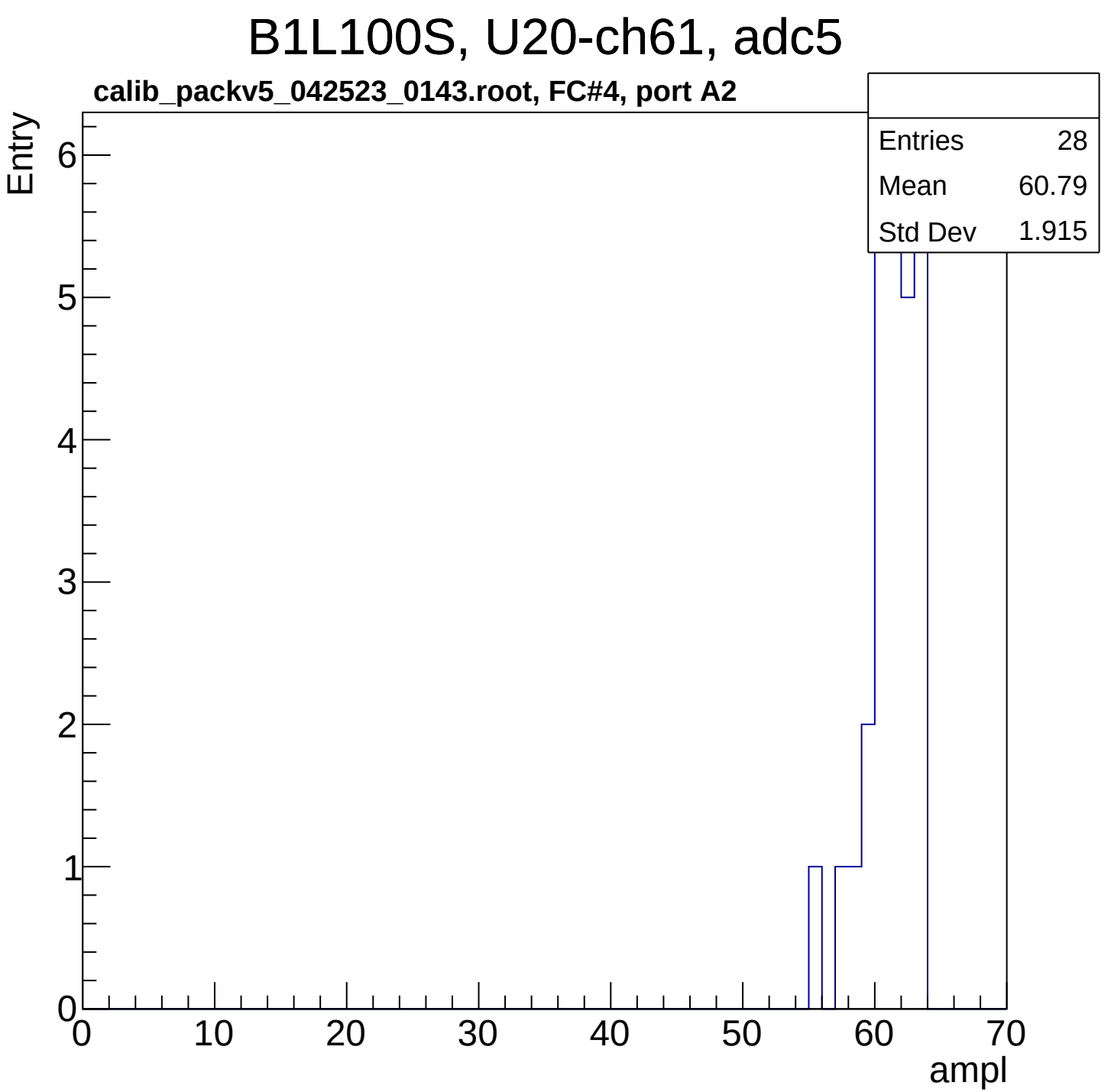
B1L100S, U20-ch61, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 60.79
Std Dev 1.915
Entry
ampl
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40
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60
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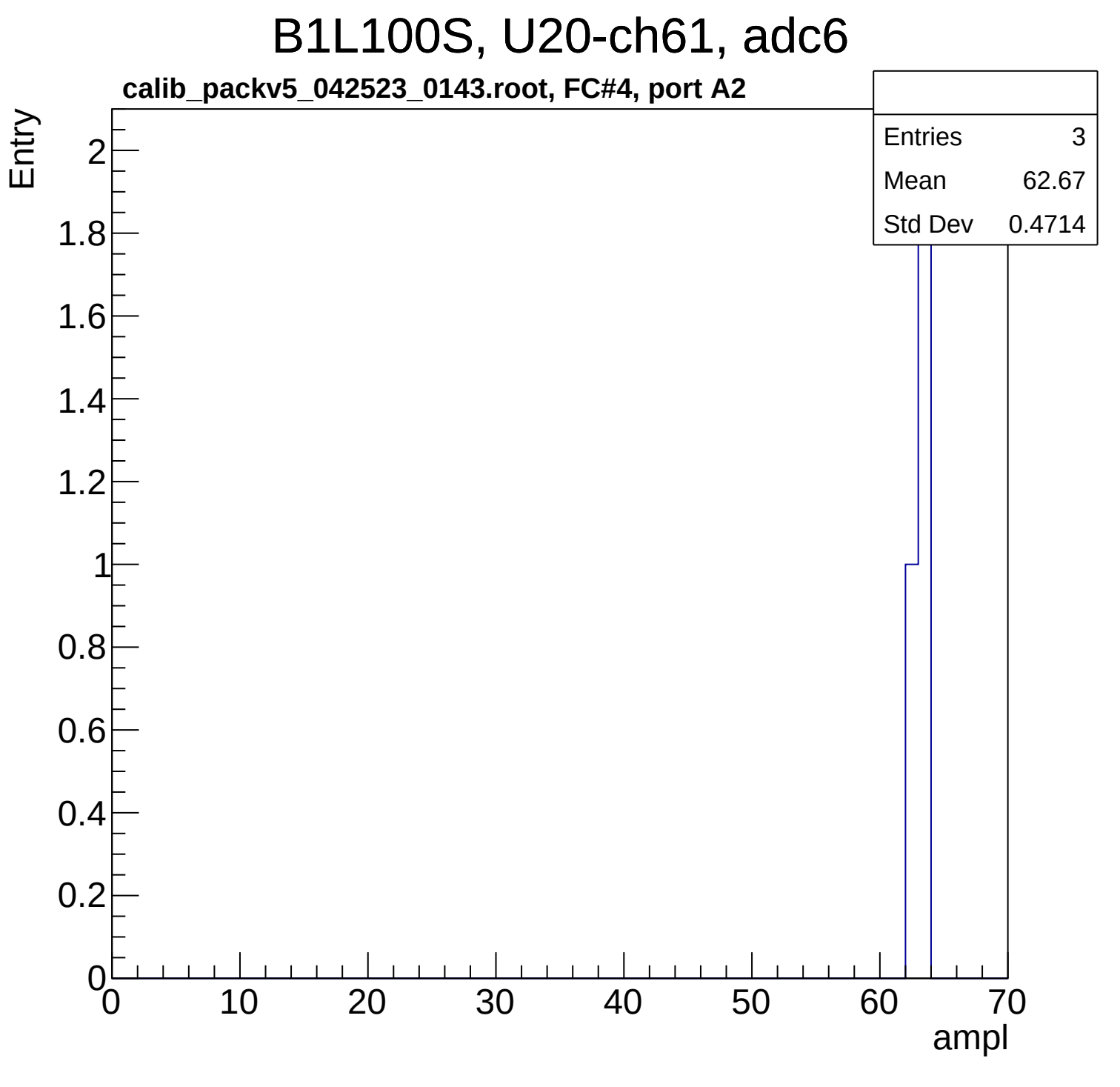
B1L100S, U20-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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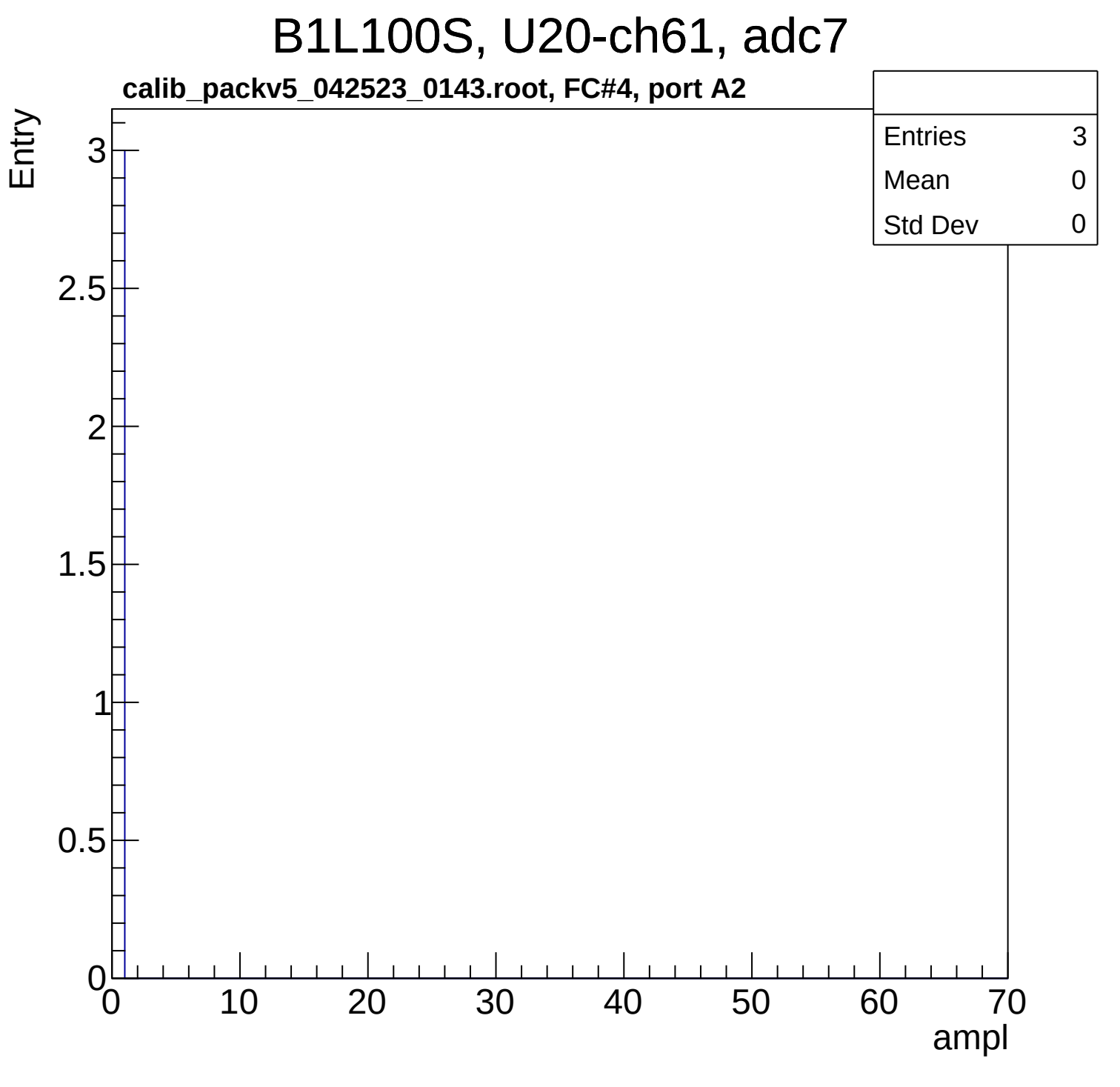
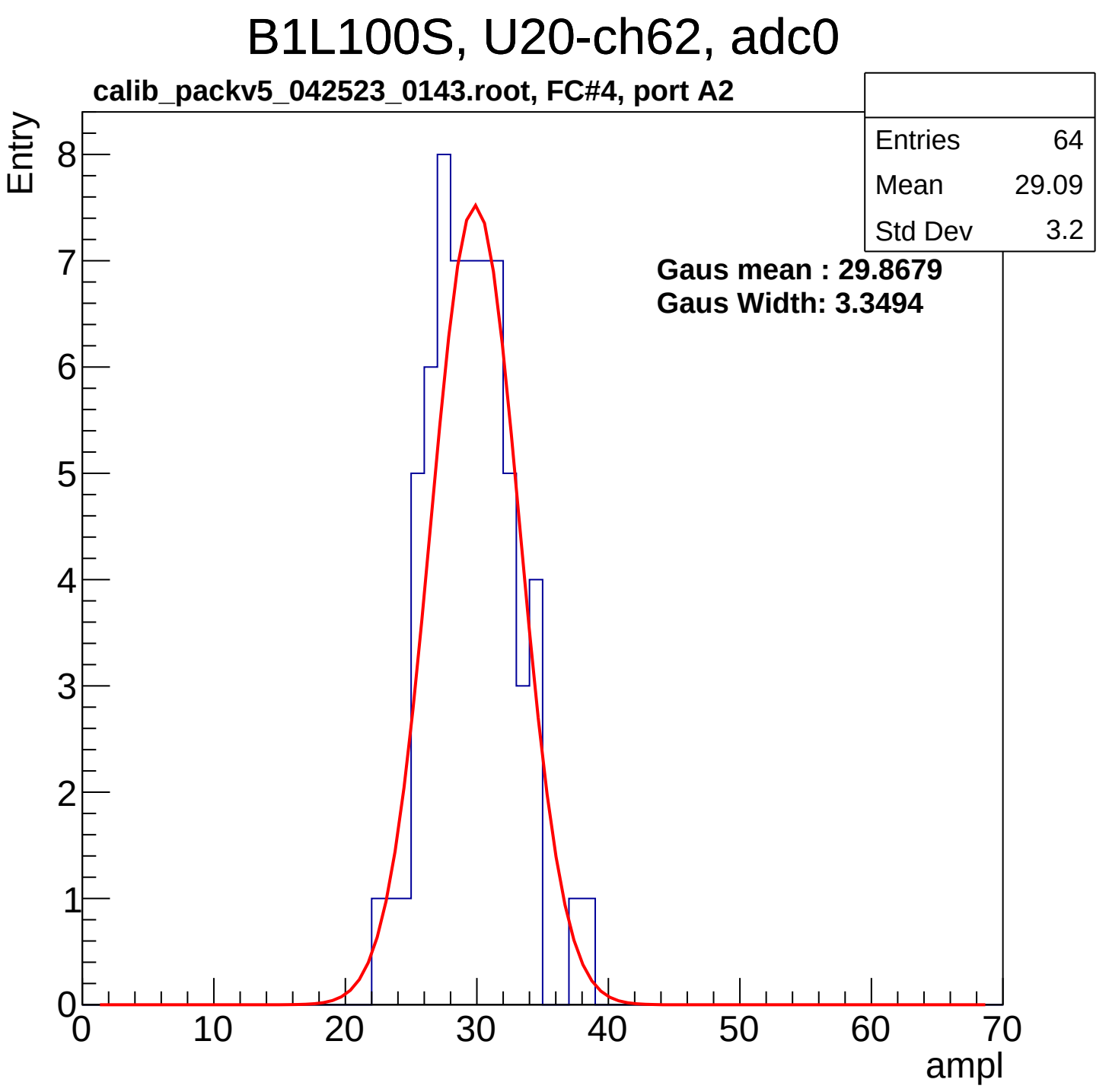

B1L100S, U20-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.09
Std Dev 3.2
Gaus mean : 29.8679
Gaus Width: 3.3494
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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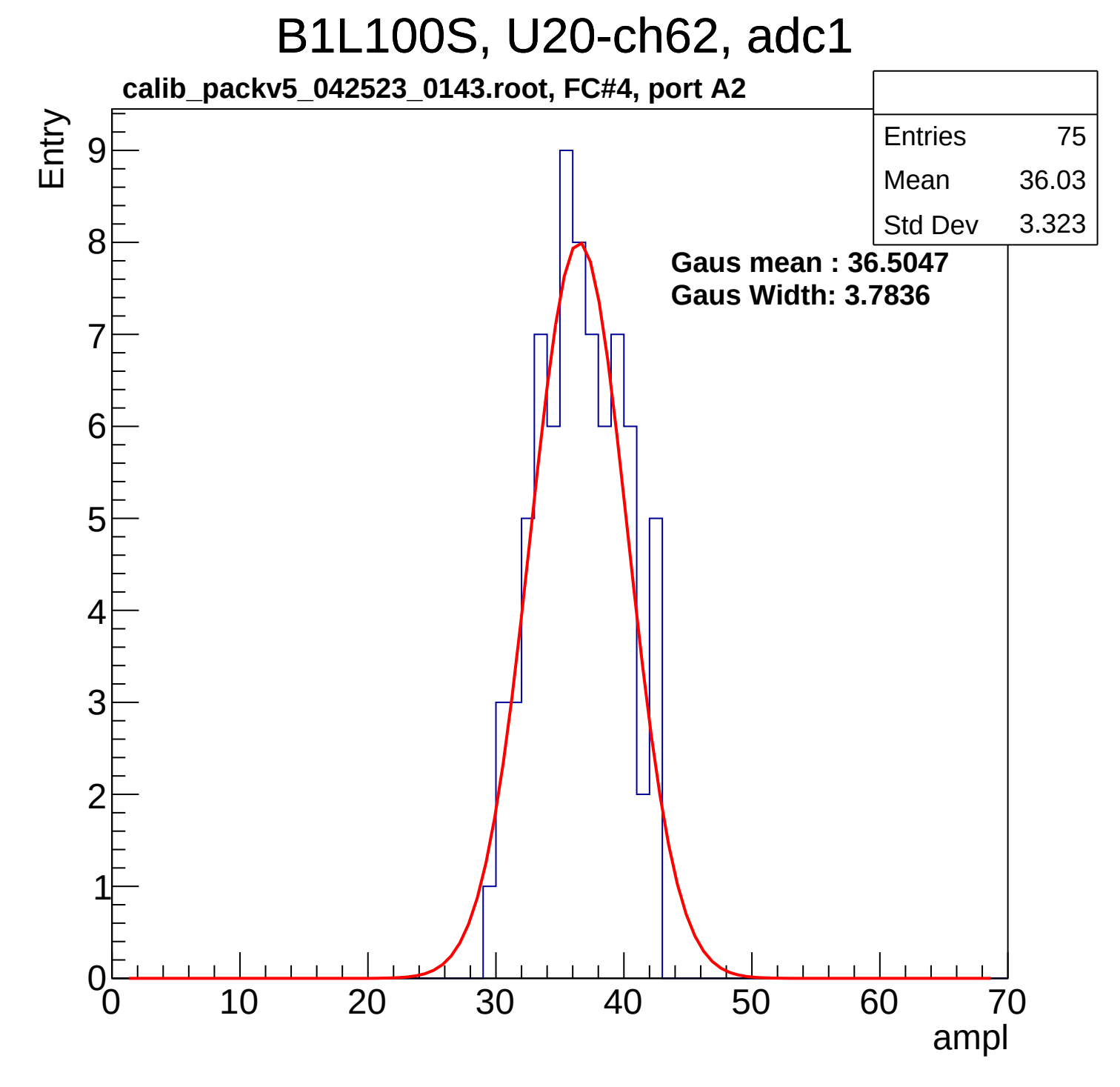
B1L100S, U20-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.03
Std Dev 3.323
Gaus mean : 36.5047
Gaus Width: 3.7836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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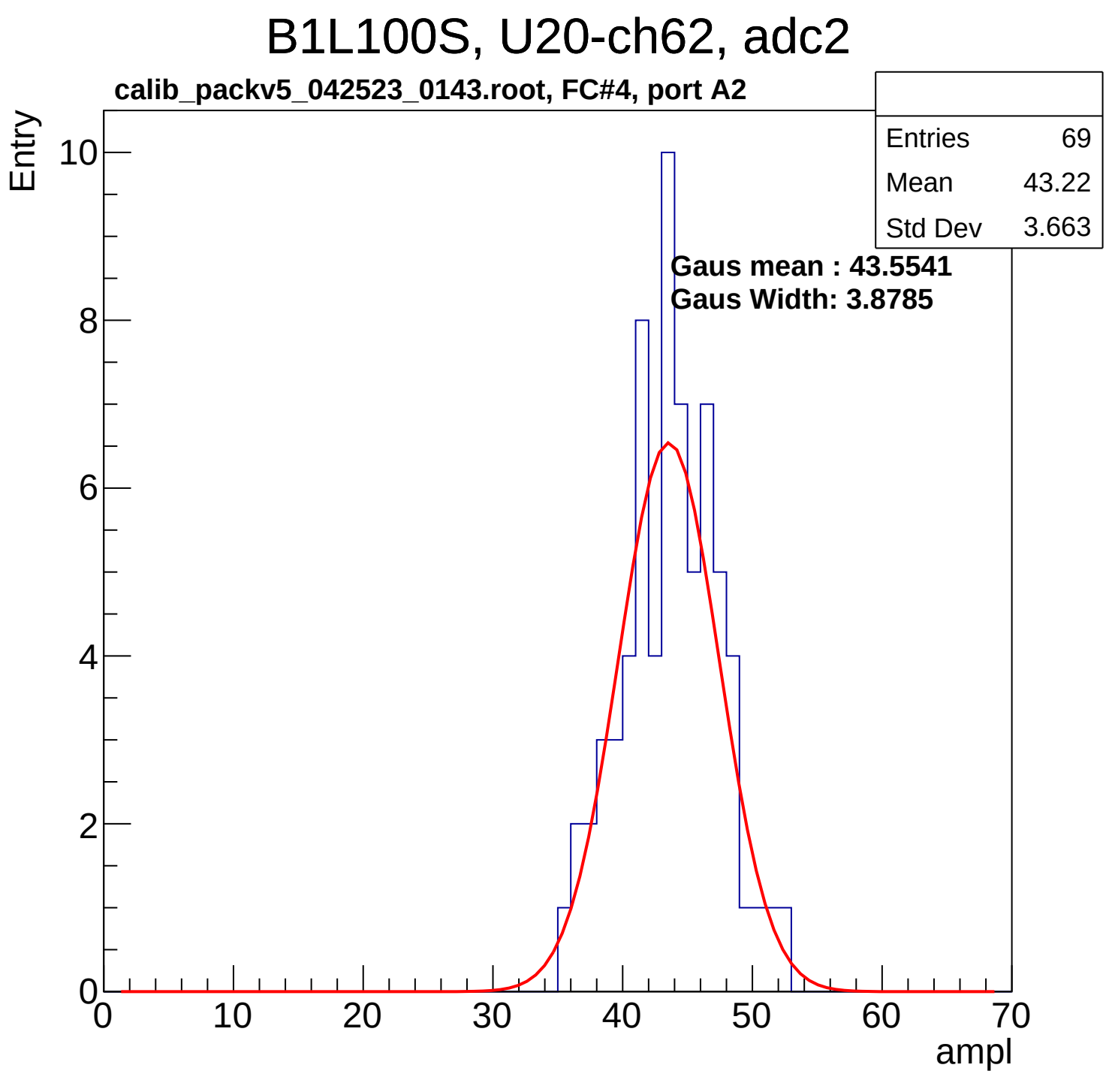
B1L100S, U20-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.22
Std Dev 3.663
Gaus mean : 43.5541
Gaus Width: 3.8785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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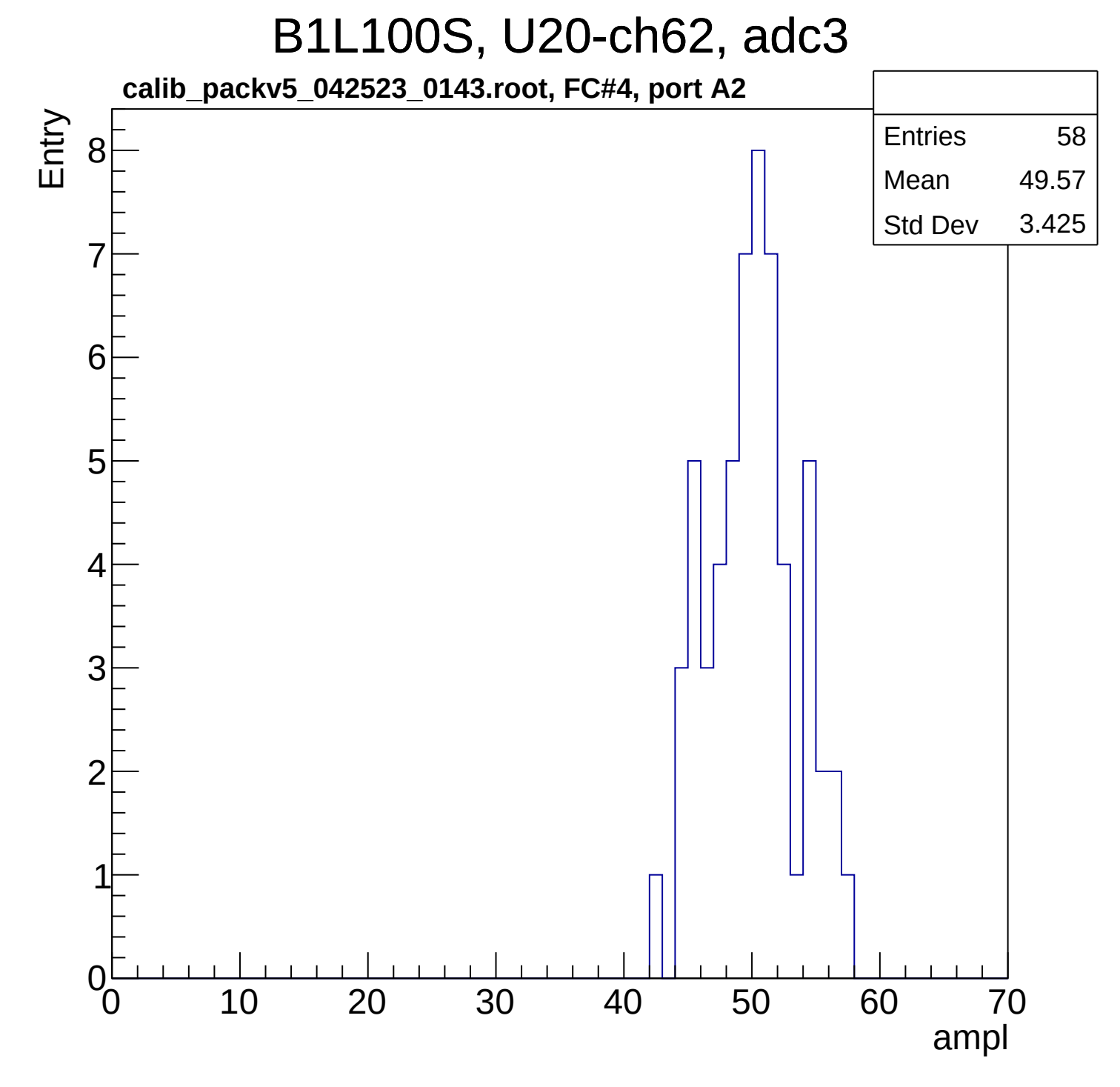
B1L100S, U20-ch62, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.57
Std Dev 3.425
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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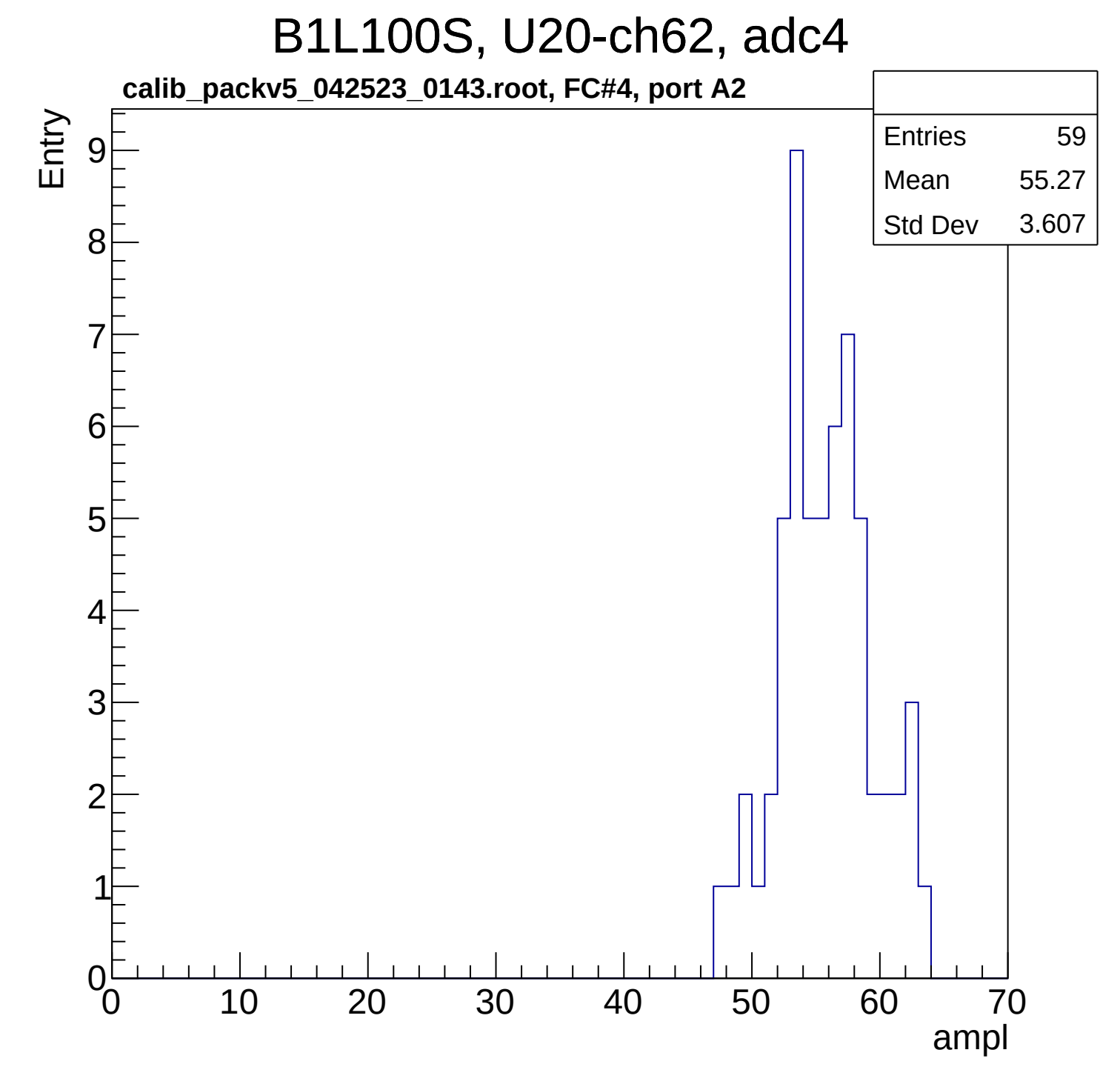
B1L100S, U20-ch62, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.27
Std Dev 3.607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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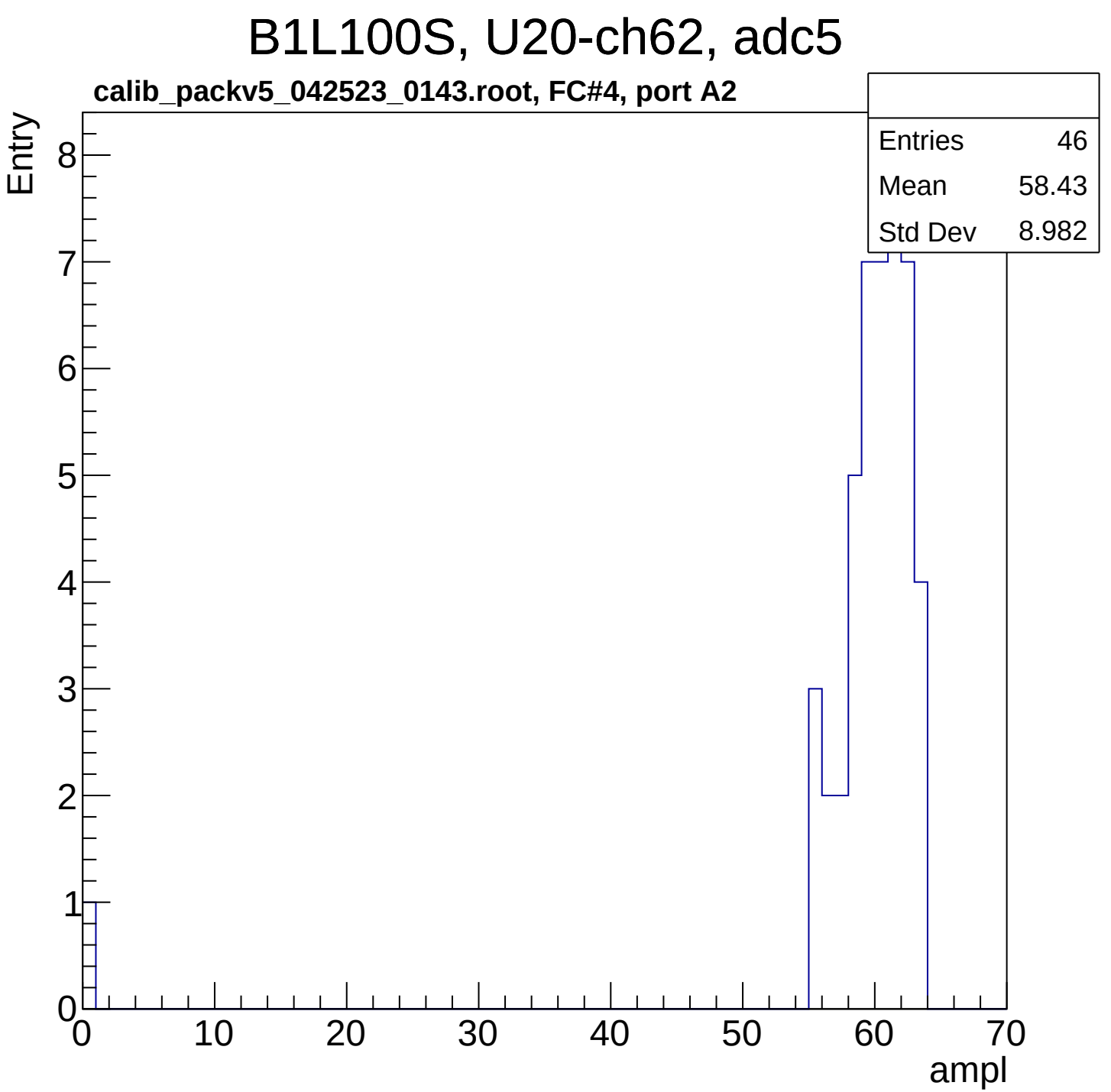
B1L100S, U20-ch62, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.43
Std Dev 8.982
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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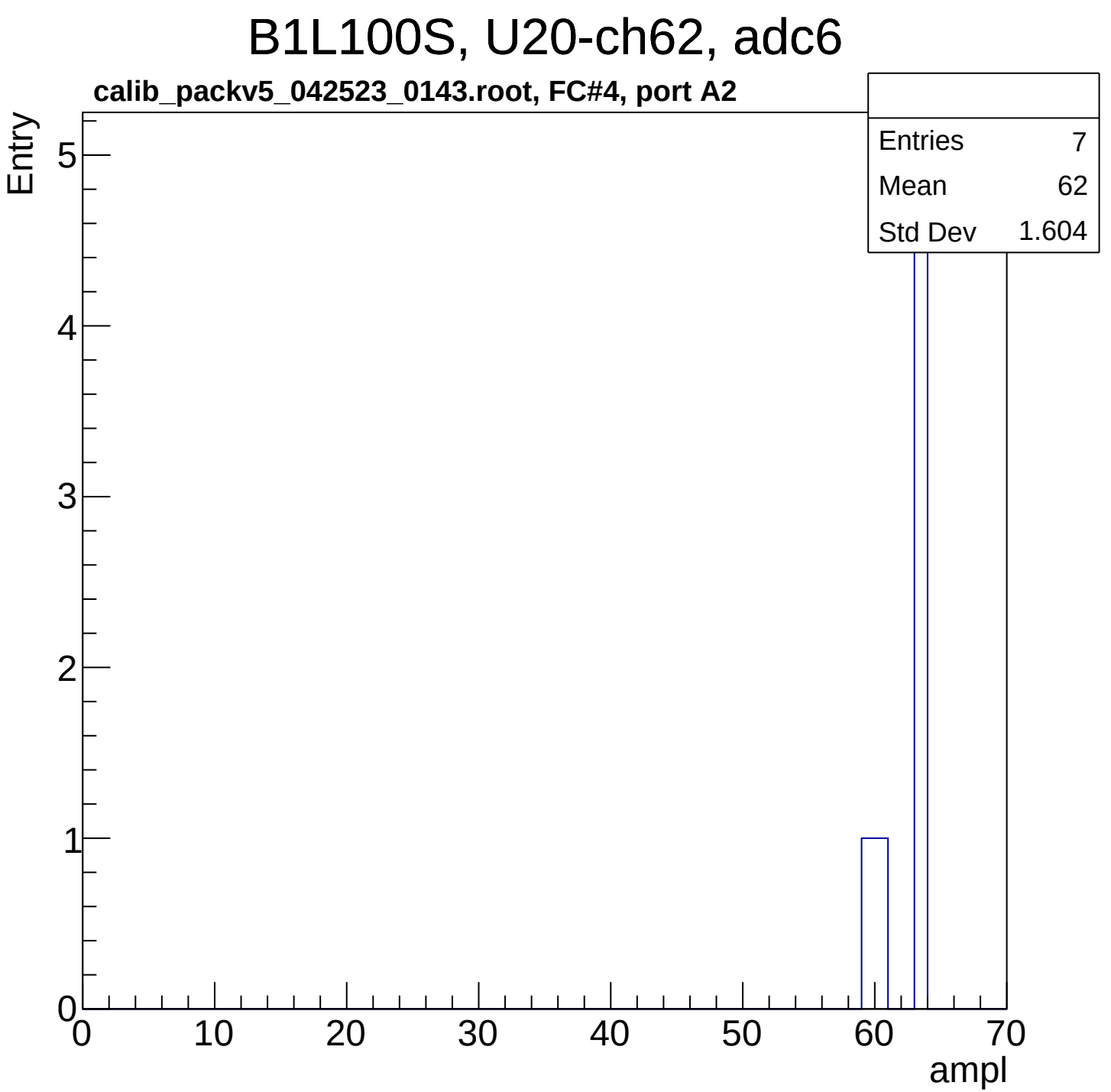
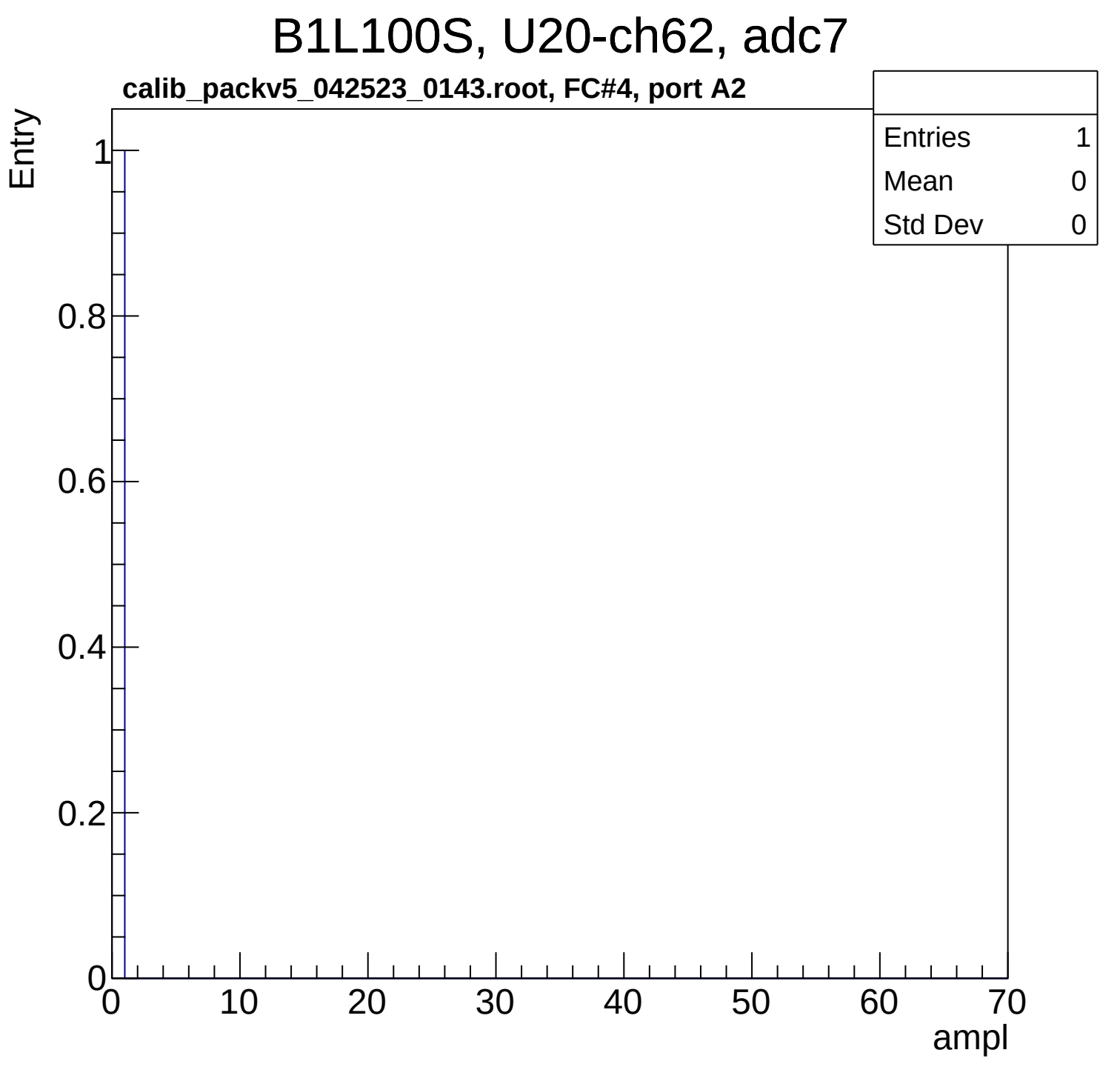
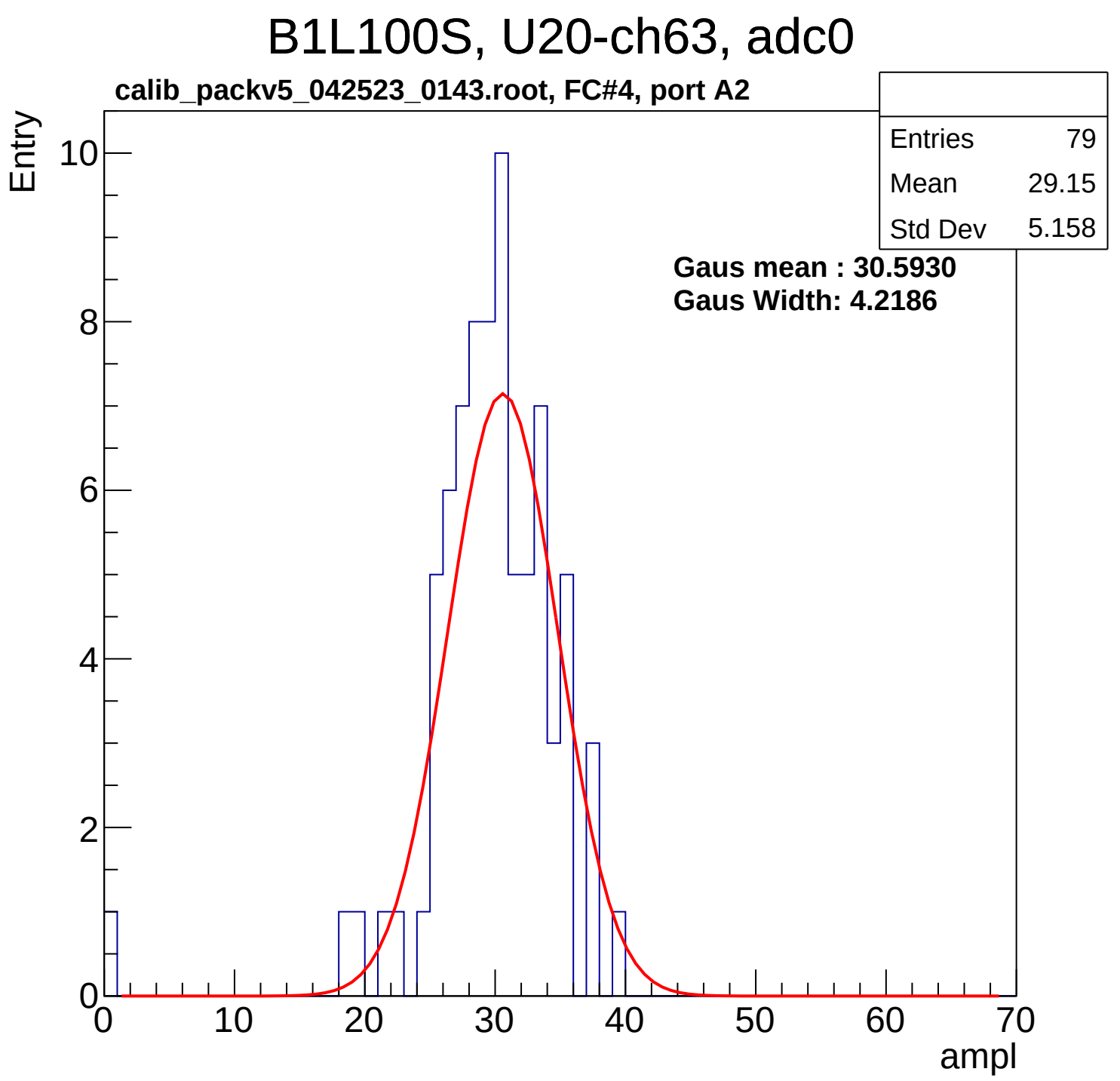
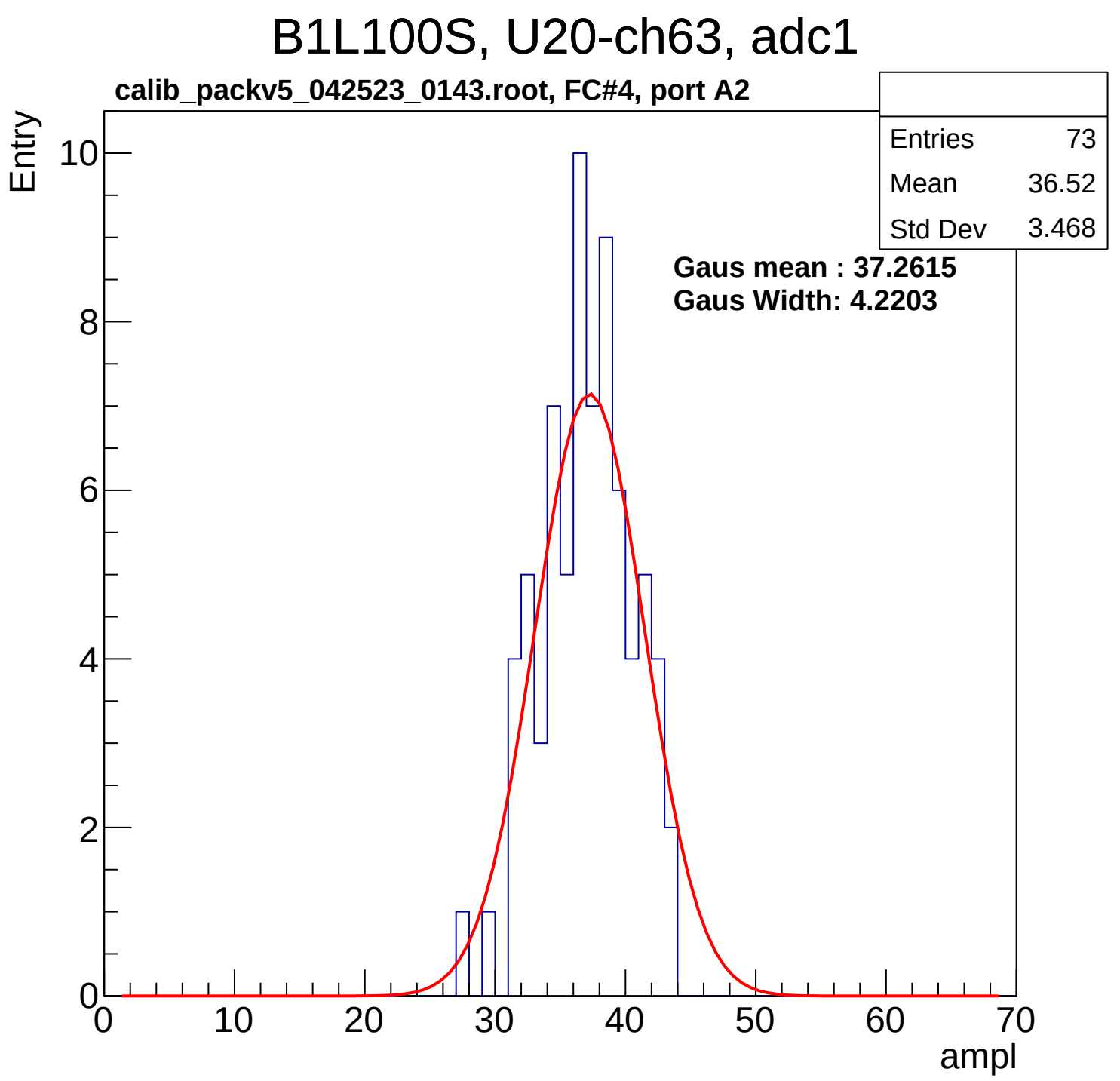
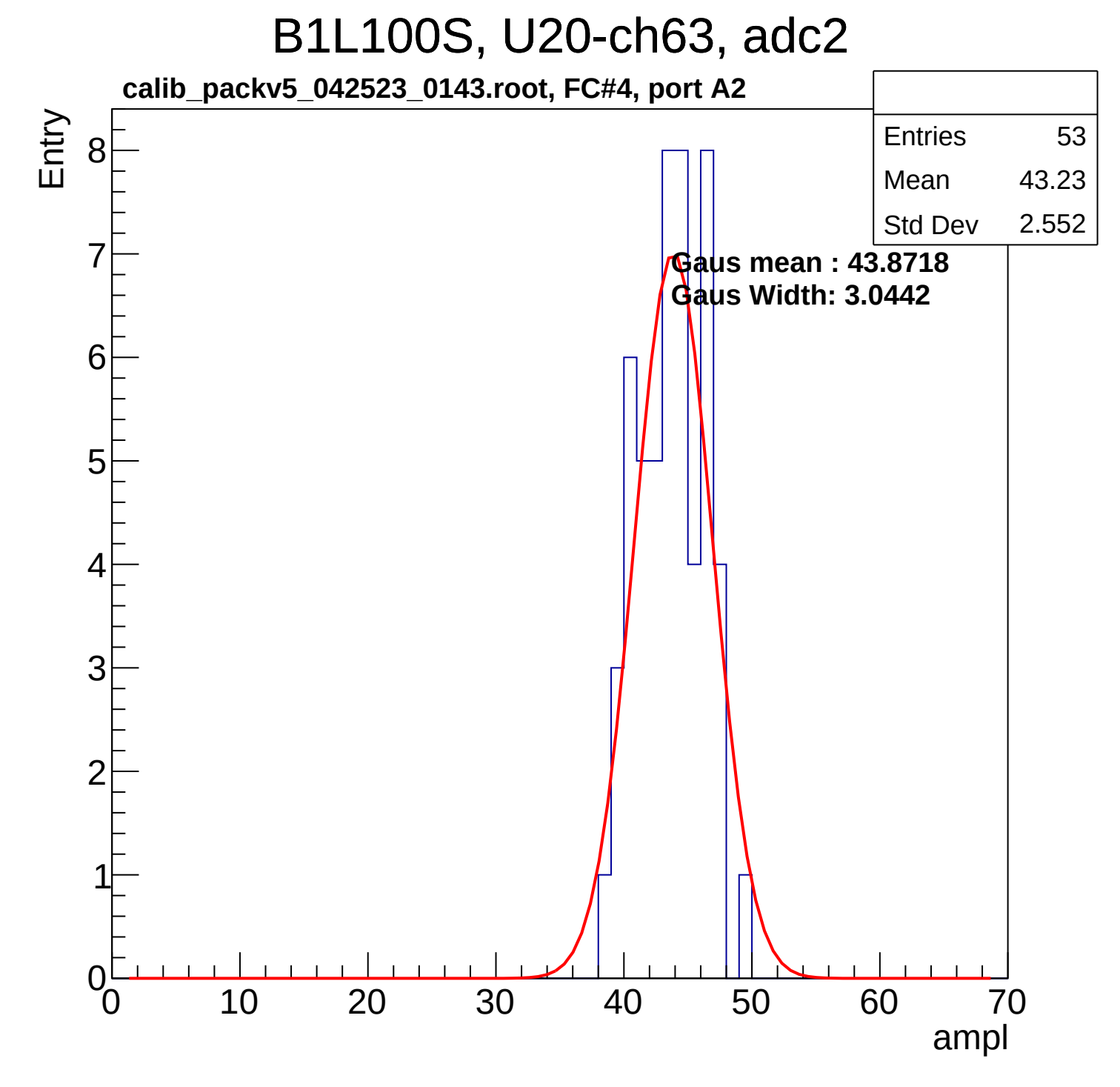
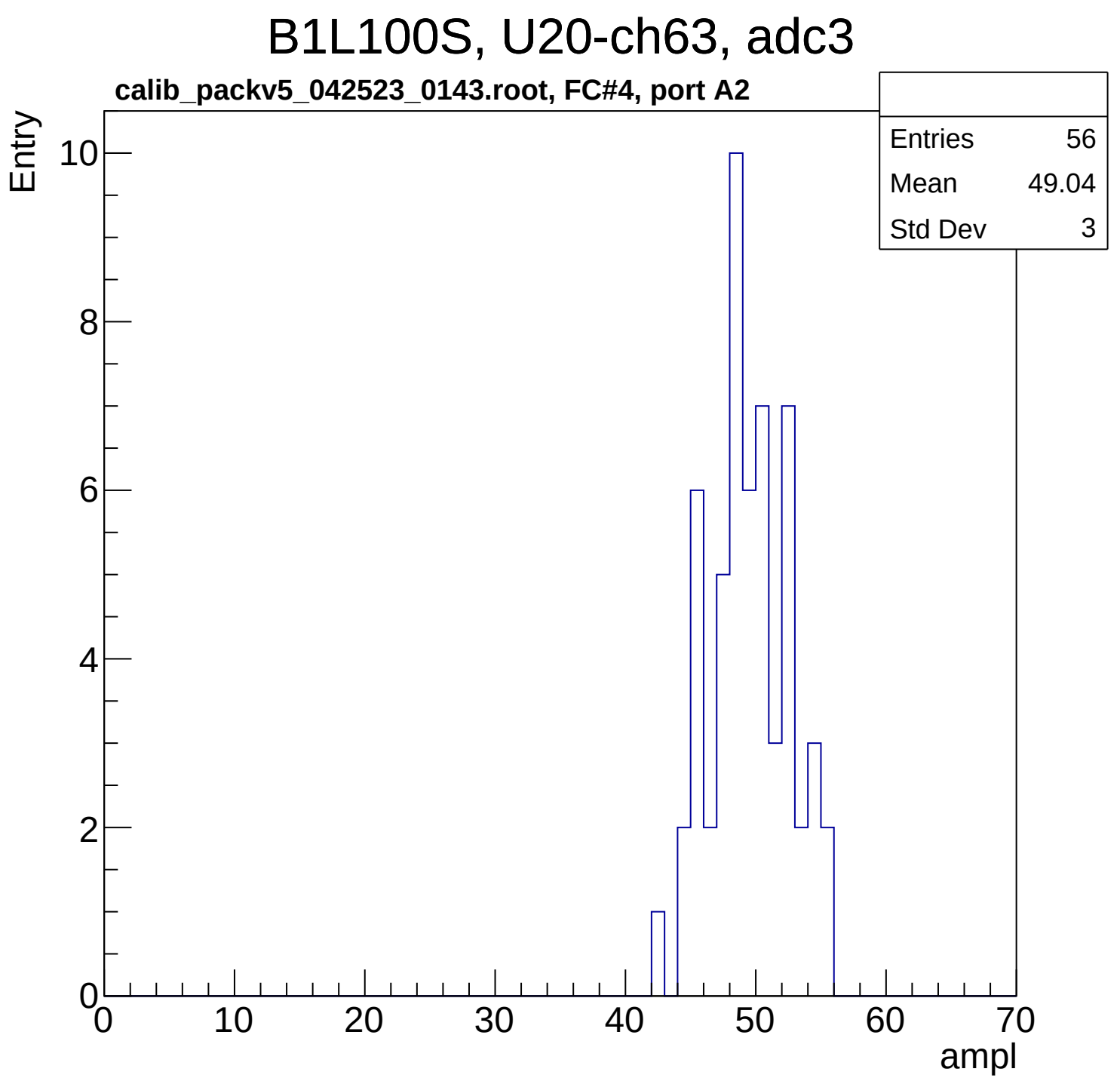

B1L100S, U20-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62
Std Dev 1.604
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

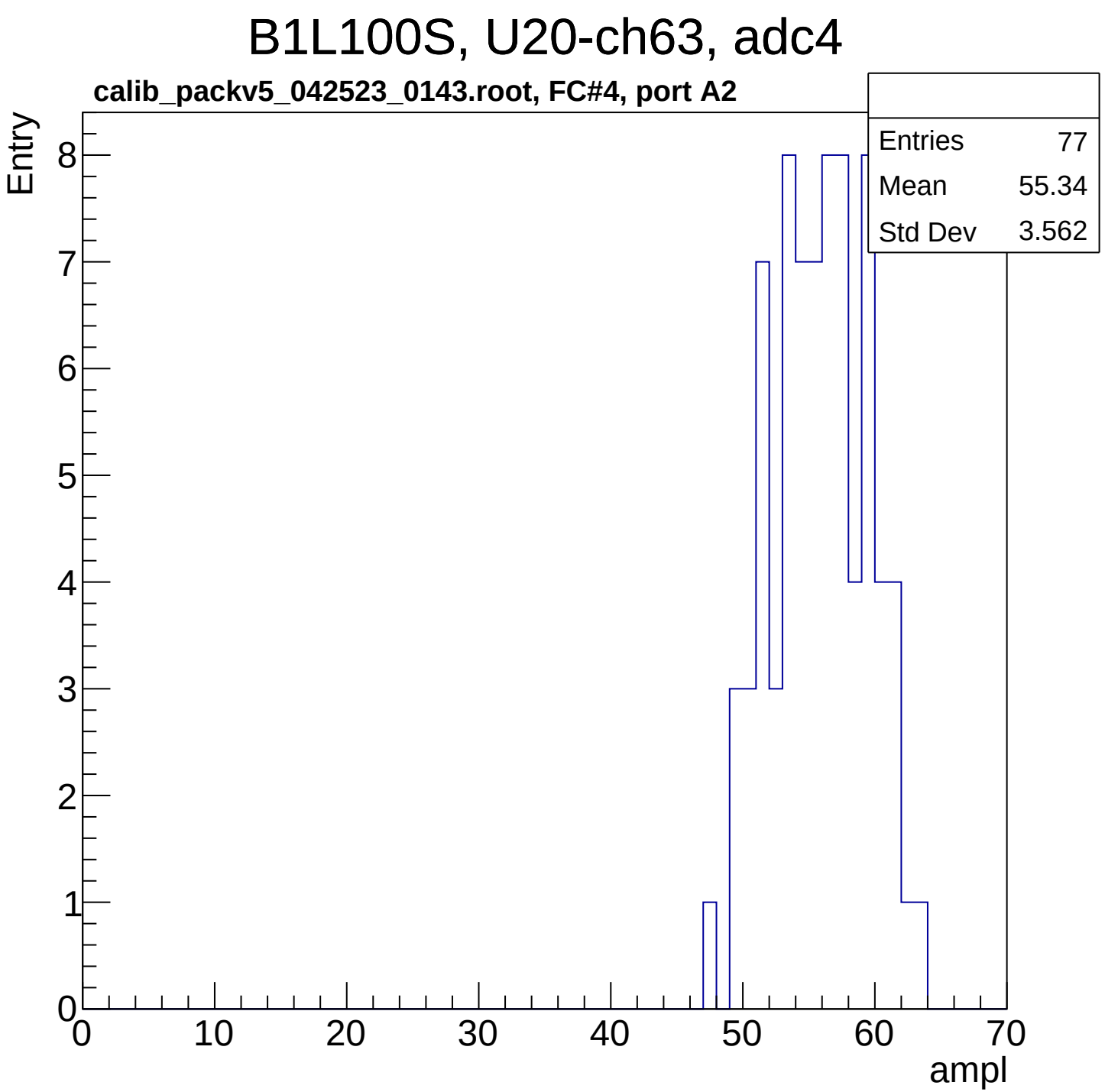

B1L100S, U20-ch62, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch63, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.15
Std Dev 5.158
Gaus mean : 30.5930
Gaus Width: 4.2186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.52
Std Dev 3.468
Gaus mean : 37.2615
Gaus Width: 4.2203
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch63, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 43.23
Std Dev 2.552
Gaus mean : 43.8718
Gaus Width: 3.0442
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch63, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 49.04
Std Dev 3
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch63, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 55.34
Std Dev 3.562
Entry
ampl
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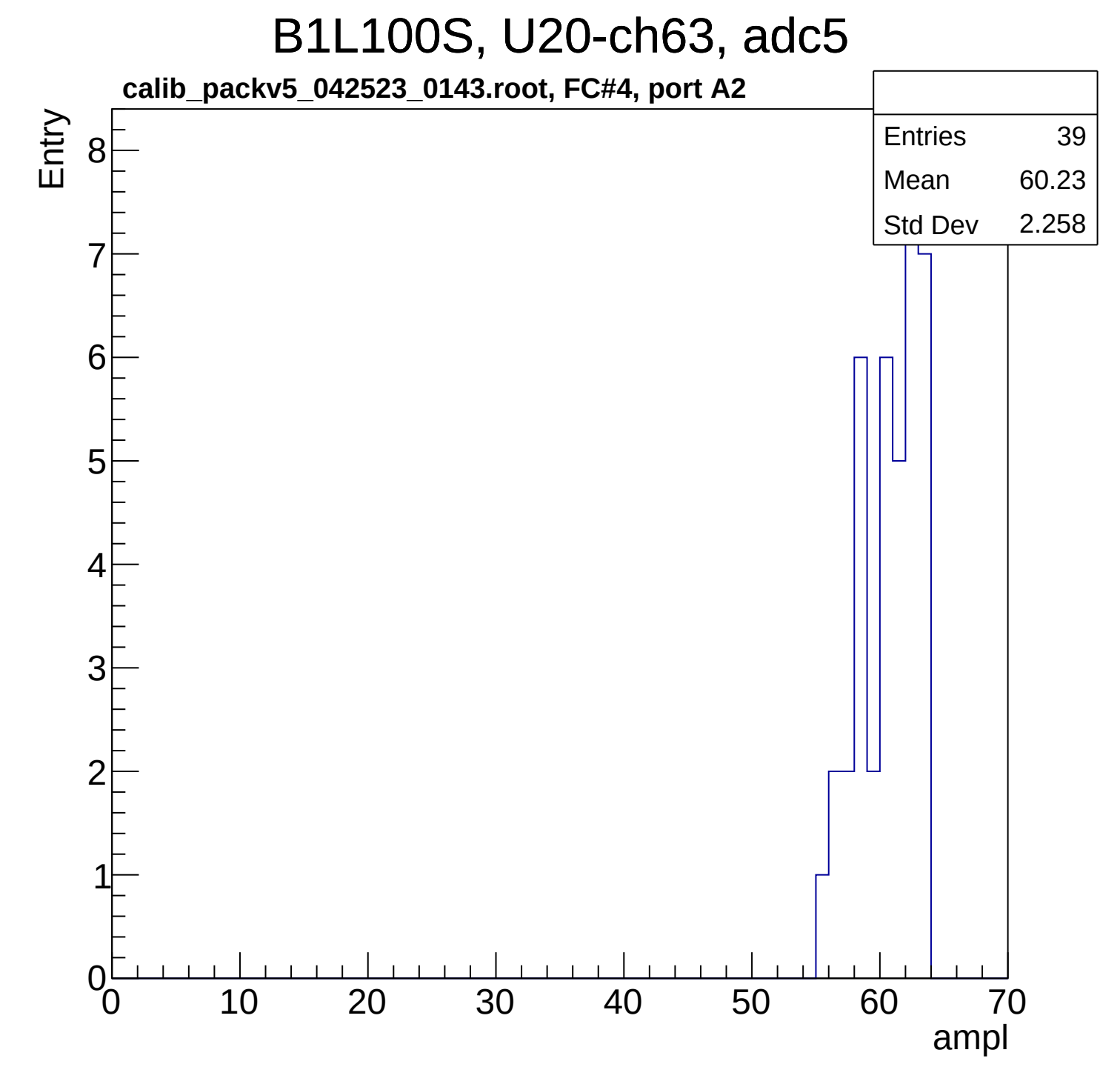
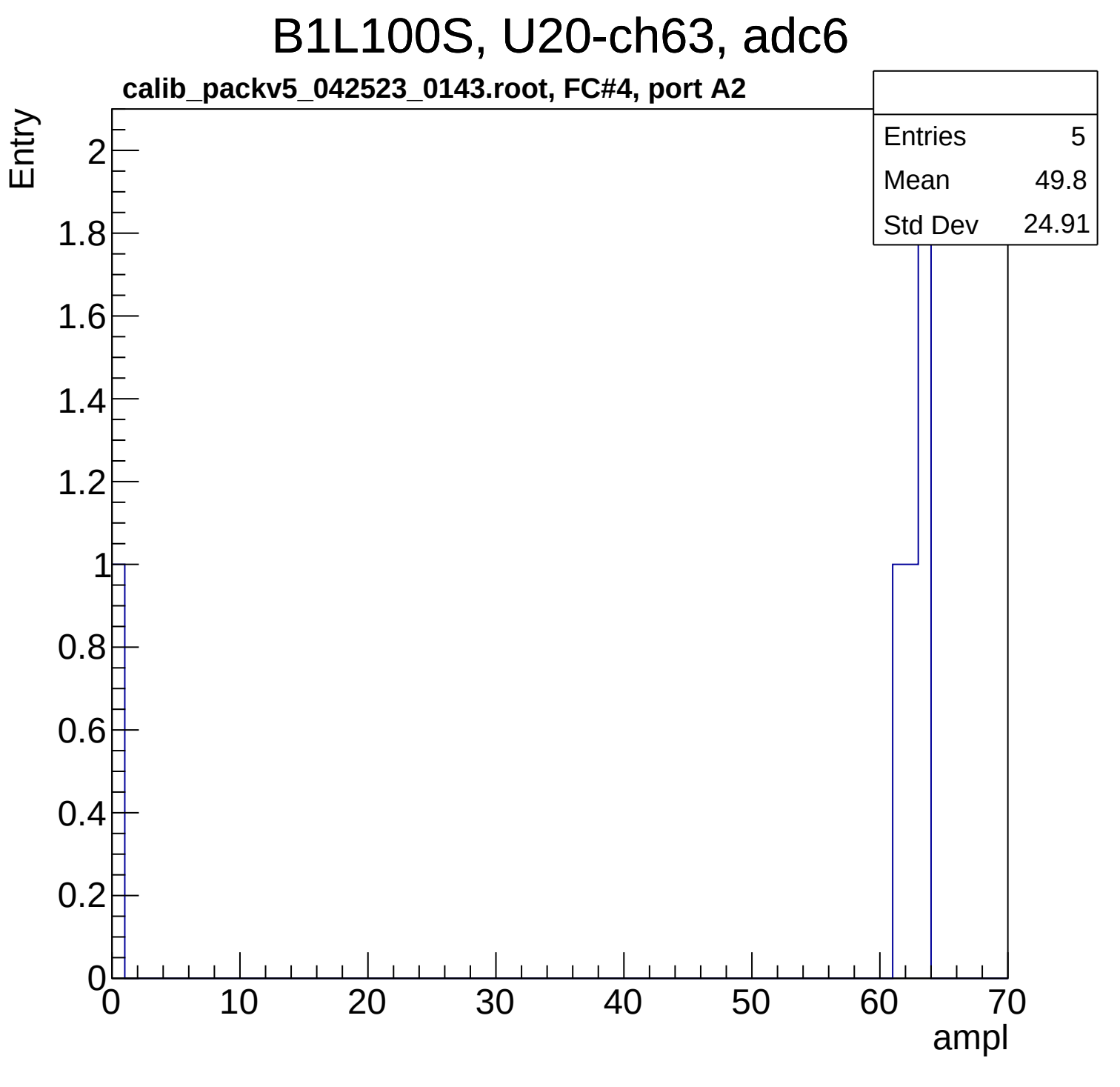
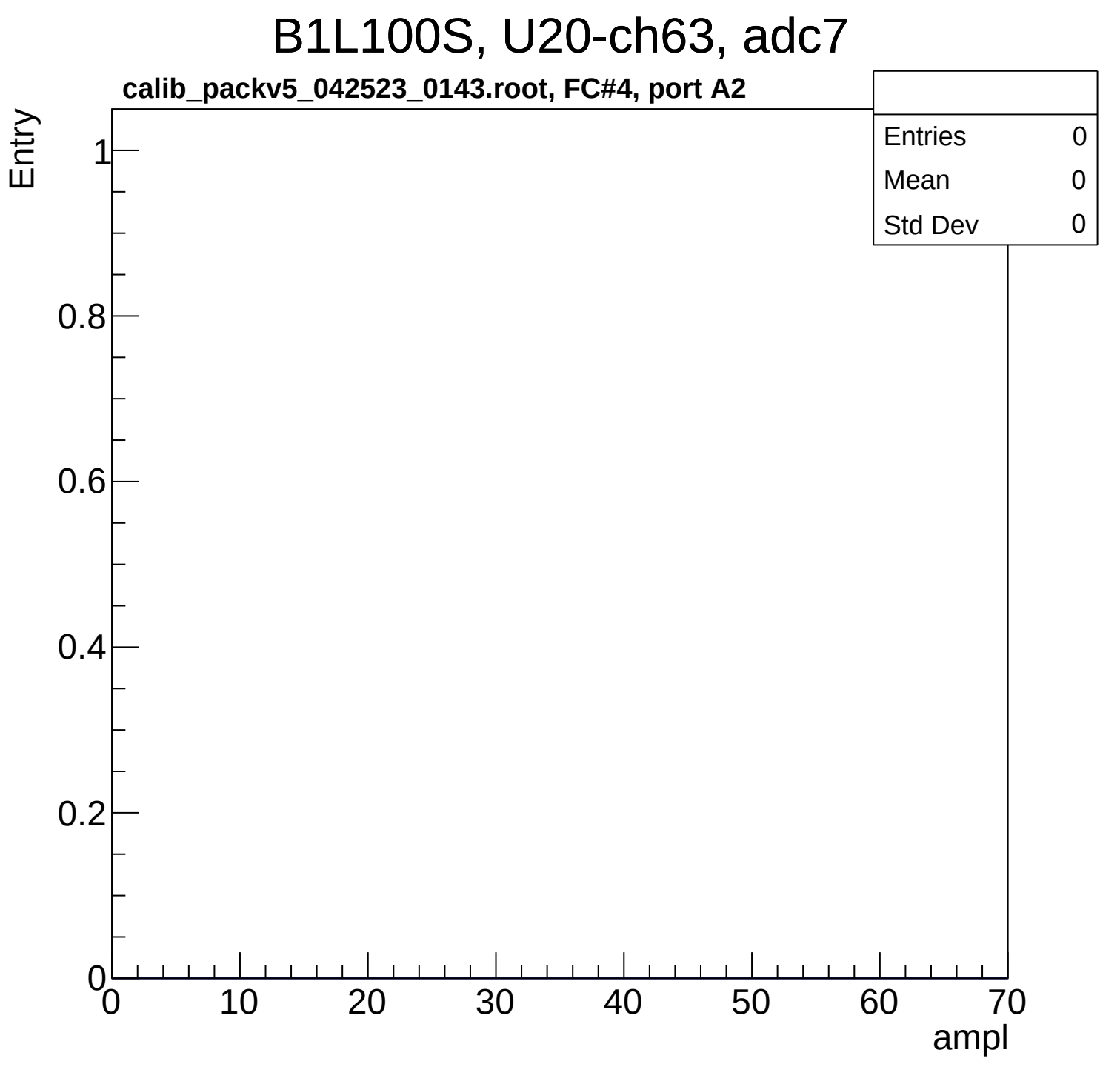
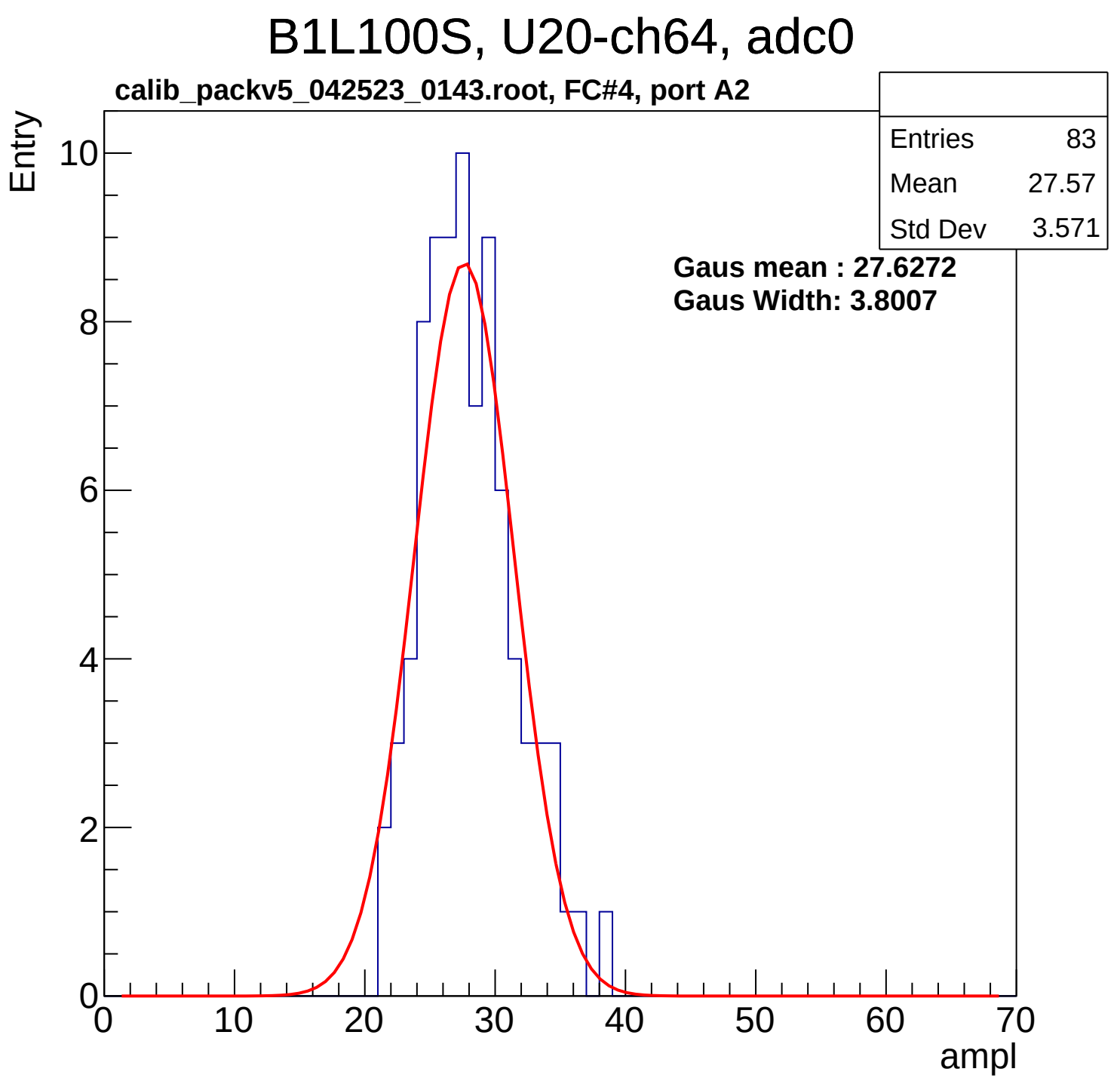
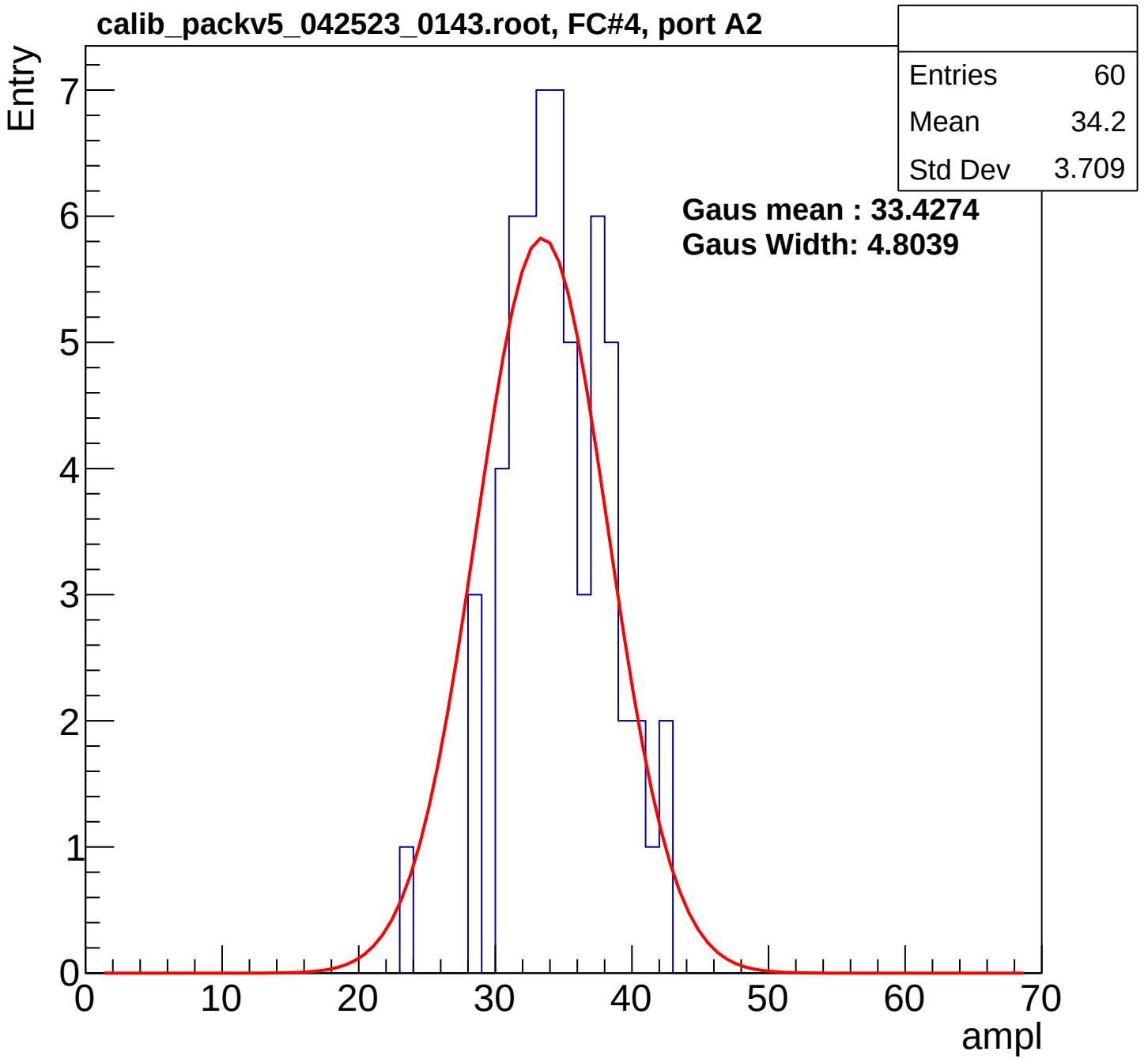
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

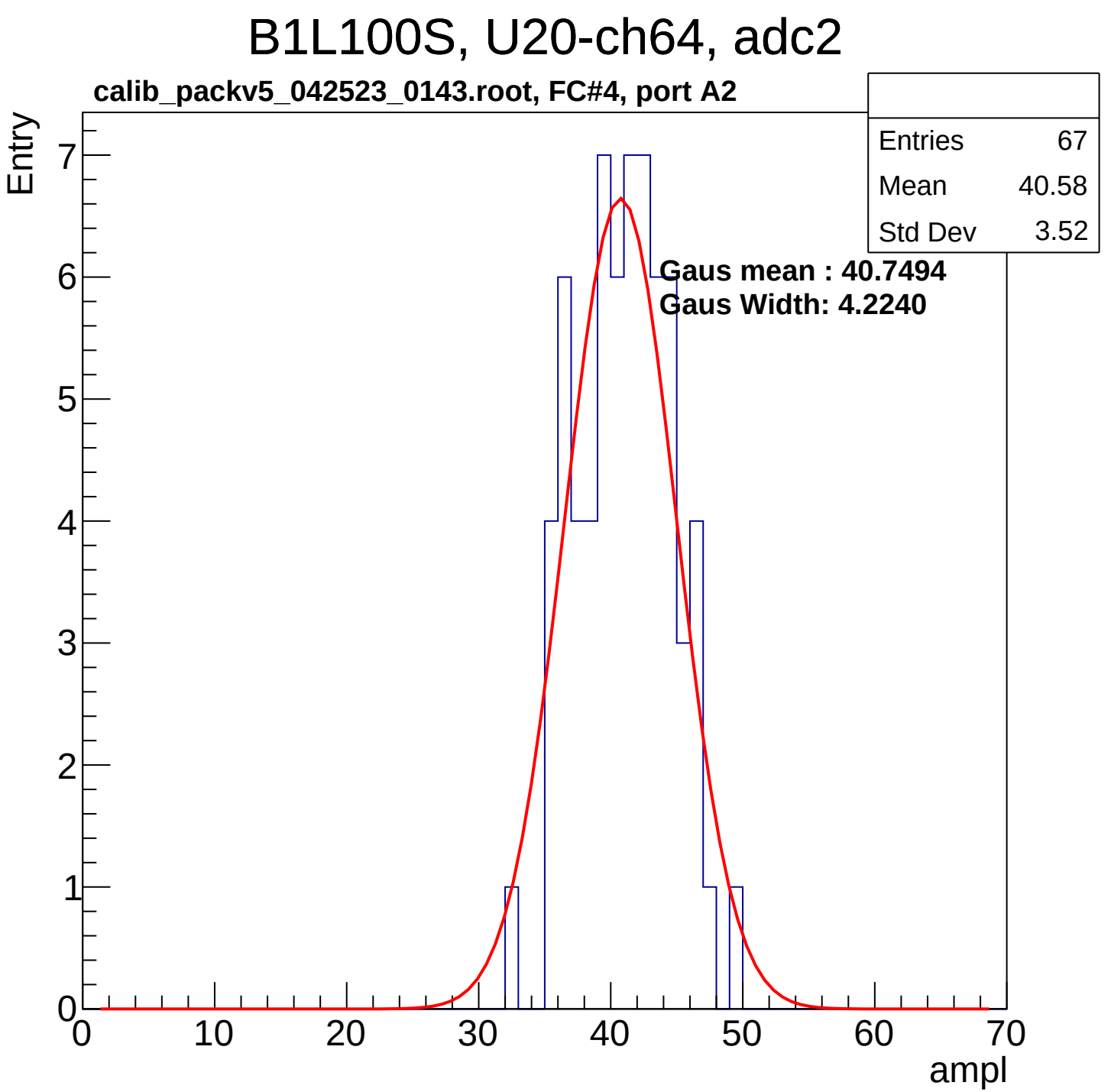

B1L100S, U20-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 60.23
Std Dev 2.258
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

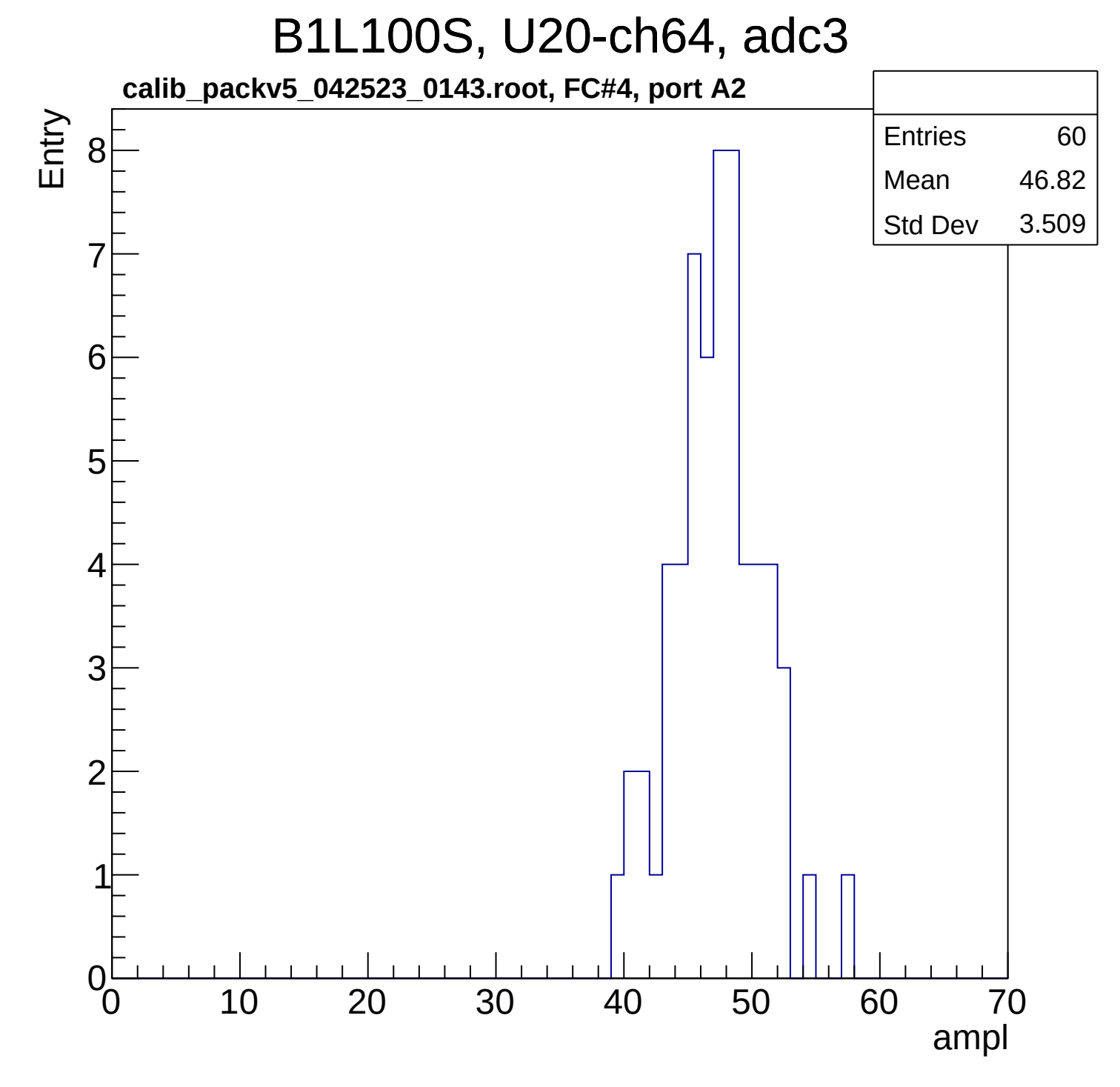

B1L100S, U20-ch63, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.8
Std Dev 24.91
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch63, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 27.57
Std Dev 3.571
Gaus mean : 27.6272
Gaus Width: 3.8007
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 34.2
Std Dev 3.709
Gaus mean : 33.4274
Gaus Width: 4.8039
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 40.58
Std Dev 3.52
Gaus mean : 40.7494
Gaus Width: 4.2240
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch64, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 46.82
Std Dev 3.509
Entry
ampl
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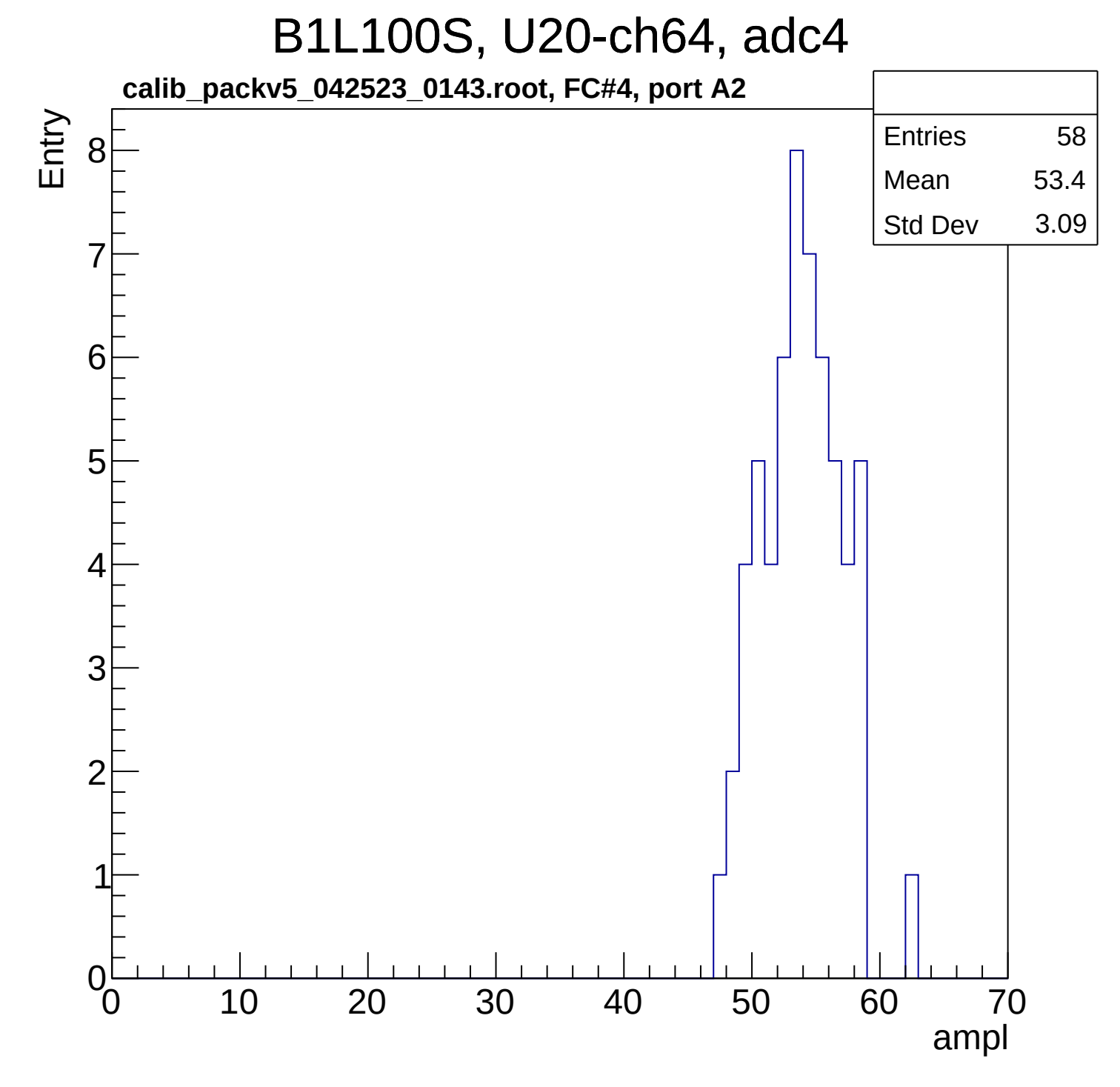
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch64, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 53.4
Std Dev 3.09
Entry
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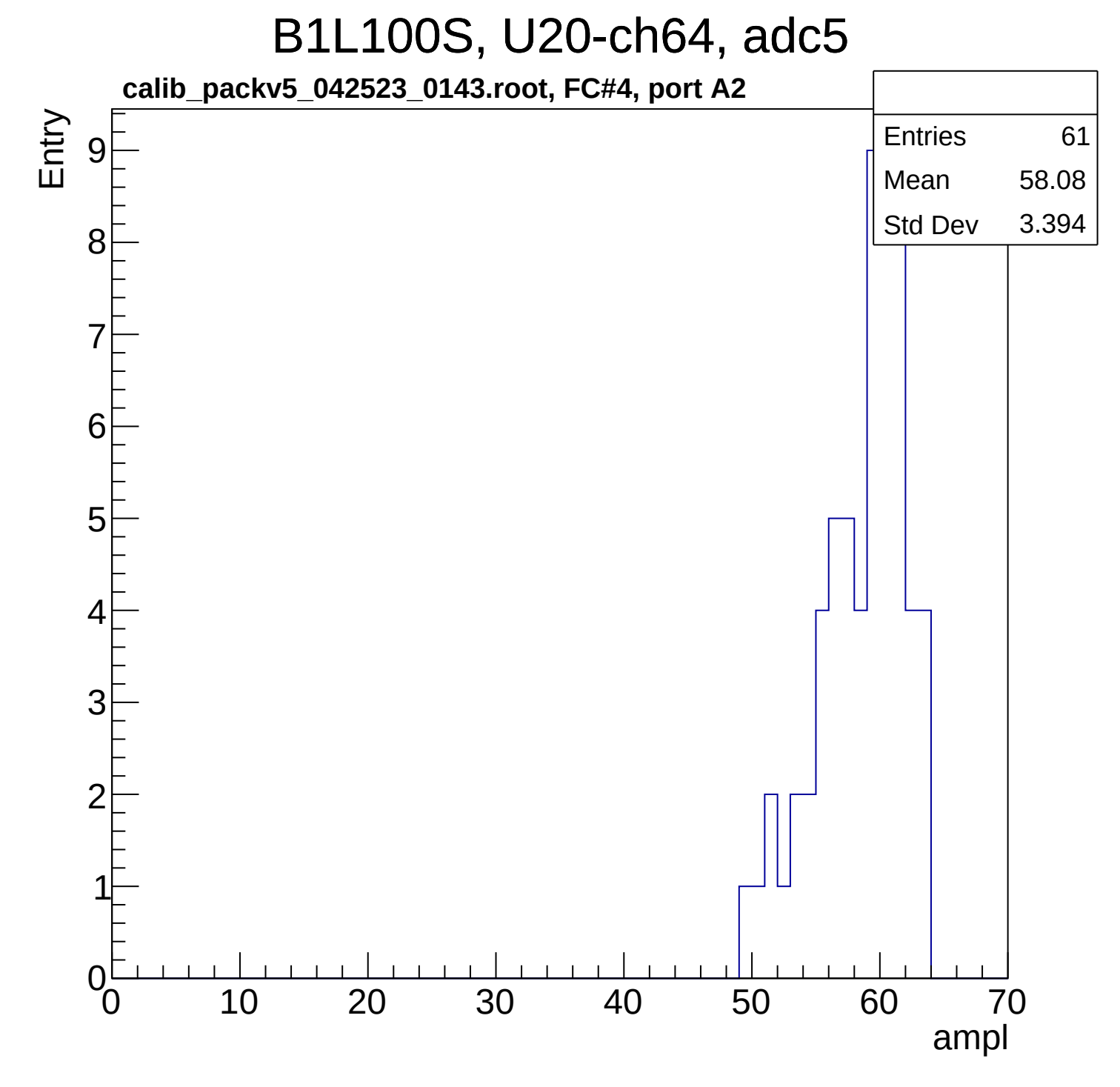
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 58.08
Std Dev 3.394
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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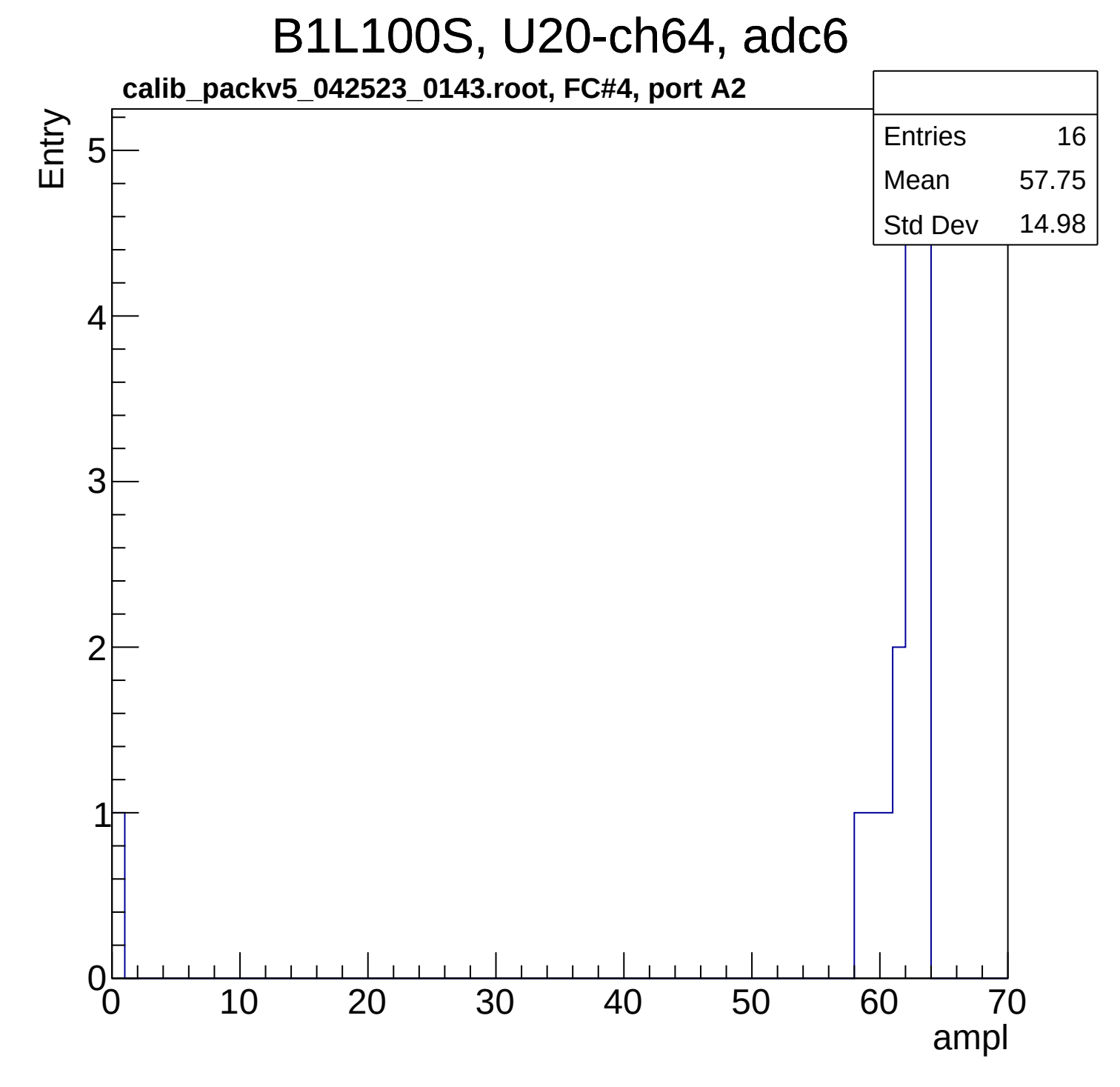
B1L100S, U20-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 57.75
Std Dev 14.98
Entry
ampl
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5
0
10
20
30
40
50
60
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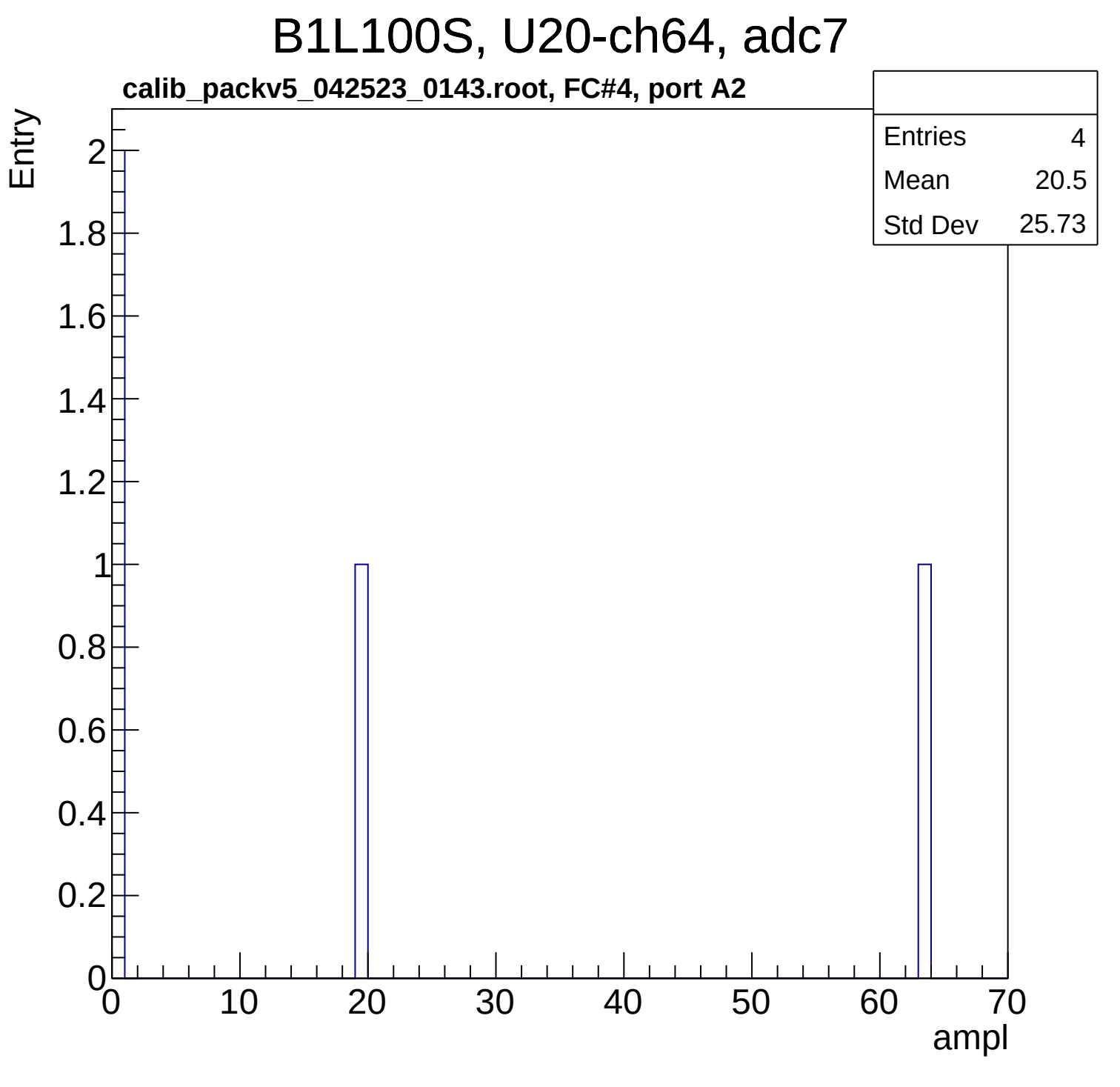
B1L100S, U20-ch64, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 20.5
Std Dev 25.73
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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20
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40
50
60
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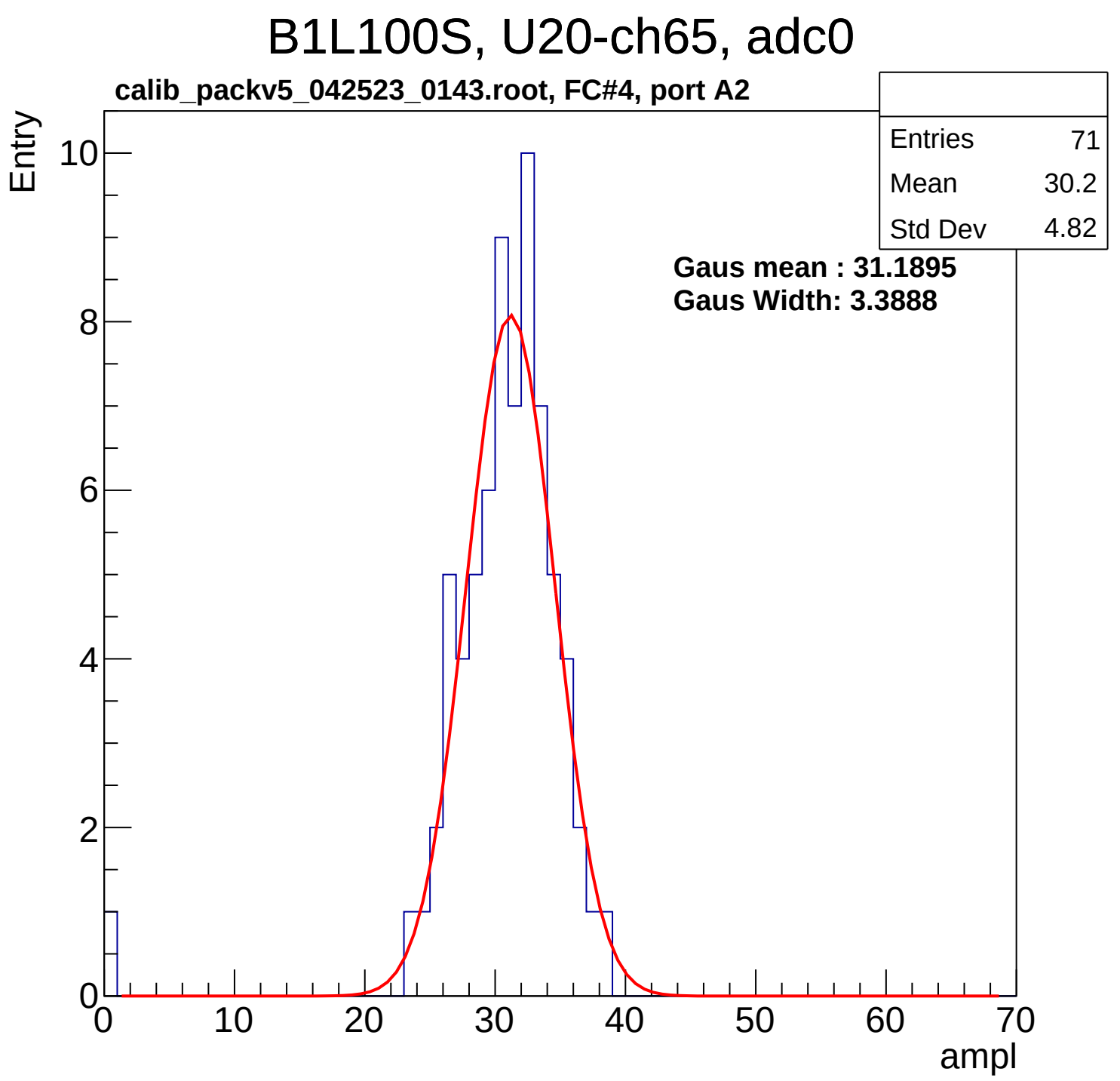
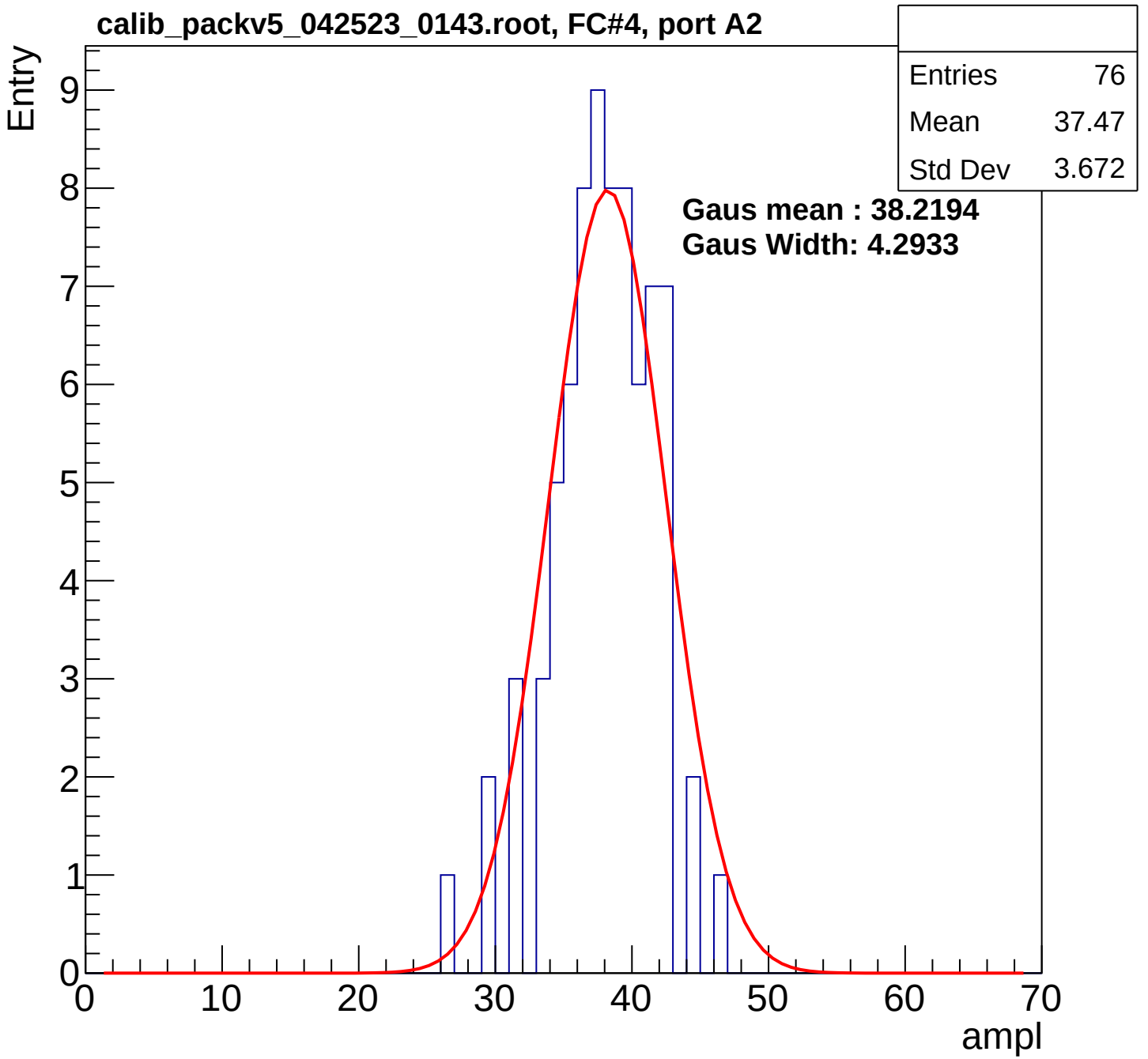
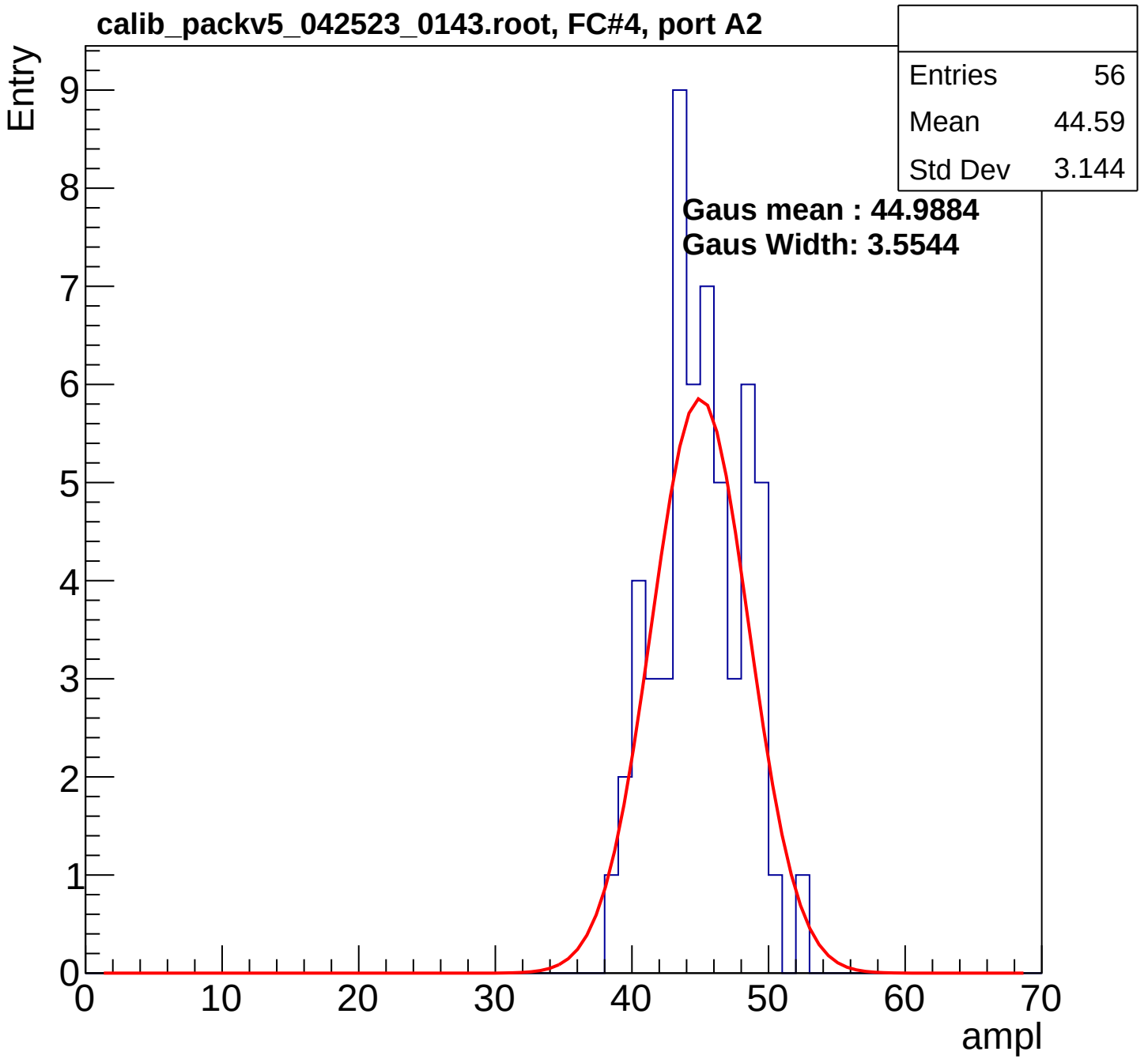

B1L100S, U20-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.2
Std Dev 4.82
Gaus mean : 31.1895
Gaus Width: 3.3888
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.47
Std Dev 3.672
Gaus mean : 38.2194
Gaus Width: 4.2933
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.59
Std Dev 3.144
Gaus mean : 44.9884
Gaus Width: 3.5544
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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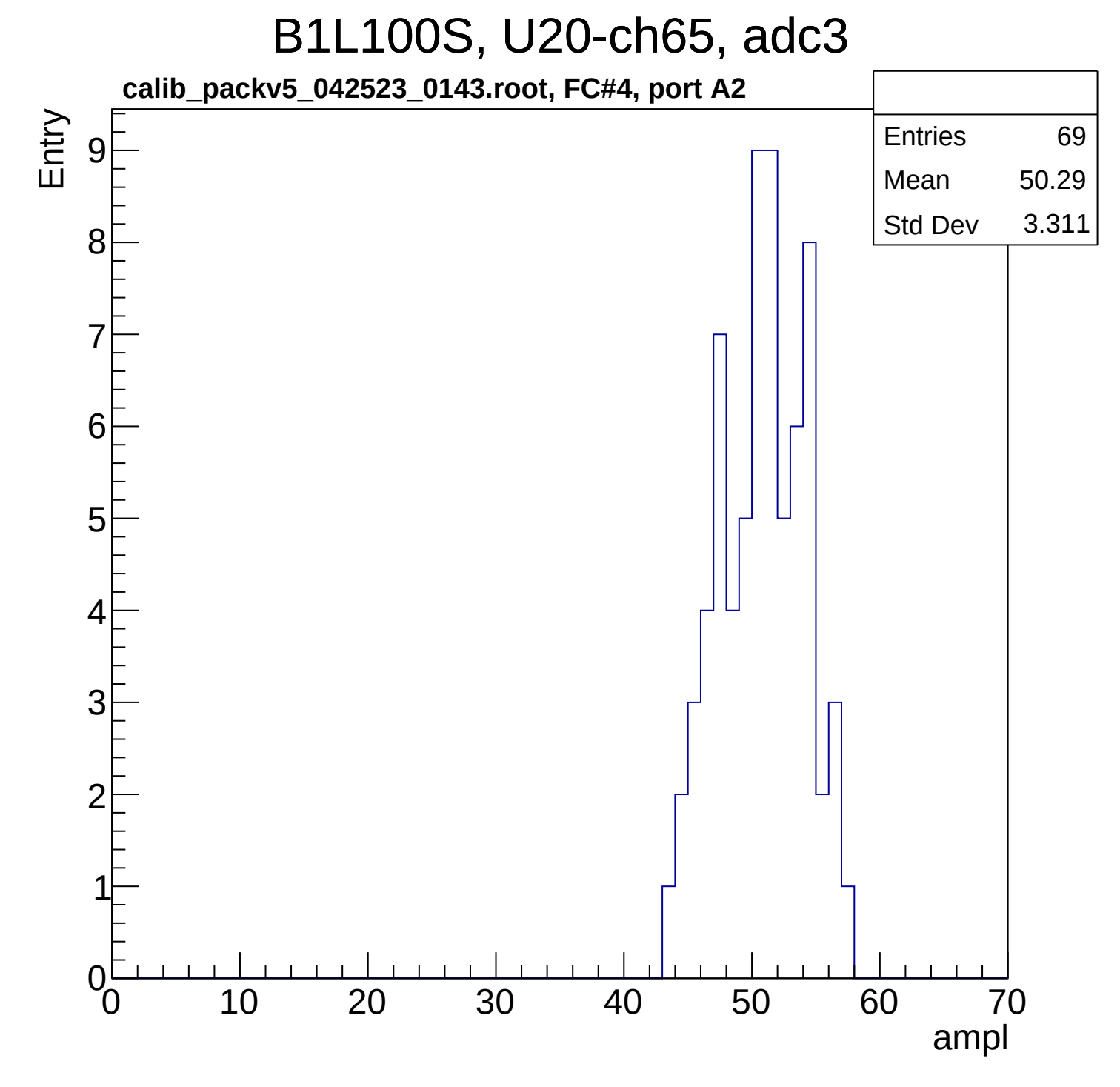
B1L100S, U20-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 50.29
Std Dev 3.311
Entry
ampl
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3
4
5
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0
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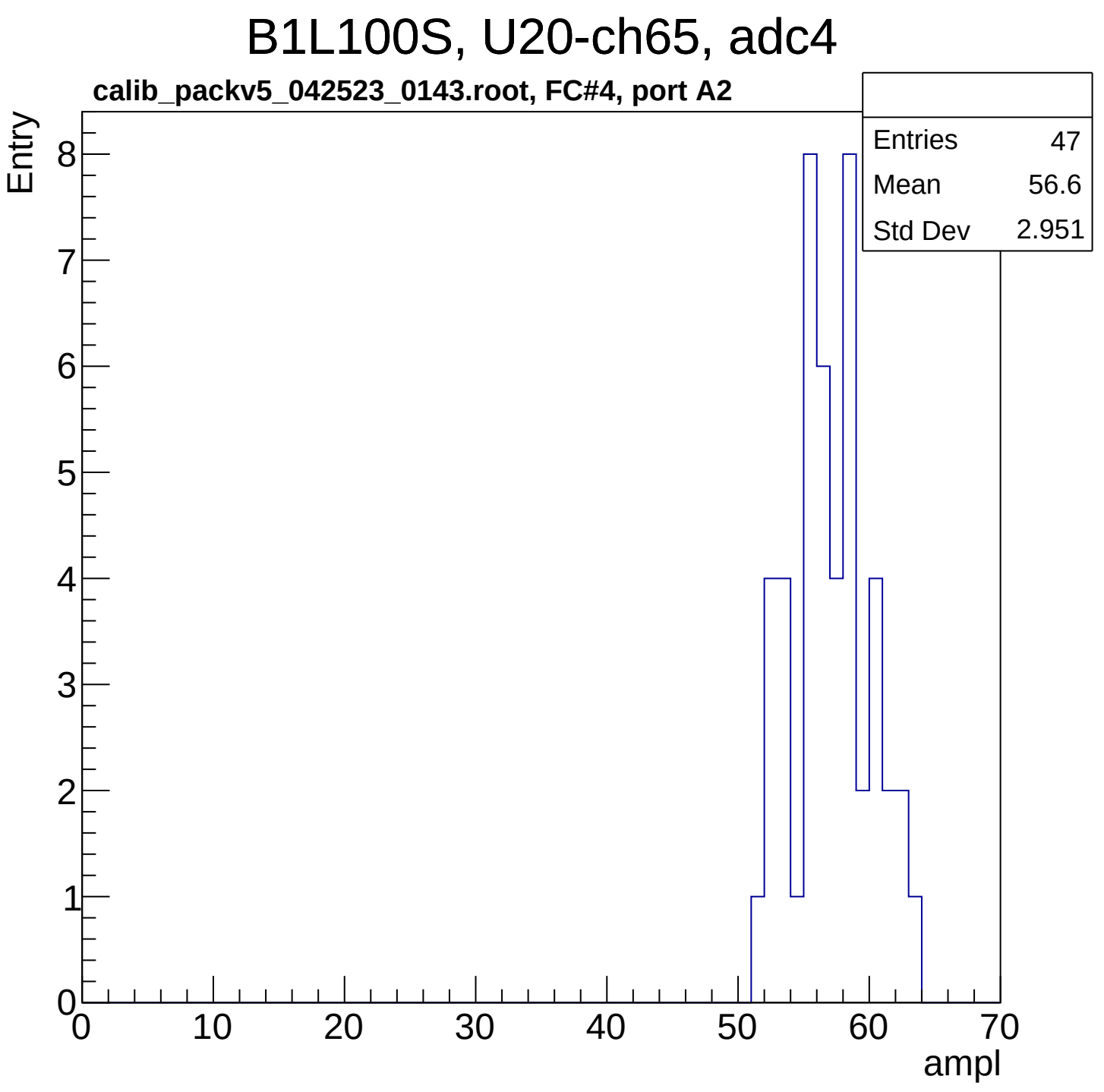
B1L100S, U20-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 56.6
Std Dev 2.951
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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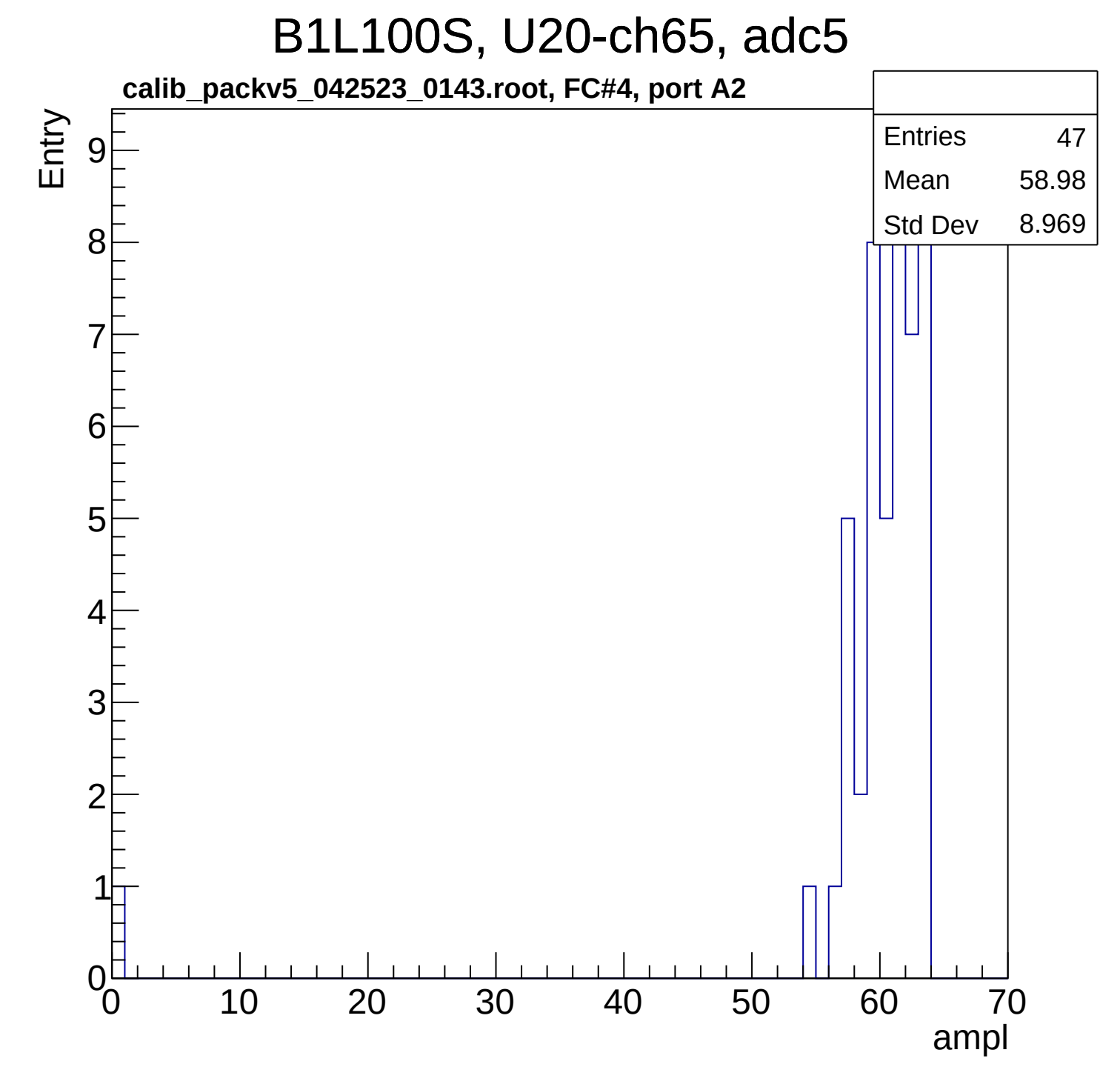
B1L100S, U20-ch65, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.98
Std Dev 8.969
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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60
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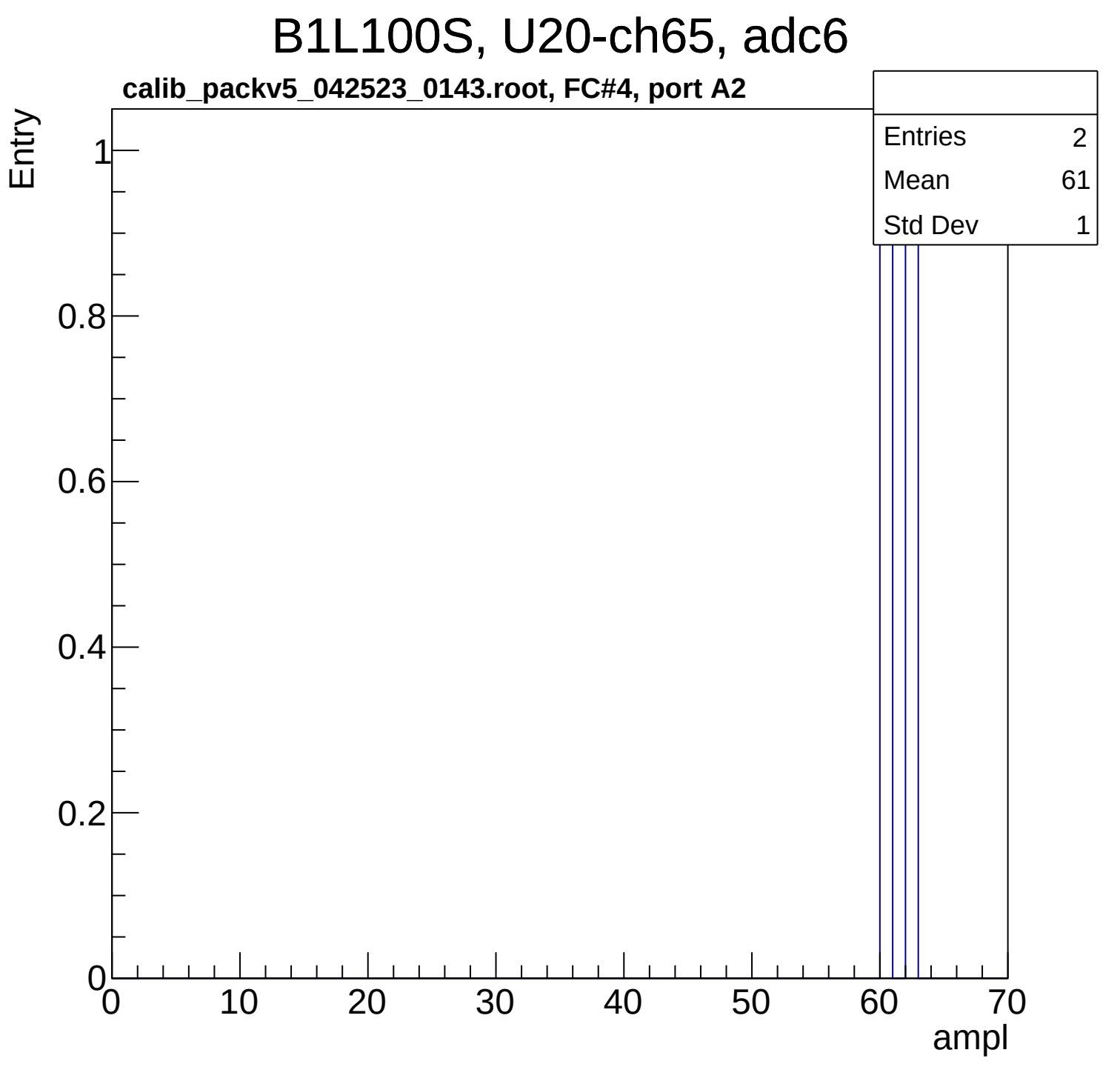
B1L100S, U20-ch65, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 1
Entry
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0.8
0.6
0.4
0.2
0
0
10
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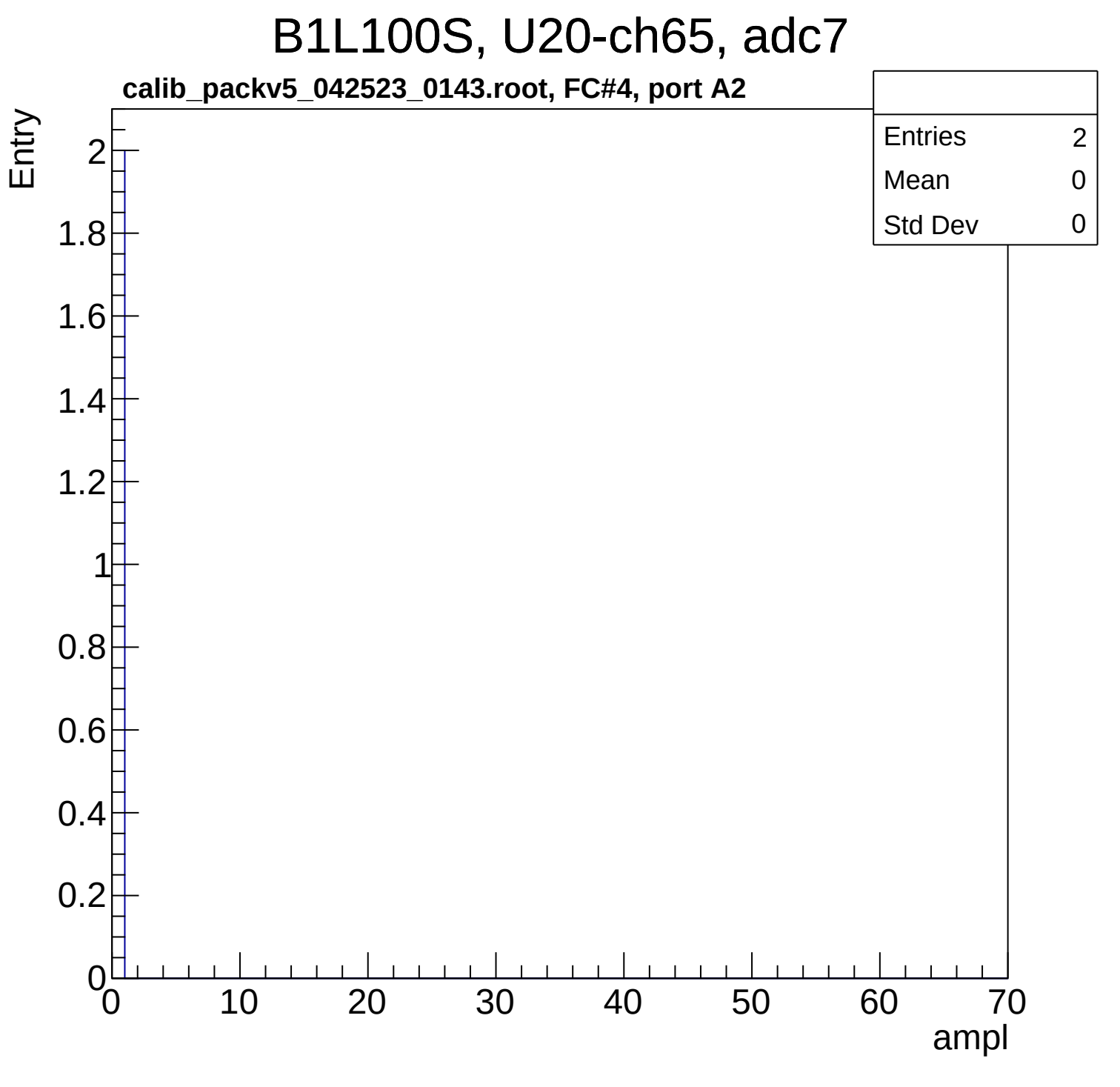
50
60
70
ampl


B1L100S, U20-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
0
0
10
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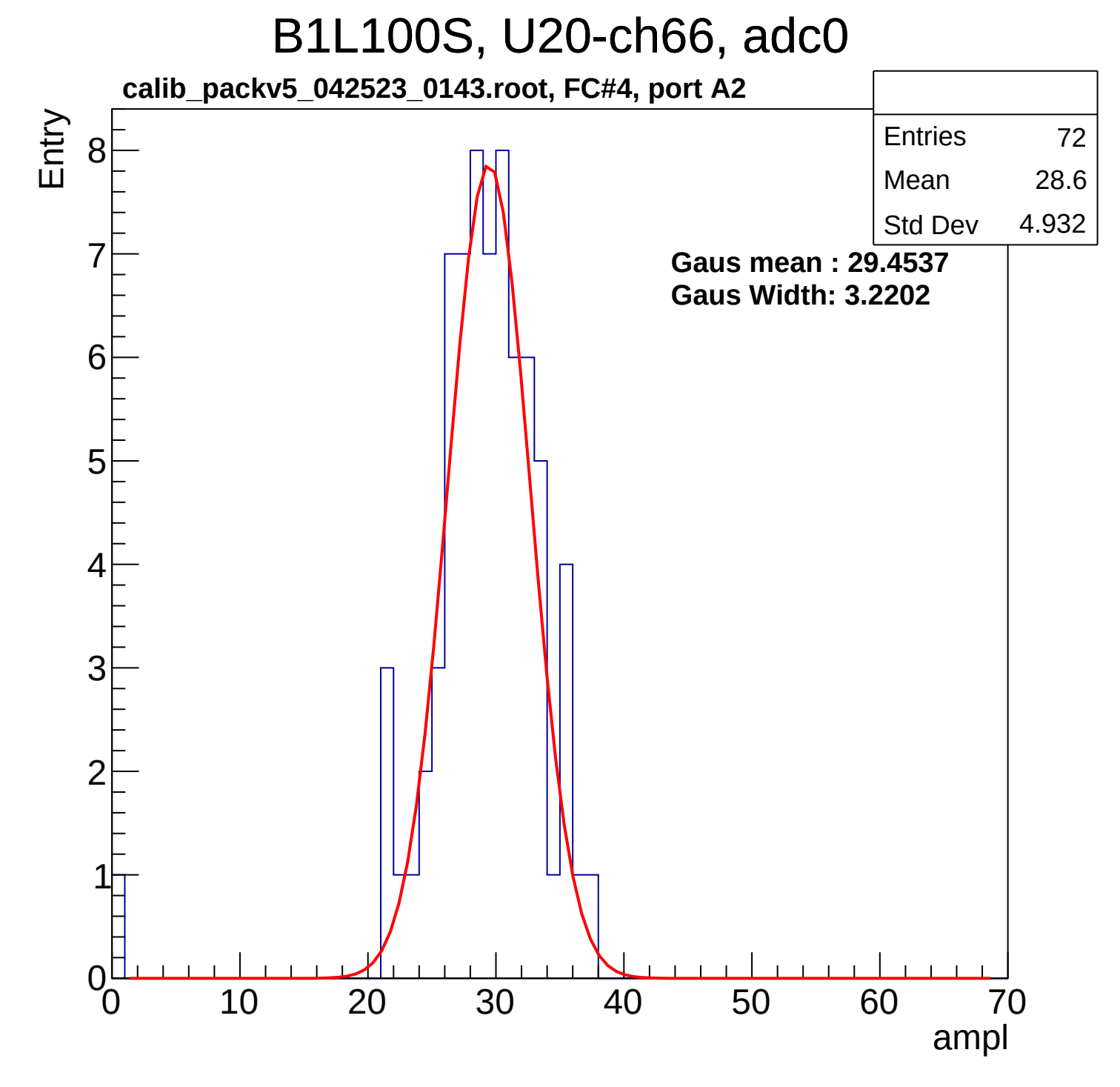
70
ampl


B1L100S, U20-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.6
Std Dev 4.932
Gaus mean : 29.4537
Gaus Width: 3.2202
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
50
60
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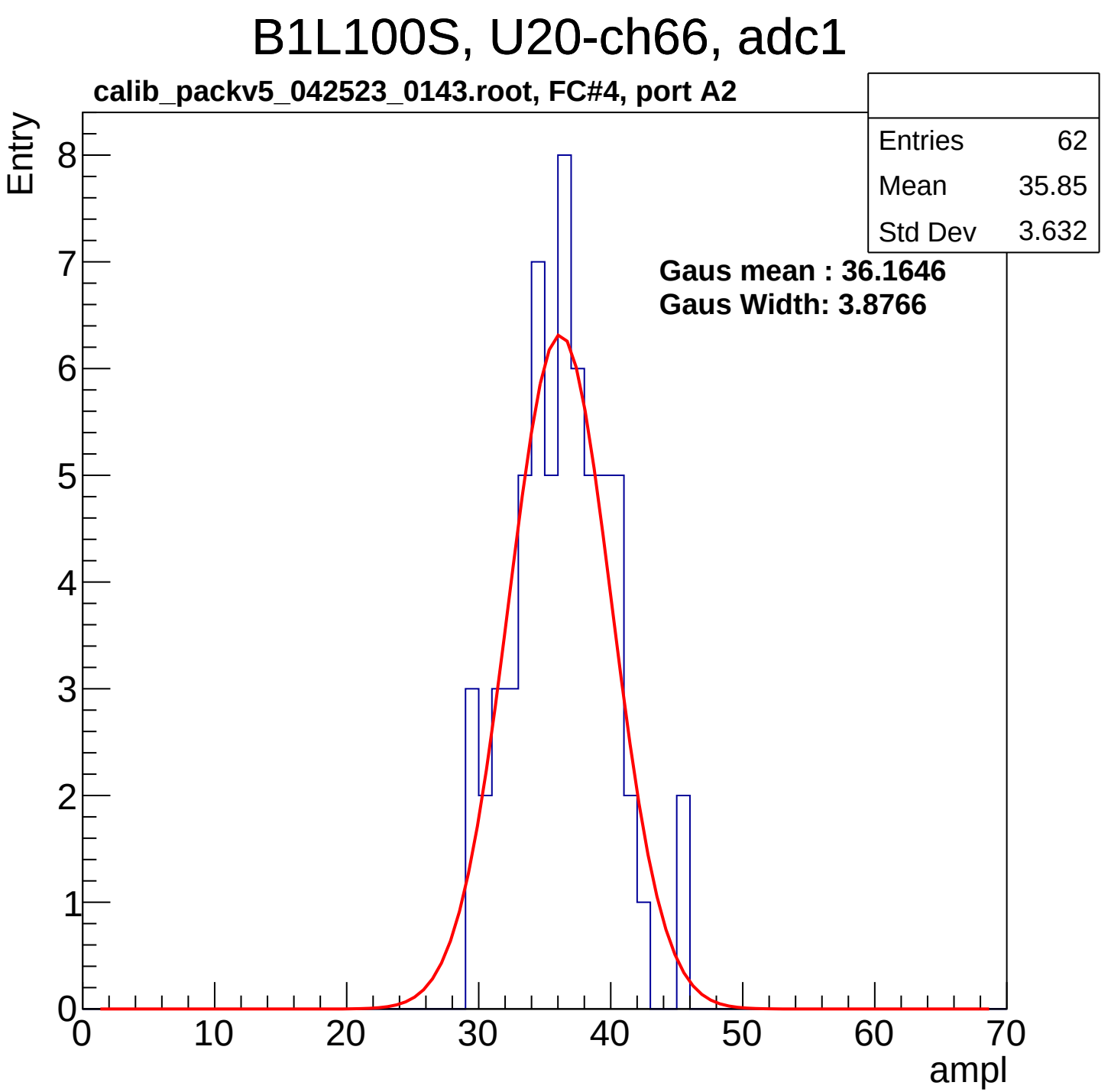
B1L100S, U20-ch66, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 35.85
Std Dev 3.632
Gaus mean : 36.1646
Gaus Width: 3.8766
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
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60
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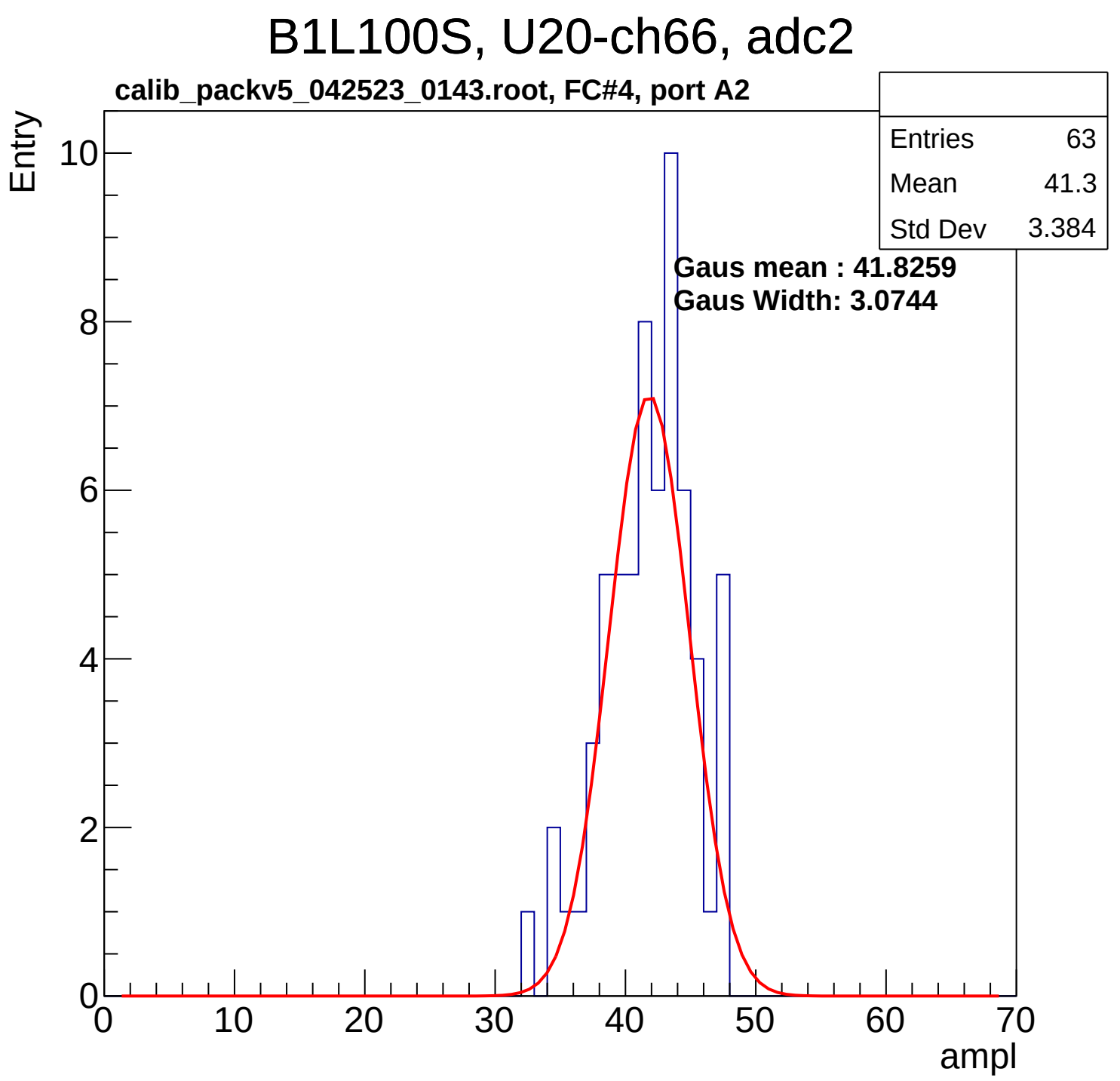
B1L100S, U20-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 41.3
Std Dev 3.384
Gaus mean : 41.8259
Gaus Width: 3.0744
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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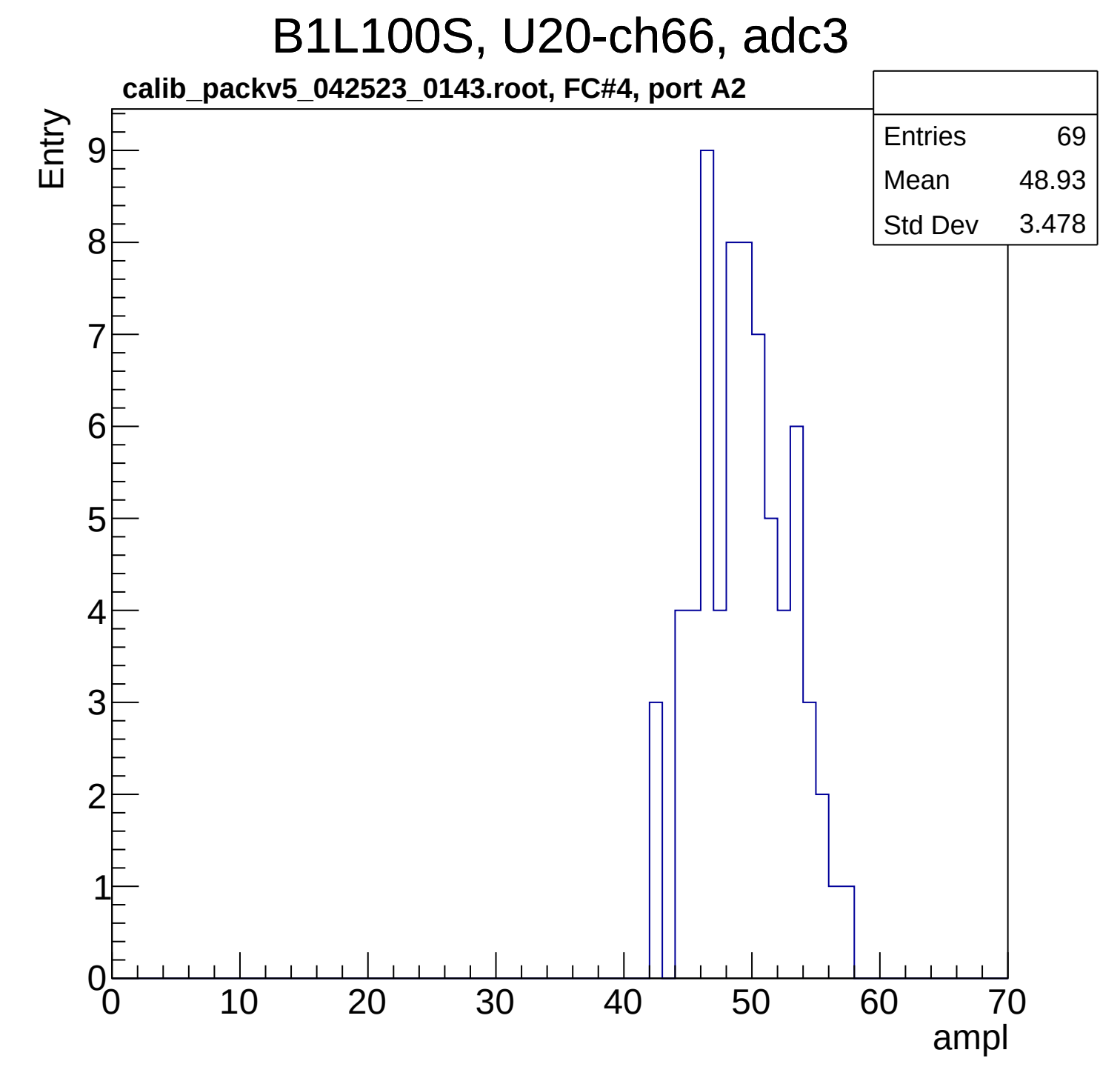
B1L100S, U20-ch66, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.93
Std Dev 3.478
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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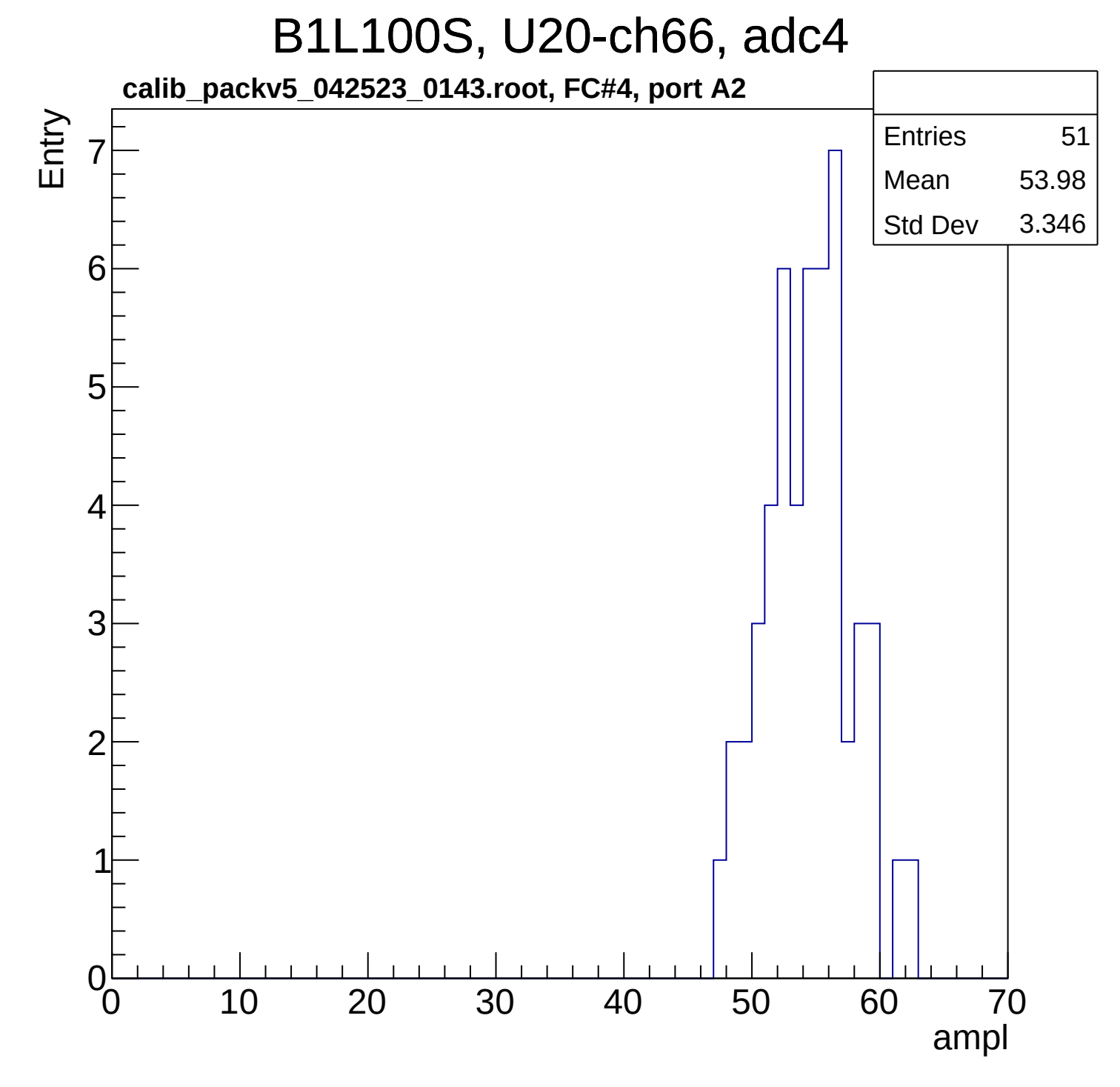
B1L100S, U20-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 53.98
Std Dev 3.346
Entry
ampl
0
1
2
3
4
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0
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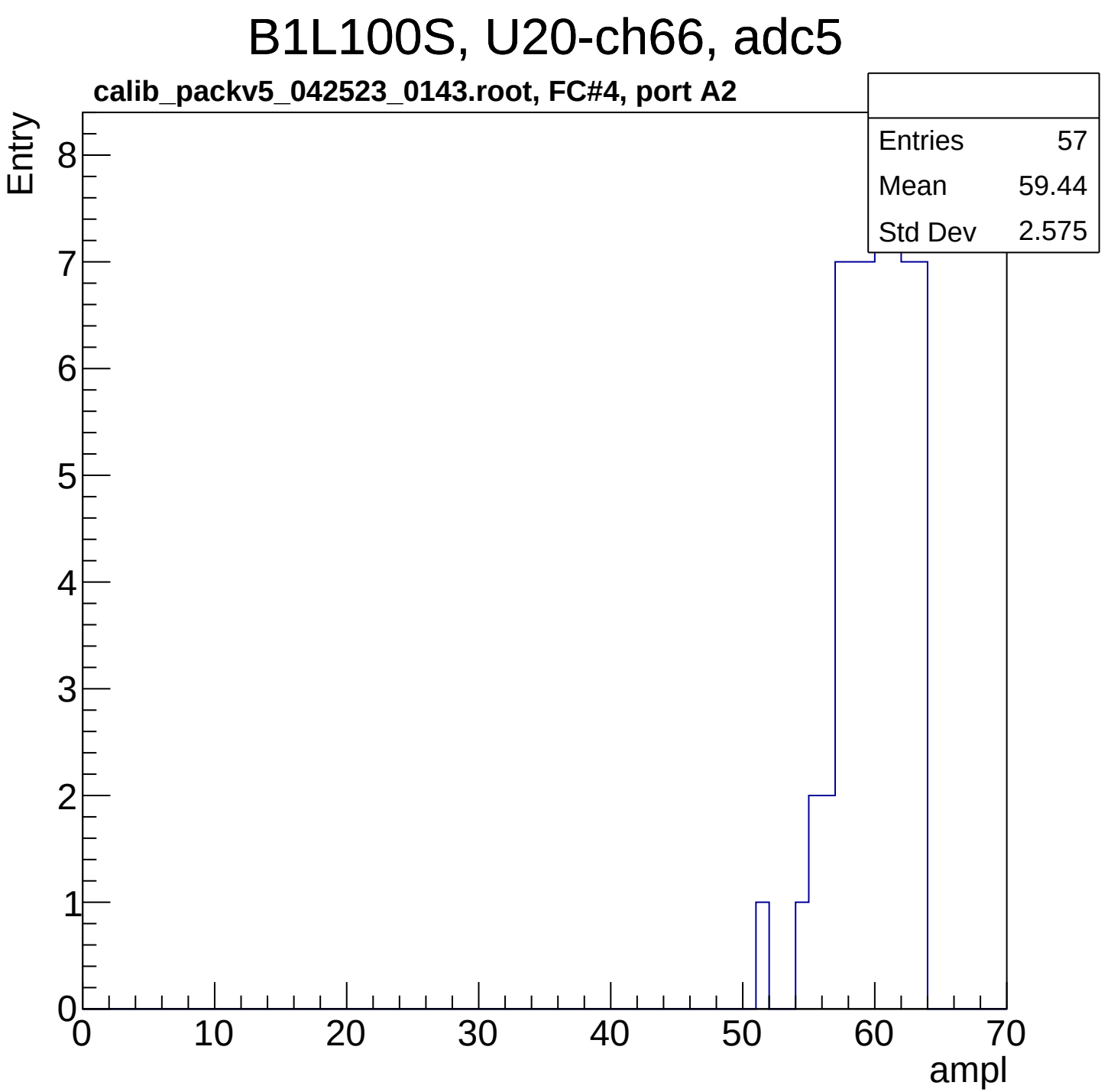
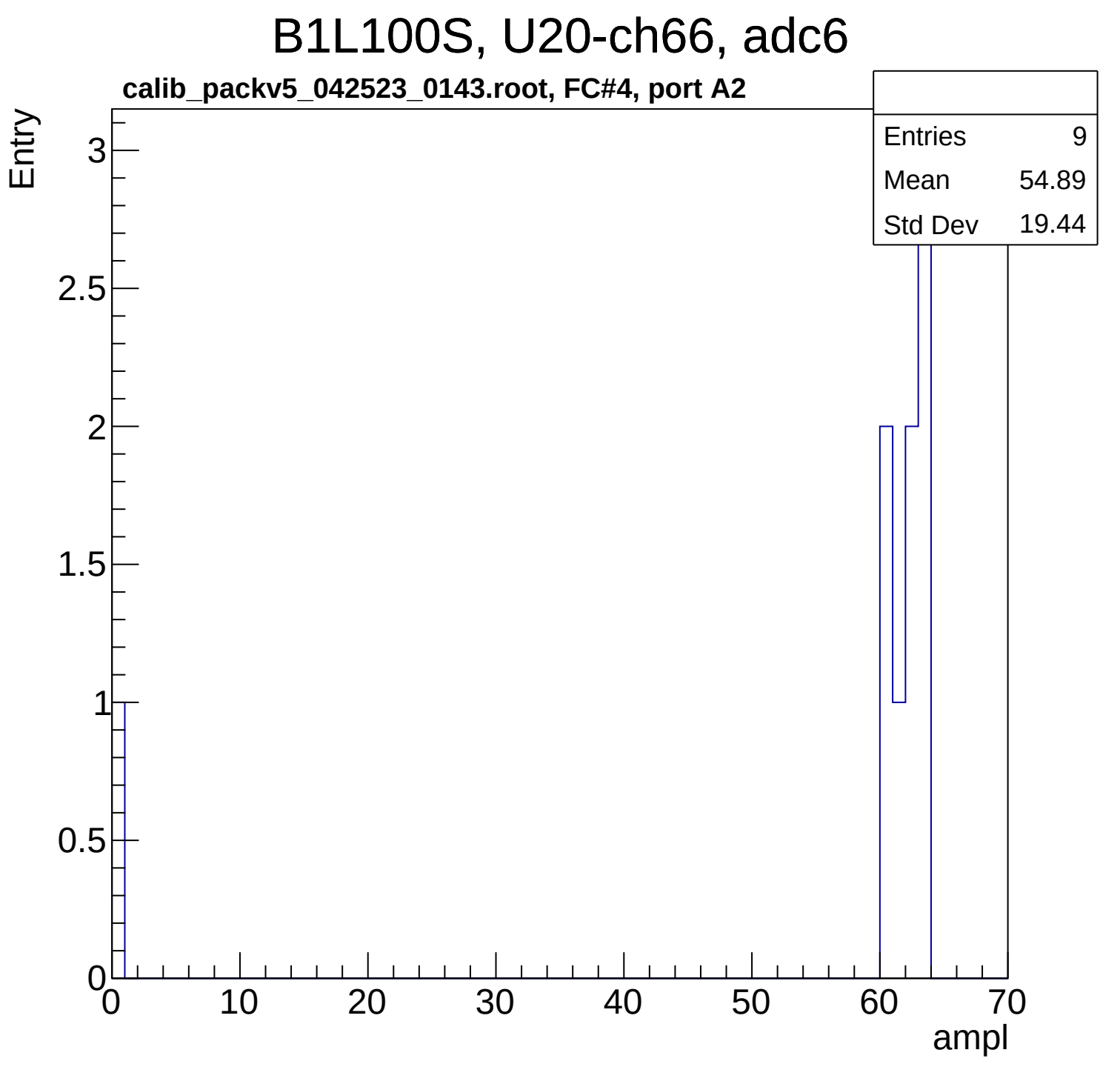
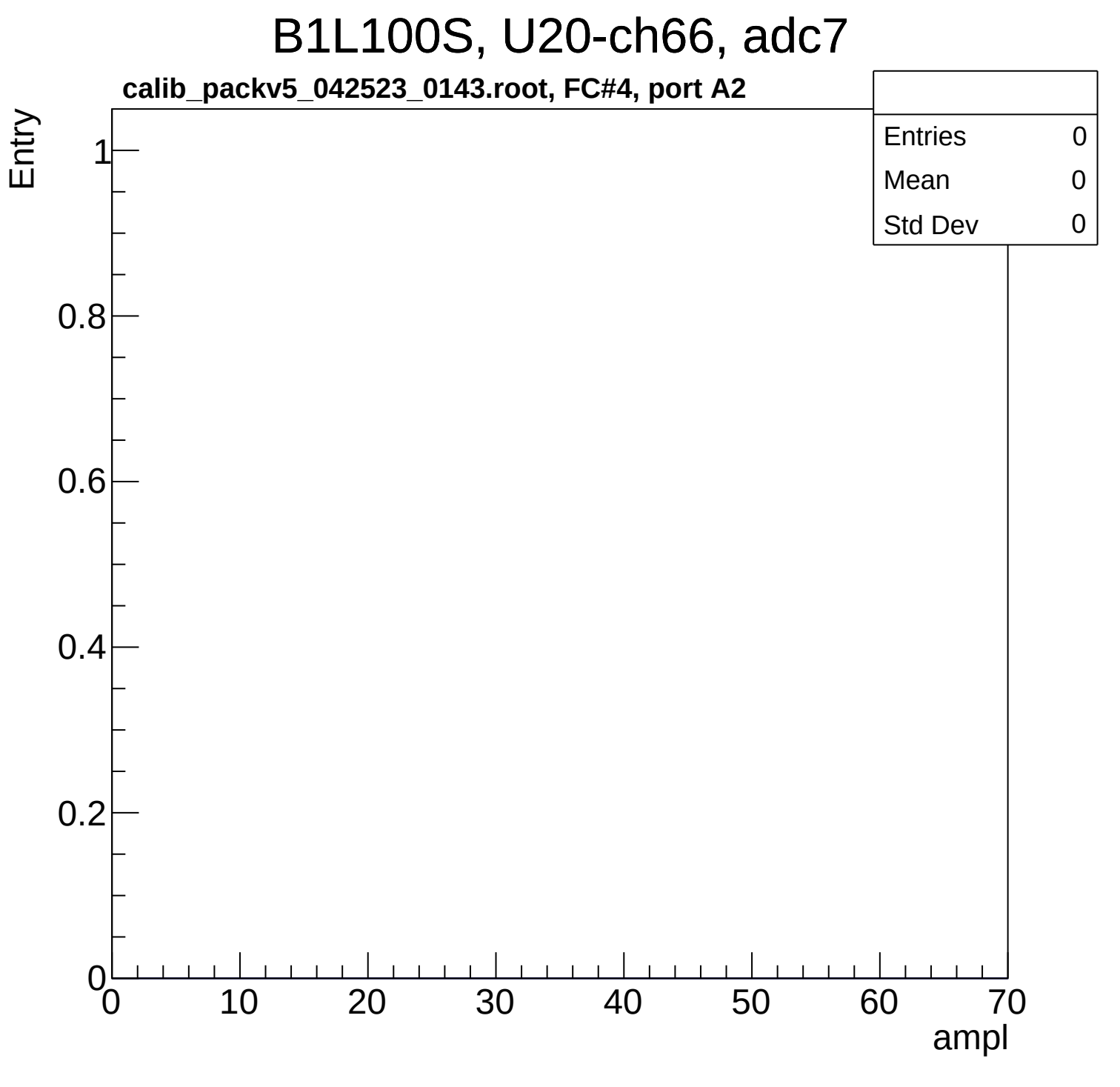
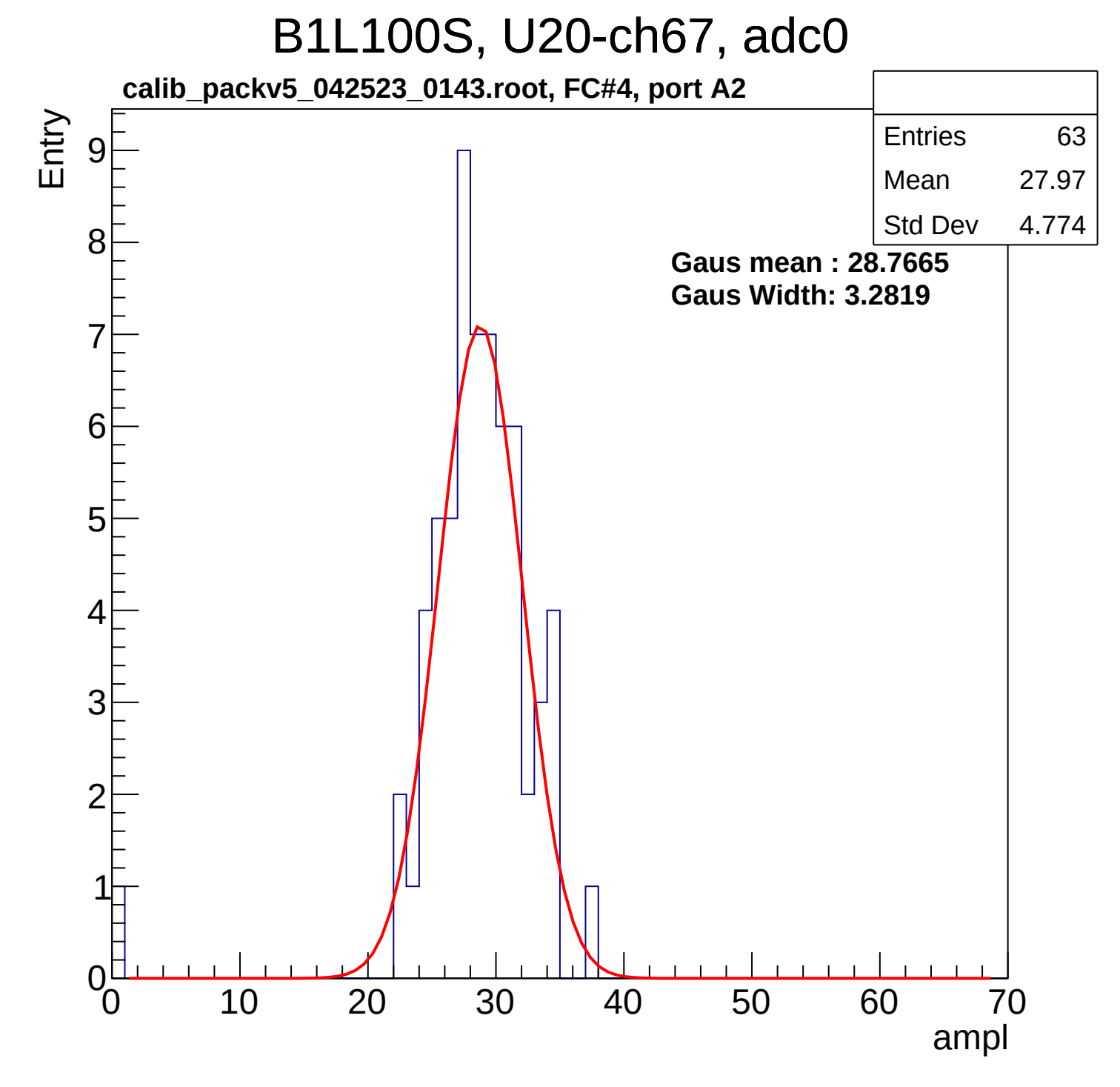

B1L100S, U20-ch66, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 59.44
Std Dev 2.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch66, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 54.89
Std Dev 19.44
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 27.97
Std Dev 4.774
Gaus mean : 28.7665
Gaus Width: 3.2819
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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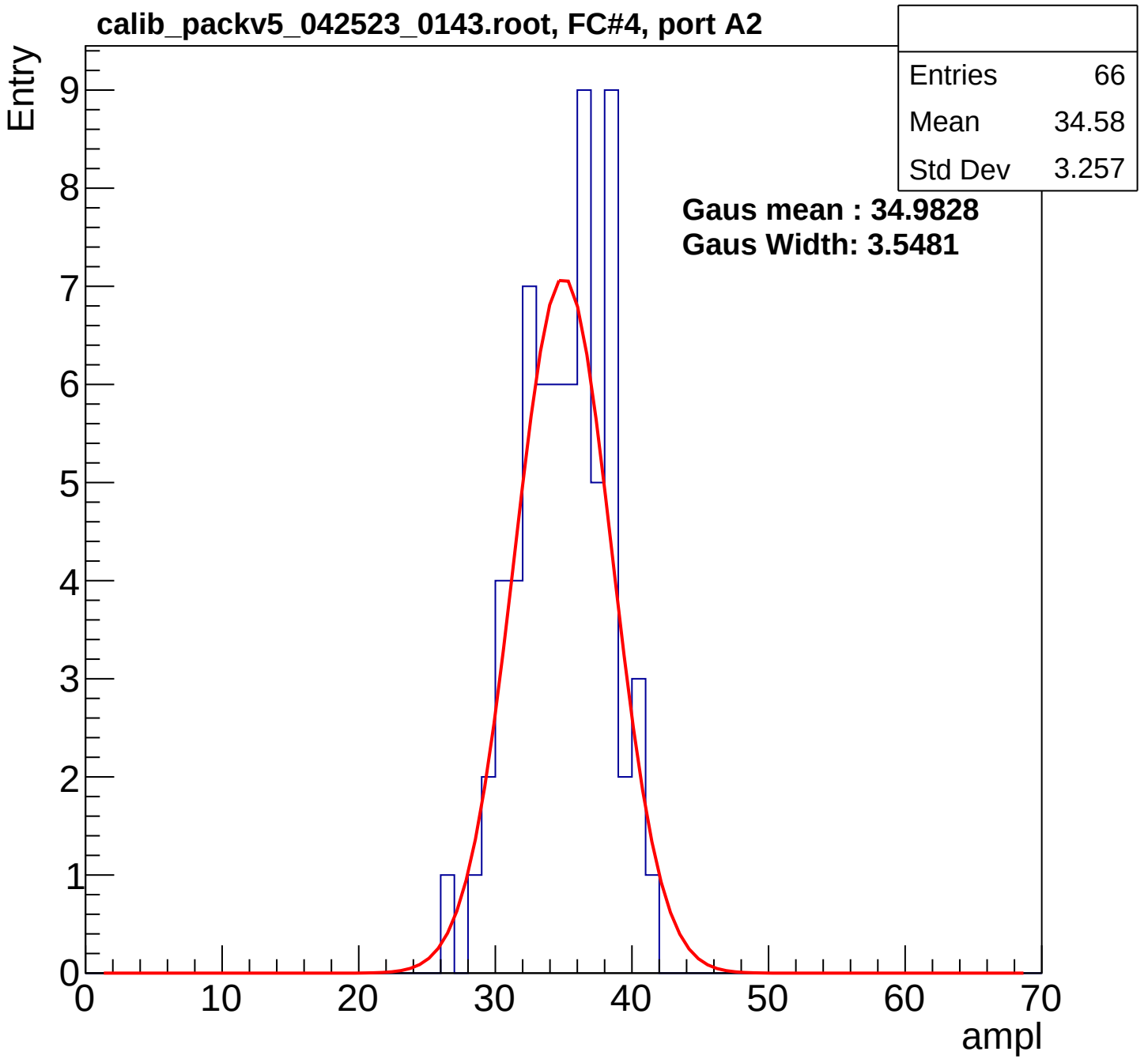
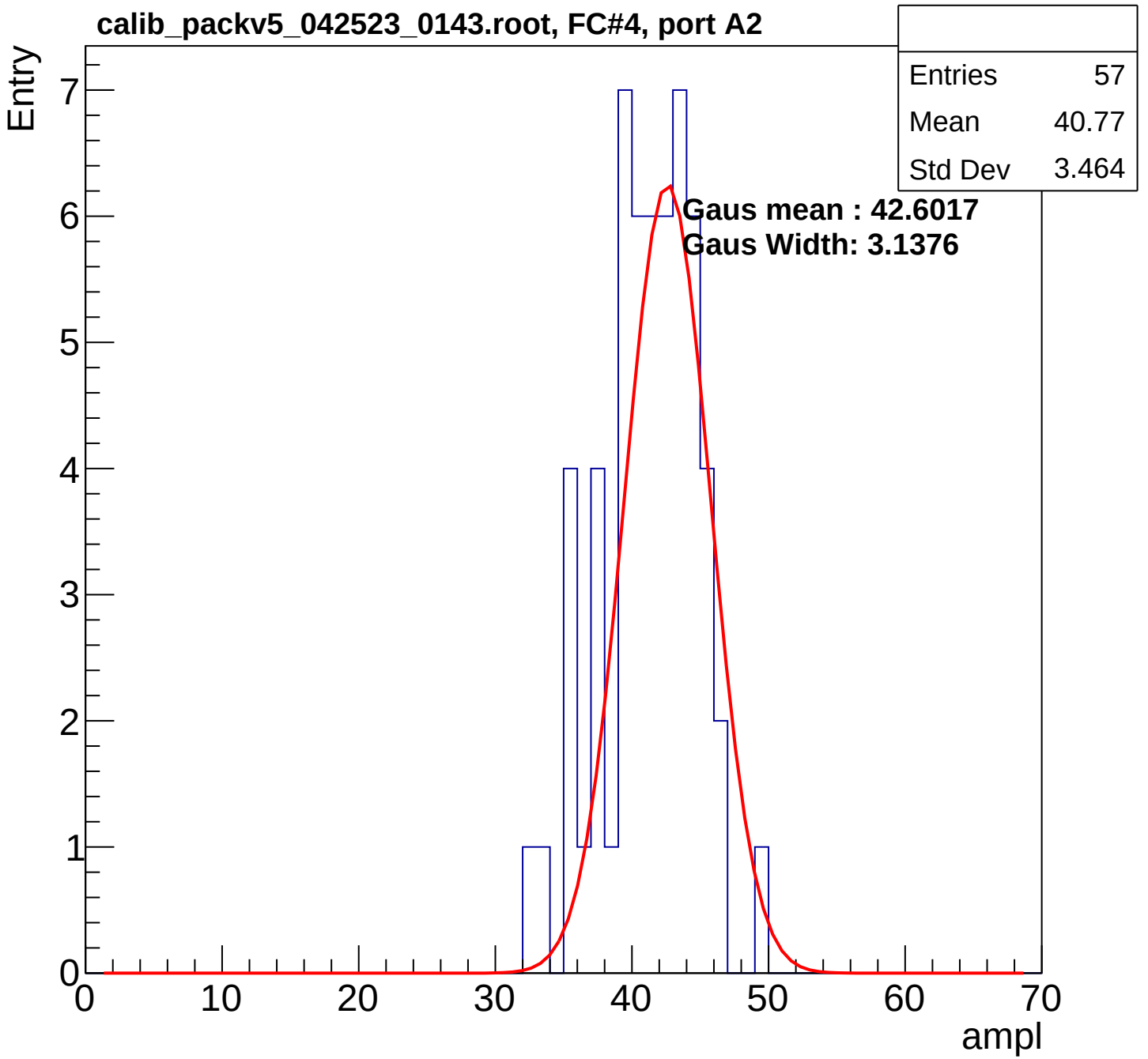
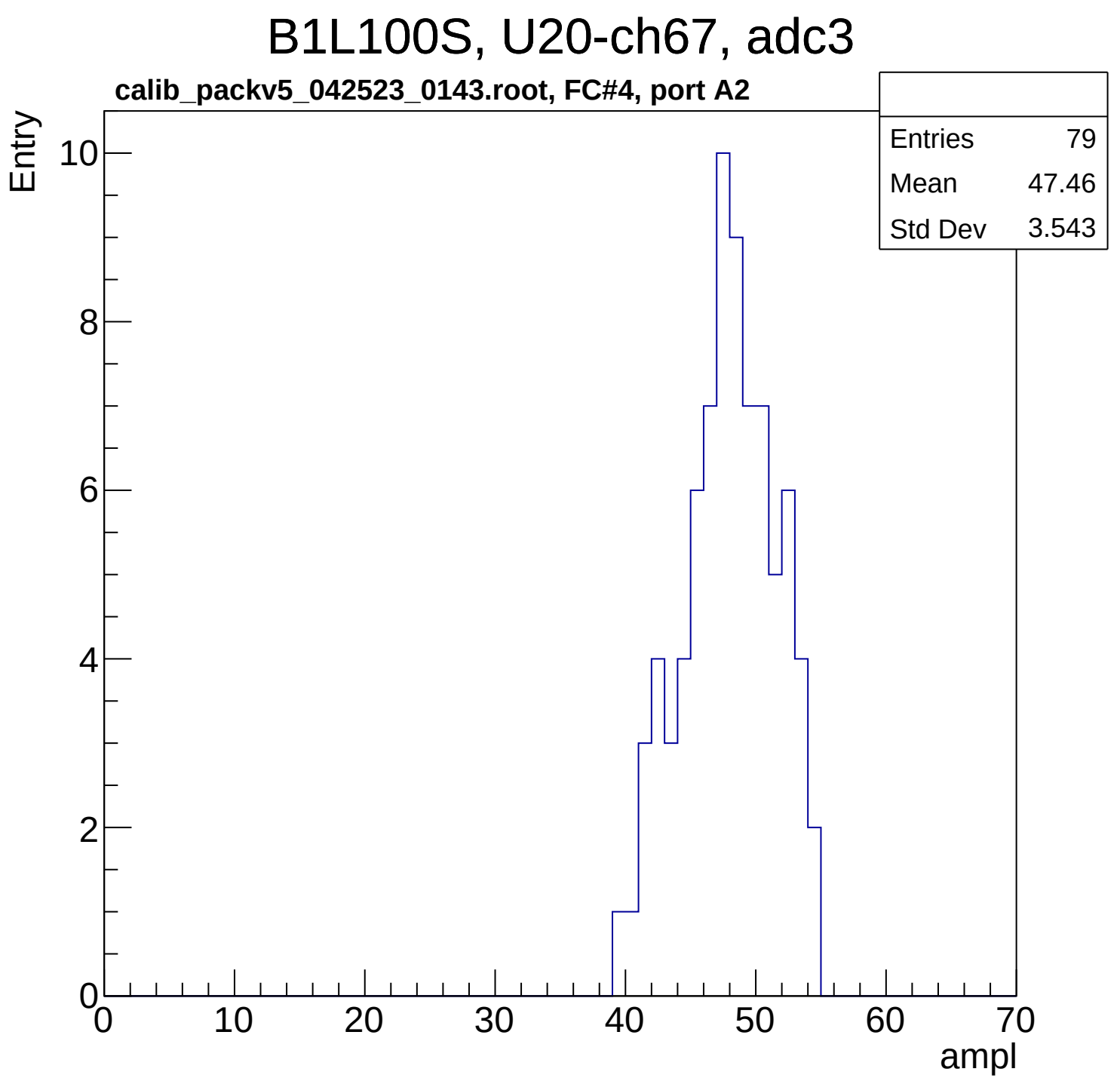
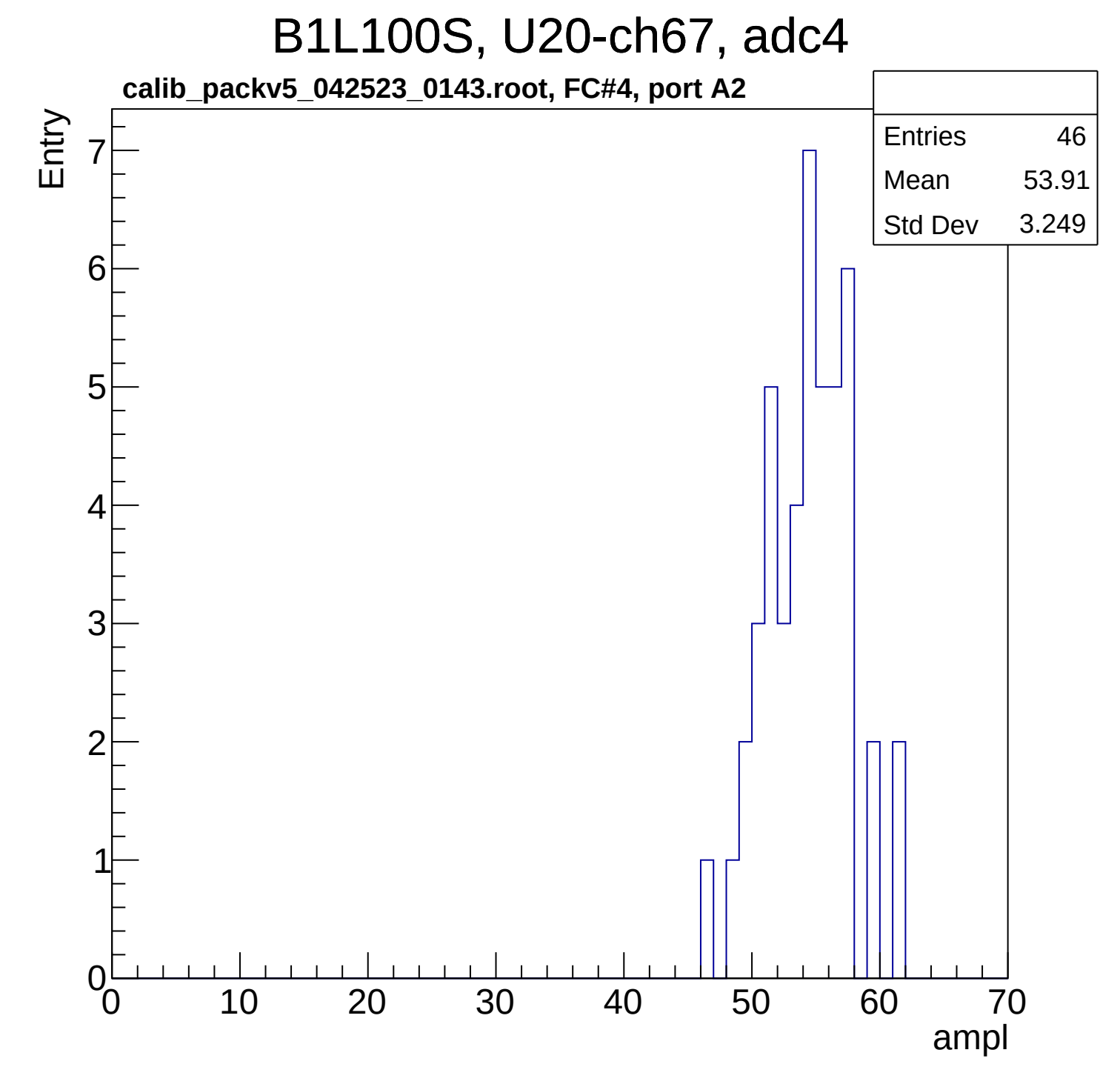
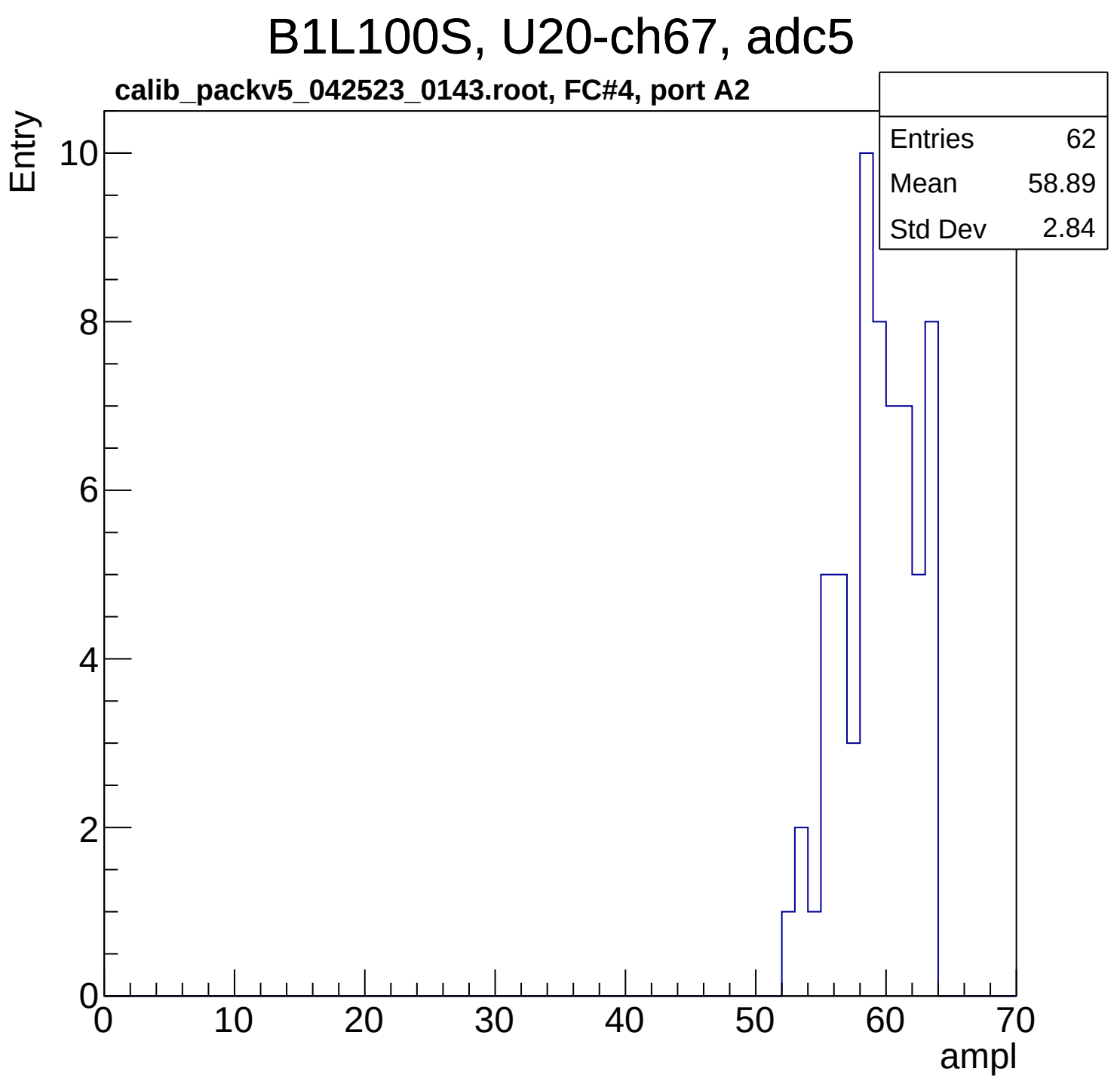

B1L100S, U20-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 34.58
Std Dev 3.257
Gaus mean : 34.9828
Gaus Width: 3.5481
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 40.77
Std Dev 3.464
Gaus mean : 42.6017
Gaus Width: 3.1376
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 47.46
Std Dev 3.543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch67, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 53.91
Std Dev 3.249
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 58.89
Std Dev 2.84
Entry
0
2
4
6
8
10
0
10
20
30
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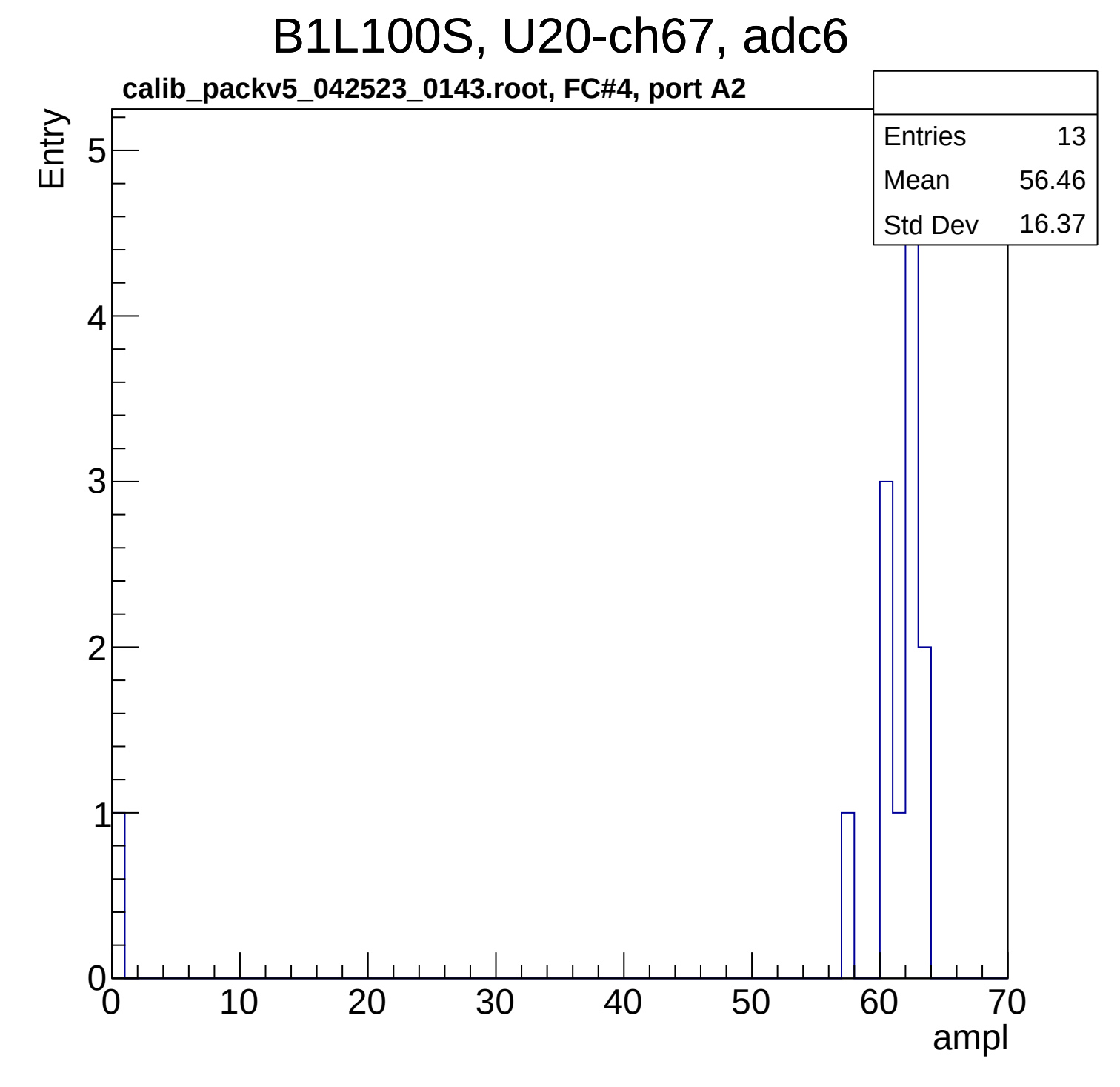
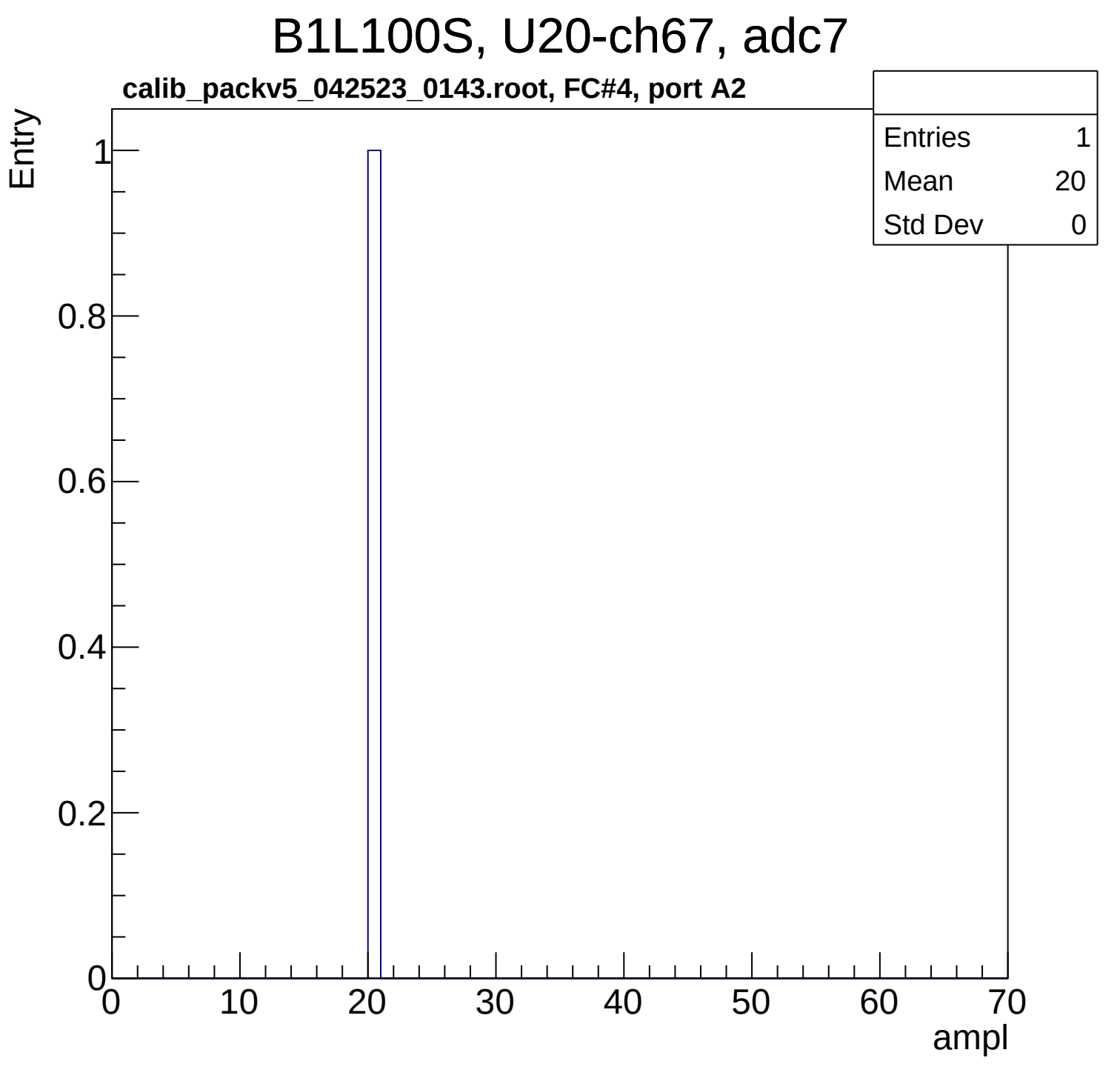
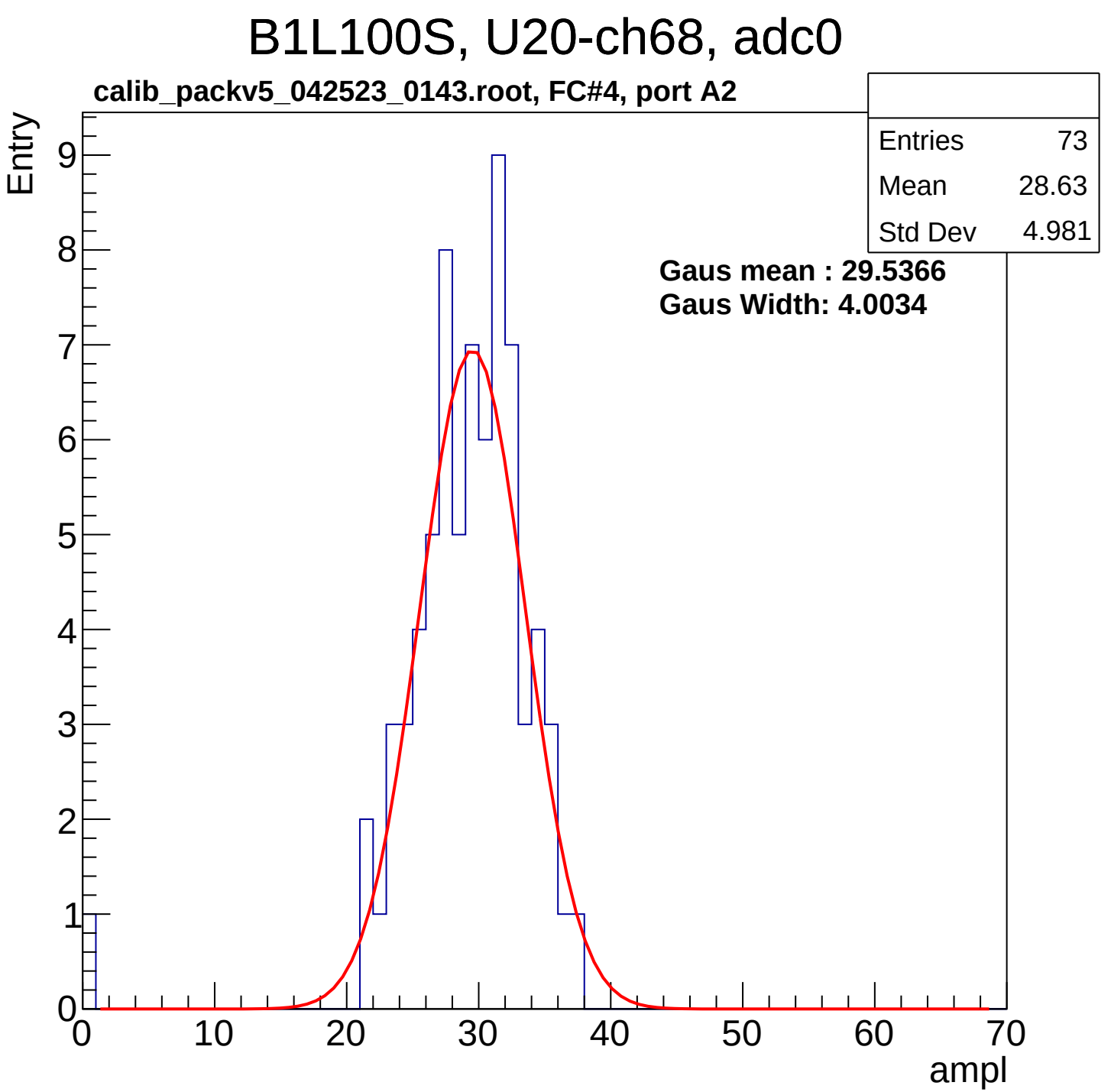
50
60
70
ampl


B1L100S, U20-ch67, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 56.46
Std Dev 16.37
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch67, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U20-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 28.63
Std Dev 4.981
Gaus mean : 29.5366
Gaus Width: 4.0034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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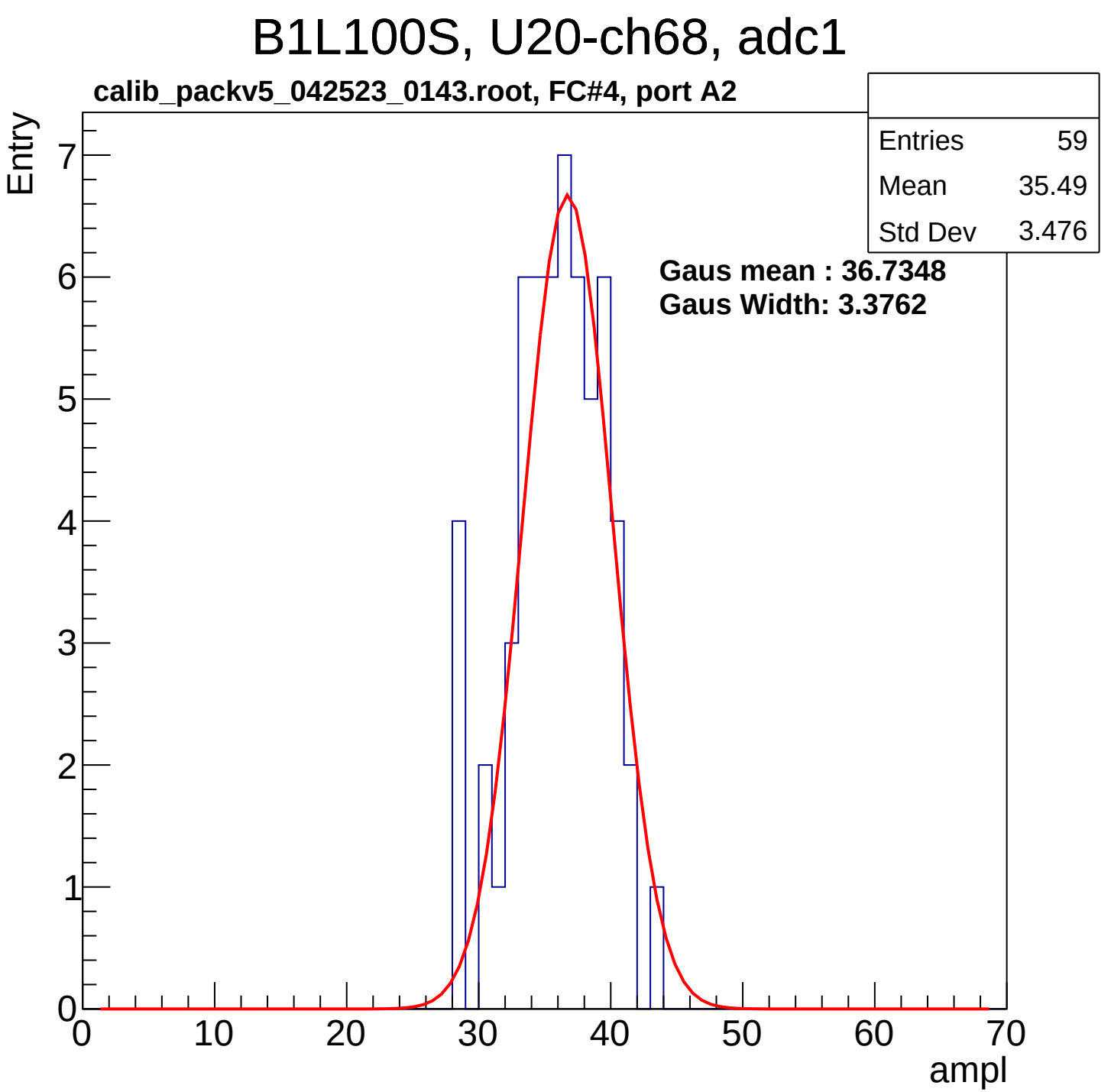
B1L100S, U20-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 35.49
Std Dev 3.476
Gaus mean : 36.7348
Gaus Width: 3.3762
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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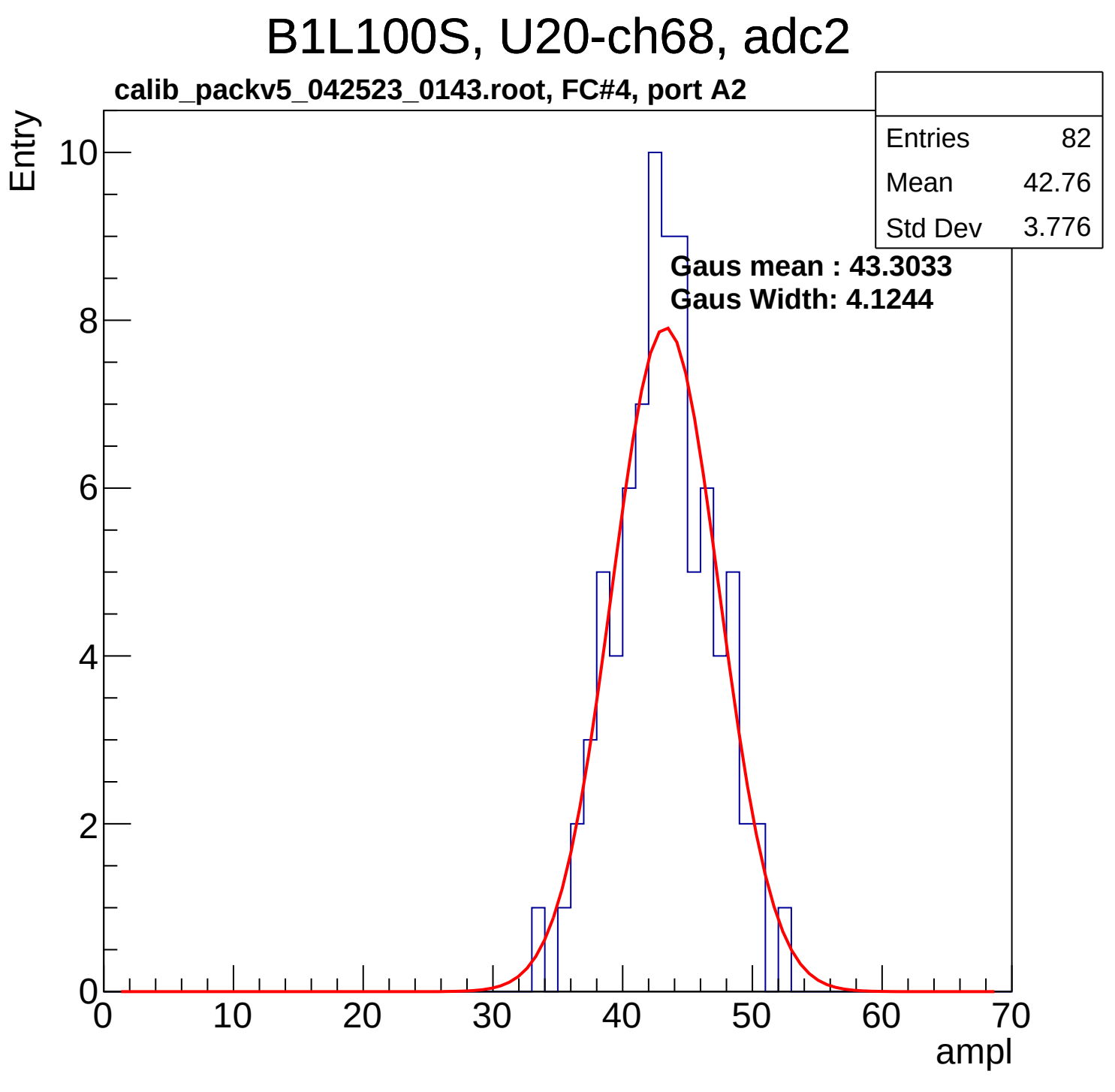
40
50
60
70


B1L100S, U20-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 42.76
Std Dev 3.776
Gaus mean : 43.3033
Gaus Width: 4.1244
Entry
ampl
0
2
4
6
8
10
0
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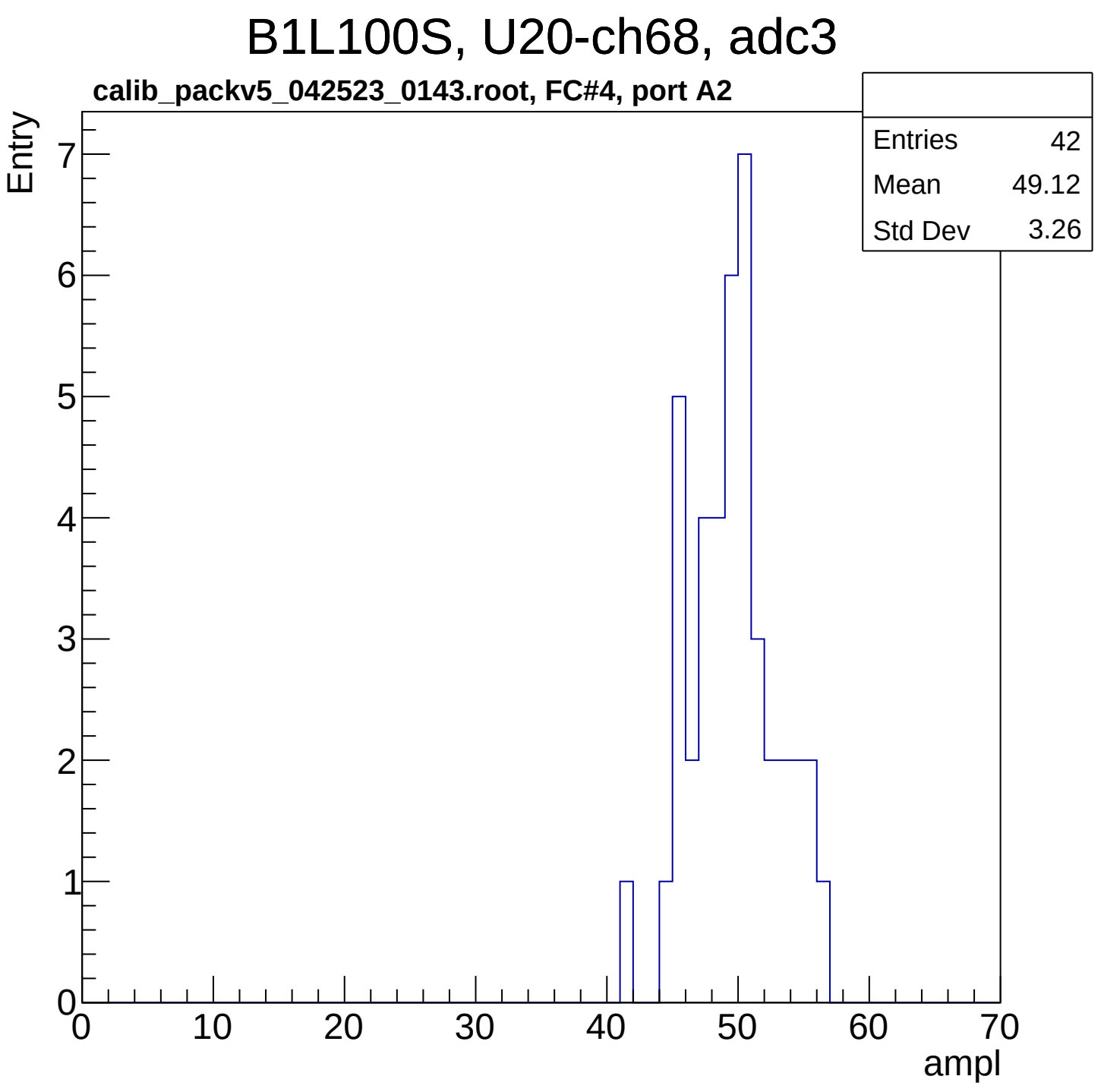
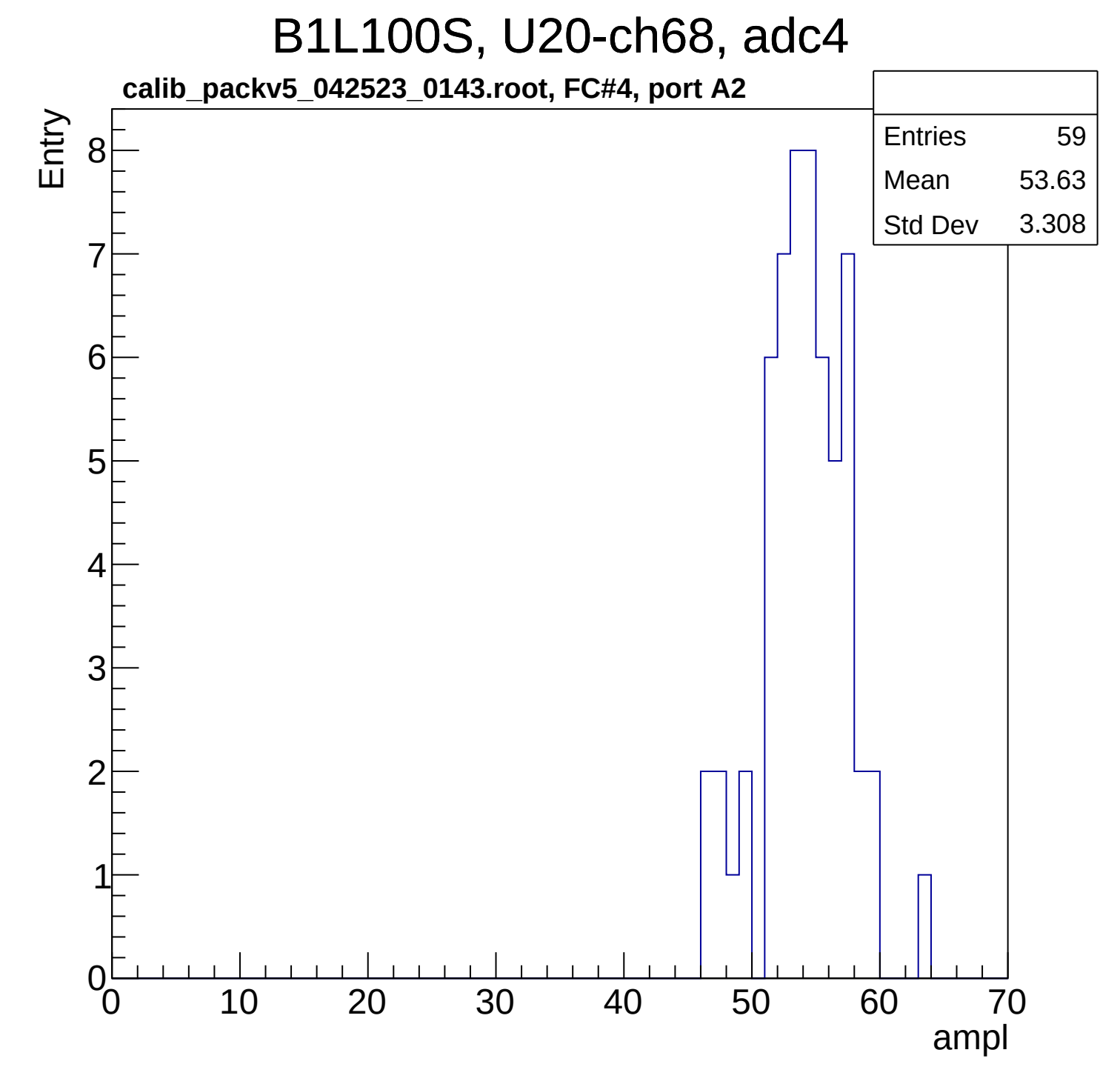
20
30
40
50
60
70


B1L100S, U20-ch68, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 49.12
Std Dev 3.26
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch68, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 53.63
Std Dev 3.308
Entry
ampl
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1
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3
4
5
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7
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0
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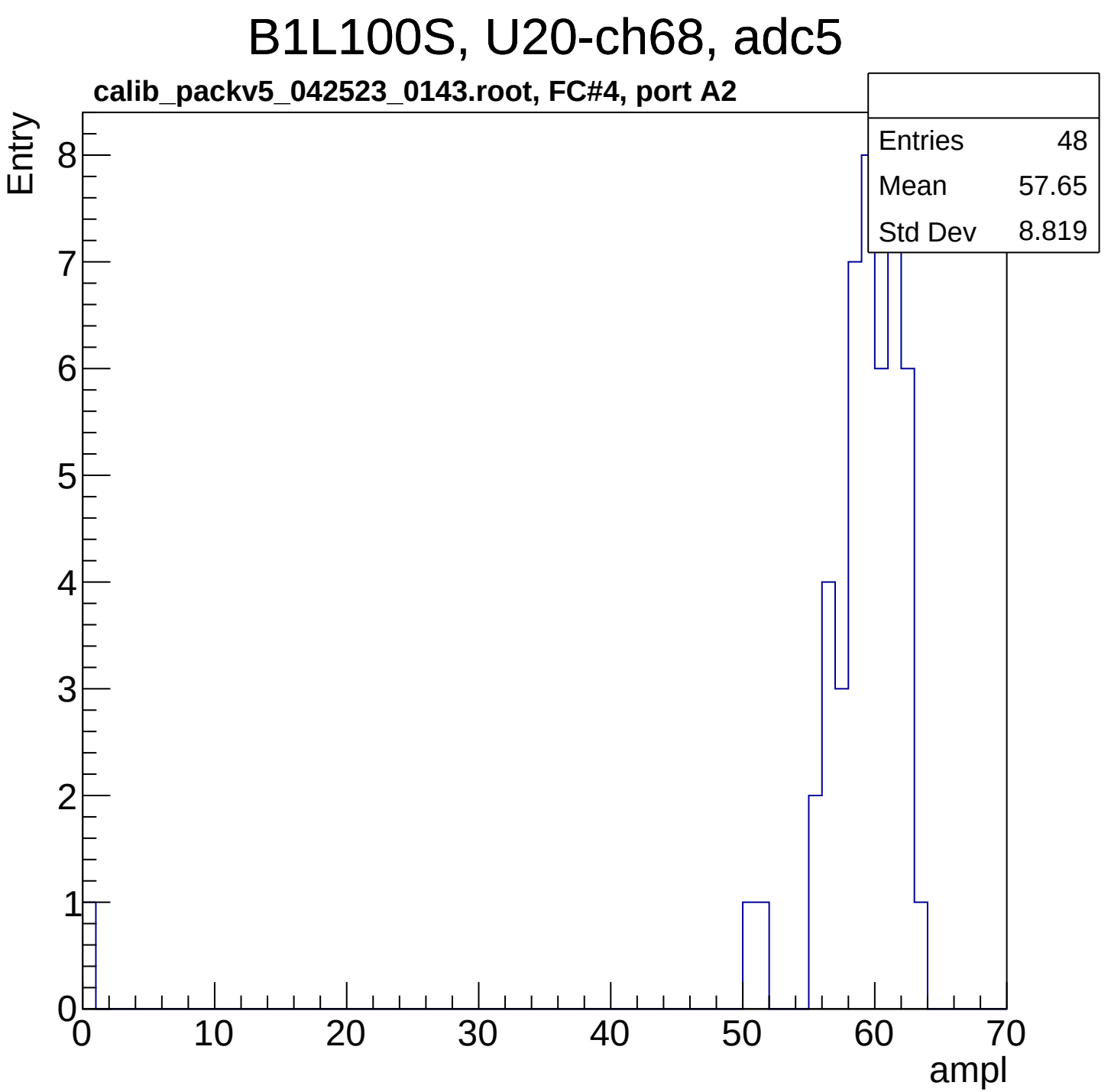
40
50
60
70


B1L100S, U20-ch68, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 57.65
Std Dev 8.819
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
50
60
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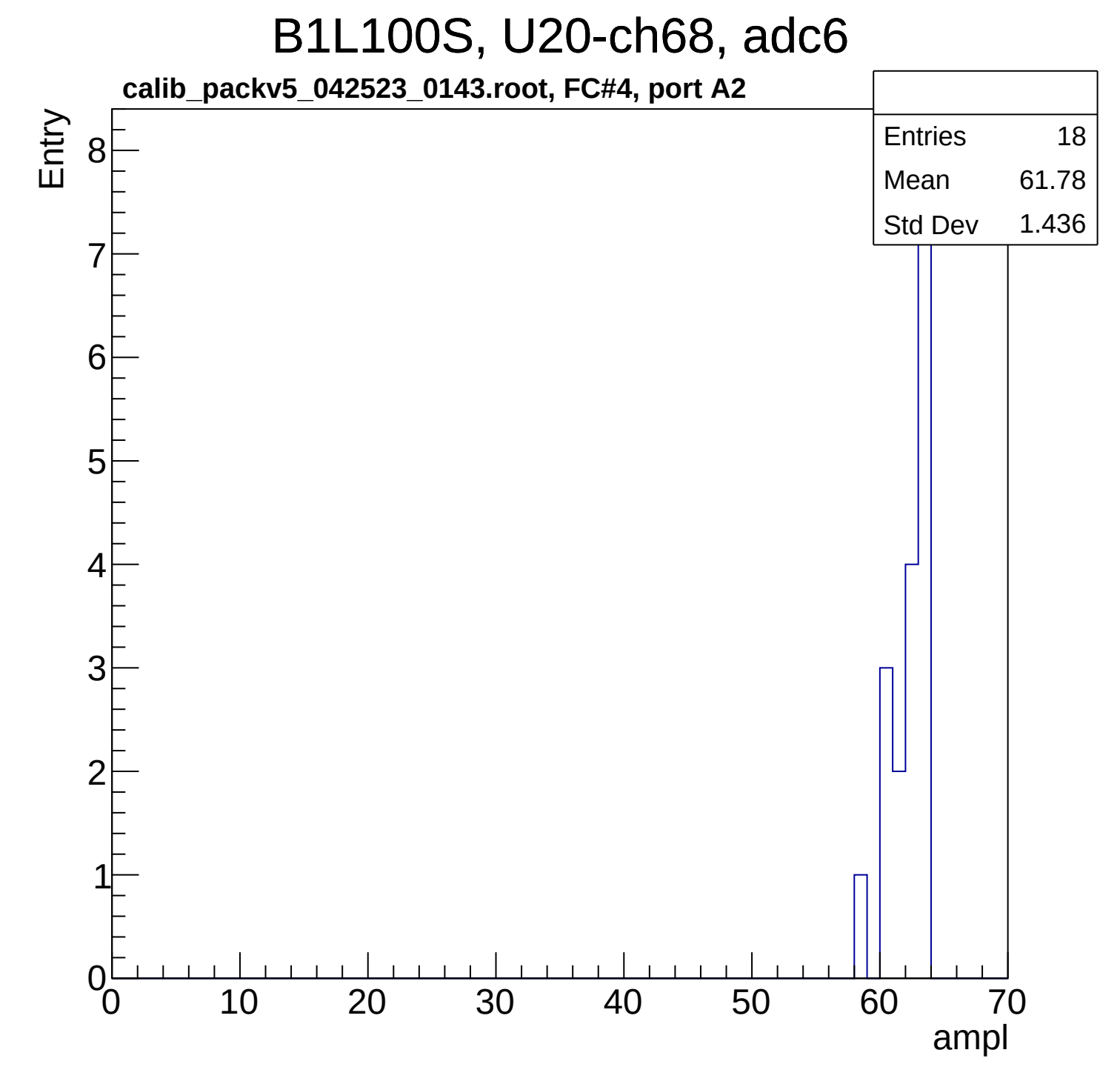
B1L100S, U20-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 18
Mean 61.78
Std Dev 1.436
Entry
ampl
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1
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3
4
5
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7
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0
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20
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40
50
60
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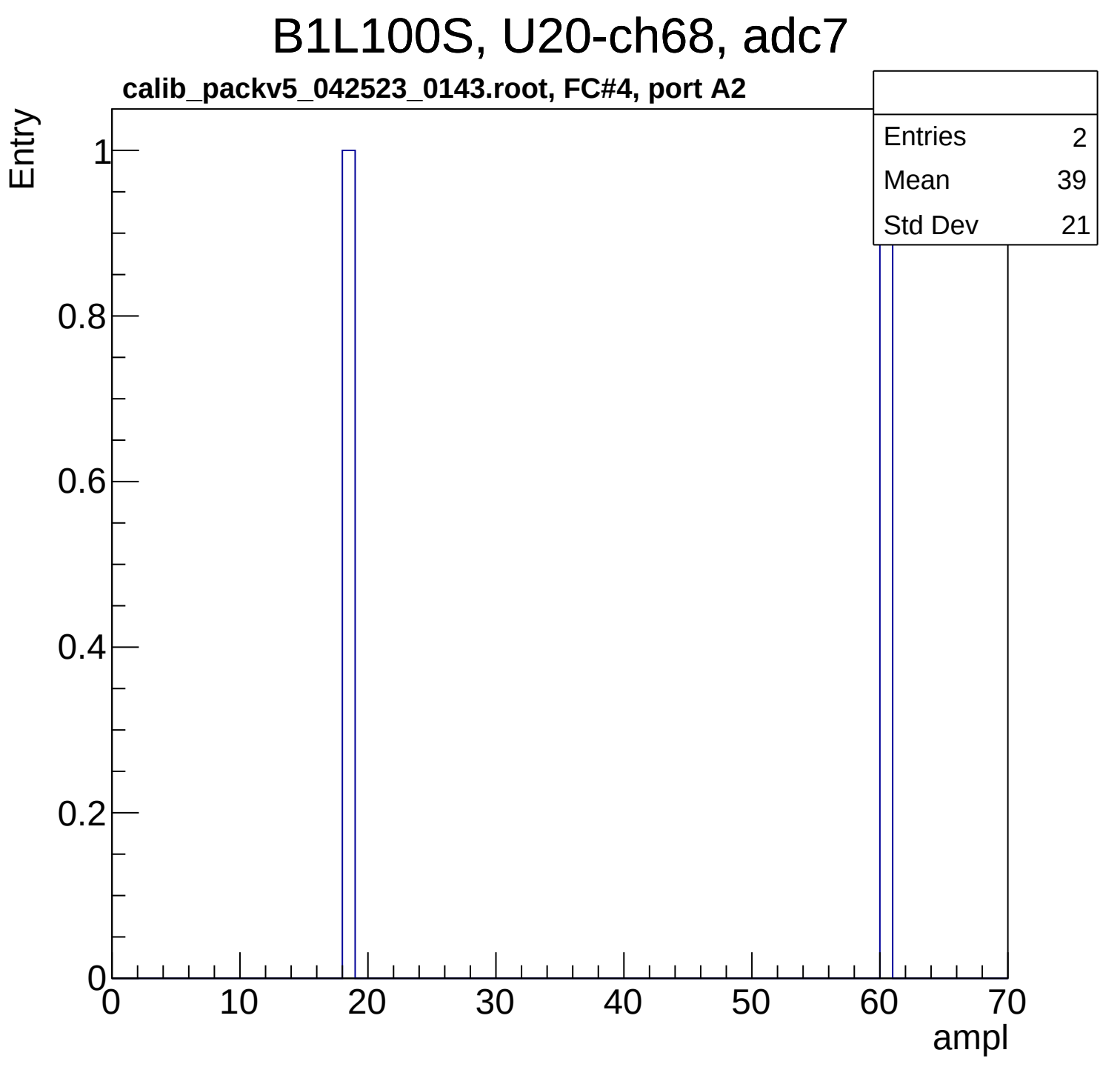
B1L100S, U20-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 39
Std Dev 21
Entry
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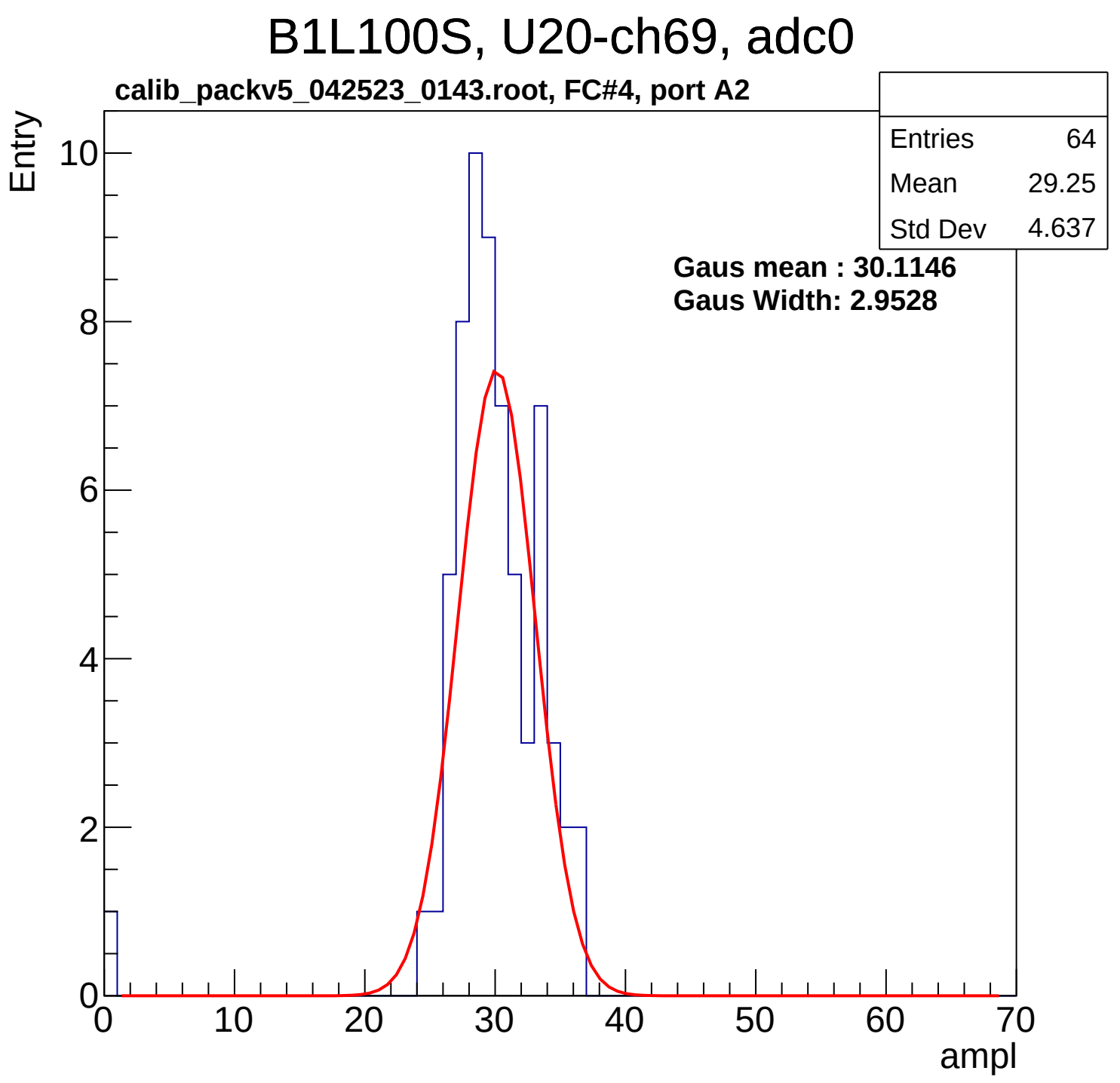
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.25
Std Dev 4.637
Gaus mean : 30.1146
Gaus Width: 2.9528
Entry
ampl
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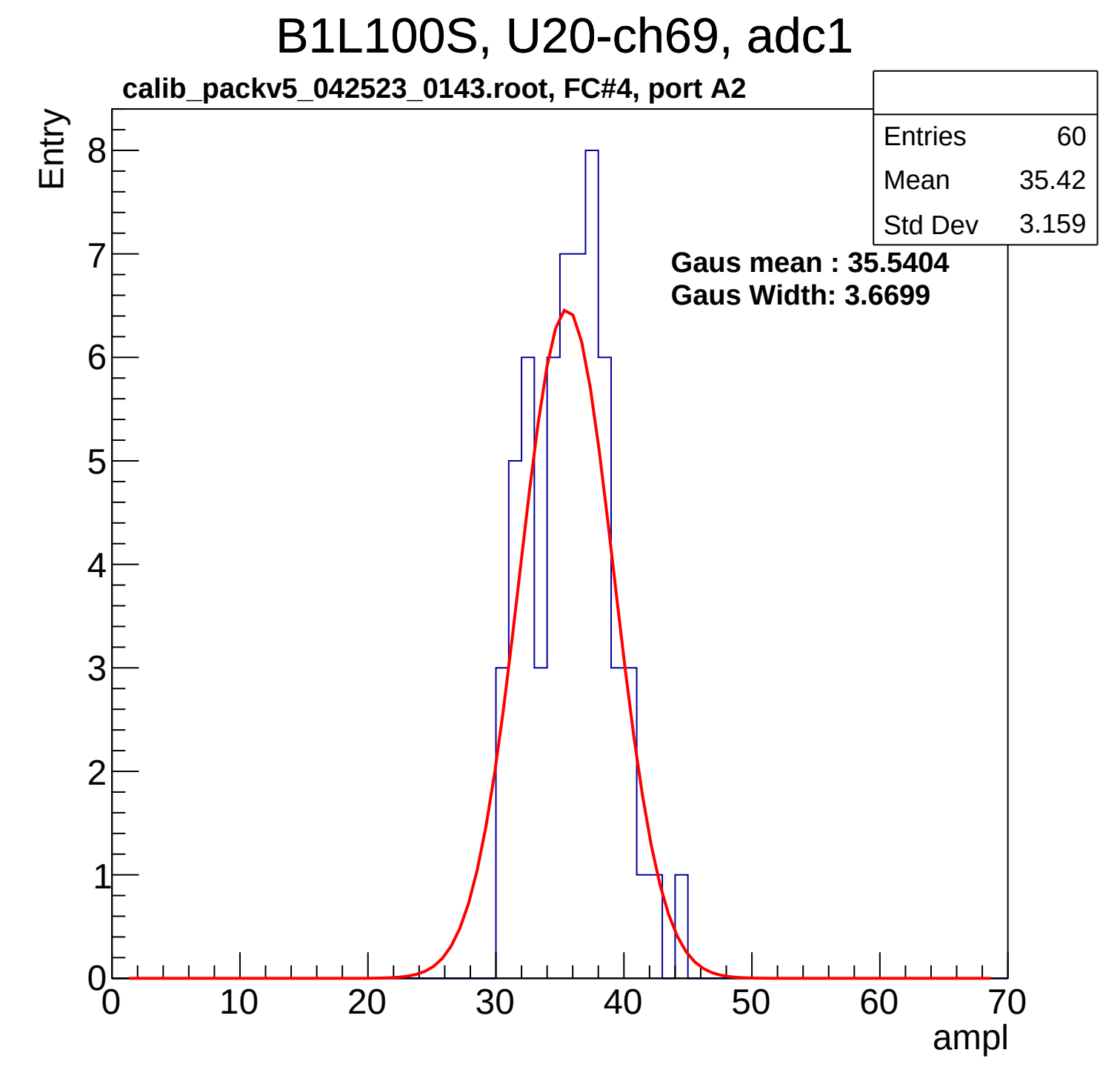
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.42
Std Dev 3.159
Gaus mean : 35.5404
Gaus Width: 3.6699
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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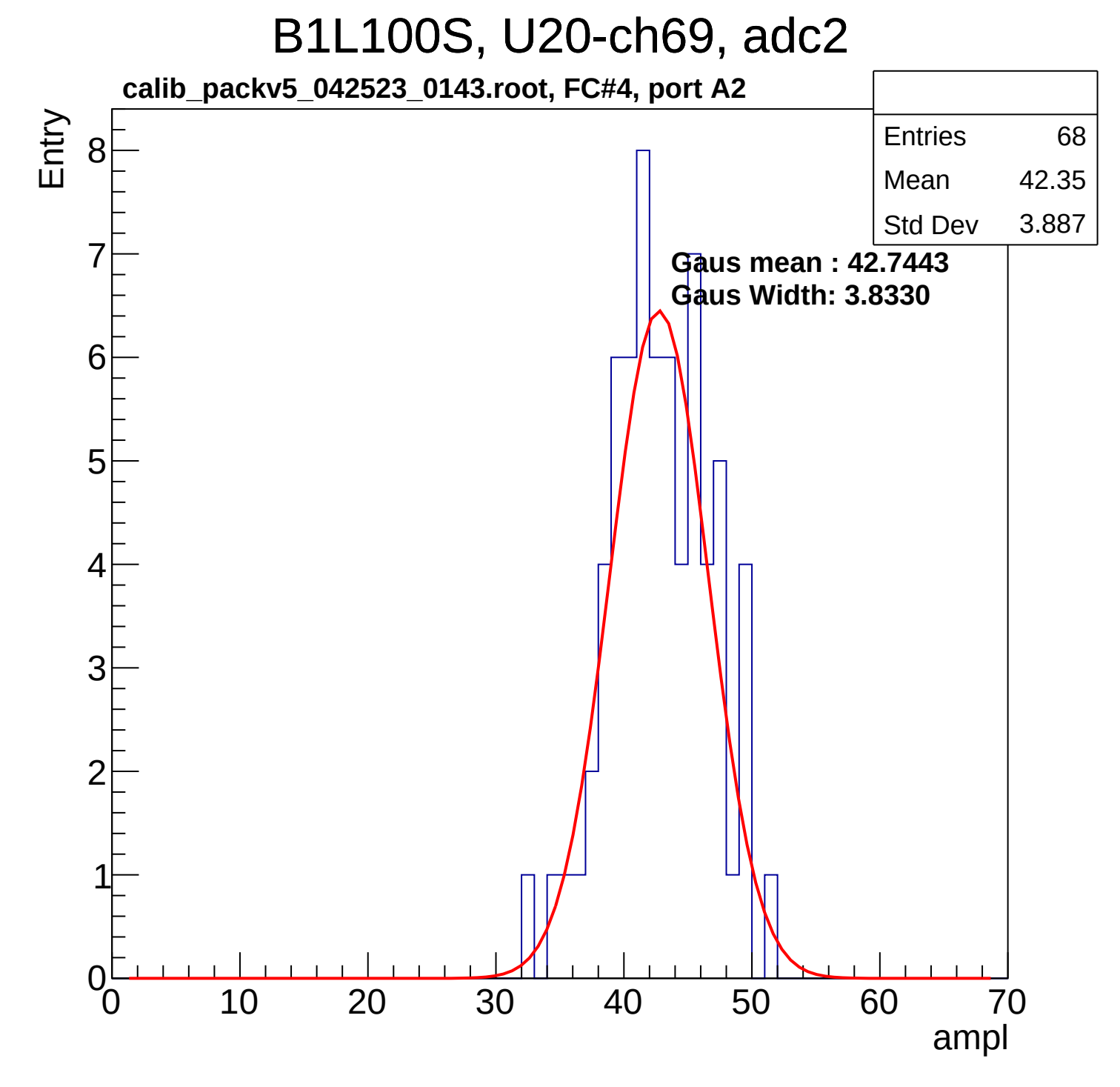
B1L100S, U20-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.35
Std Dev 3.887
Gaus mean : 42.7443
Gaus Width: 3.8330
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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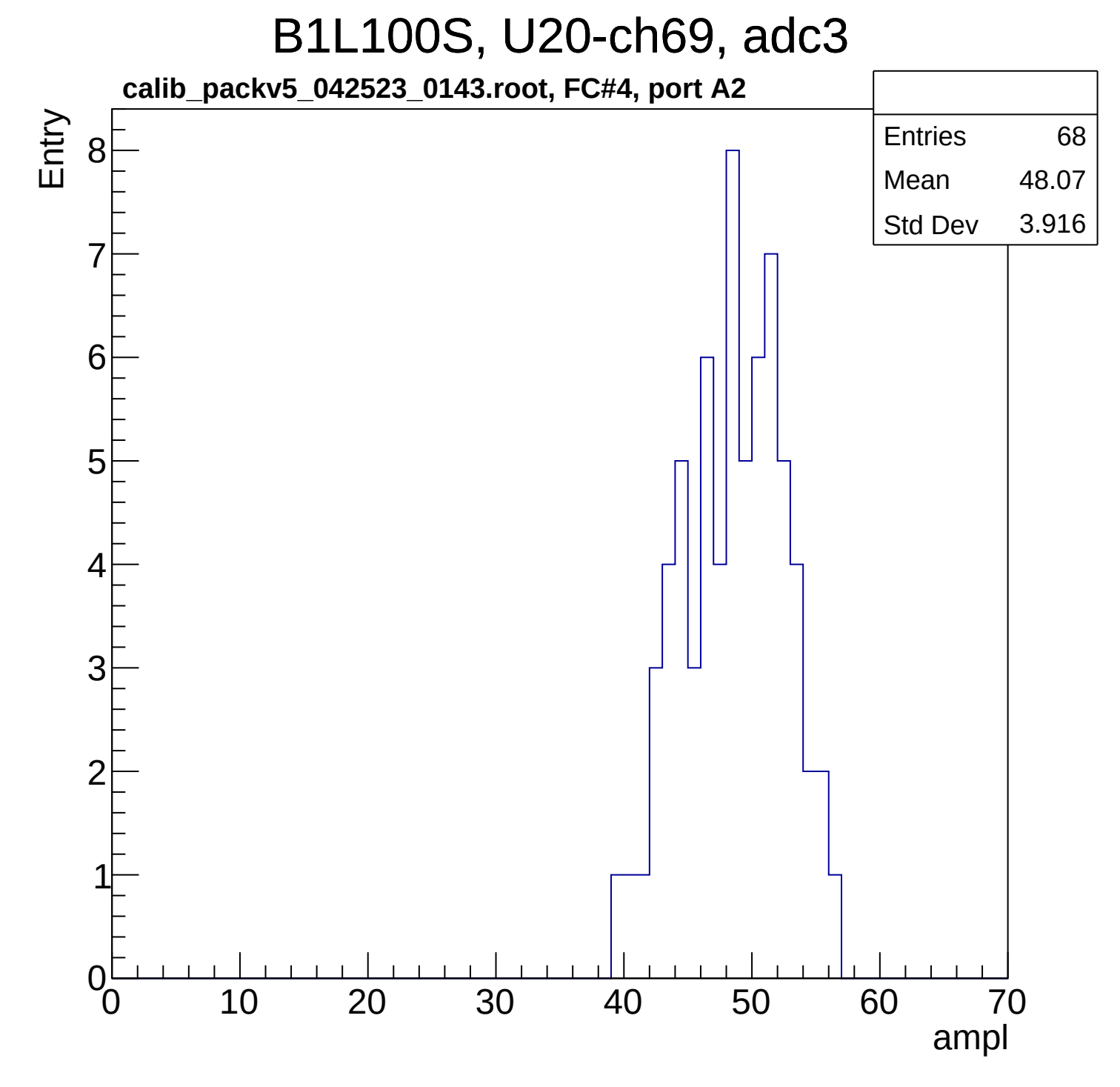
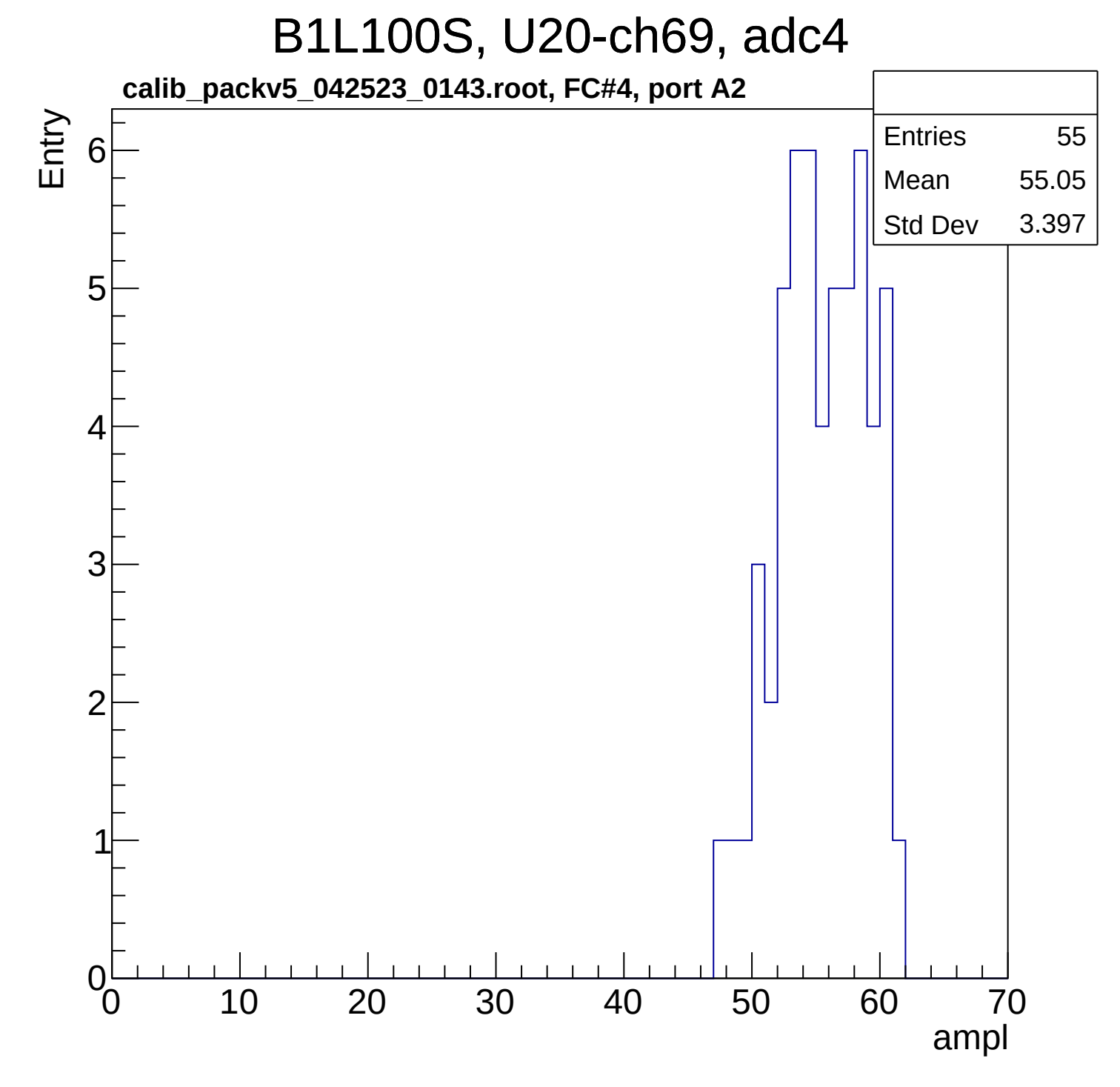
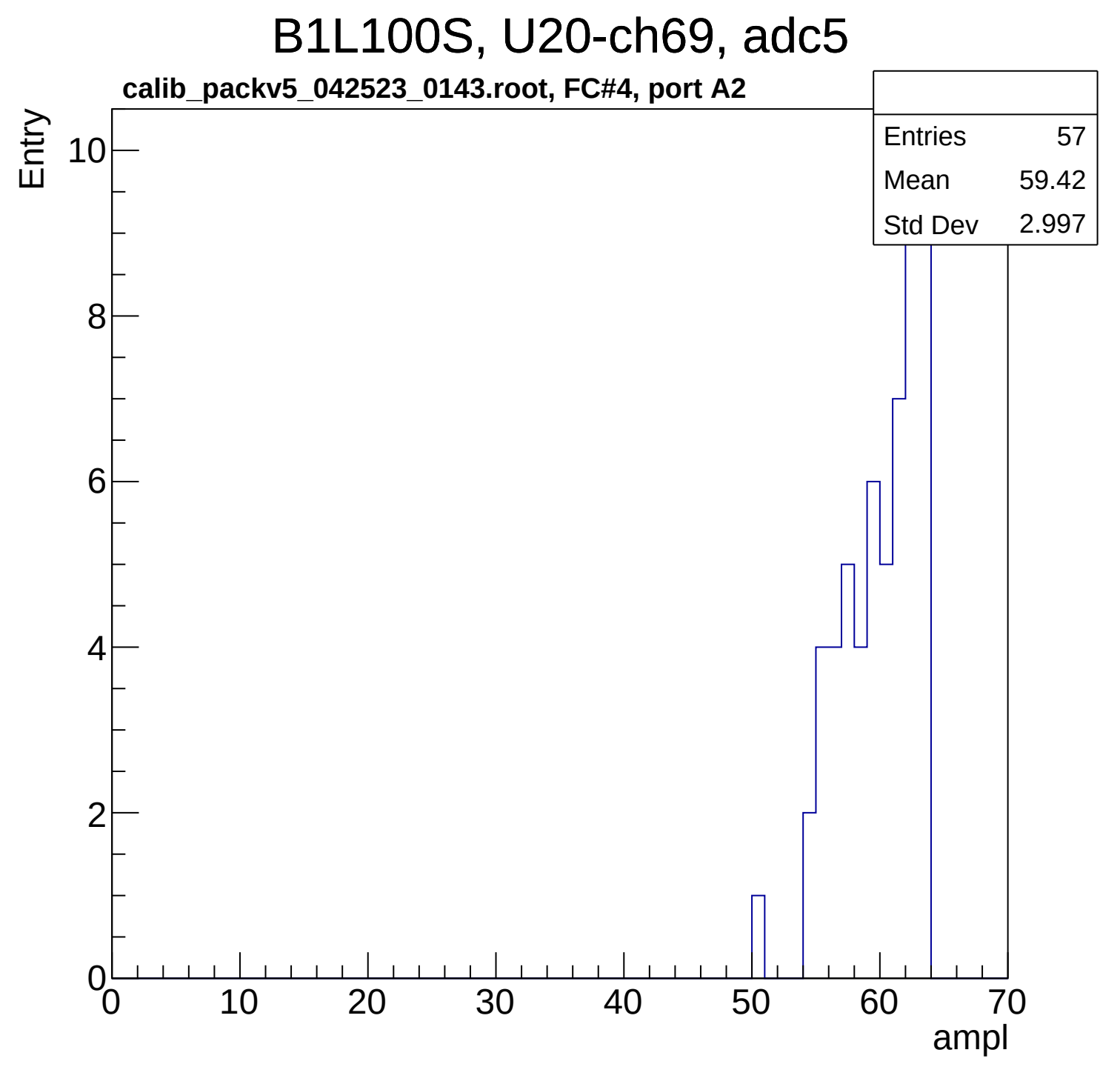

B1L100S, U20-ch69, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.07
Std Dev 3.916
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch69, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.05
Std Dev 3.397
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 59.42
Std Dev 2.997
Entry
ampl
0
2
4
6
8
10
0
10
20
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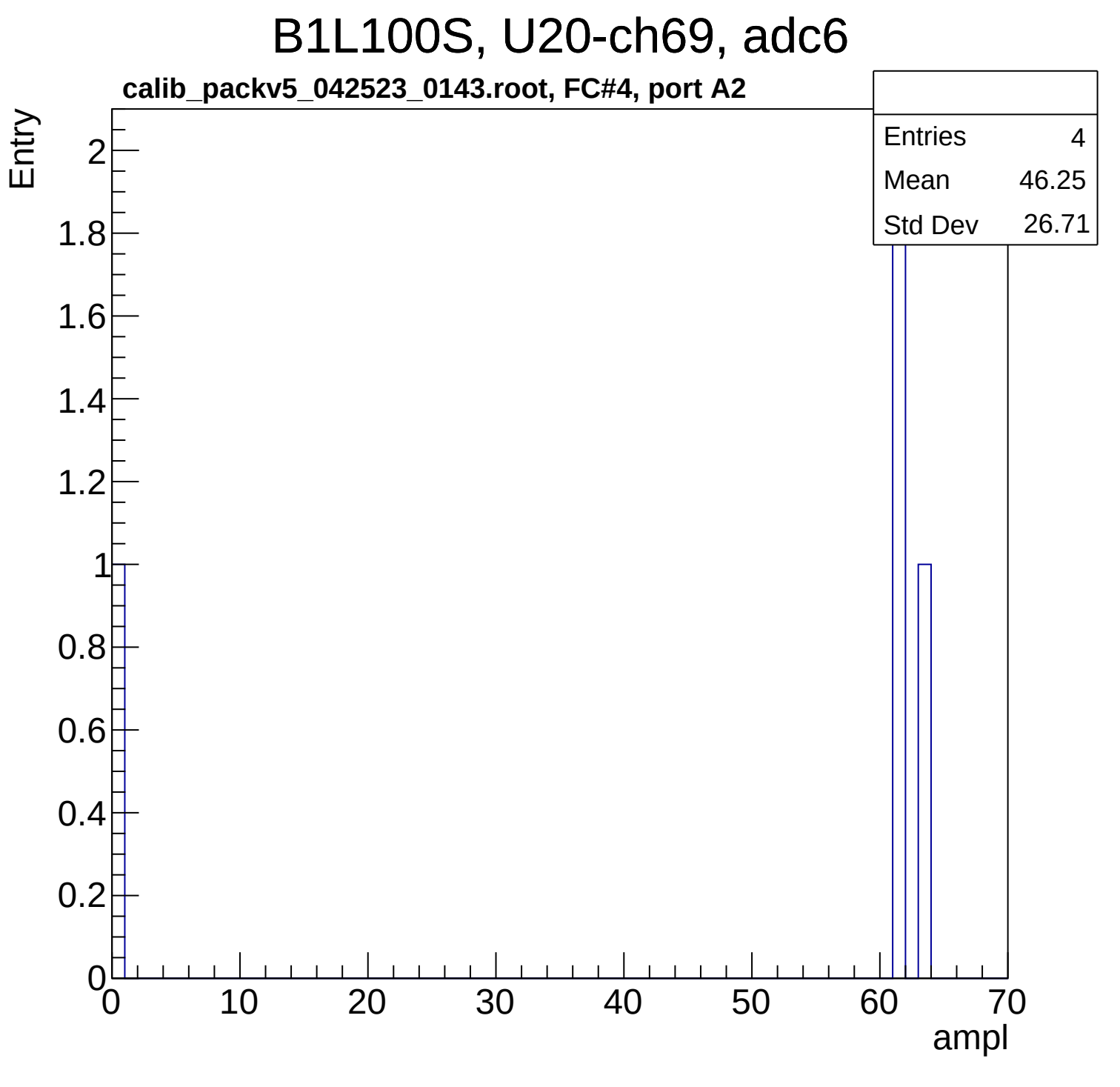
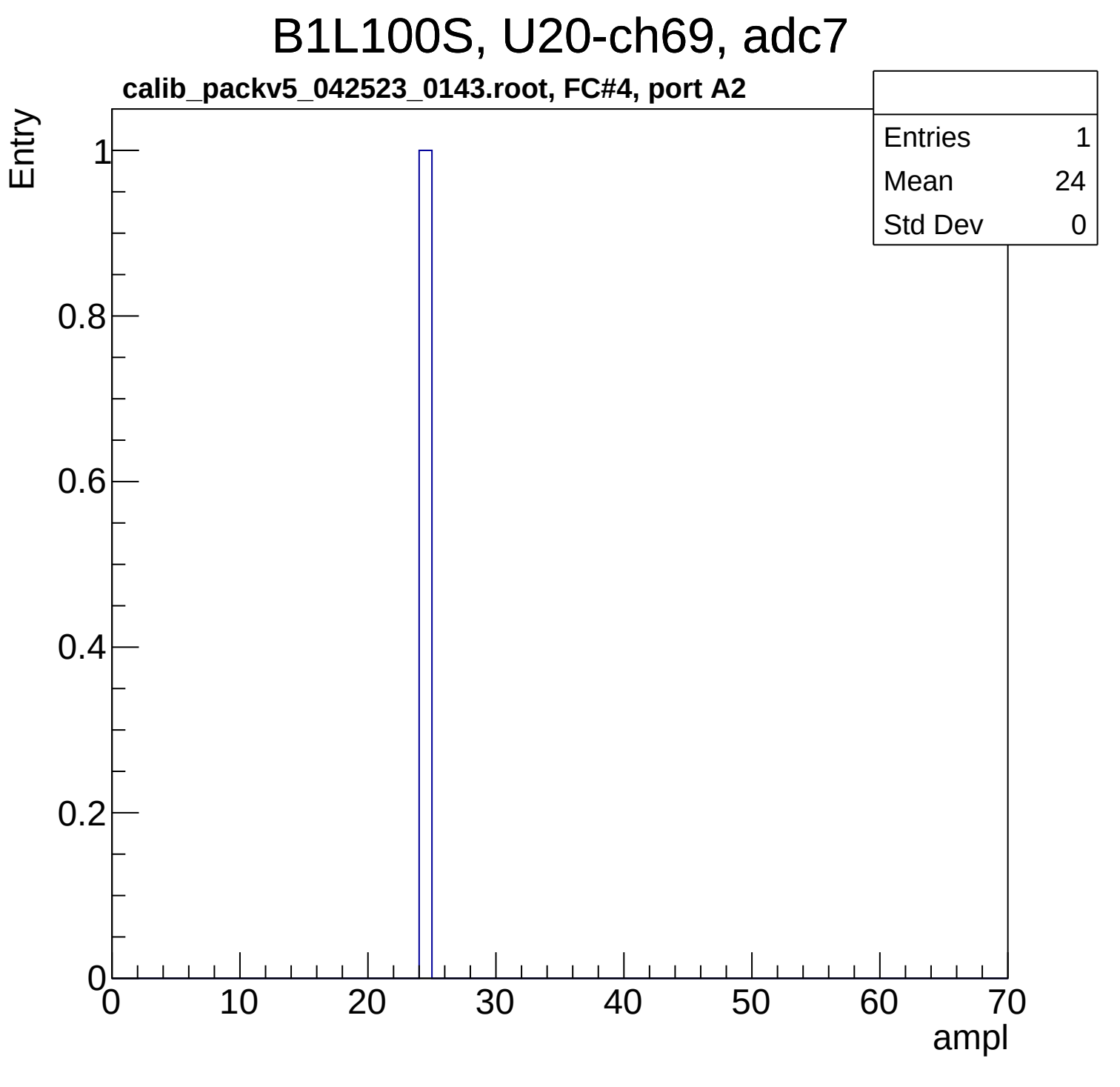
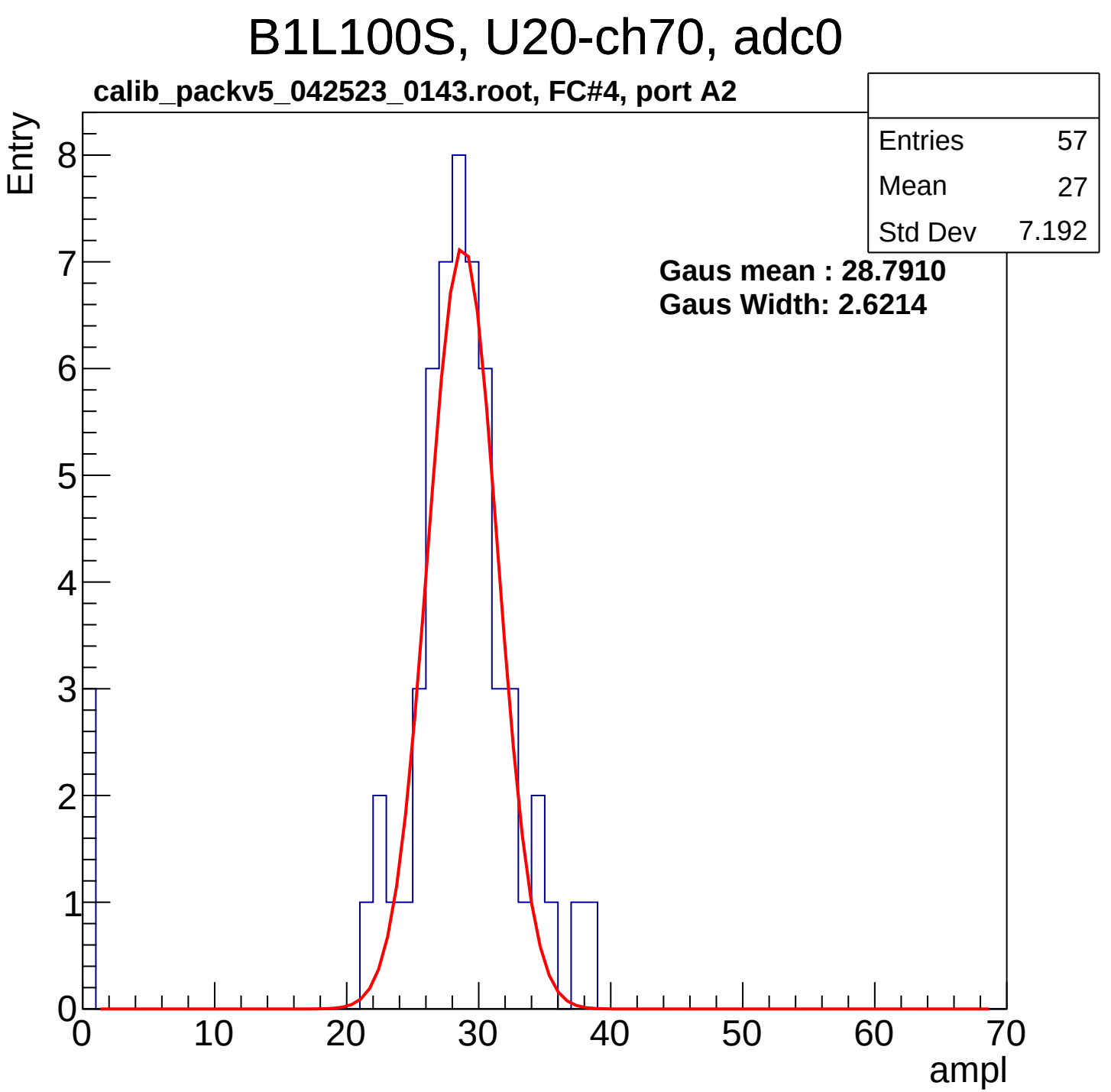
40
50
60
70


B1L100S, U20-ch69, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.25
Std Dev 26.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U20-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 27
Std Dev 7.192
Gaus mean : 28.7910
Gaus Width: 2.6214
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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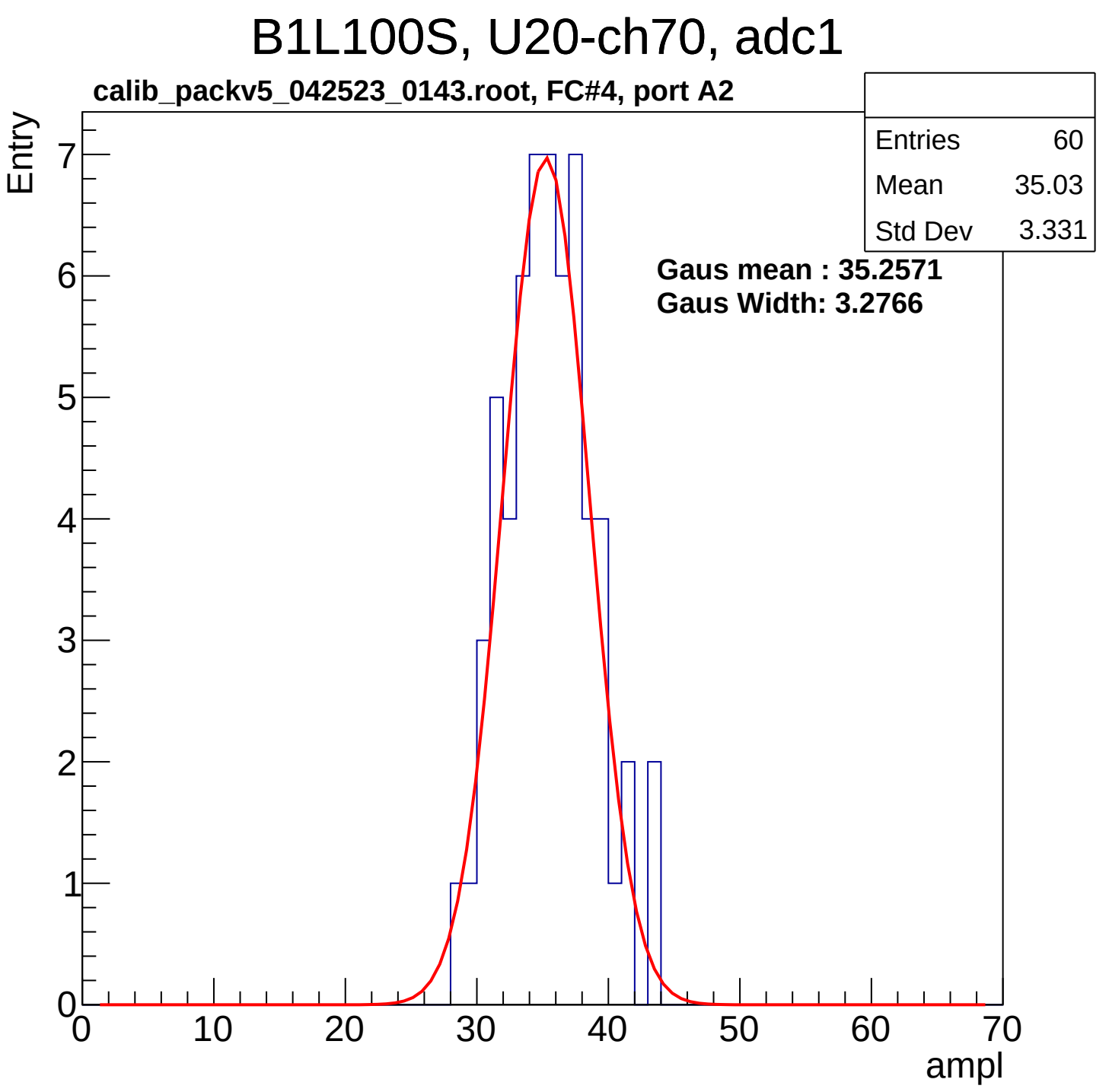
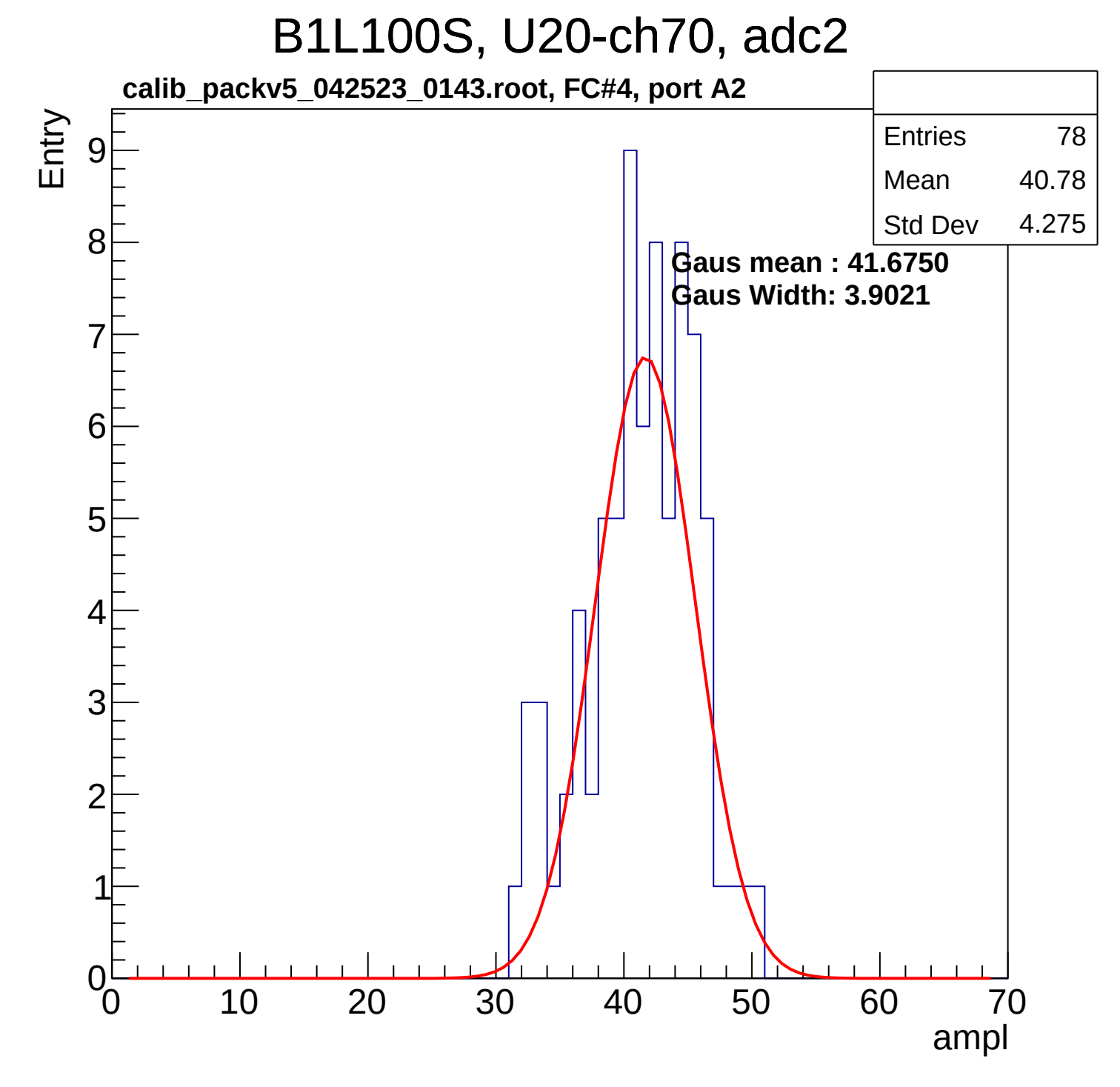

B1L100S, U20-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.03
Std Dev 3.331
Gaus mean : 35.2571
Gaus Width: 3.2766
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 40.78
Std Dev 4.275
Gaus mean : 41.6750
Gaus Width: 3.9021
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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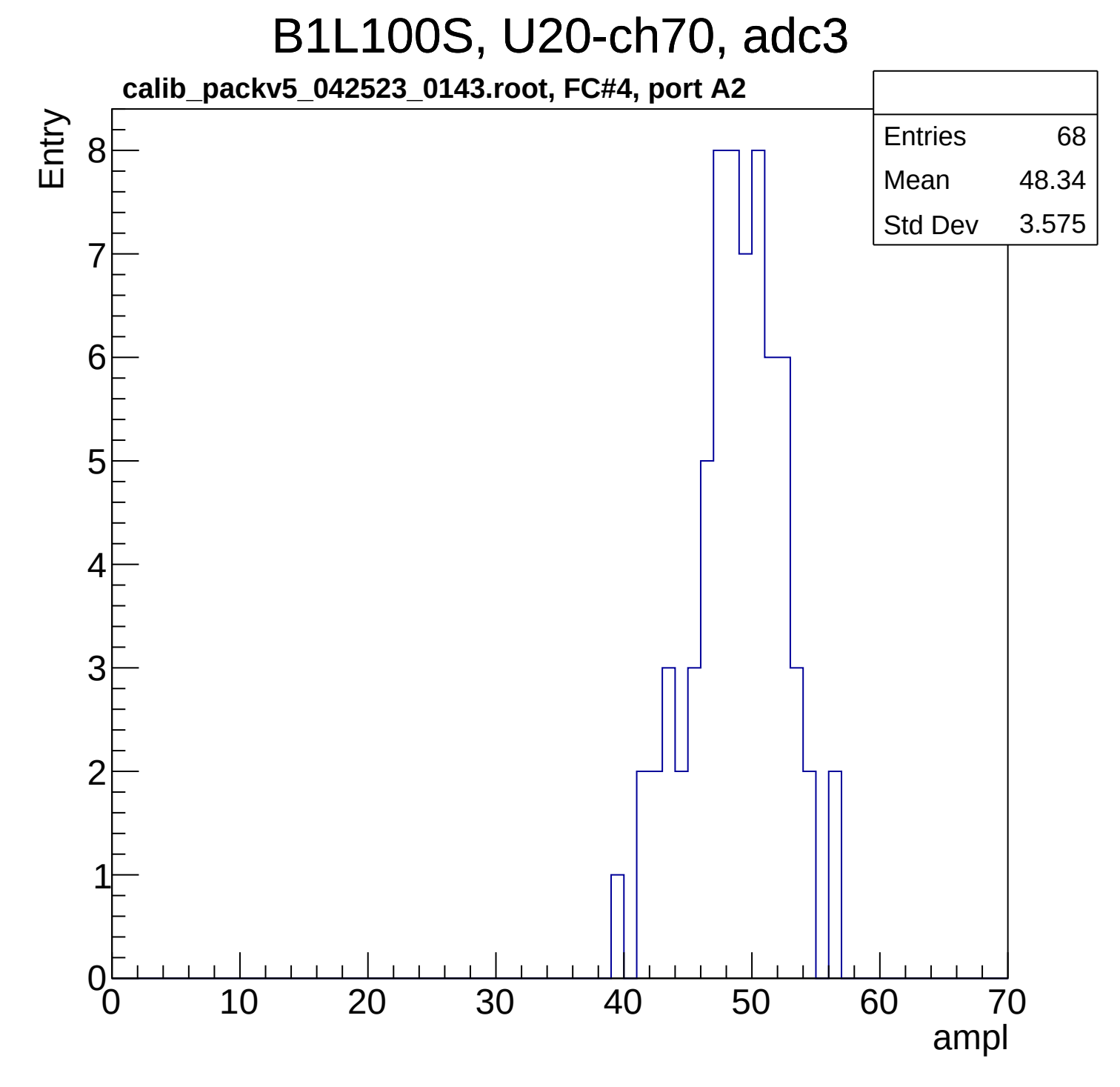
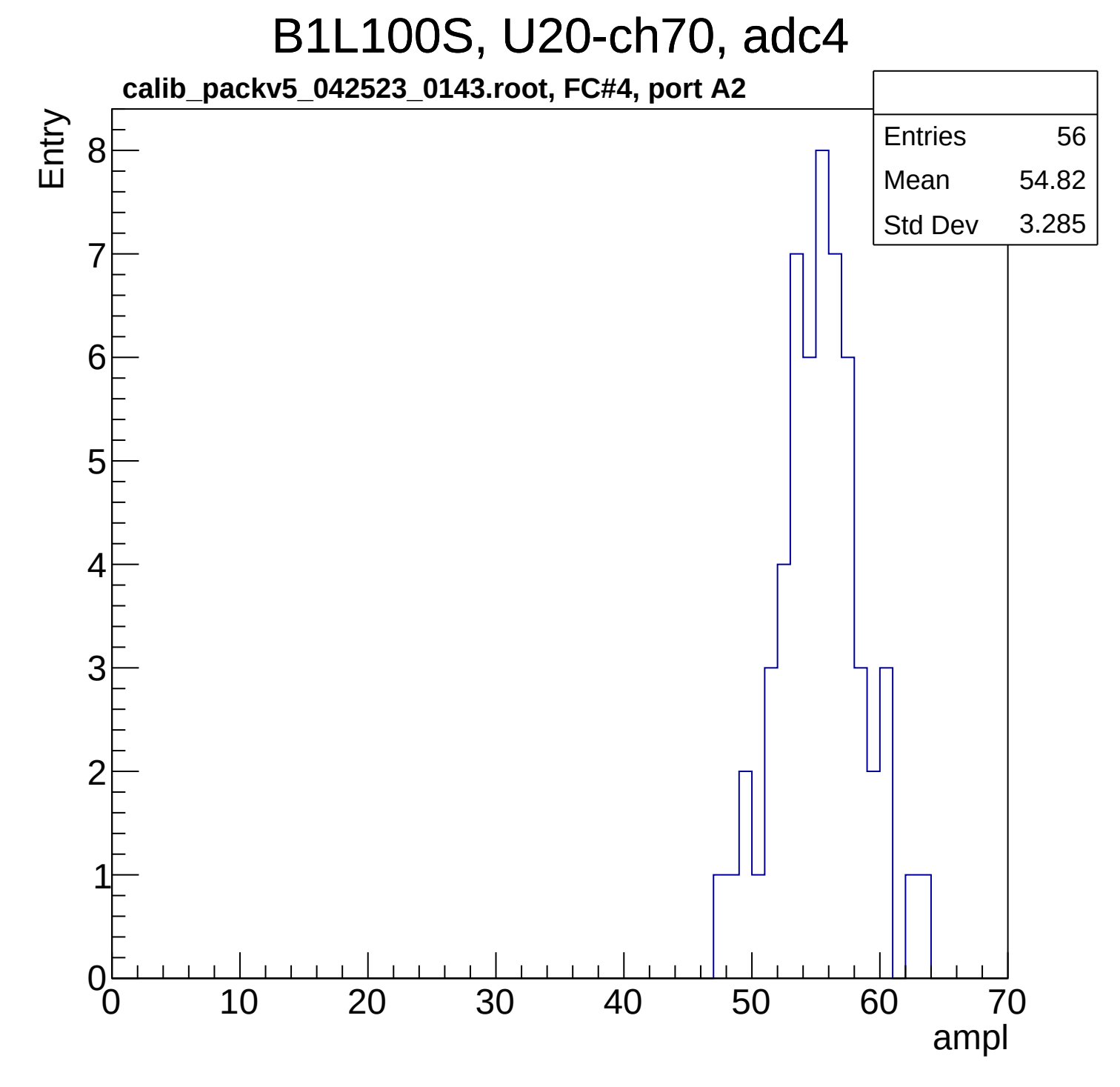
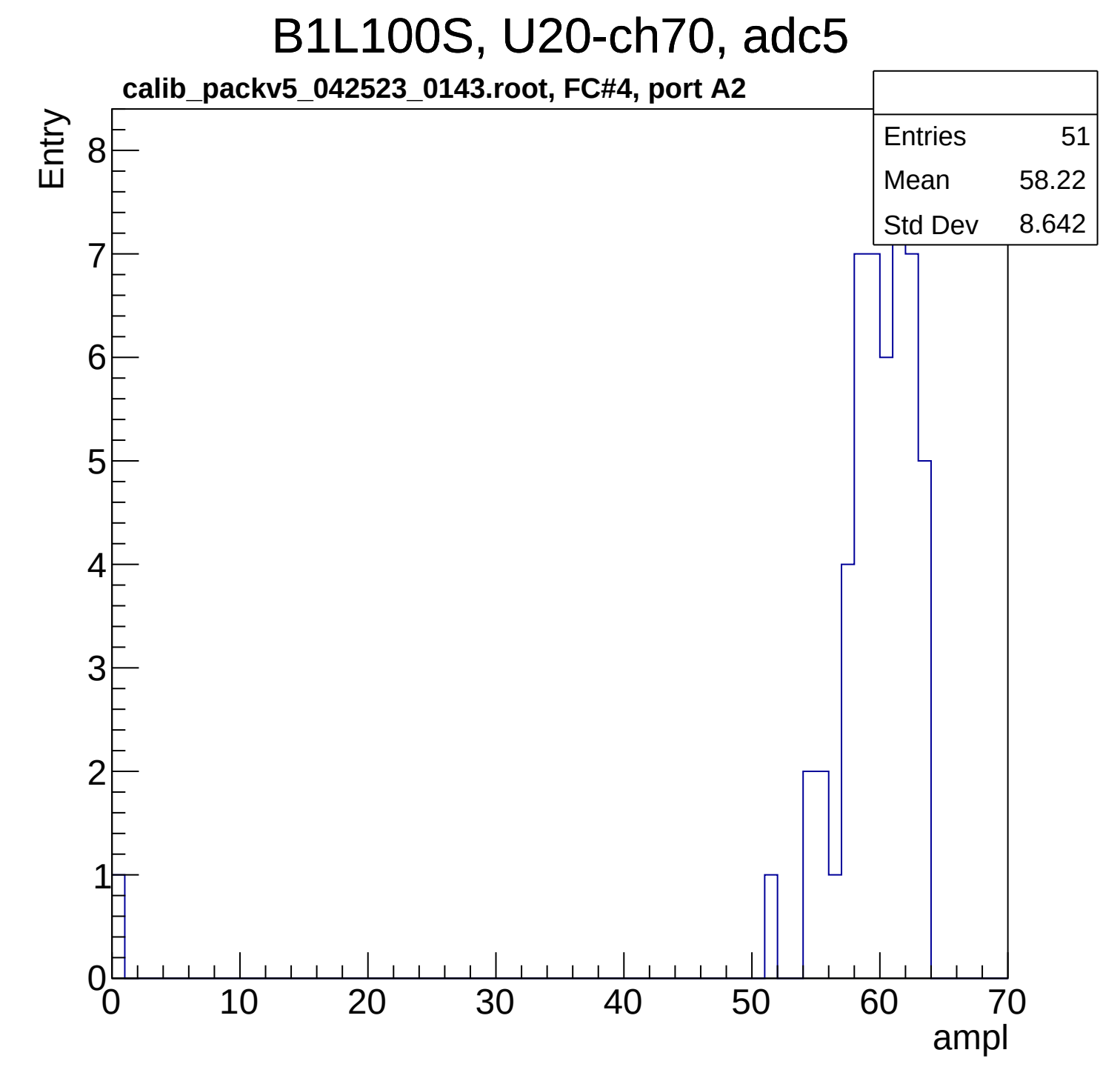

B1L100S, U20-ch70, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.34
Std Dev 3.575
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch70, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 54.82
Std Dev 3.285
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.22
Std Dev 8.642
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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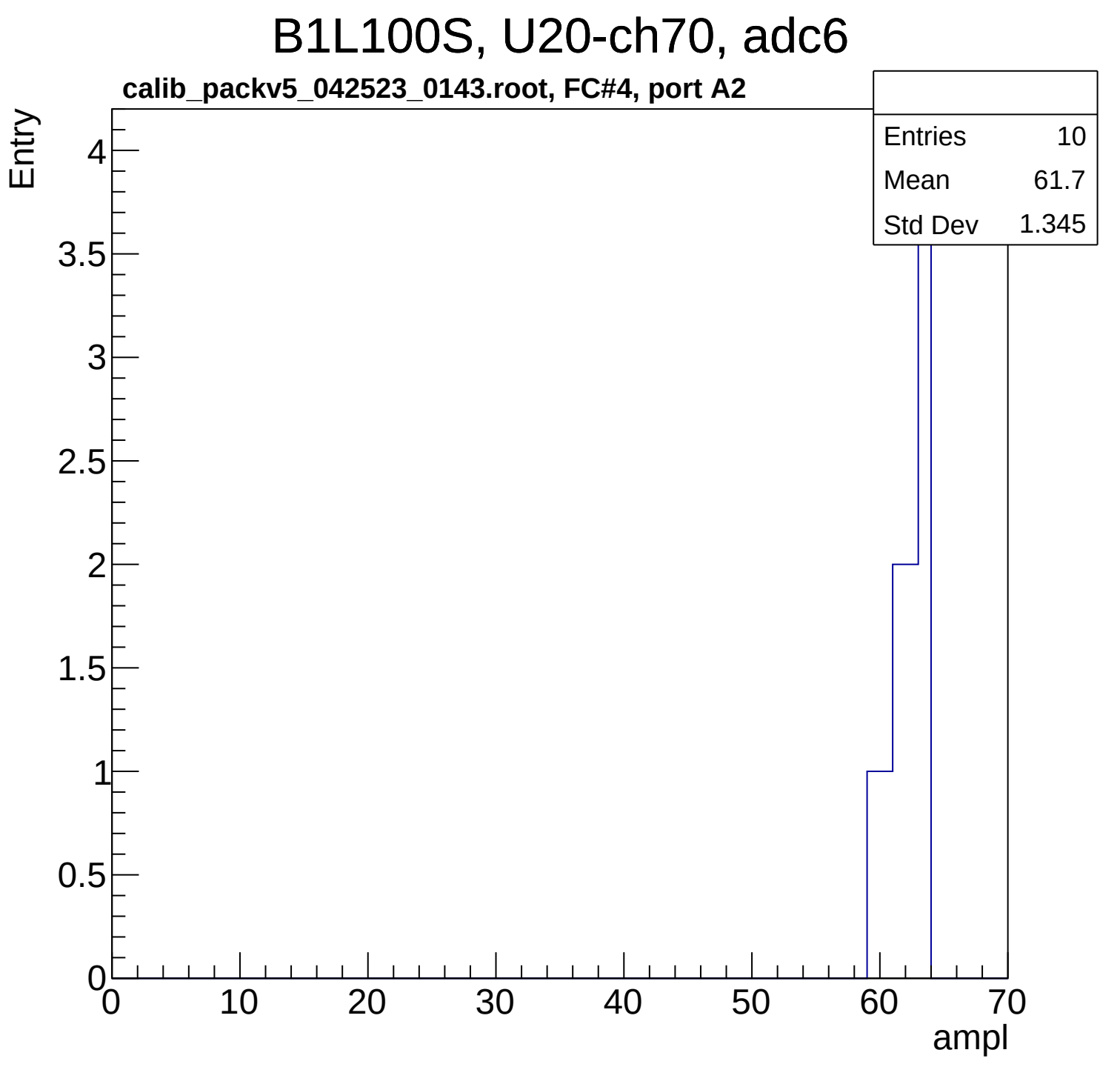
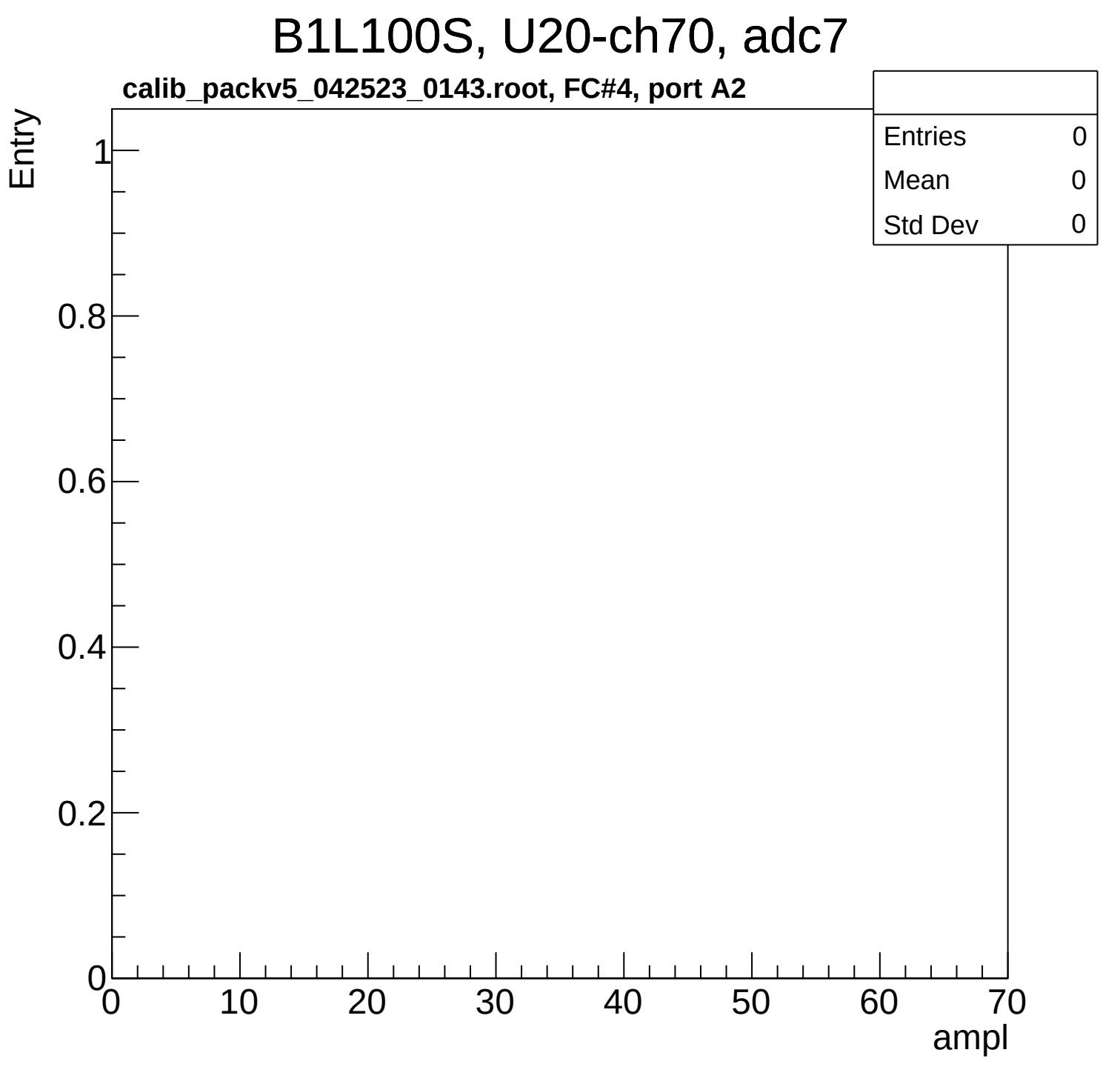

B1L100S, U20-ch70, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.7
Std Dev 1.345
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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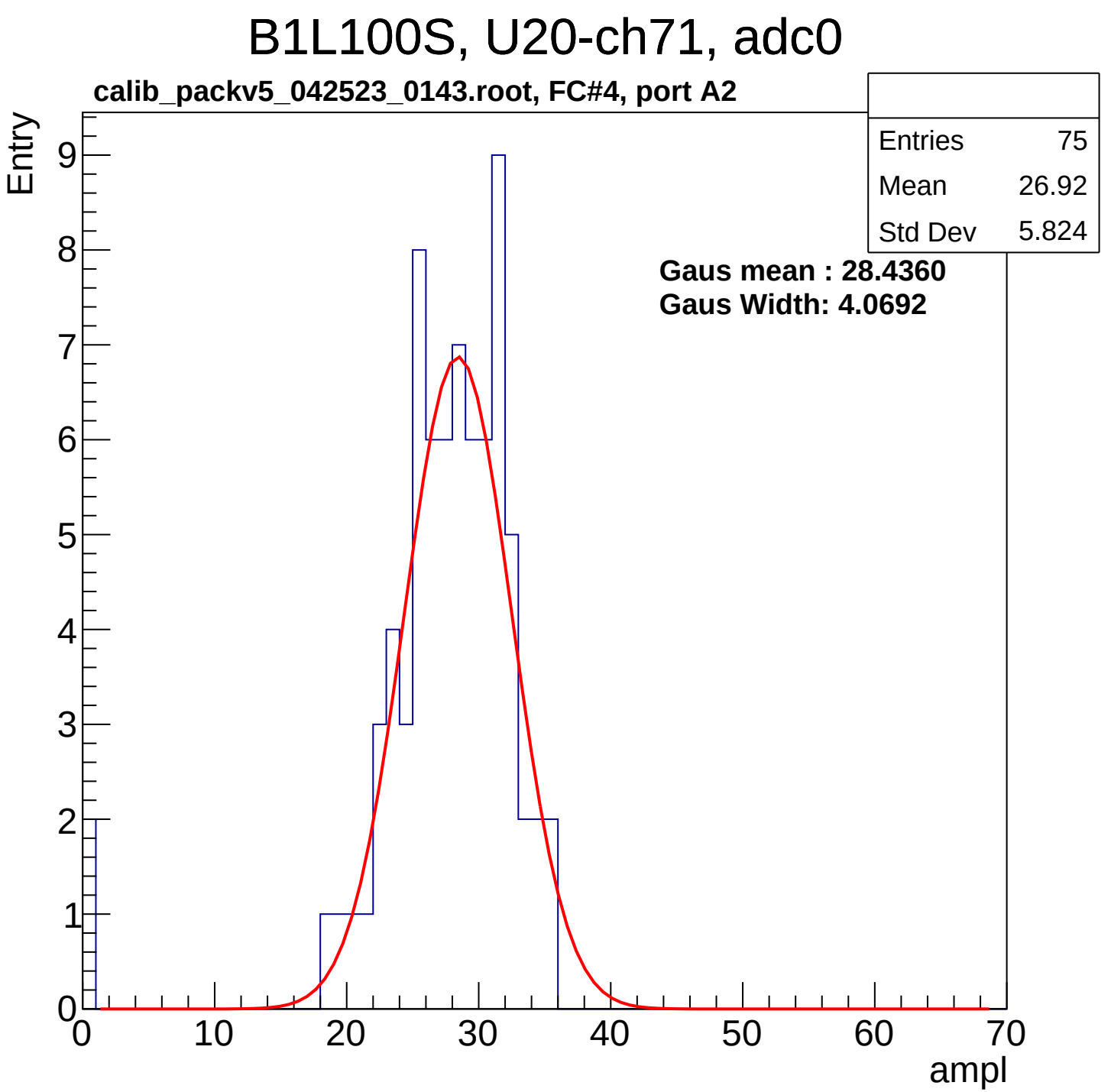
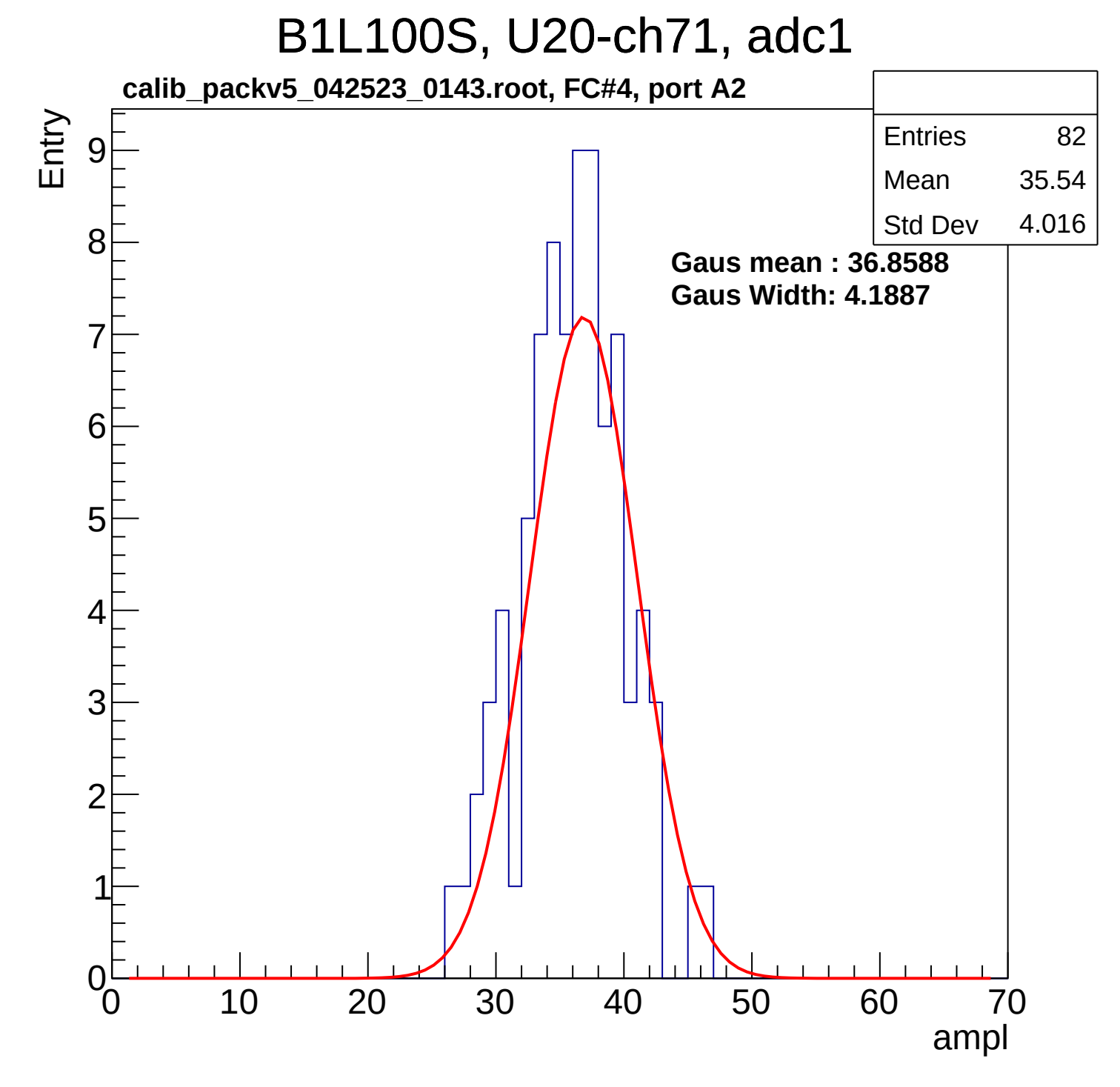
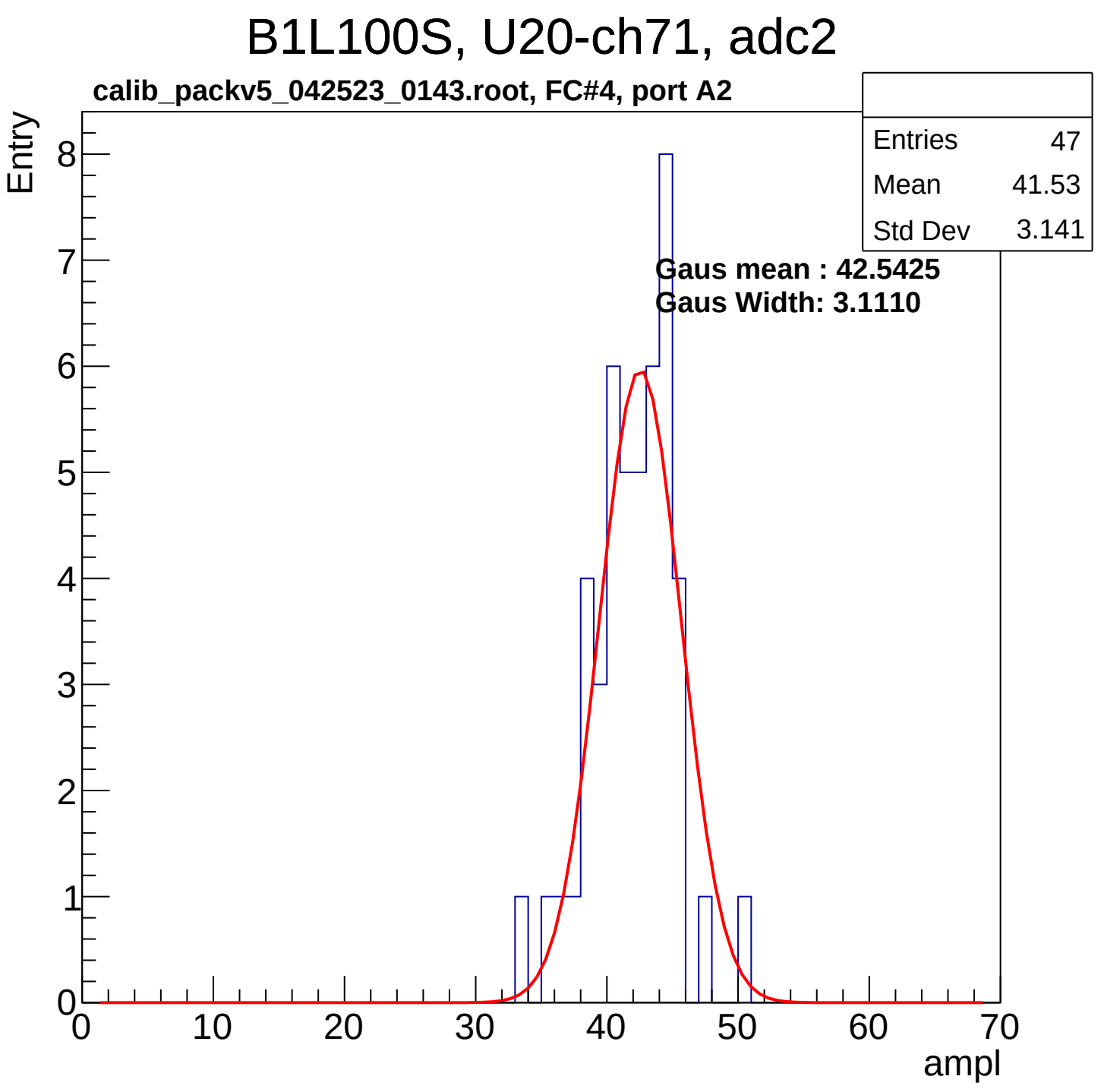
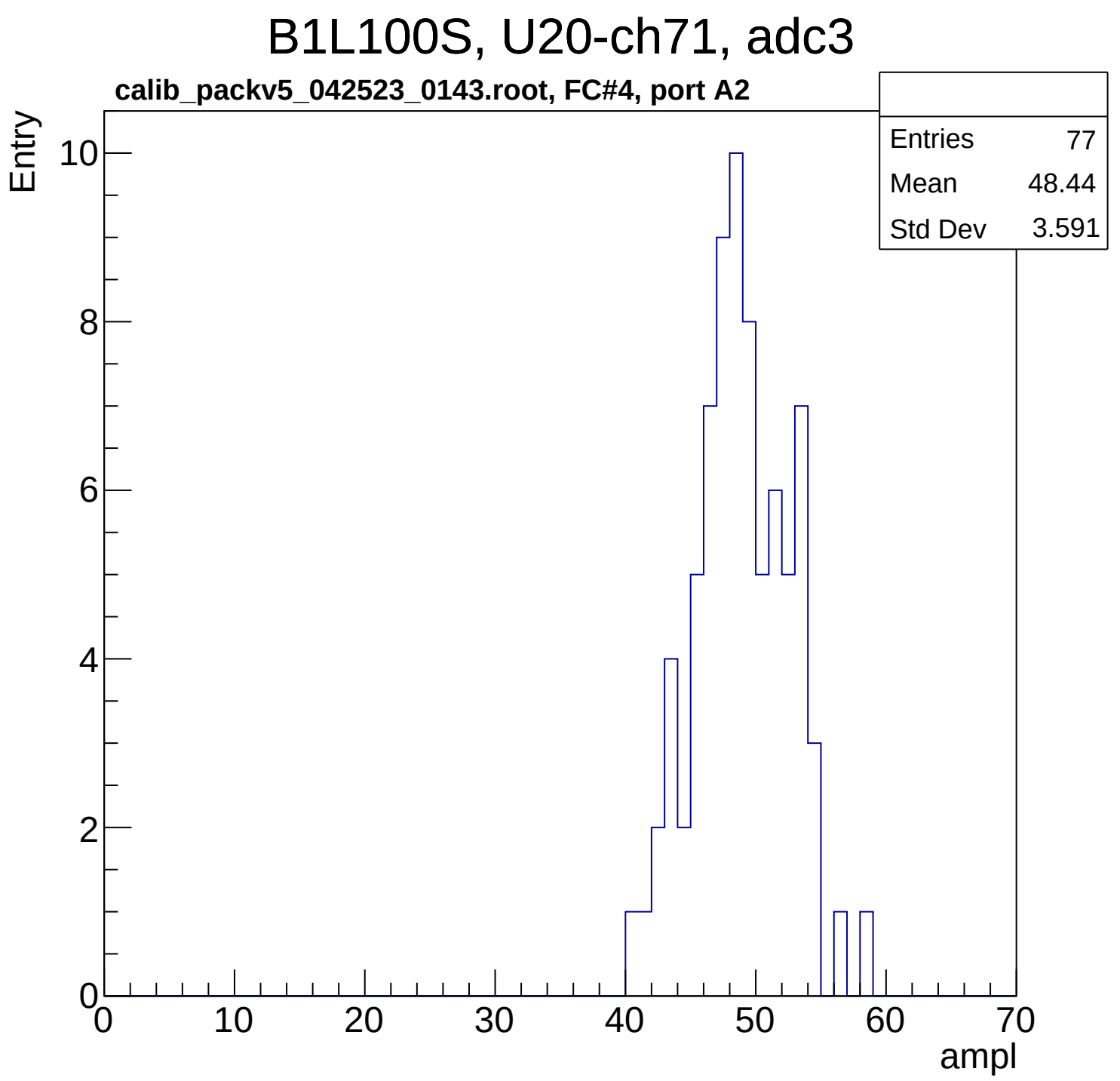
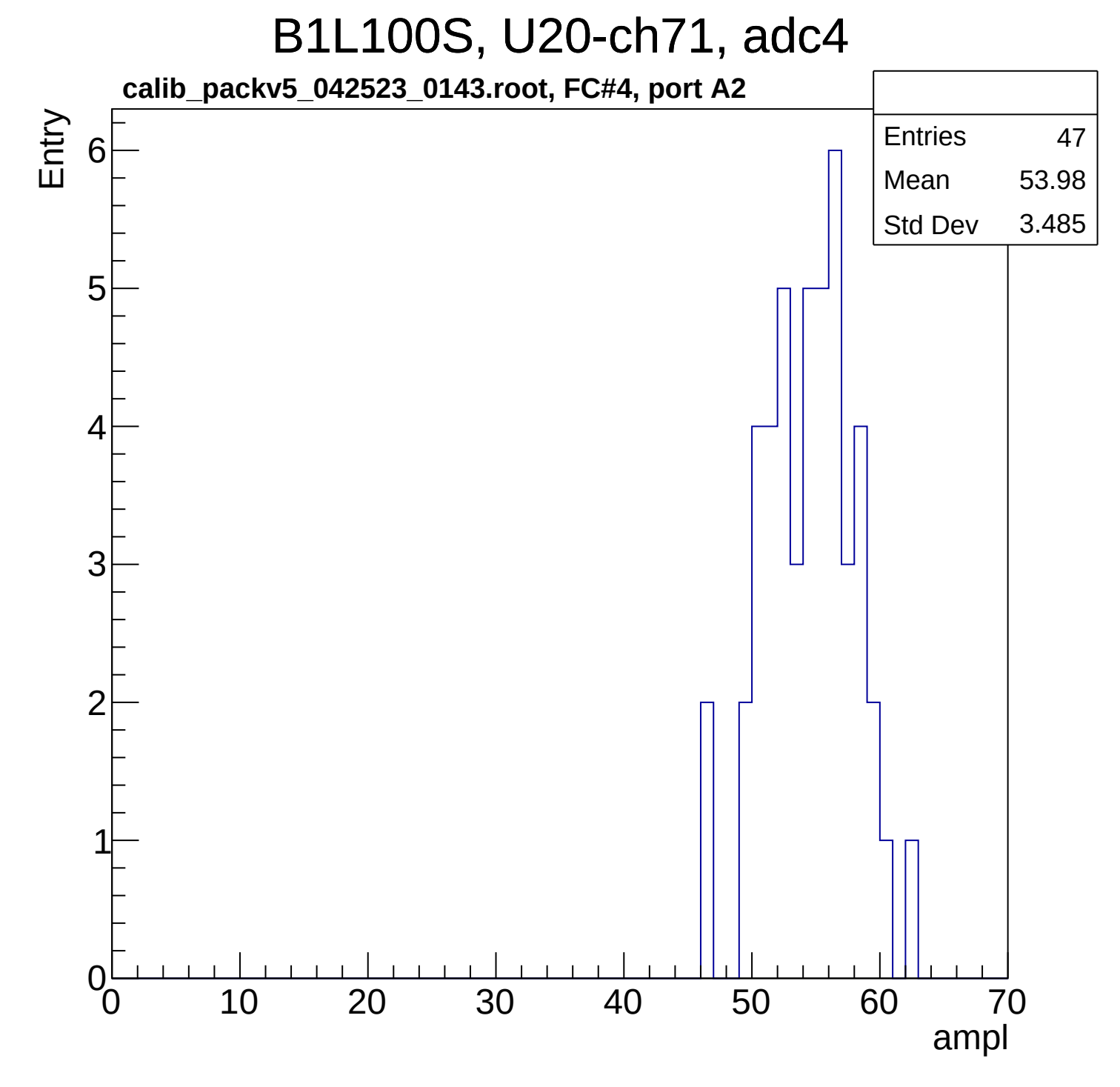
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

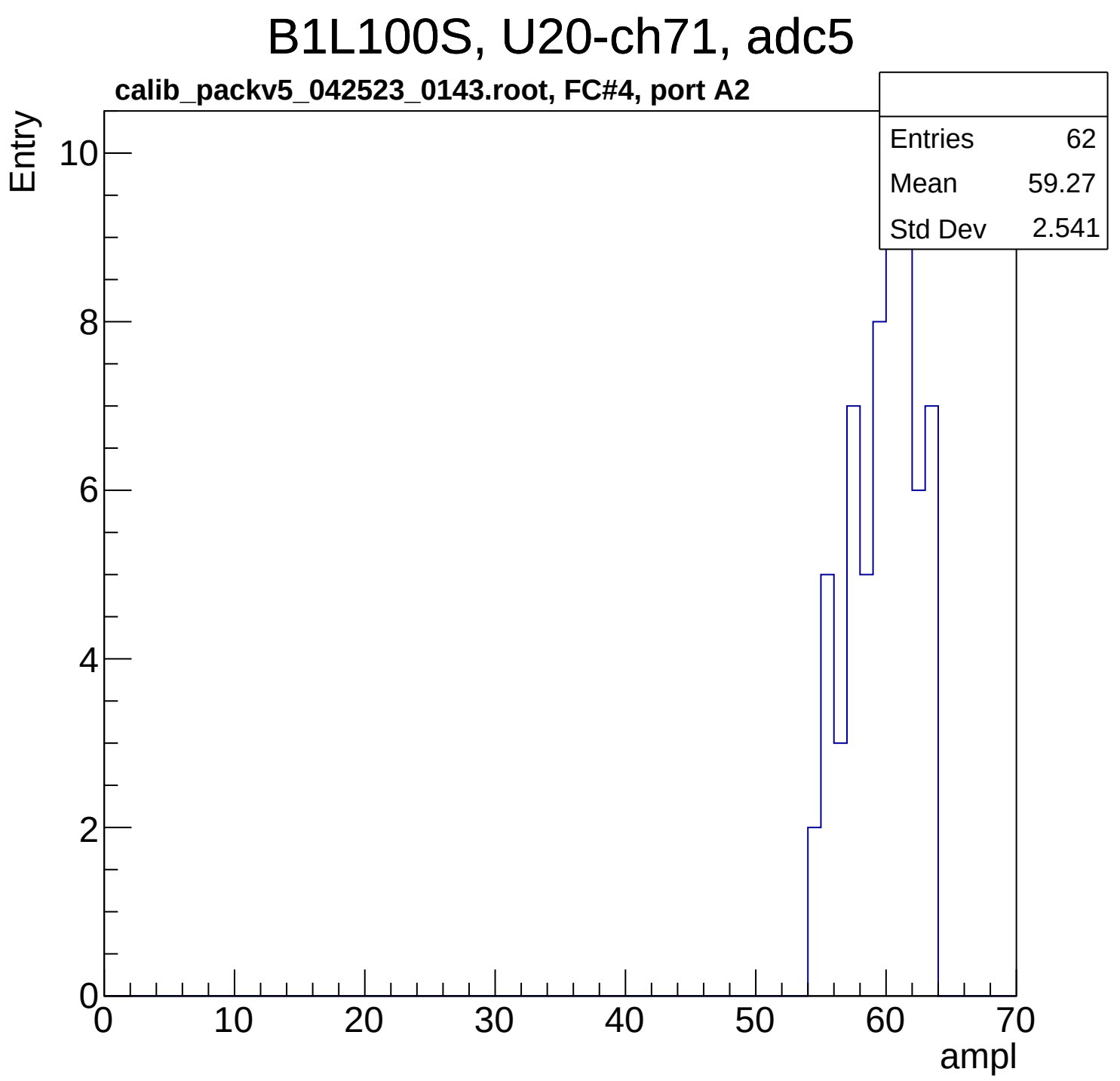

B1L100S, U20-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 26.92
Std Dev 5.824
Gaus mean : 28.4360
Gaus Width: 4.0692
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

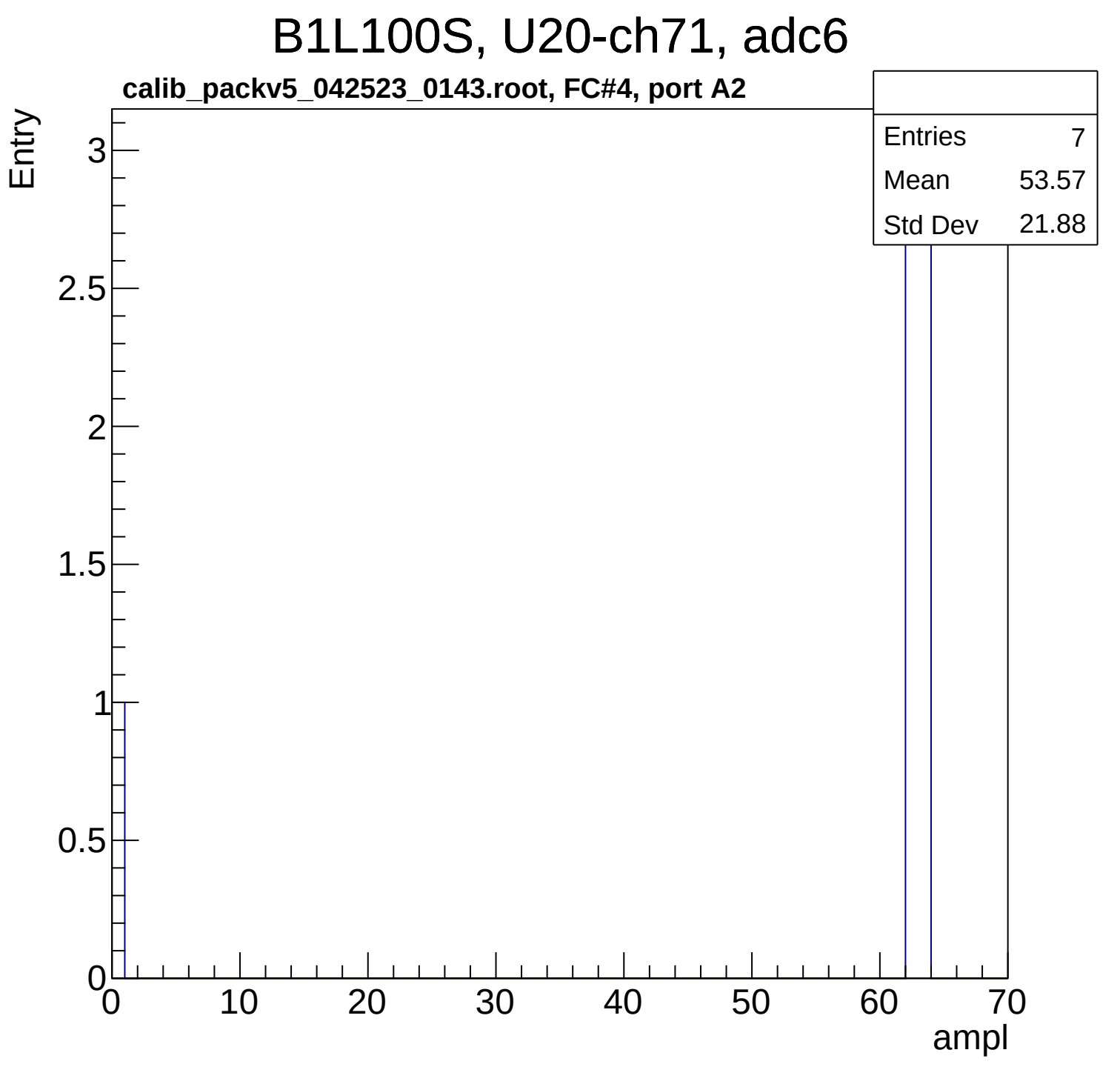

B1L100S, U20-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 35.54
Std Dev 4.016
Gaus mean : 36.8588
Gaus Width: 4.1887
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 41.53
Std Dev 3.141
Gaus mean : 42.5425
Gaus Width: 3.1110
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 48.44
Std Dev 3.591
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch71, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 53.98
Std Dev 3.485
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U20-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 59.27
Std Dev 2.541
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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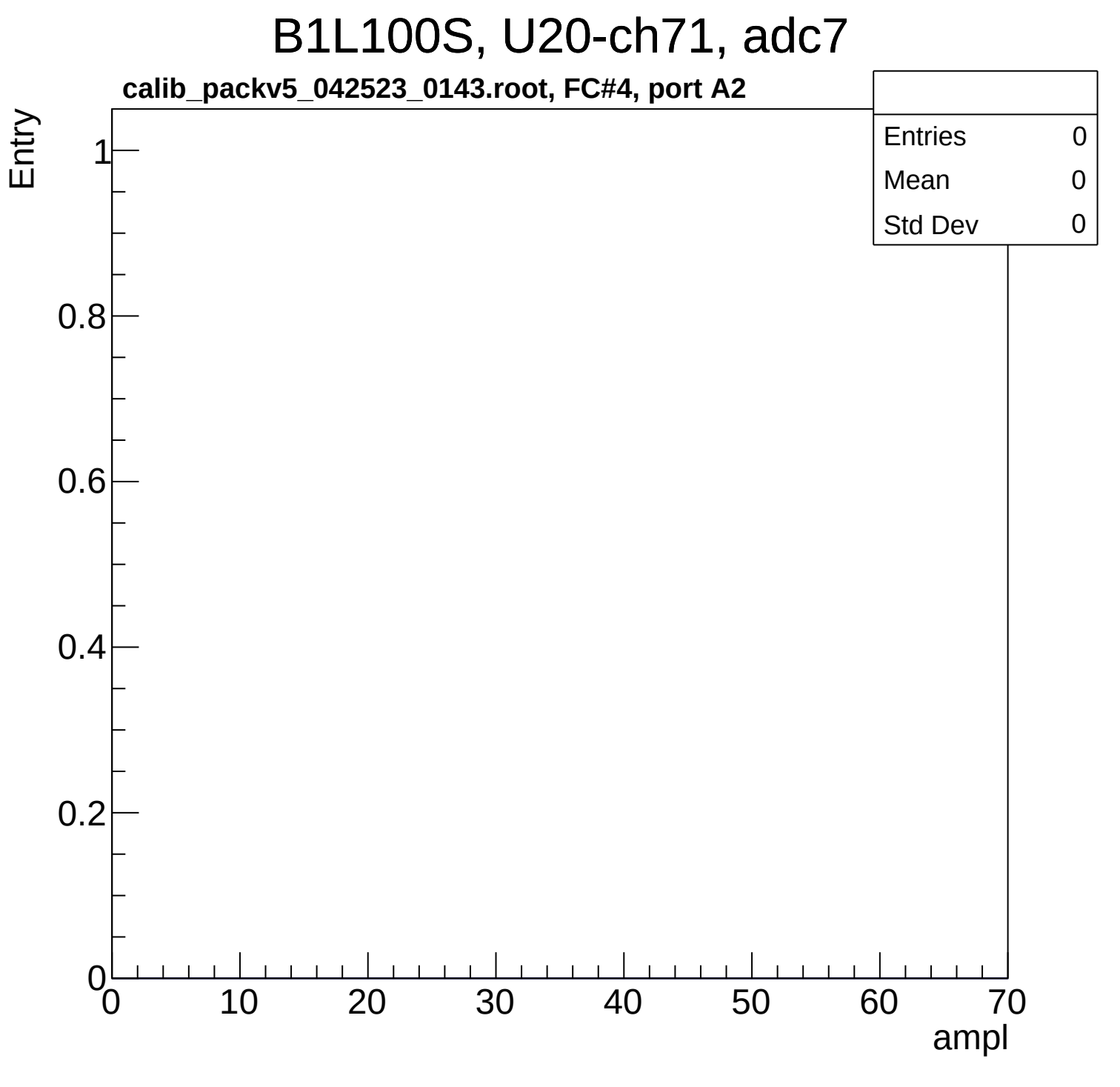
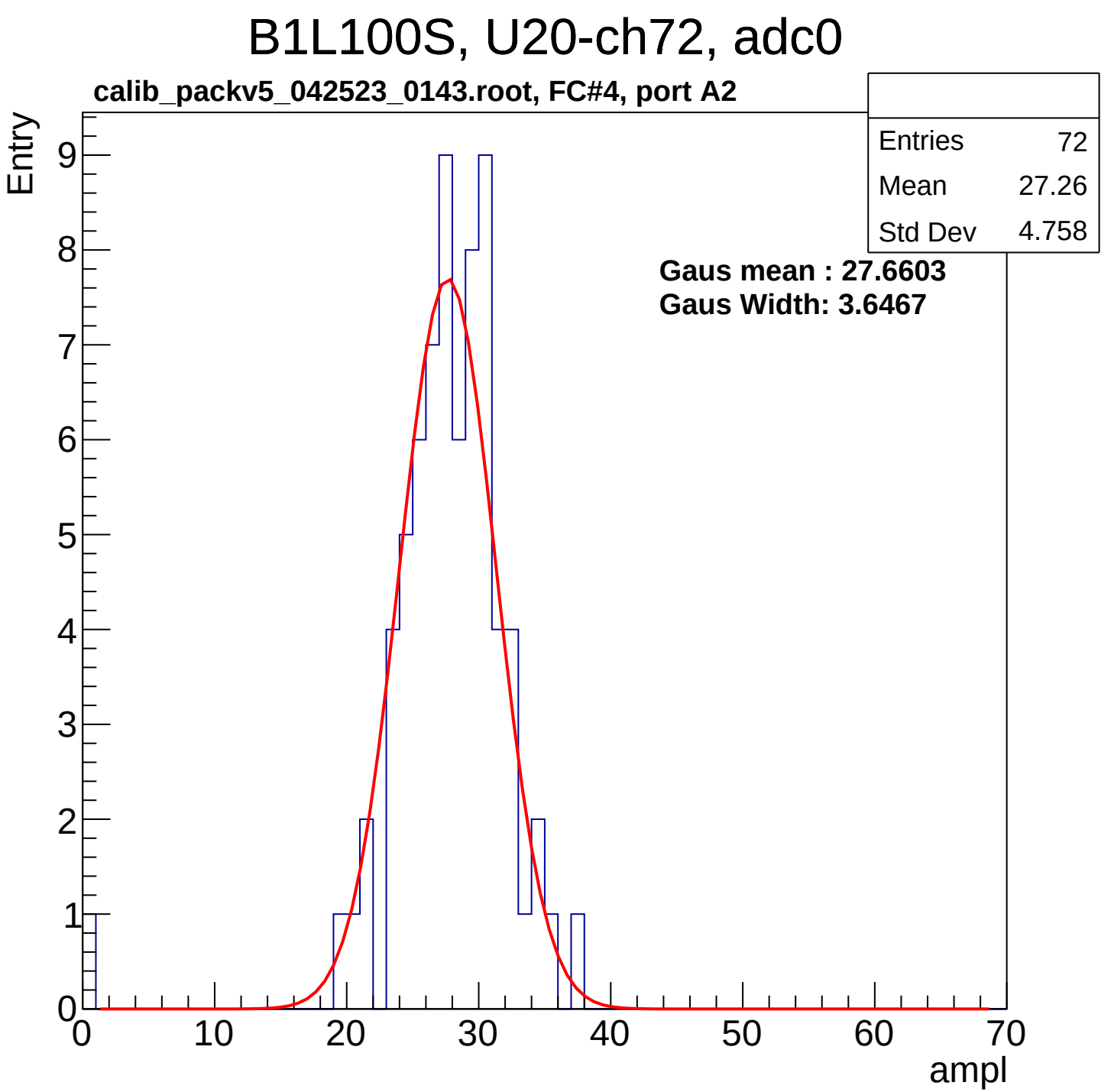
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch71, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 27.26
Std Dev 4.758
Gaus mean : 27.6603
Gaus Width: 3.6467
Entry
ampl
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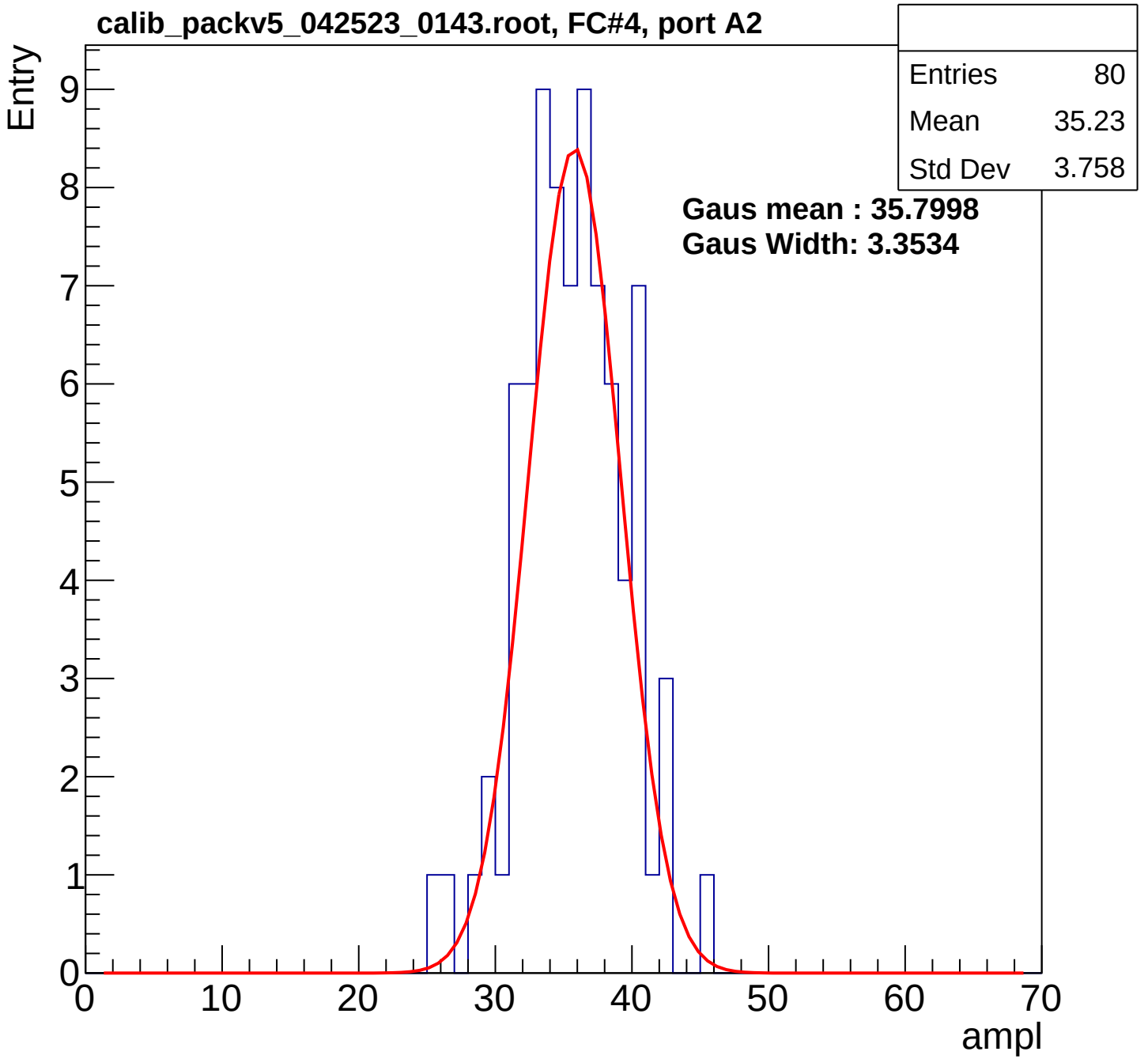
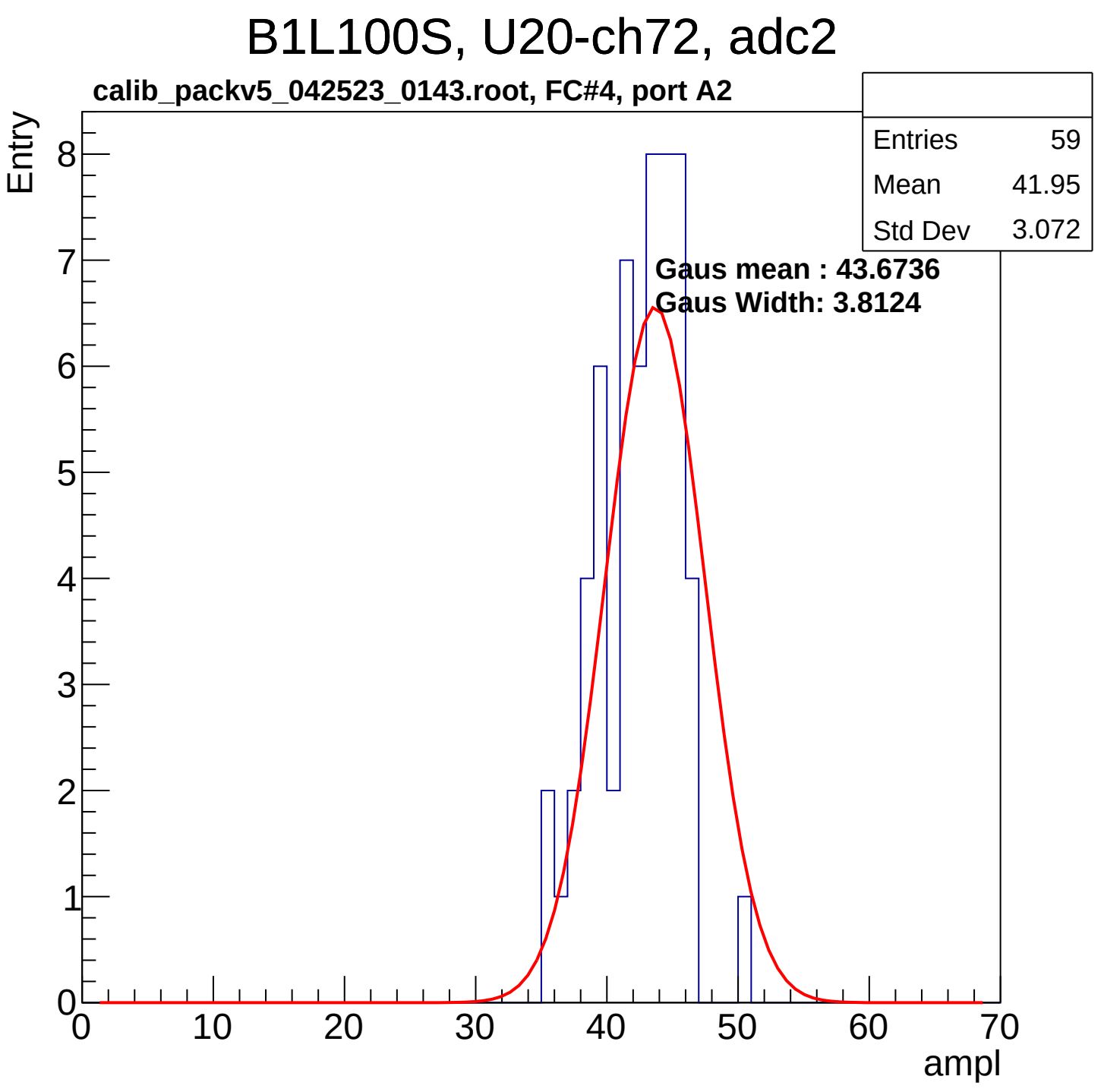
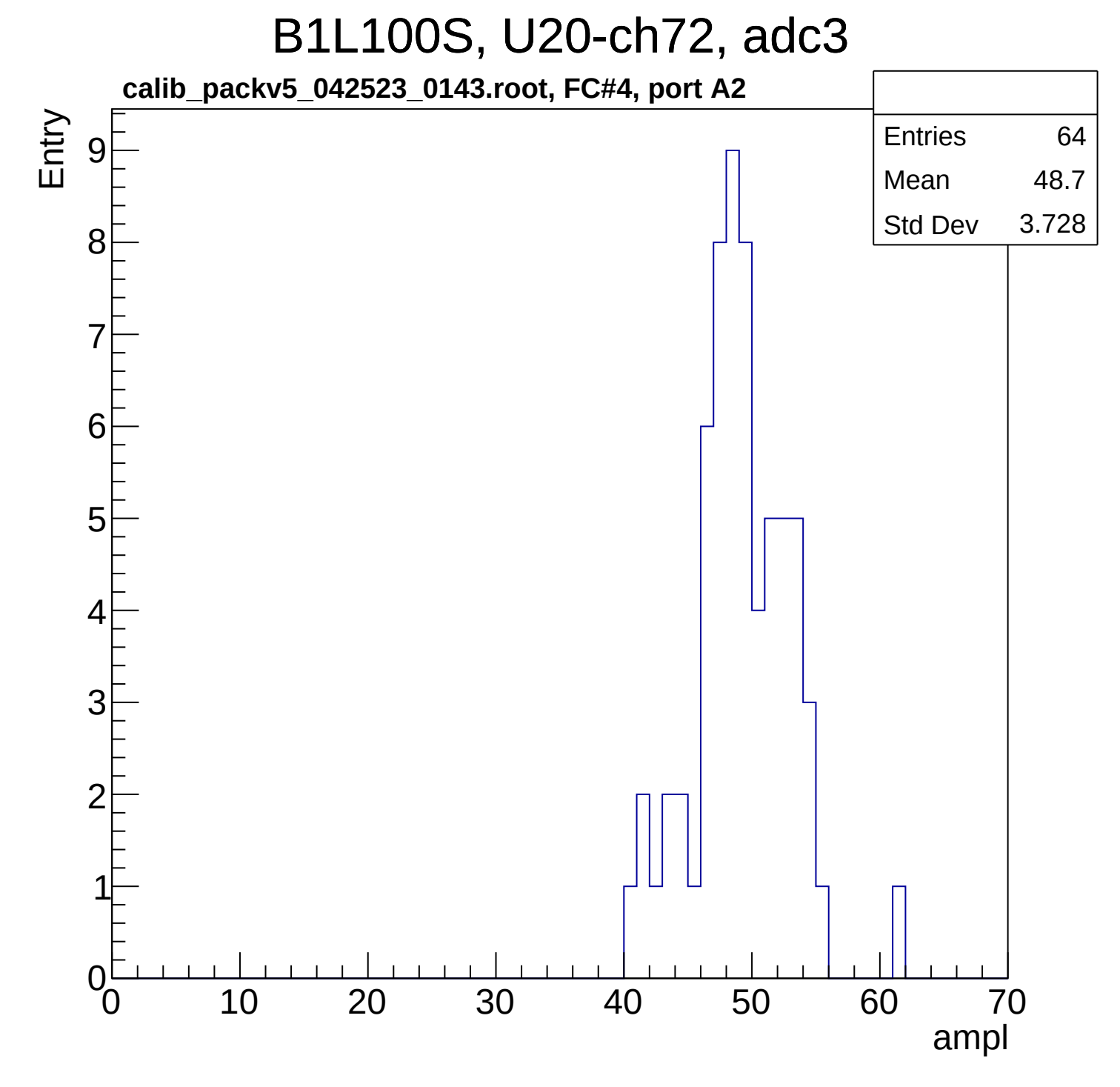
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.23
Std Dev 3.758
Gaus mean : 35.7998
Gaus Width: 3.3534
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 41.95
Std Dev 3.072
Gaus mean : 43.6736
Gaus Width: 3.8124
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch72, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 48.7
Std Dev 3.728
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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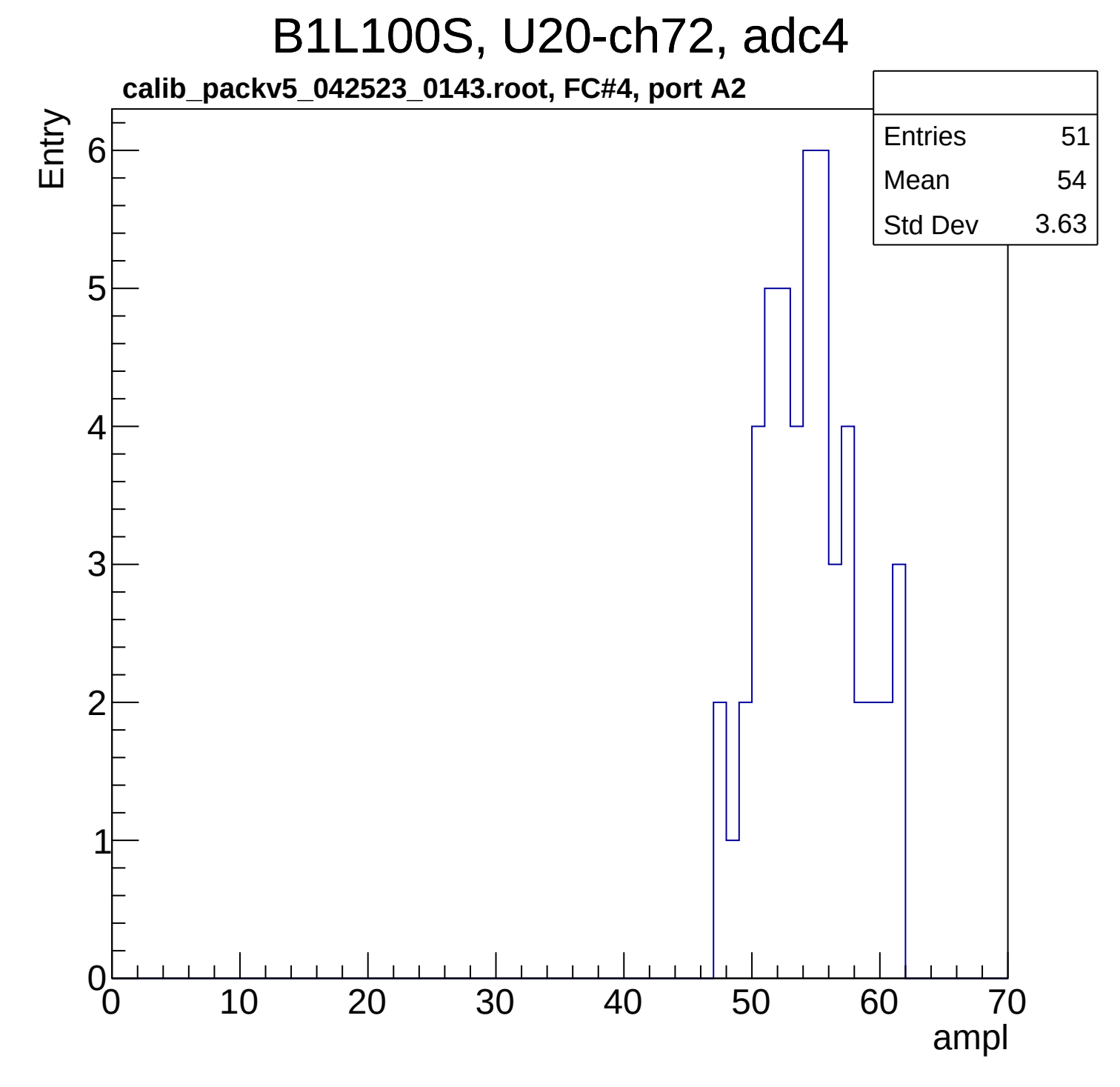
B1L100S, U20-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54
Std Dev 3.63
Entry
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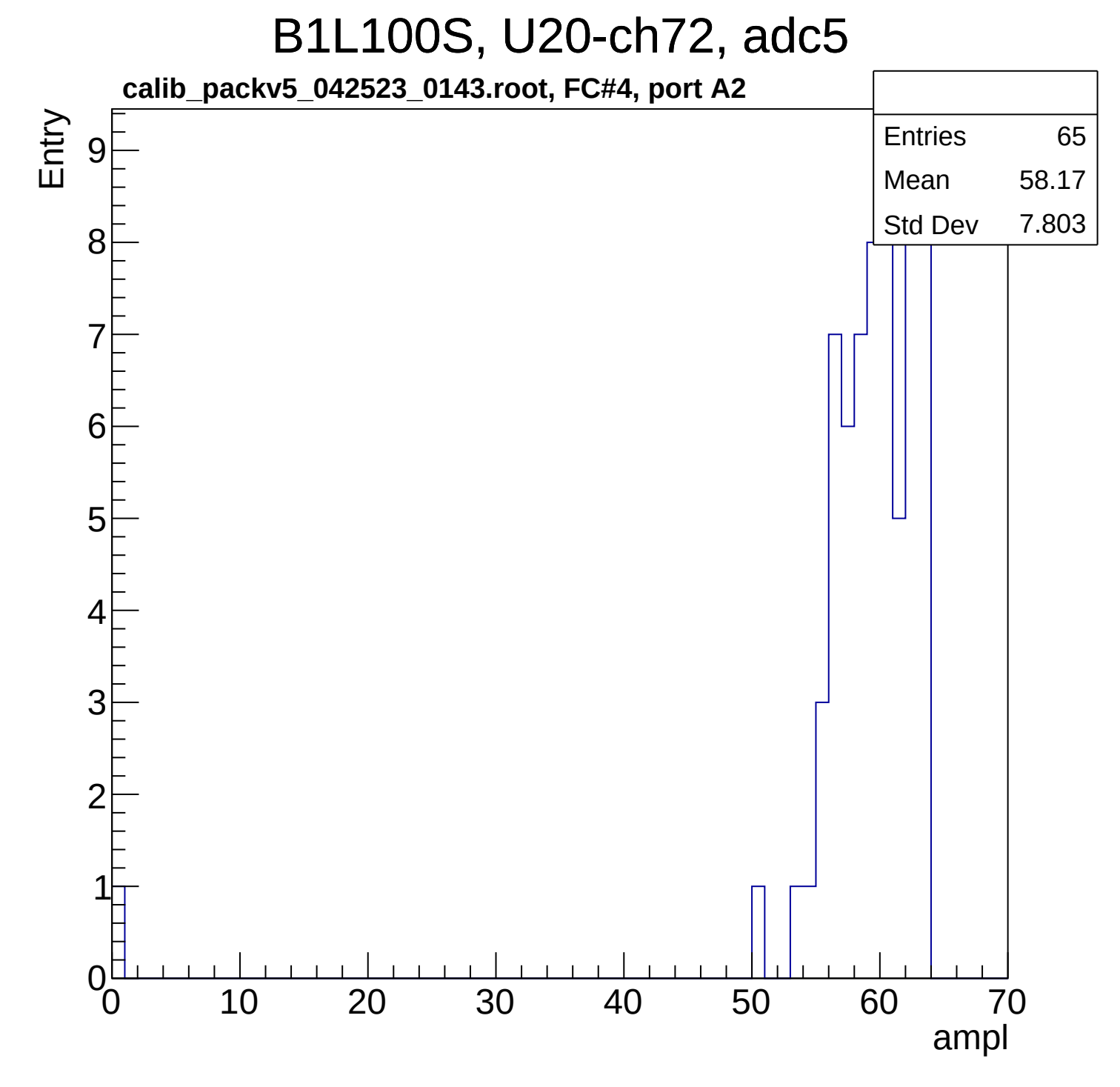
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 58.17
Std Dev 7.803
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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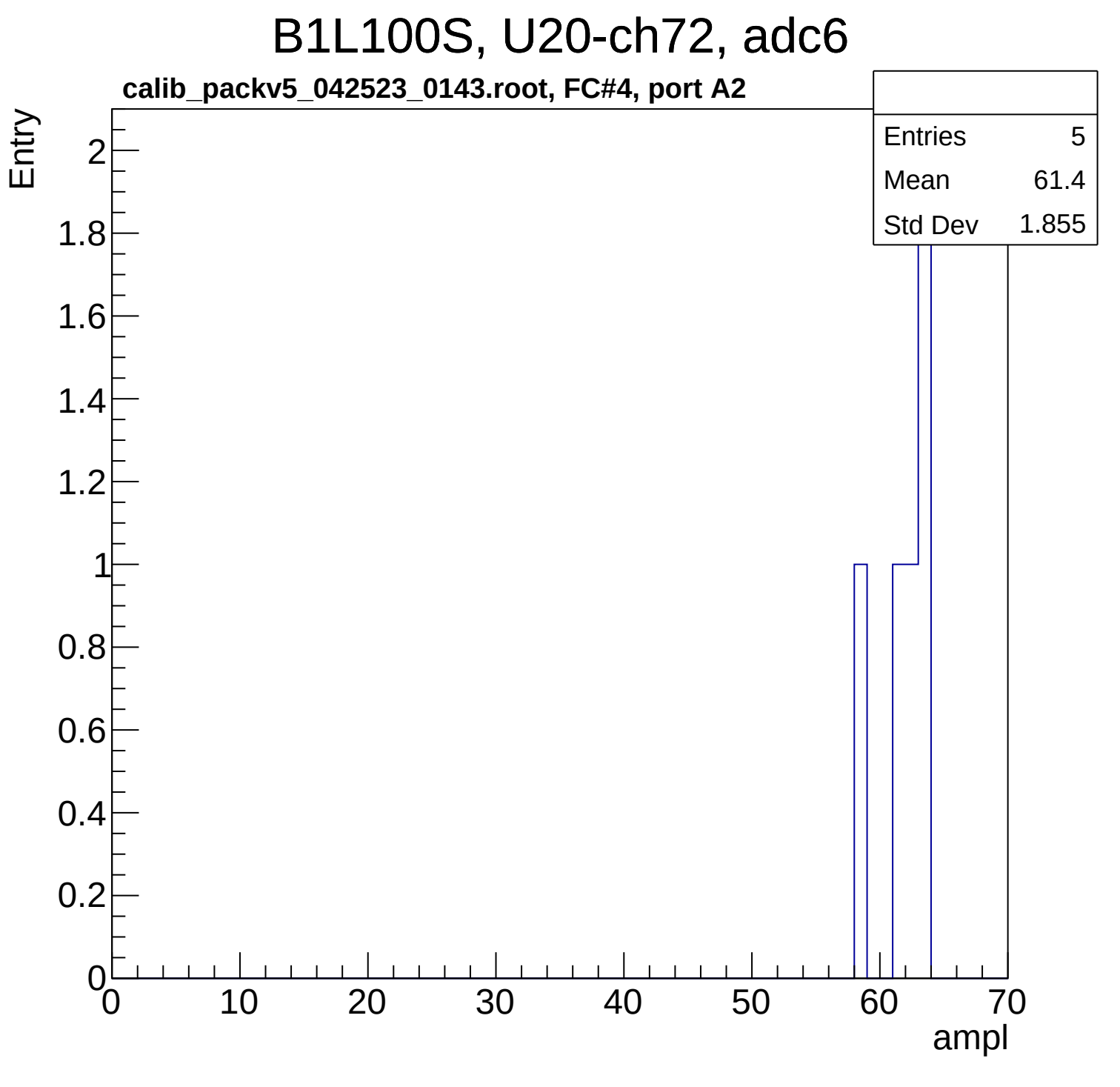
B1L100S, U20-ch72, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.4
Std Dev 1.855
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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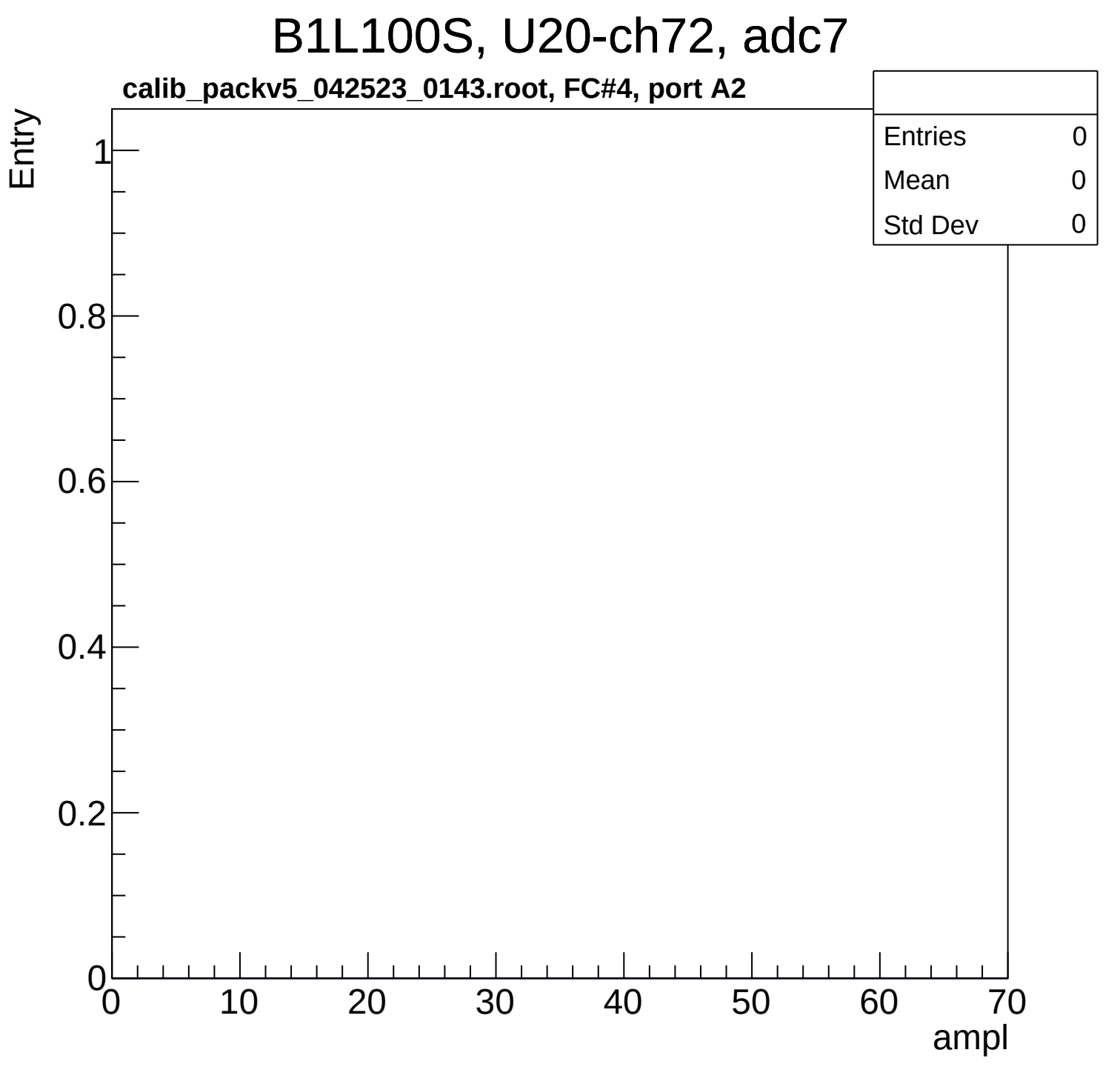
40
50
60
70


B1L100S, U20-ch72, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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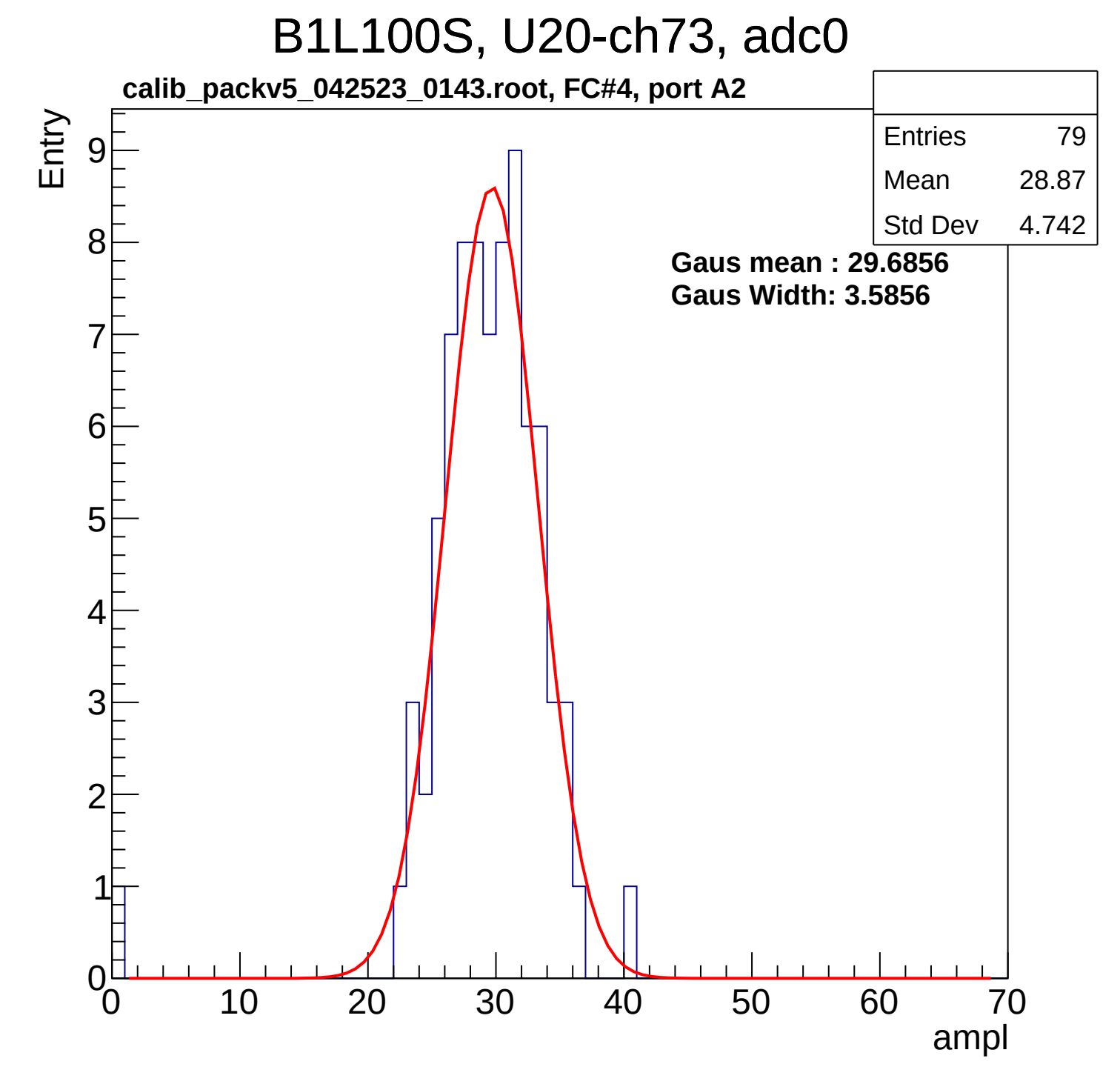
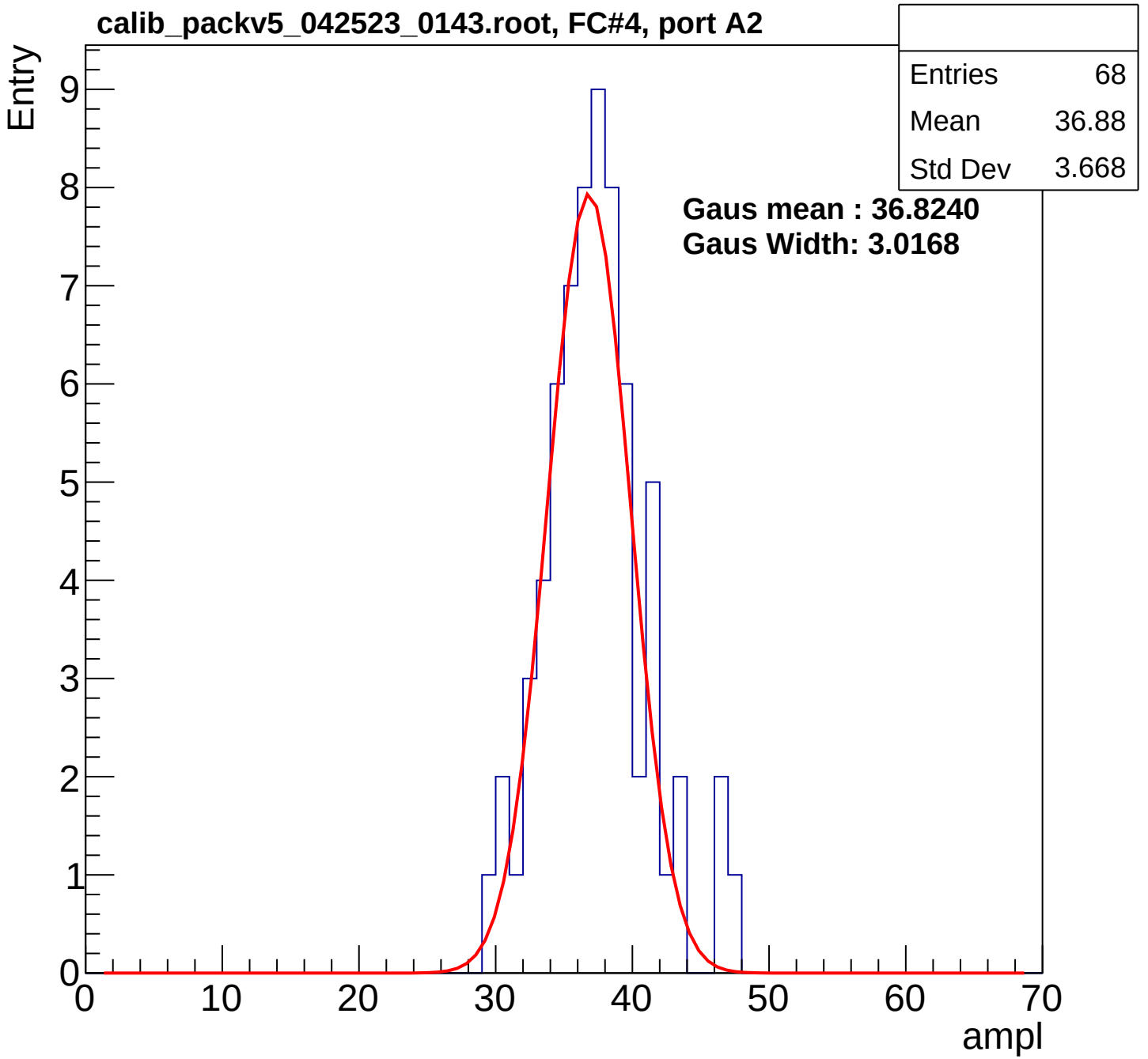
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 28.87
Std Dev 4.742
Gaus mean : 29.6856
Gaus Width: 3.5856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 36.88
Std Dev 3.668
Gaus mean : 36.8240
Gaus Width: 3.0168
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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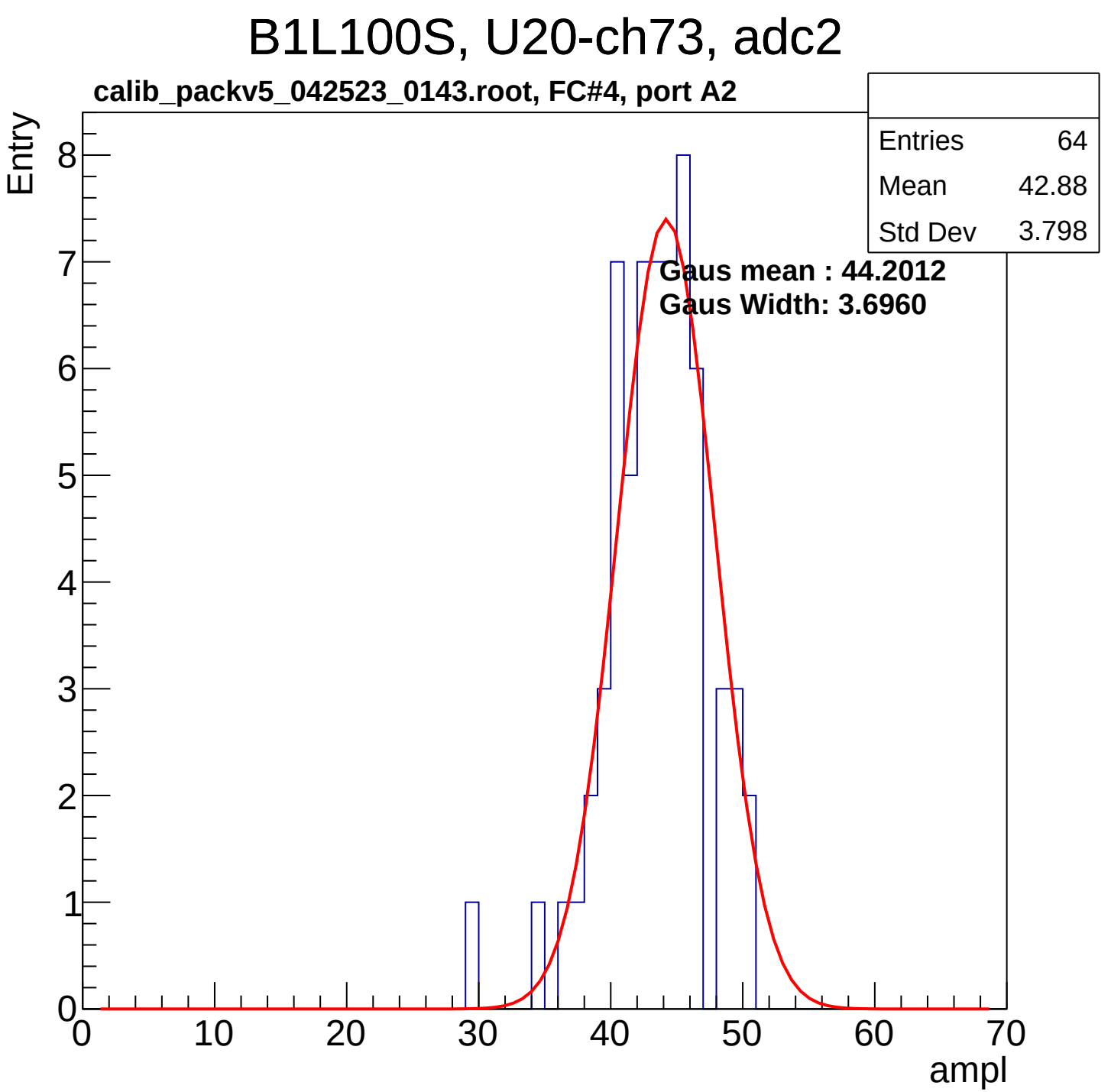
B1L100S, U20-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.88
Std Dev 3.798
Gaus mean : 44.2012
Gaus Width: 3.6960
Entry
ampl
0
1
2
3
4
5
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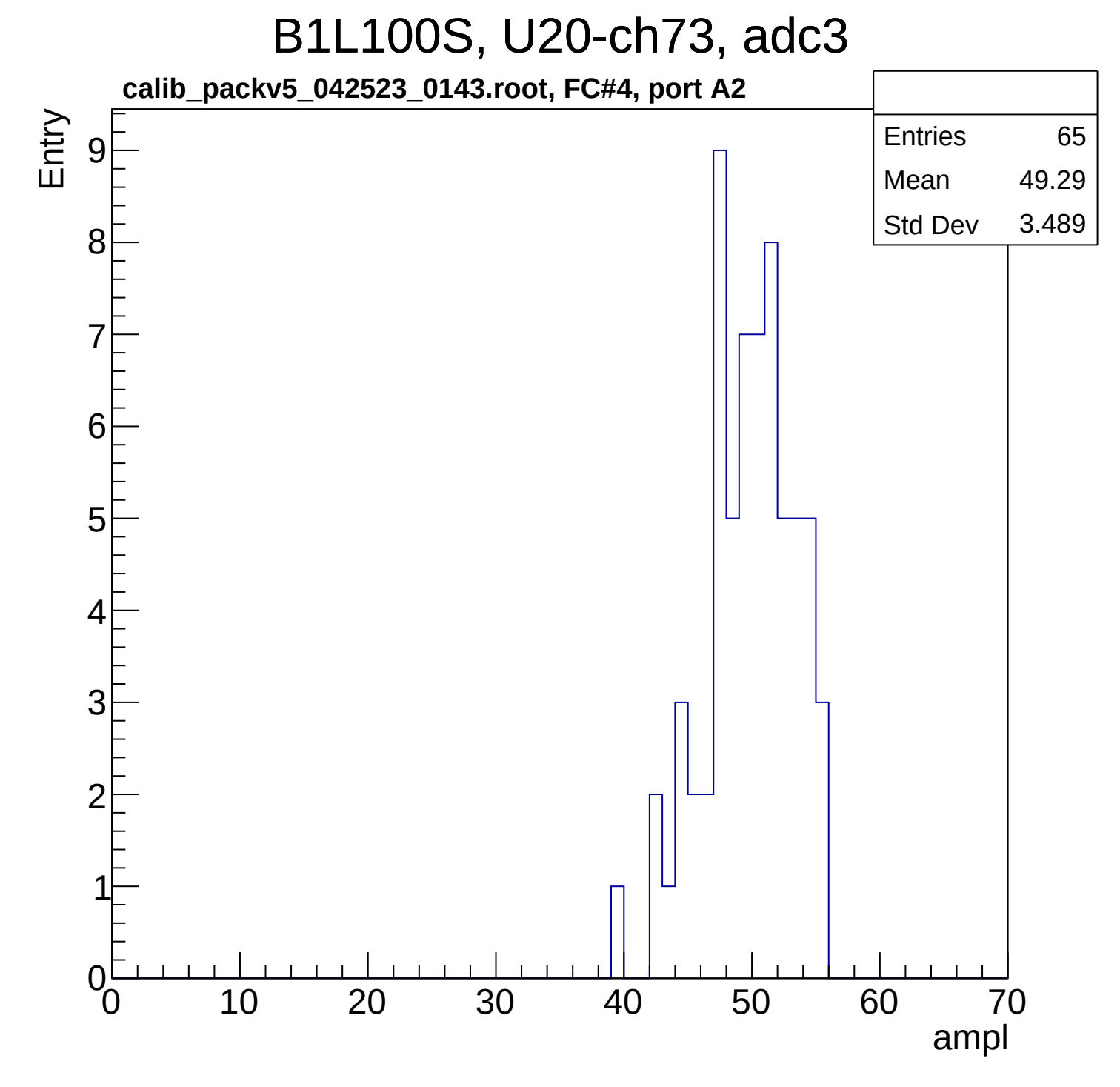
B1L100S, U20-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.29
Std Dev 3.489
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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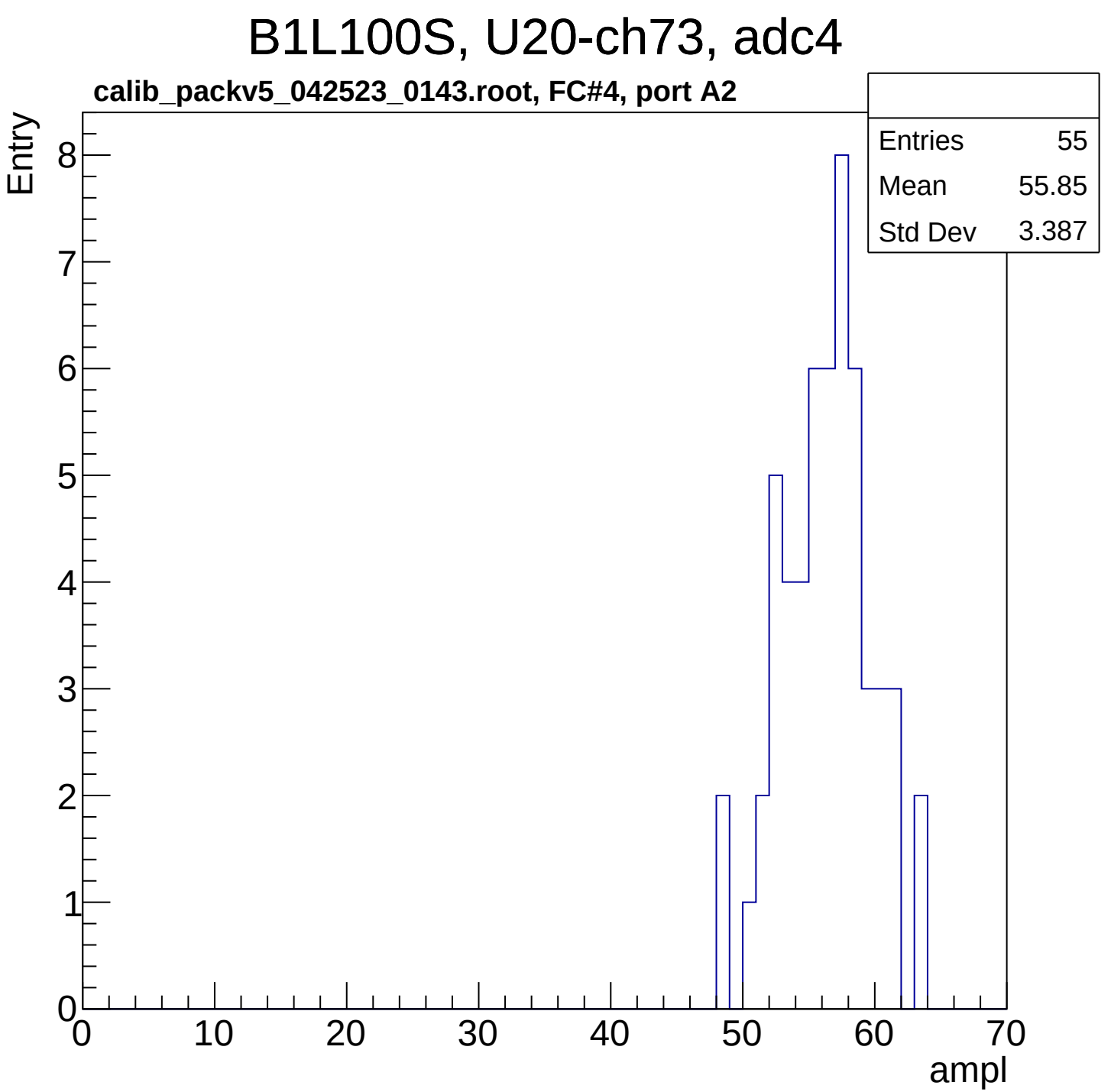
B1L100S, U20-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.85
Std Dev 3.387
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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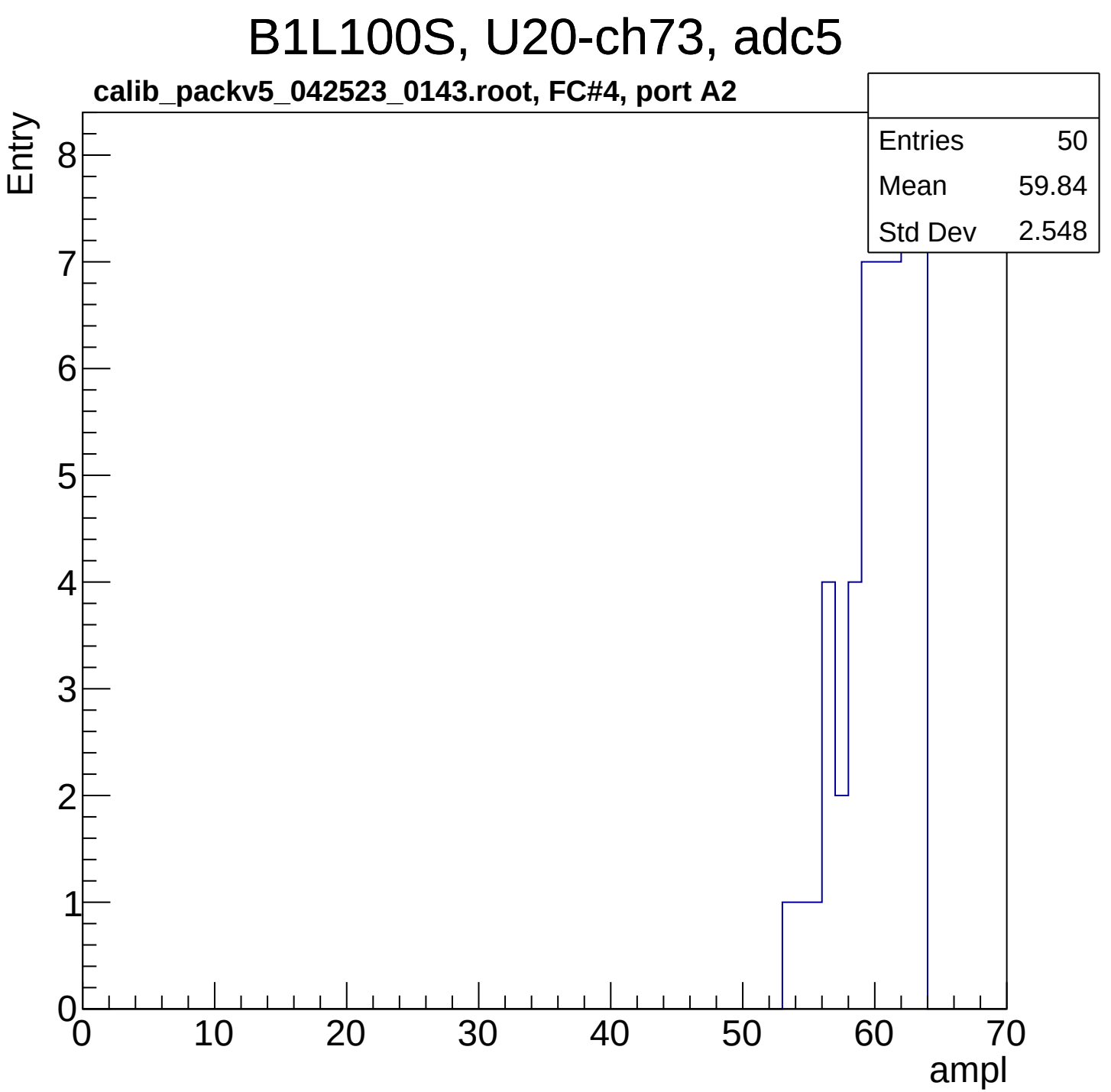
40
50
60
70


B1L100S, U20-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 59.84
Std Dev 2.548
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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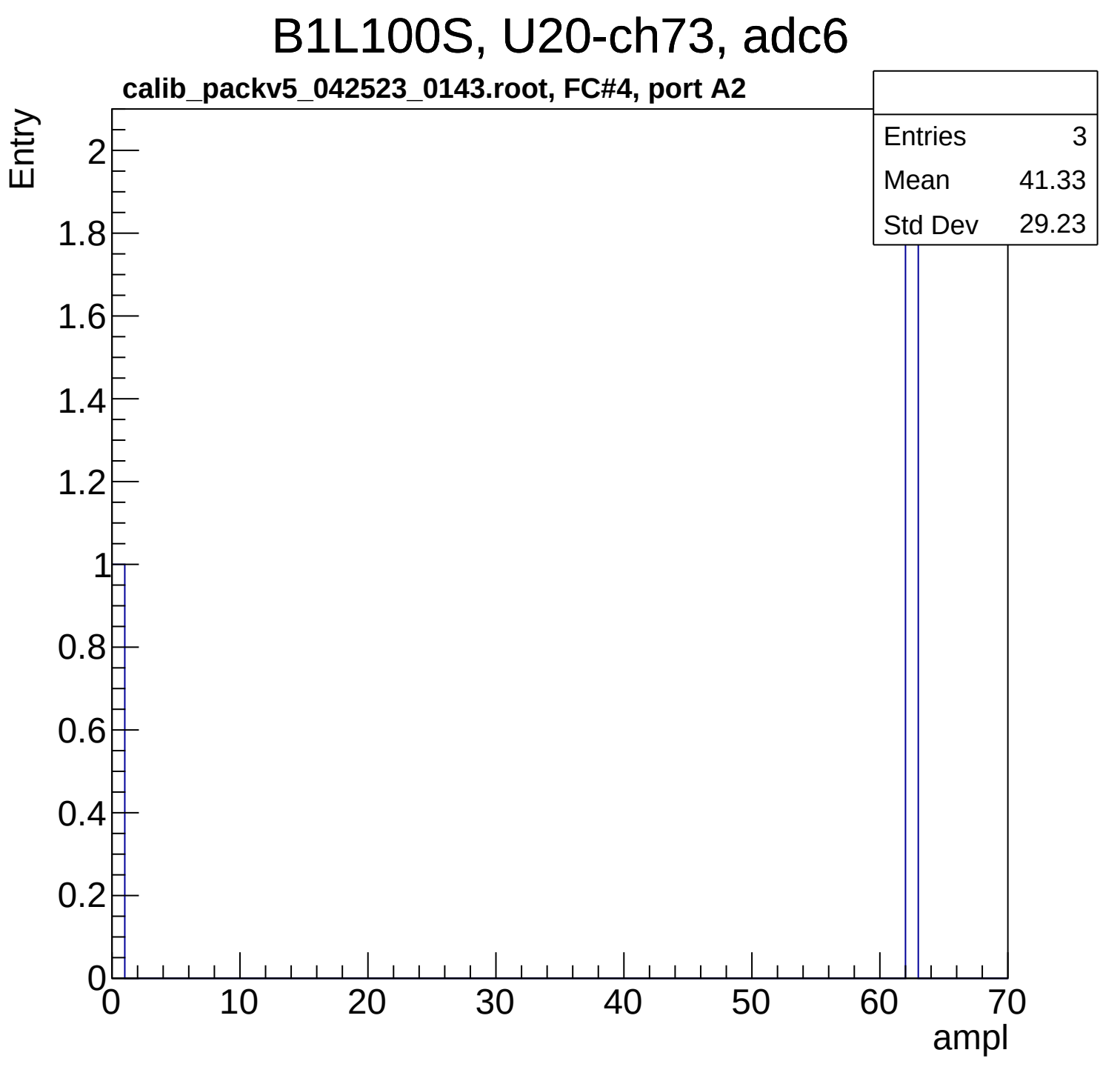
B1L100S, U20-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
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40
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60
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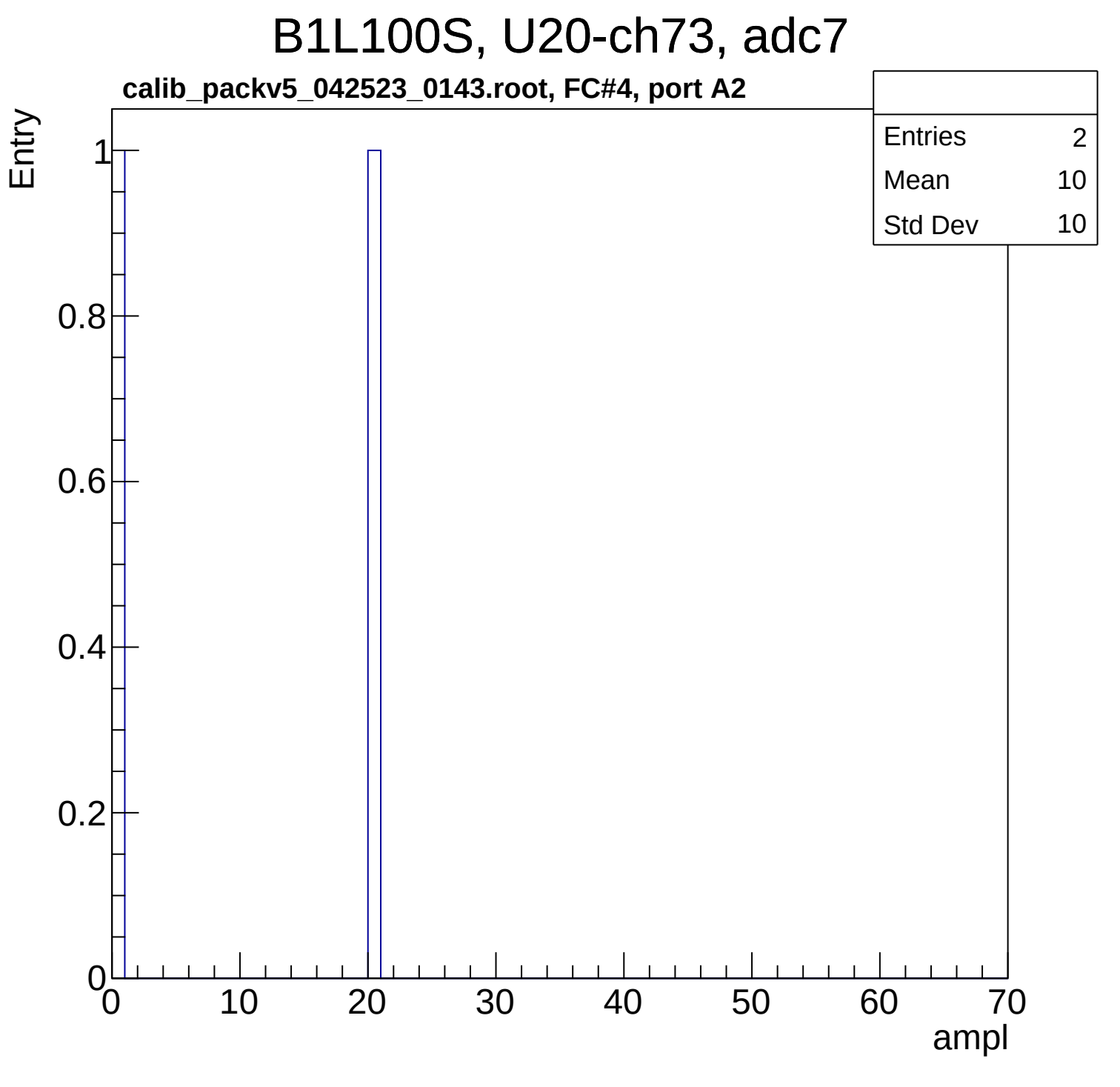
B1L100S, U20-ch73, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
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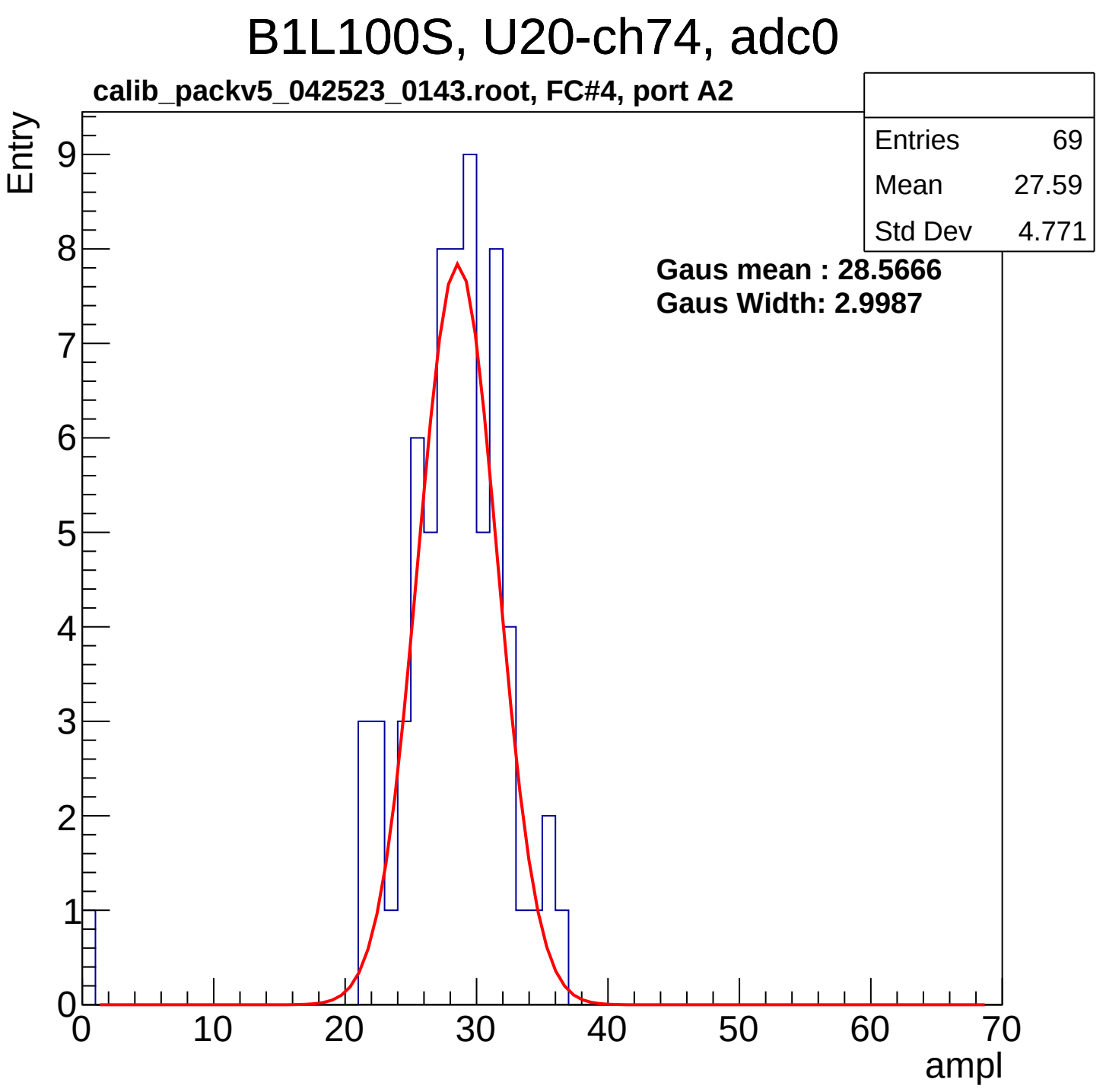
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 27.59
Std Dev 4.771
Gaus mean : 28.5666
Gaus Width: 2.9987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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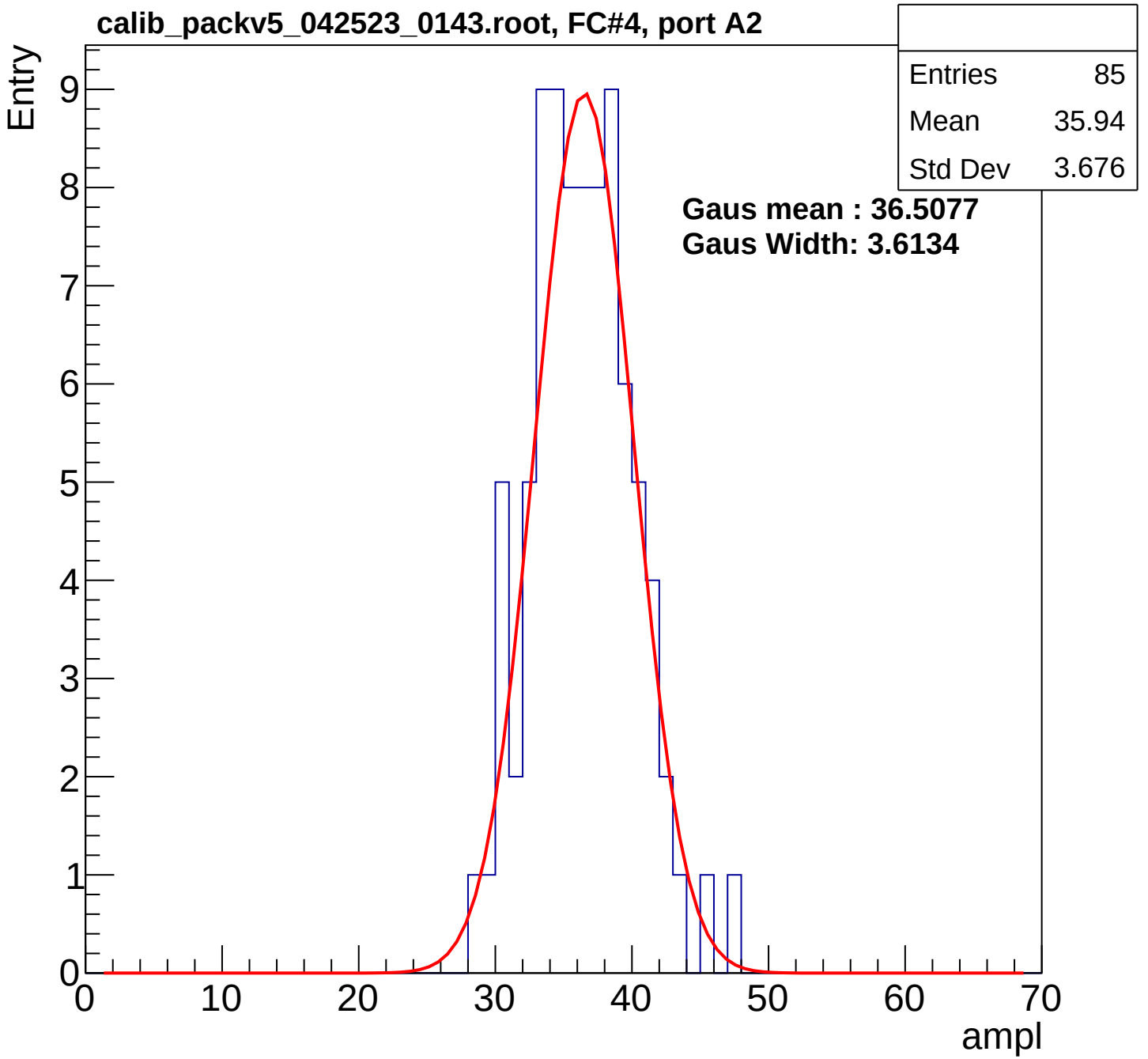
B1L100S, U20-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 35.94
Std Dev 3.676
Gaus mean : 36.5077
Gaus Width: 3.6134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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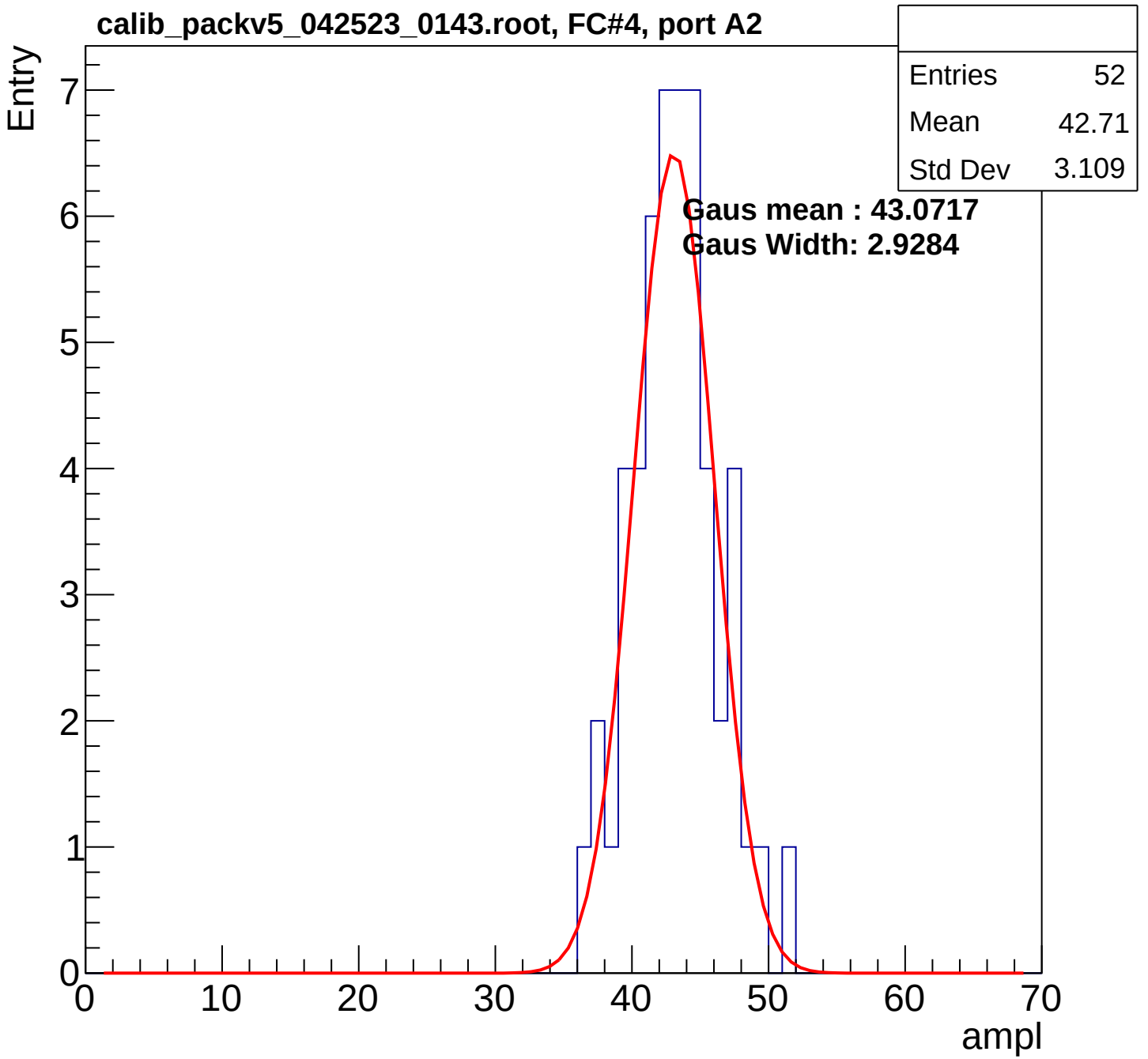
B1L100S, U20-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 42.71
Std Dev 3.109
Gaus mean : 43.0717
Gaus Width: 2.9284
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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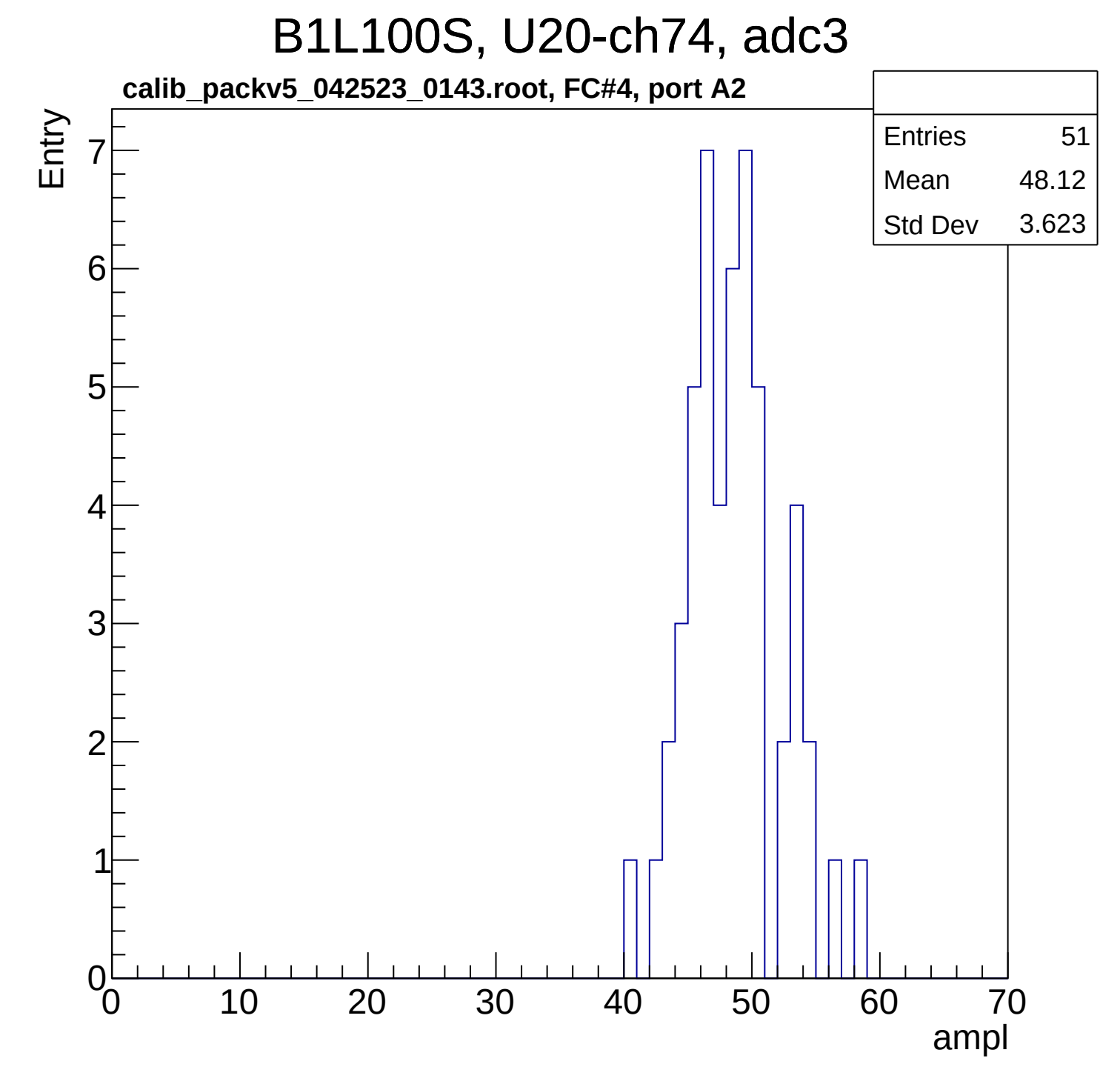
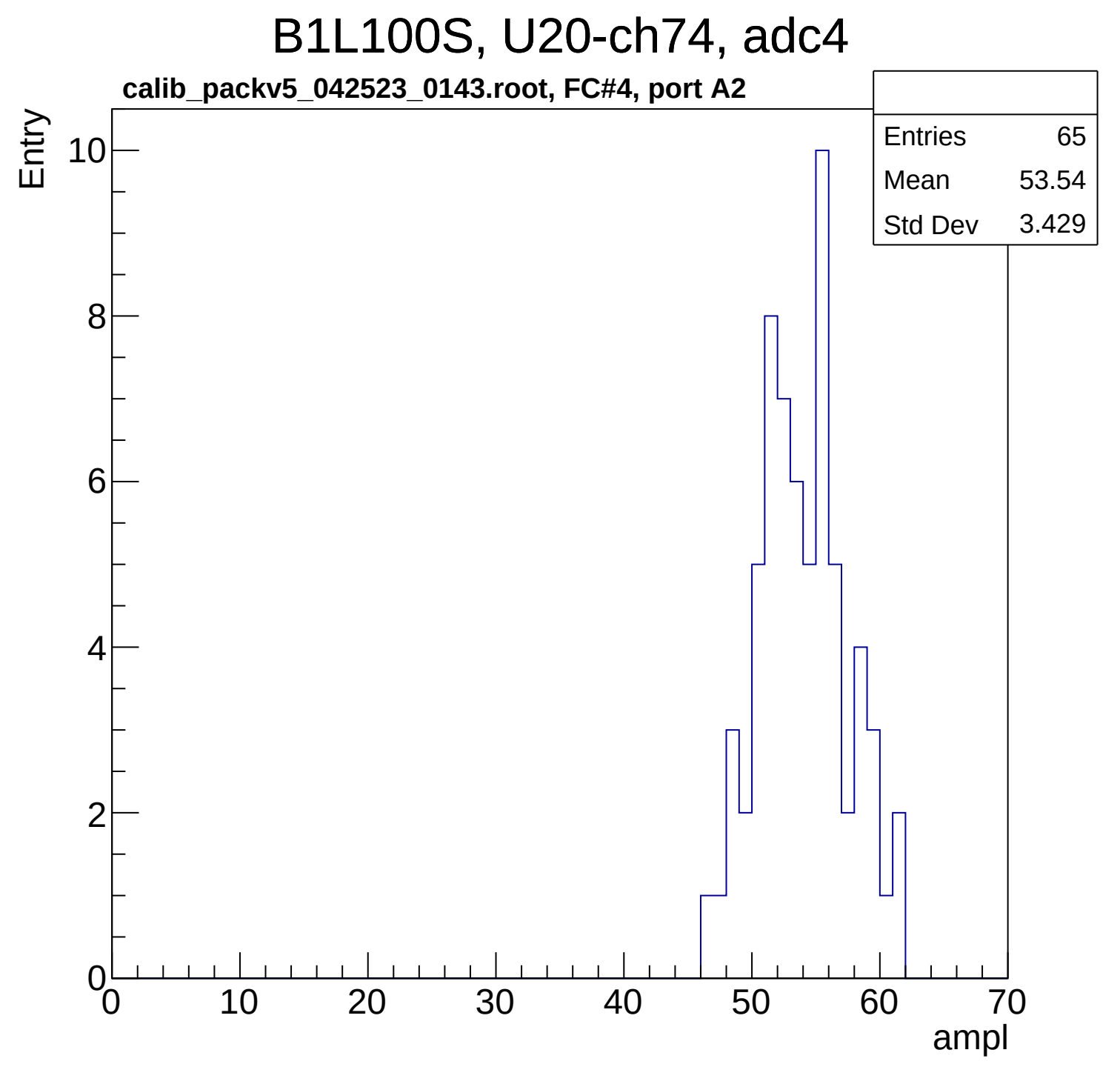
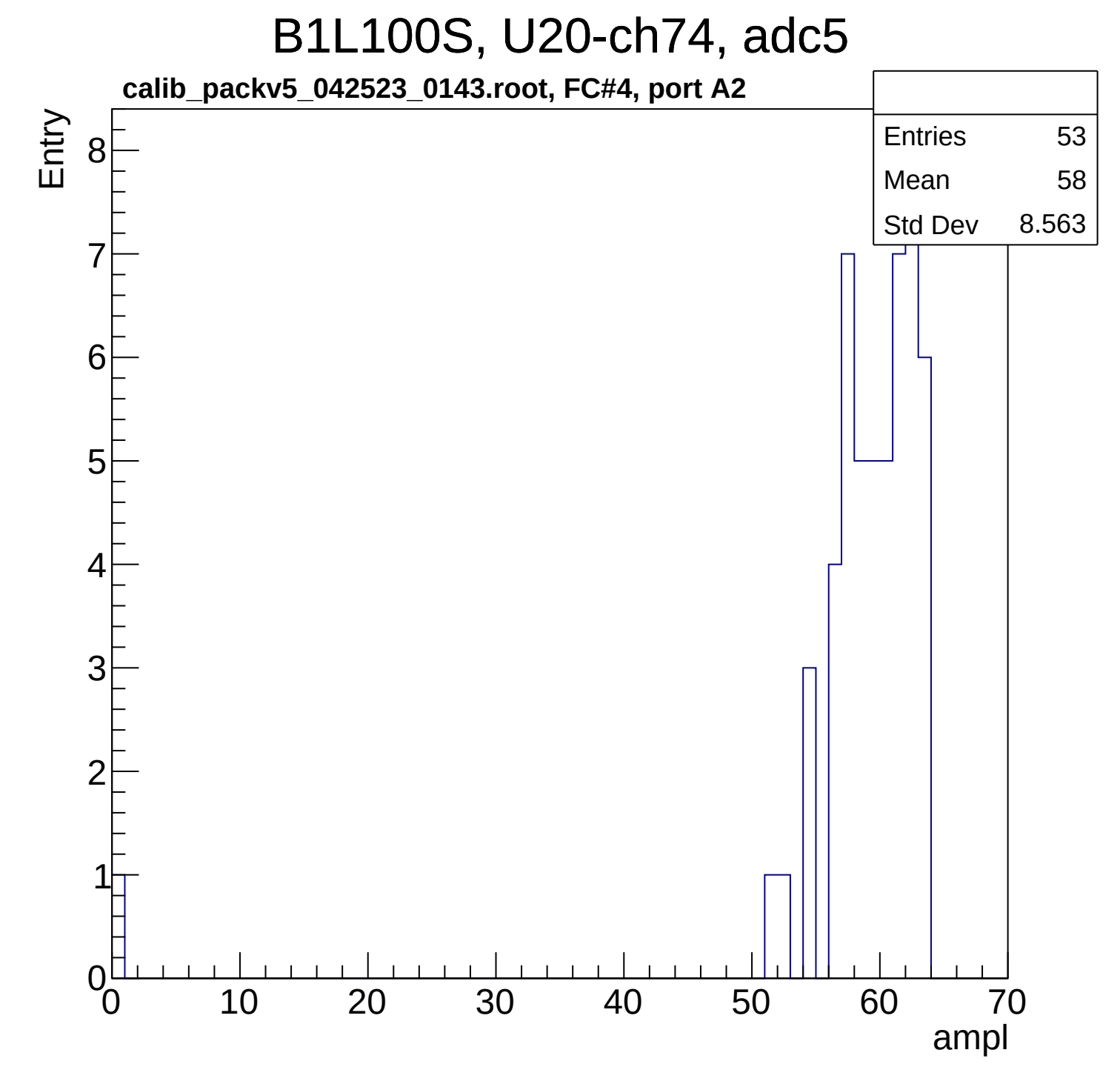

B1L100S, U20-ch74, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 48.12
Std Dev 3.623
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 53.54
Std Dev 3.429
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58
Std Dev 8.563
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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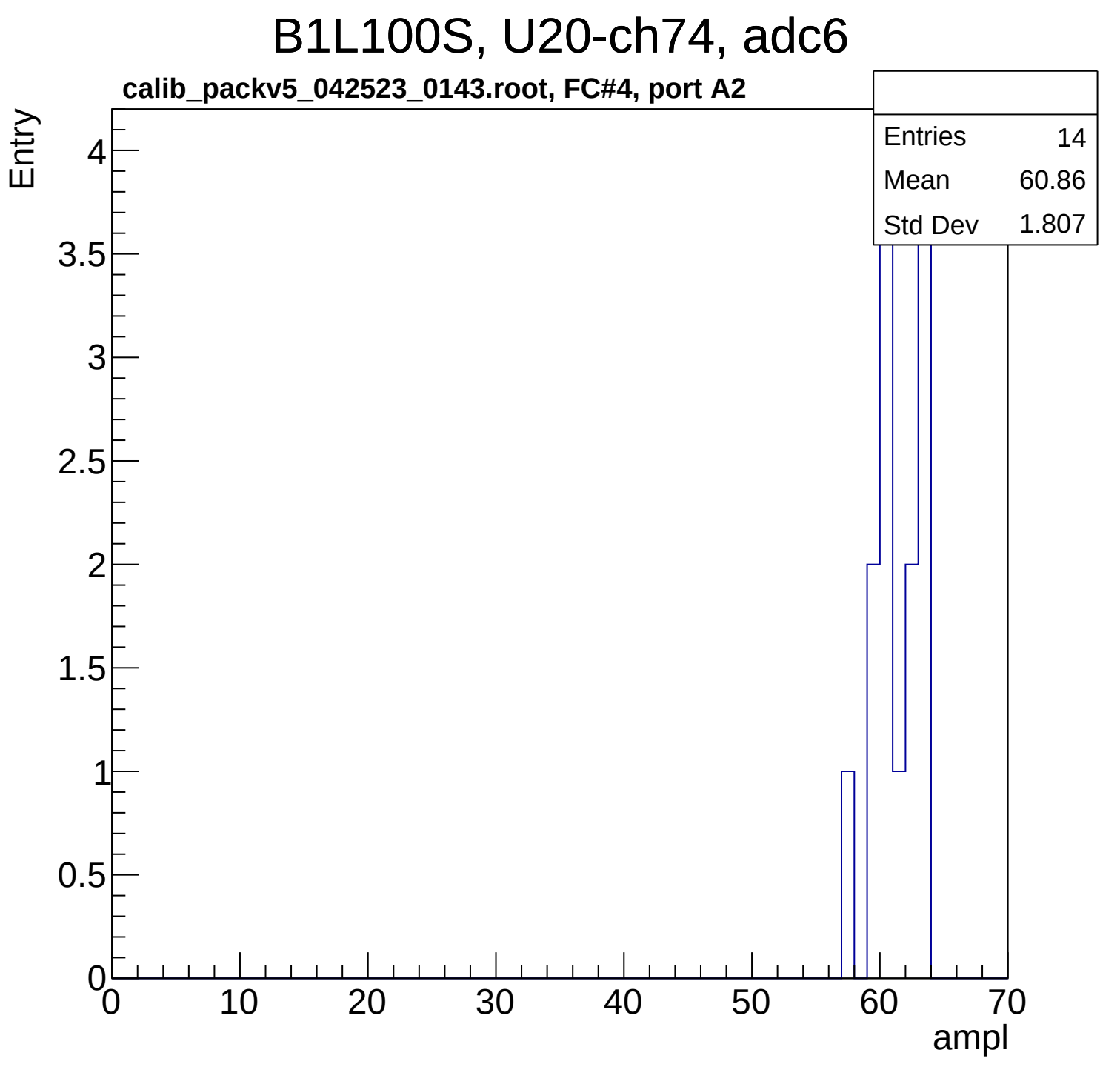
B1L100S, U20-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 60.86
Std Dev 1.807
Entry
ampl
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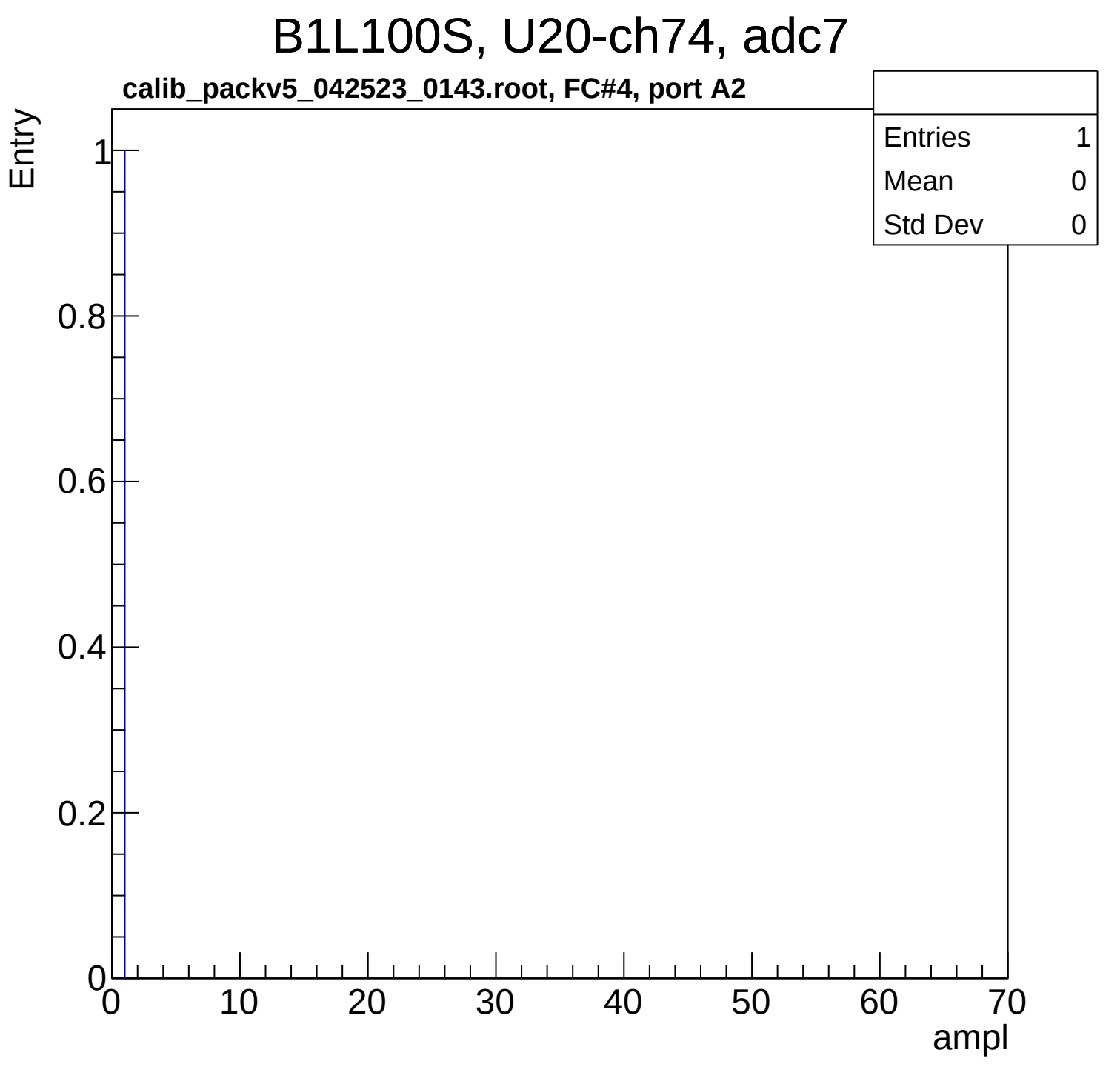
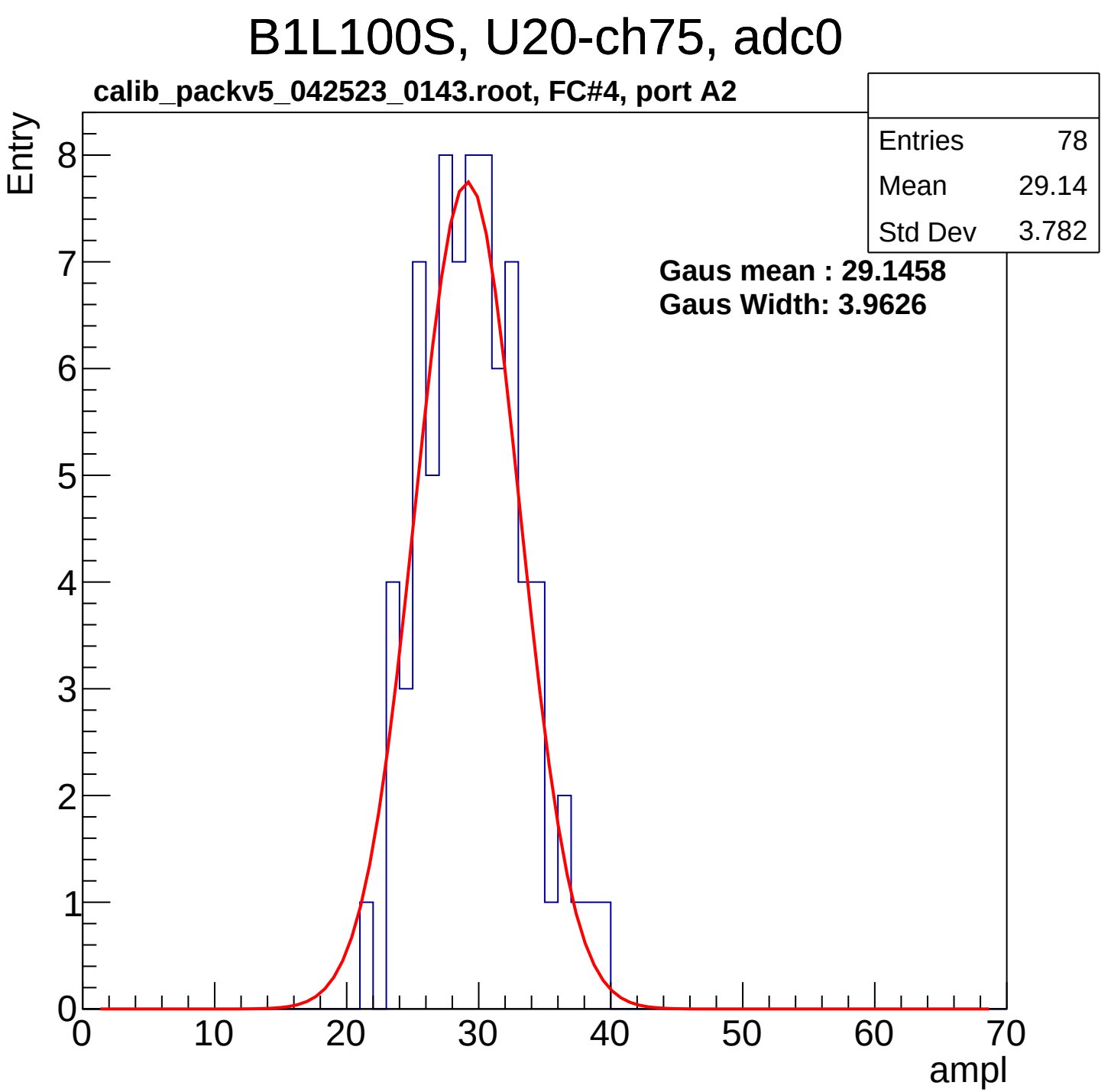
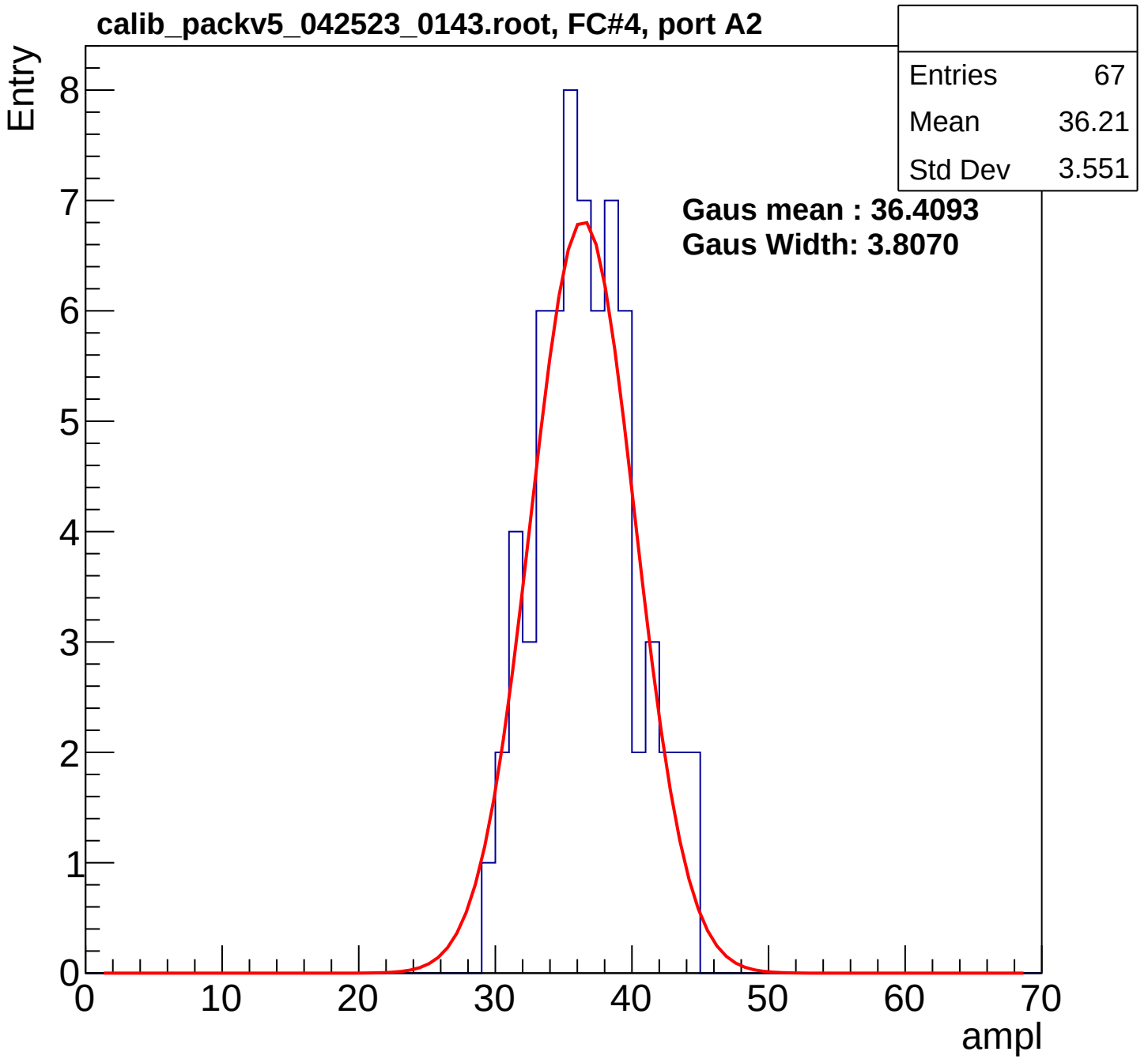
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.14
Std Dev 3.782
Gaus mean : 29.1458
Gaus Width: 3.9626
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.21
Std Dev 3.551
Gaus mean : 36.4093
Gaus Width: 3.8070
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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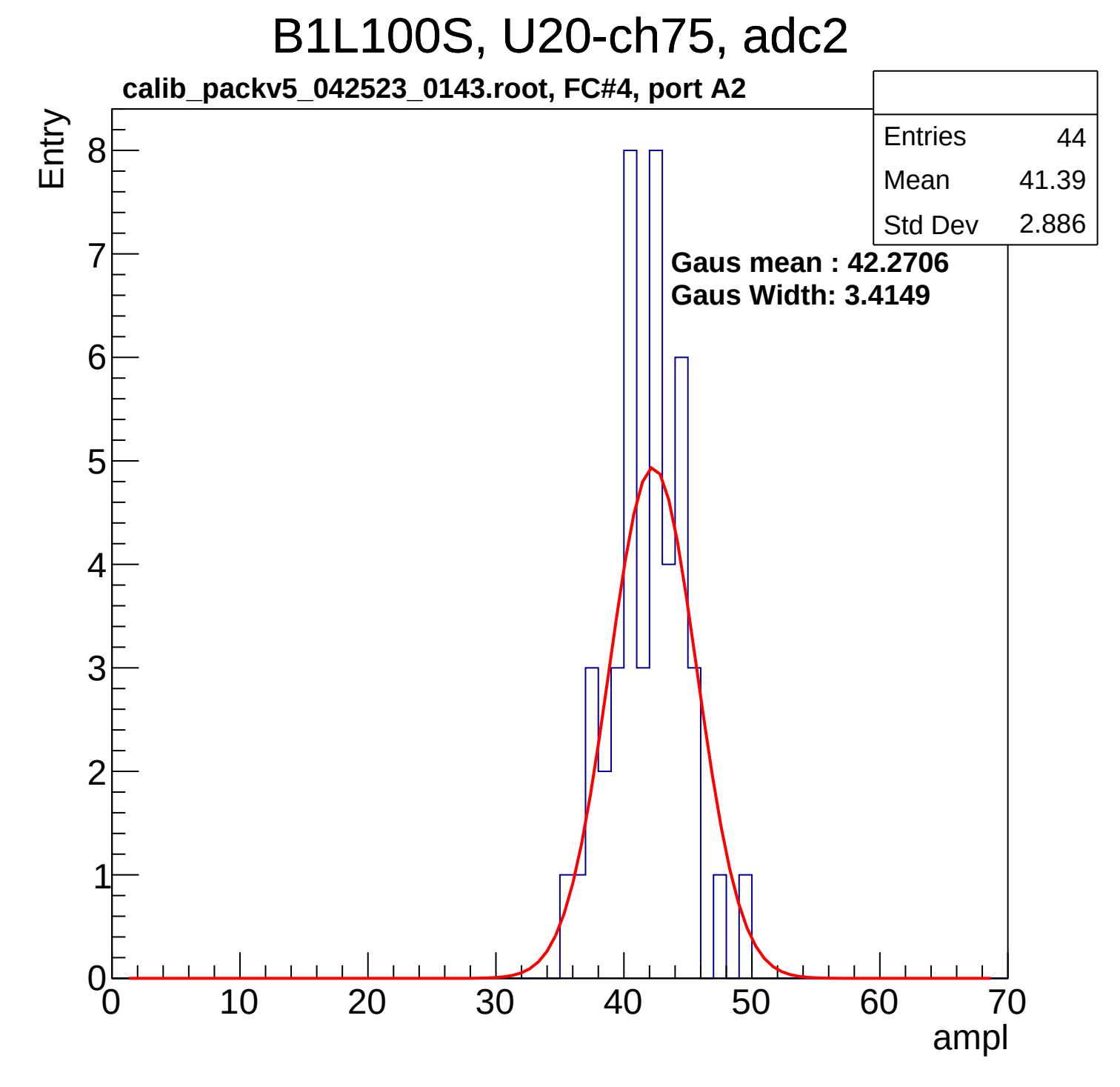
B1L100S, U20-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 41.39
Std Dev 2.886
Gaus mean : 42.2706
Gaus Width: 3.4149
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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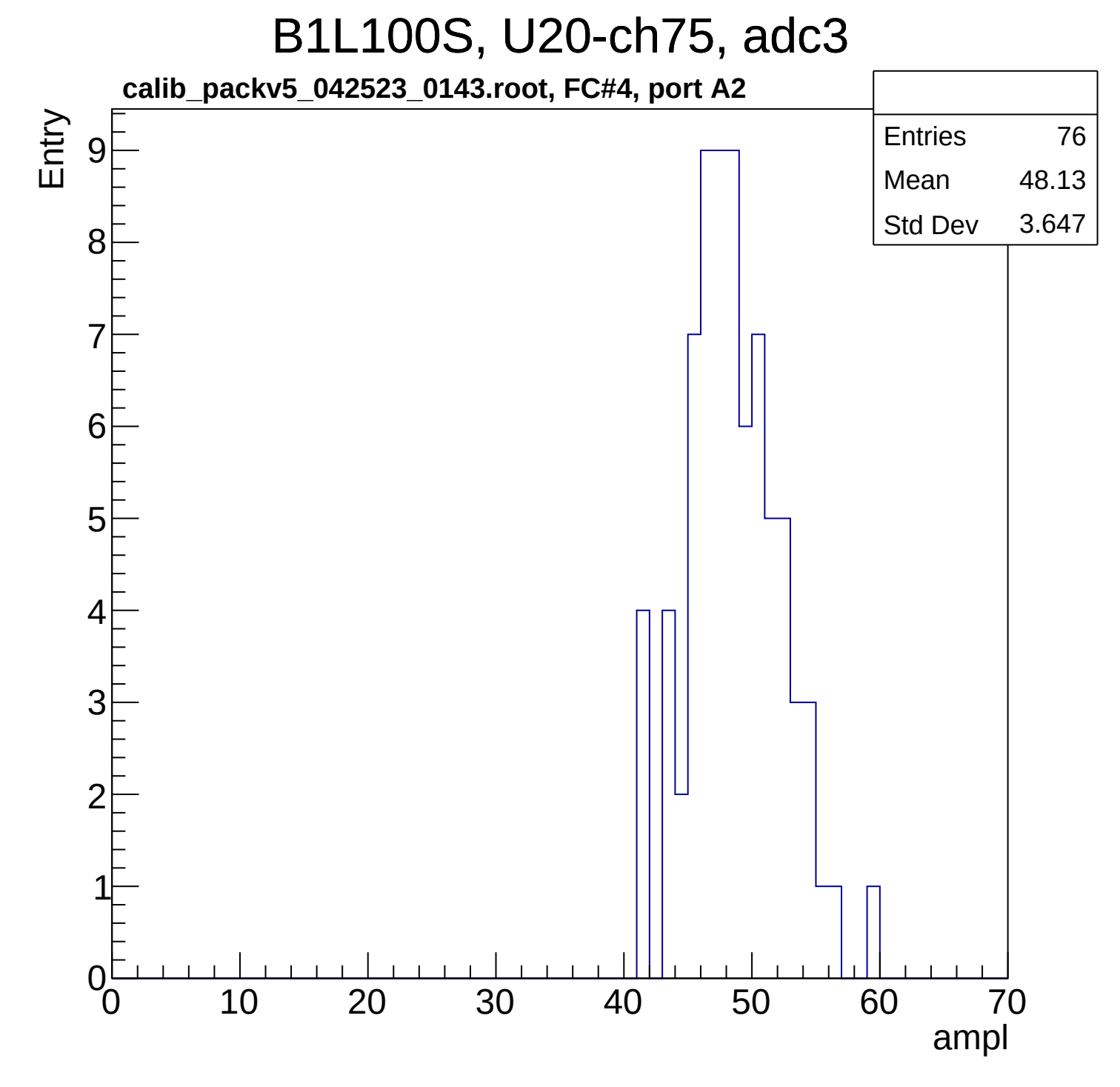
B1L100S, U20-ch75, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 48.13
Std Dev 3.647
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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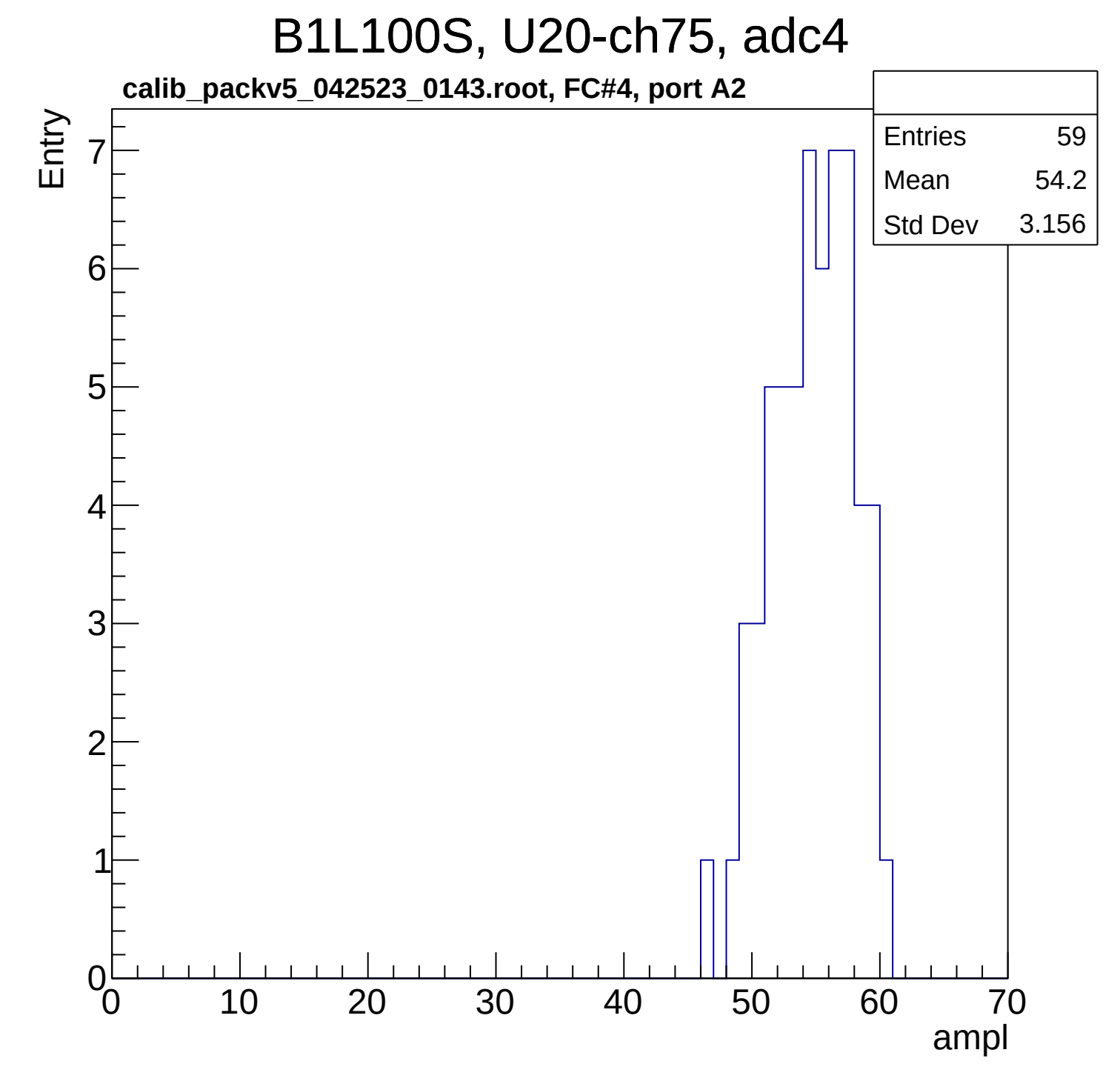
B1L100S, U20-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.2
Std Dev 3.156
Entry
ampl
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1
2
3
4
5
6
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0
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20
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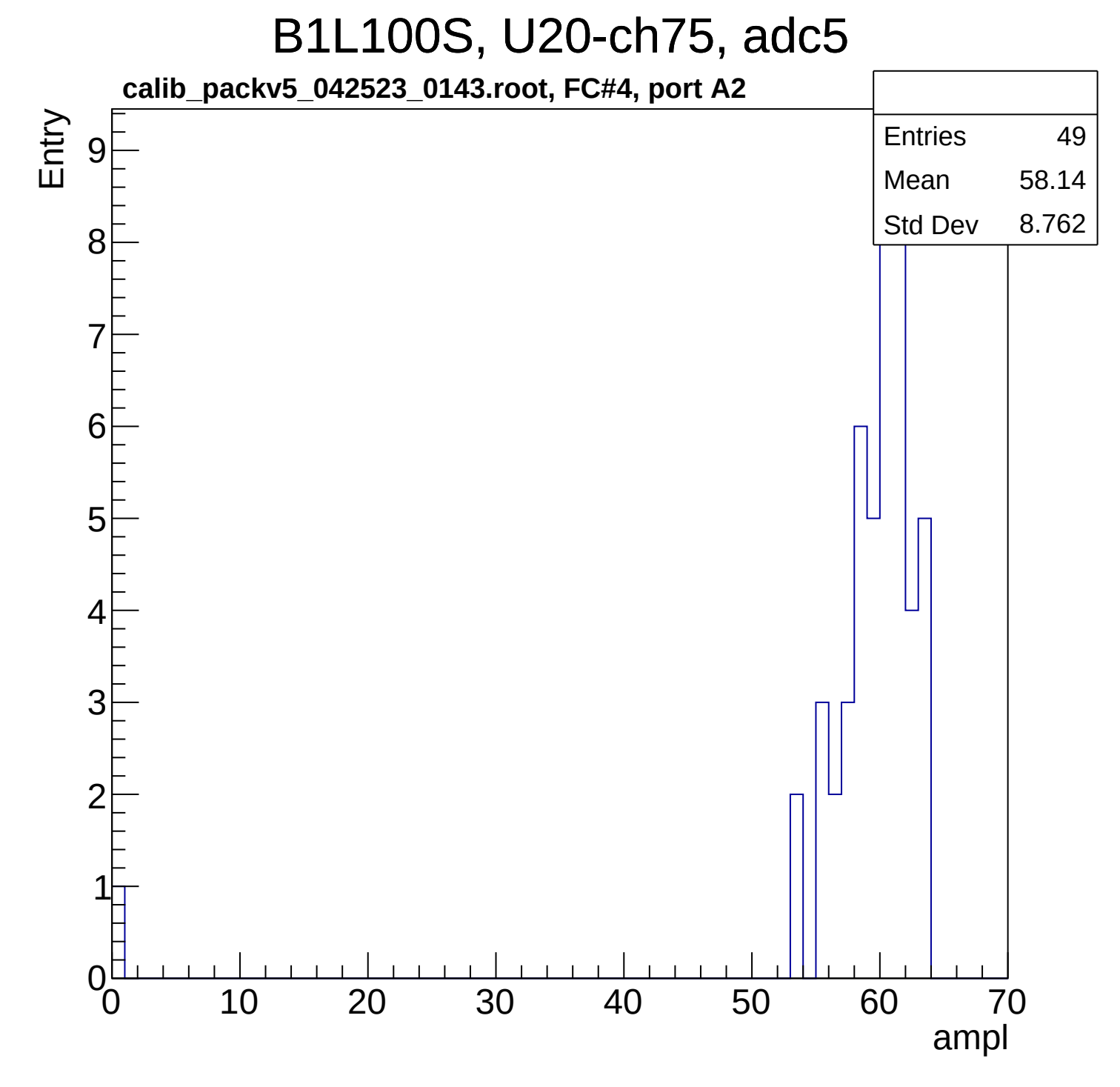
40
50
60
70


B1L100S, U20-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.14
Std Dev 8.762
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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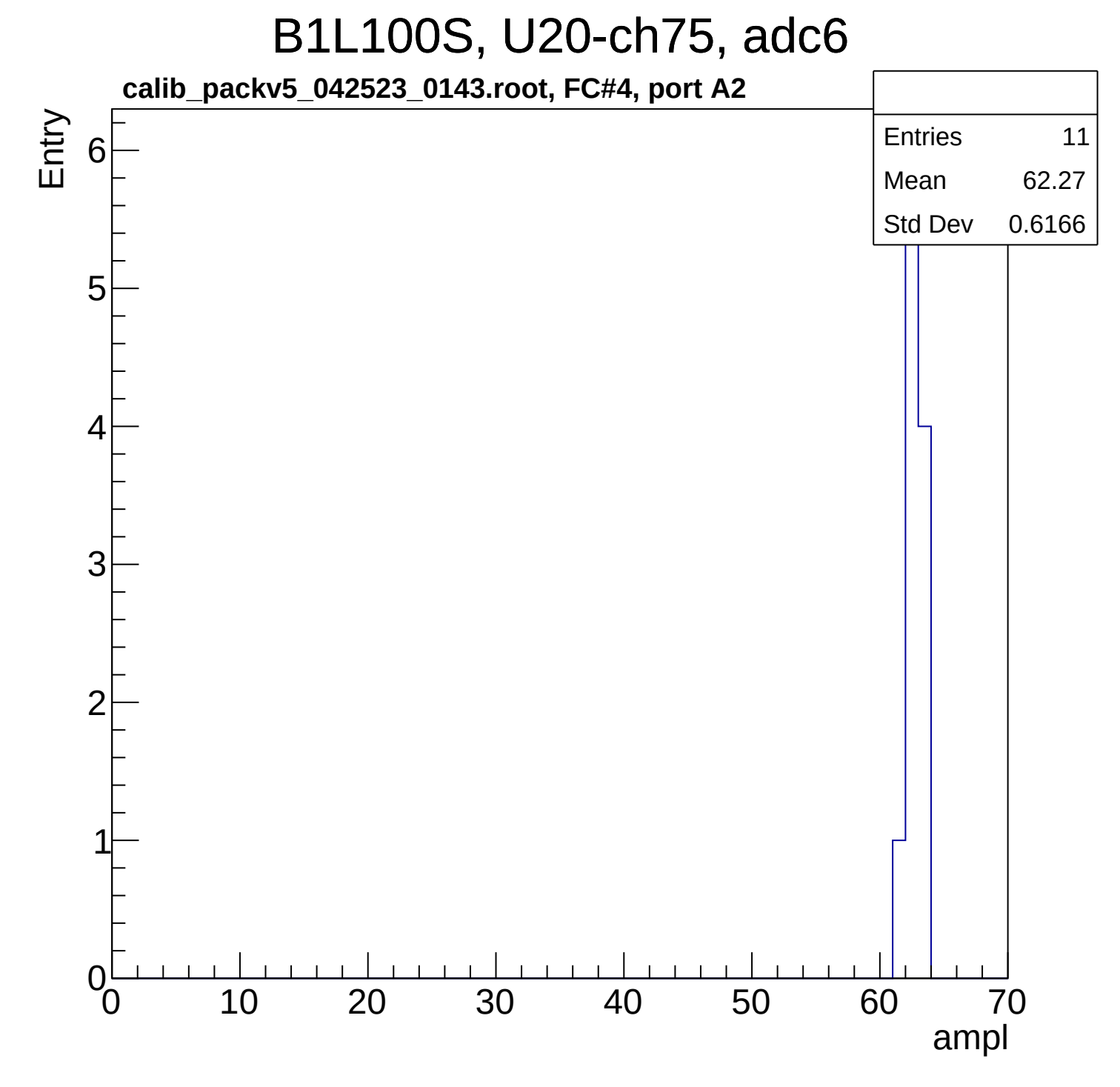
B1L100S, U20-ch75, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 62.27
Std Dev 0.6166
Entry
ampl
0
1
2
3
4
5
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0
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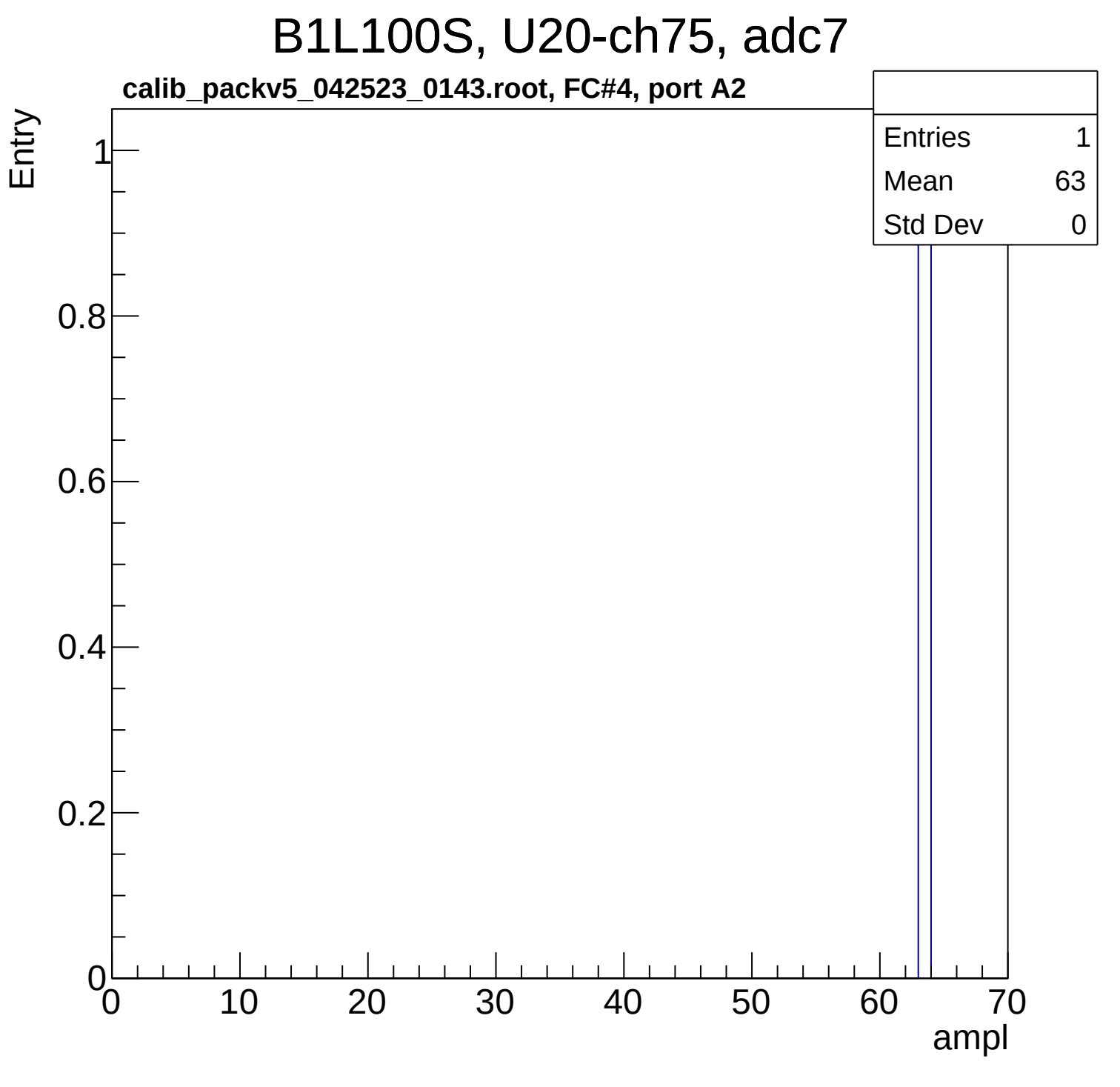
20
30
40
50
60
70


B1L100S, U20-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
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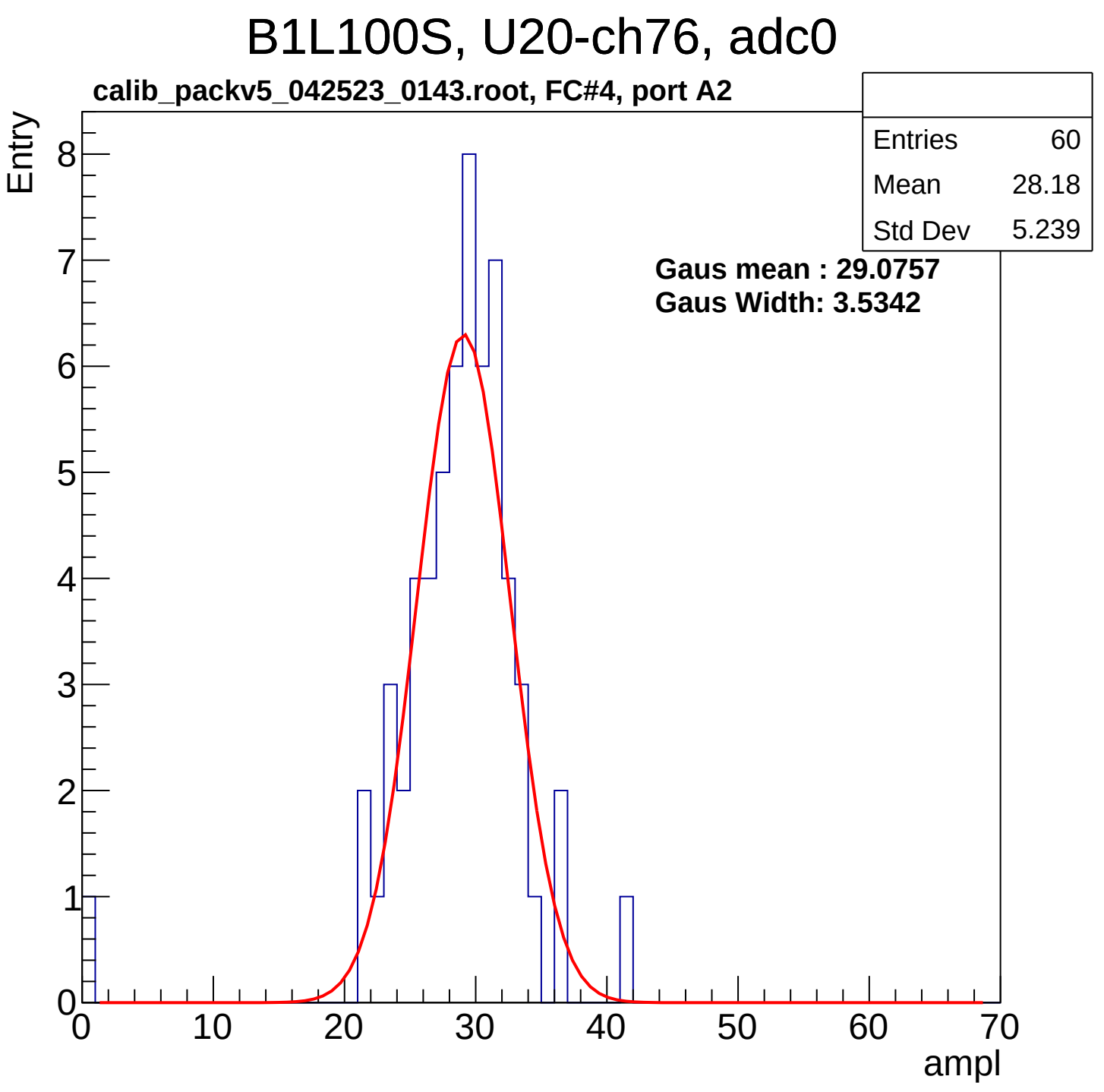
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 28.18
Std Dev 5.239
Gaus mean : 29.0757
Gaus Width: 3.5342
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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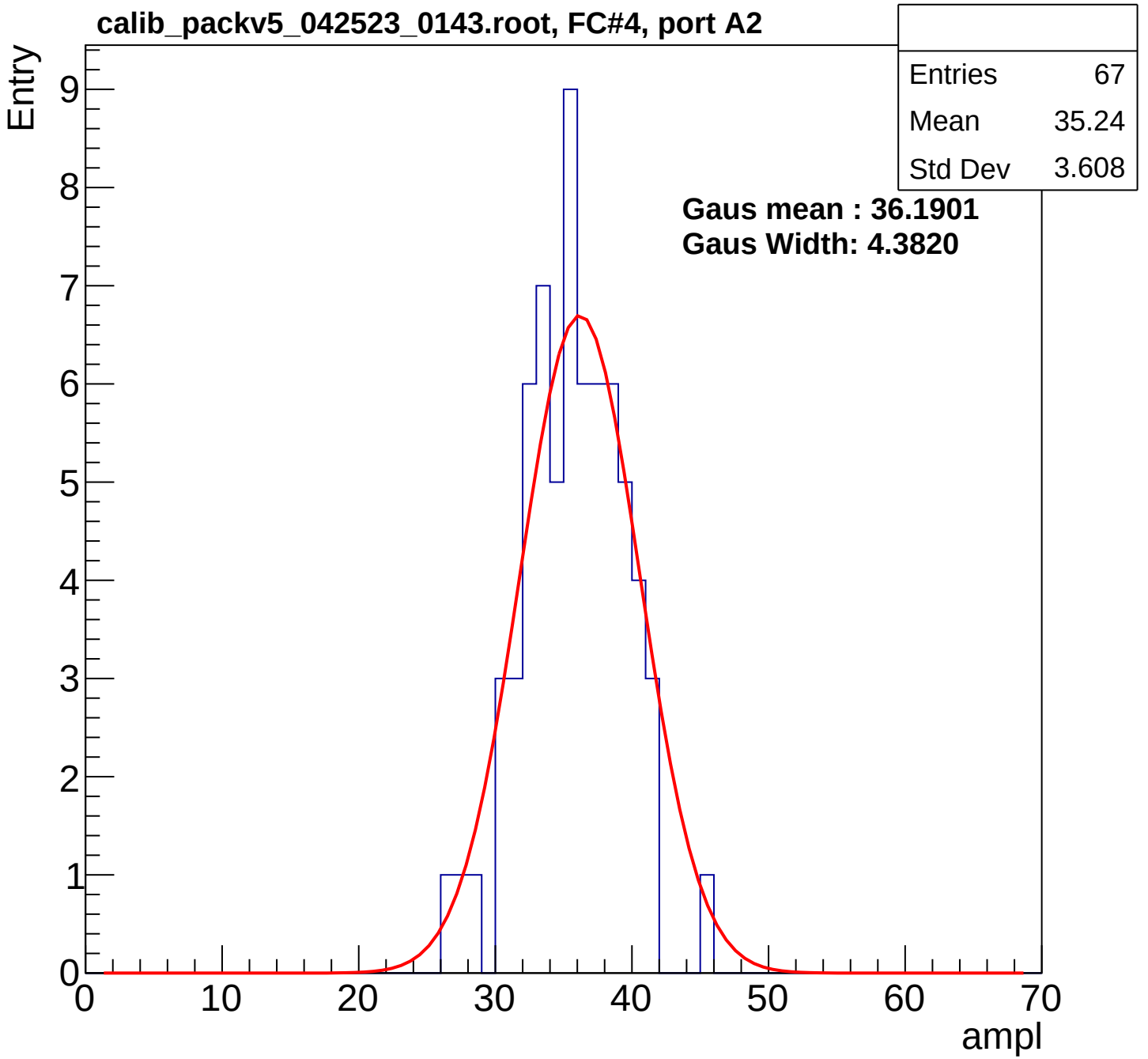
60
70


B1L100S, U20-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 35.24
Std Dev 3.608
Gaus mean : 36.1901
Gaus Width: 4.3820
Entry
ampl
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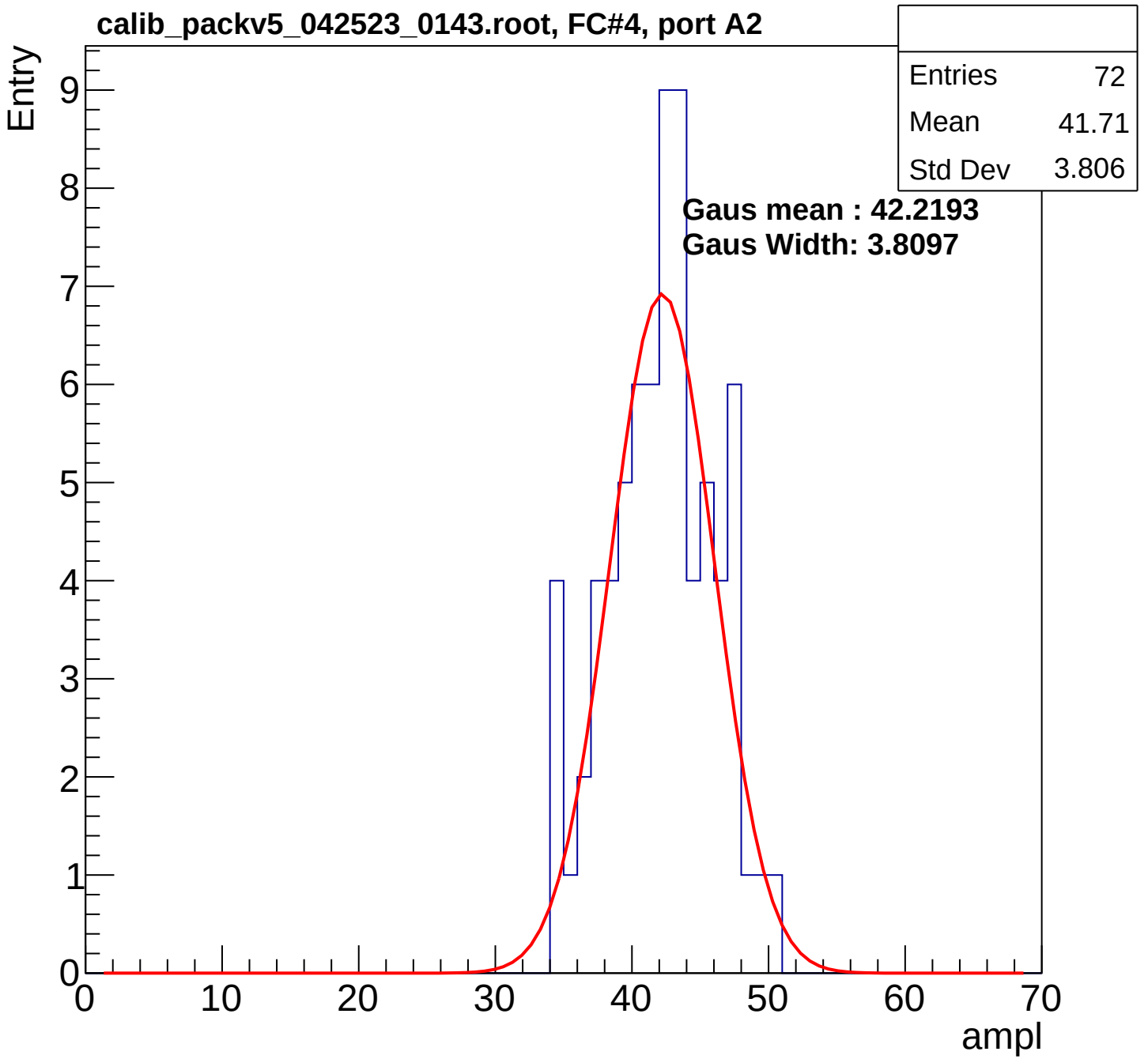
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 41.71
Std Dev 3.806
Gaus mean : 42.2193
Gaus Width: 3.8097
Entry
ampl
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2
3
4
5
6
7
8
9
0
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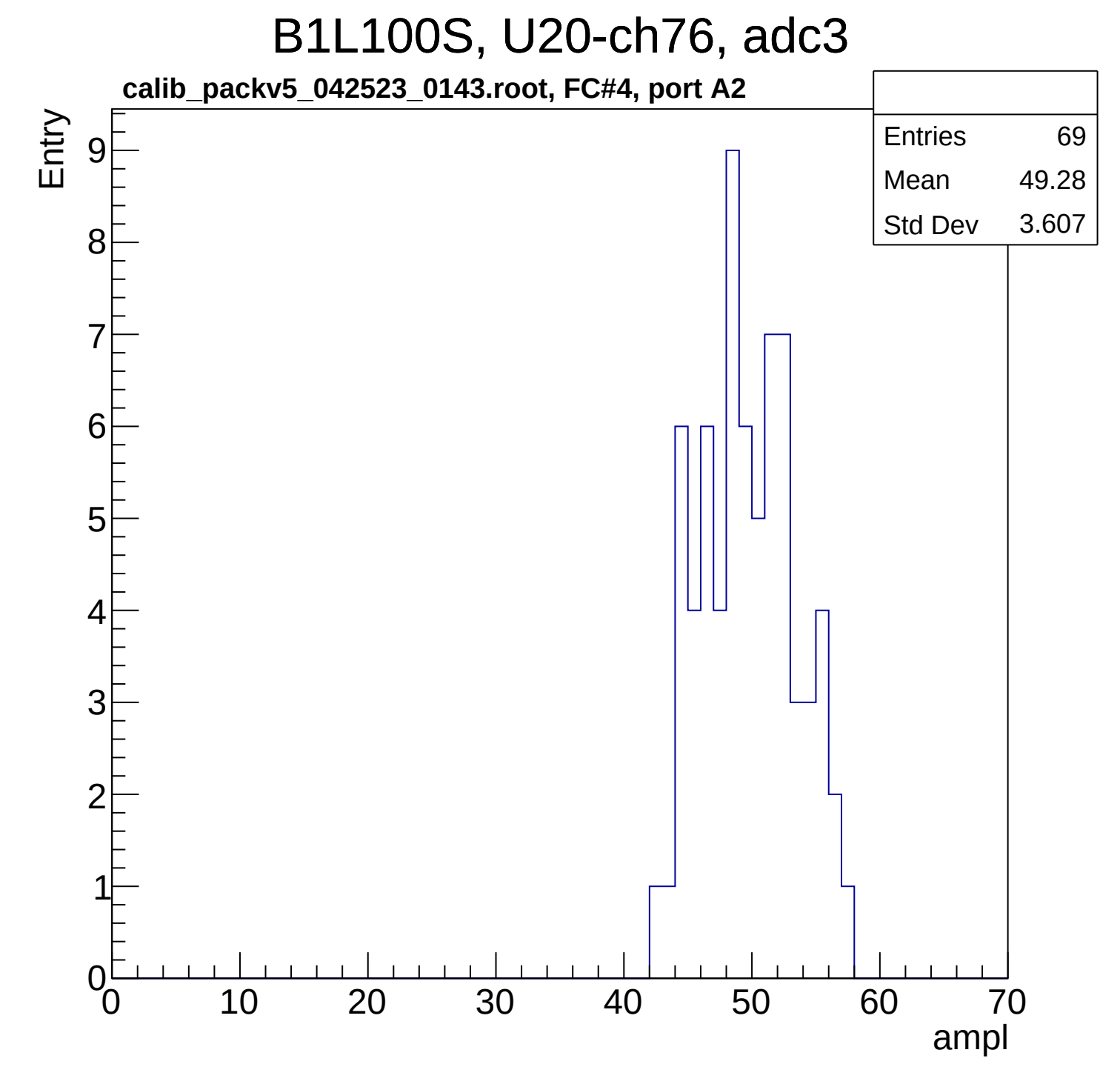
B1L100S, U20-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.28
Std Dev 3.607
Entry
ampl
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3
4
5
6
7
8
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0
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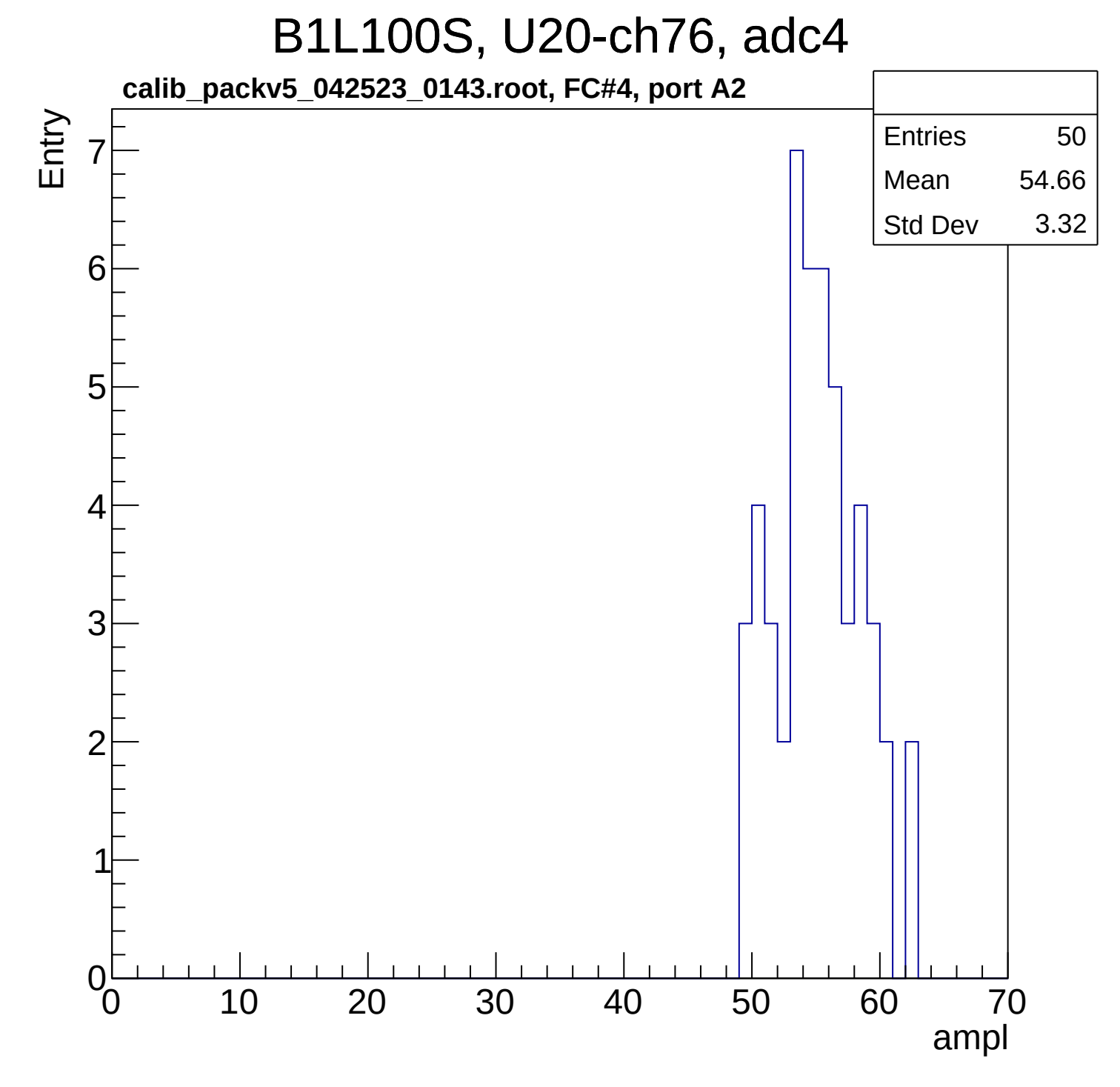
B1L100S, U20-ch76, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.66
Std Dev 3.32
Entry
ampl
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2
3
4
5
6
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0
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40
50
60
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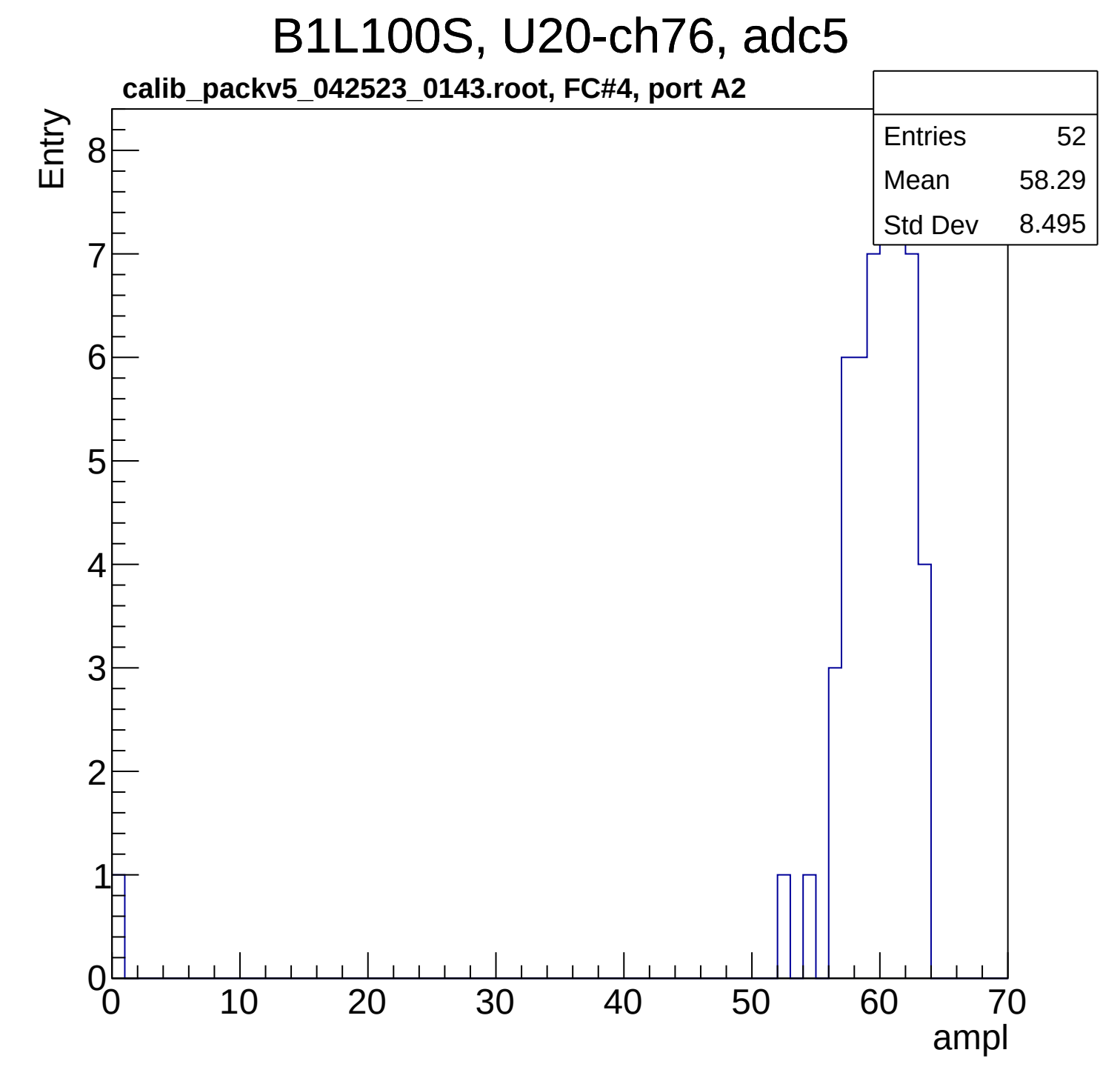
B1L100S, U20-ch76, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.29
Std Dev 8.495
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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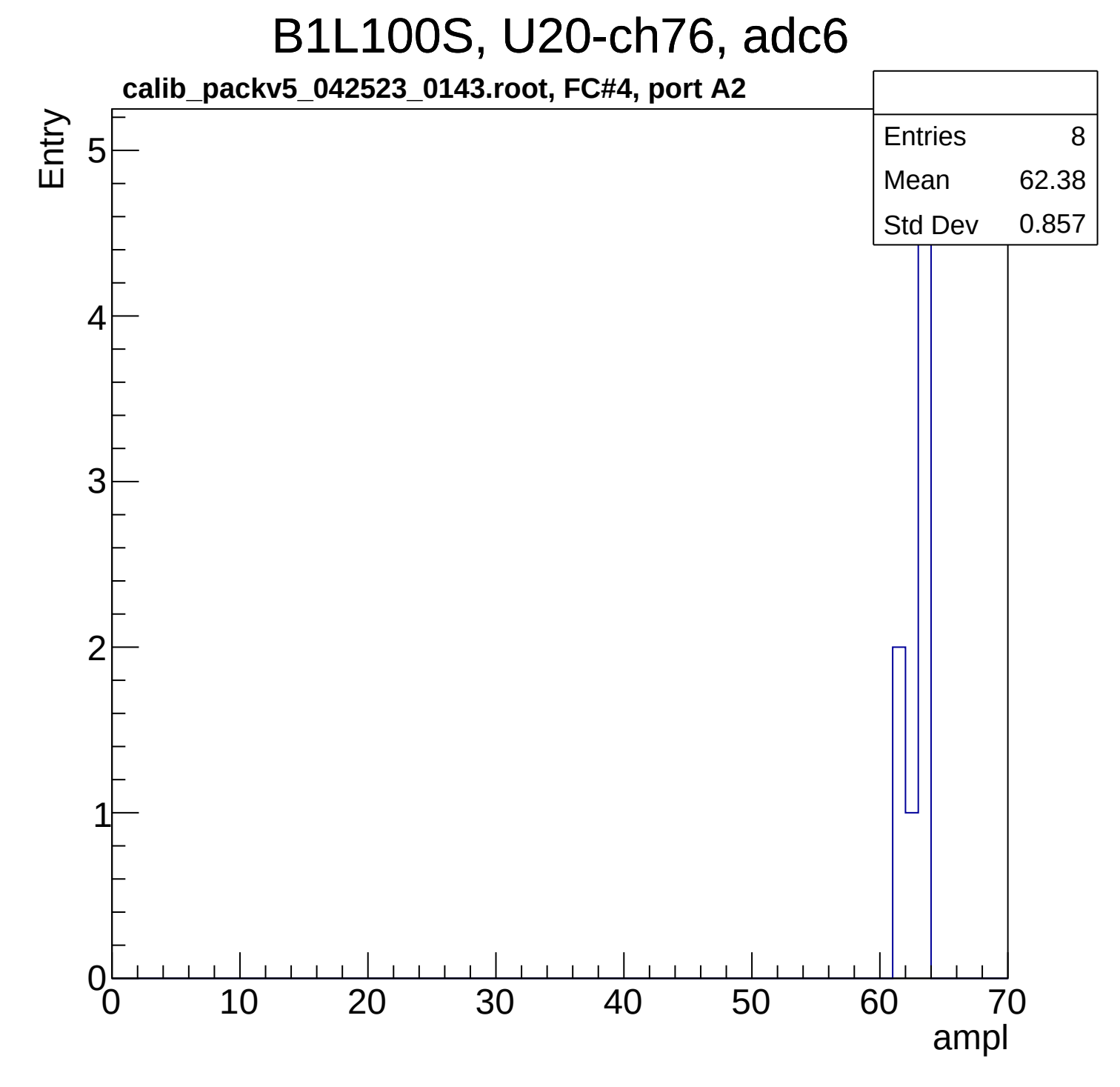
B1L100S, U20-ch76, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.38
Std Dev 0.857
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
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60
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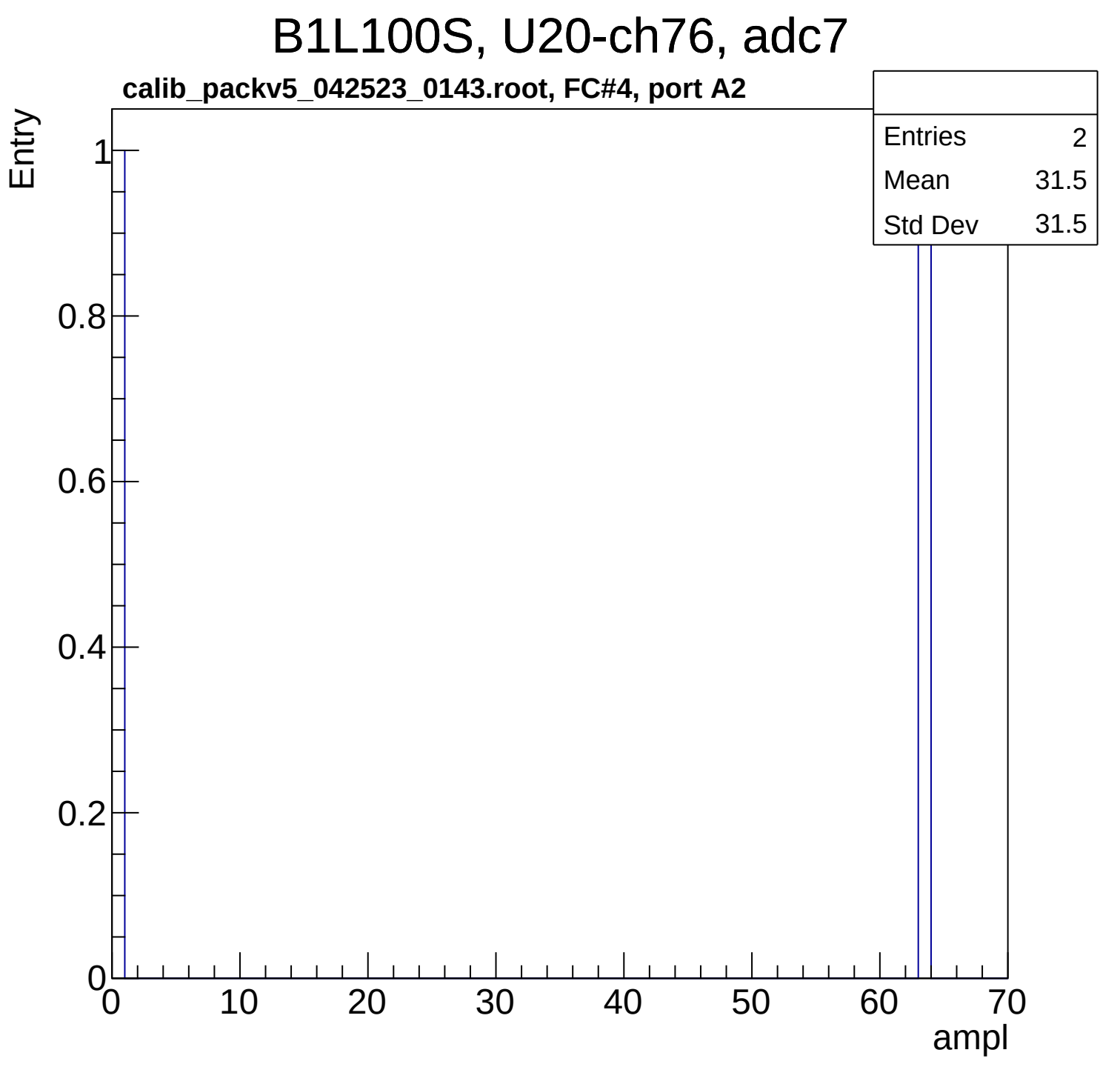
B1L100S, U20-ch76, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
0.2
0
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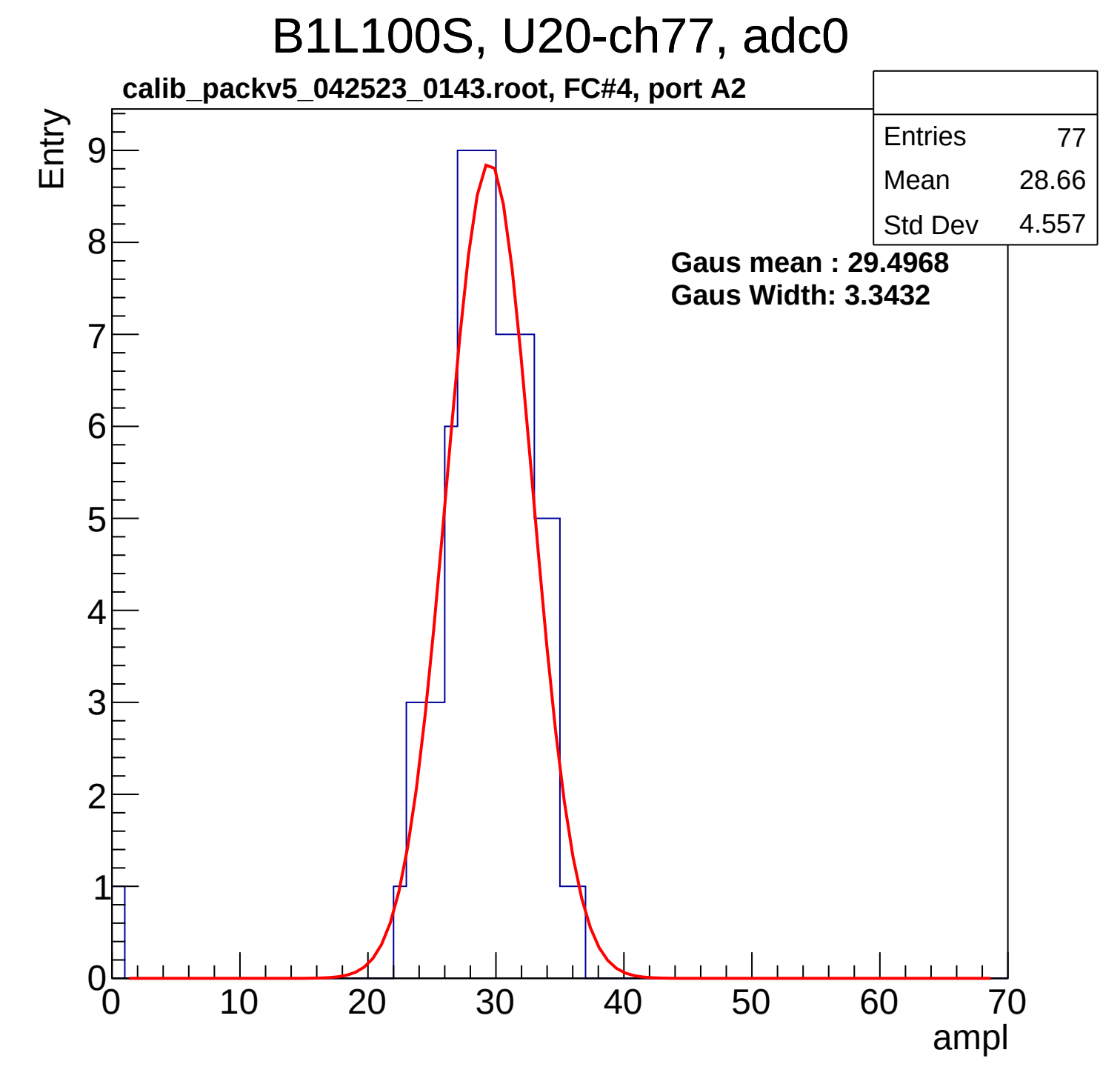
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.66
Std Dev 4.557
Gaus mean : 29.4968
Gaus Width: 3.3432
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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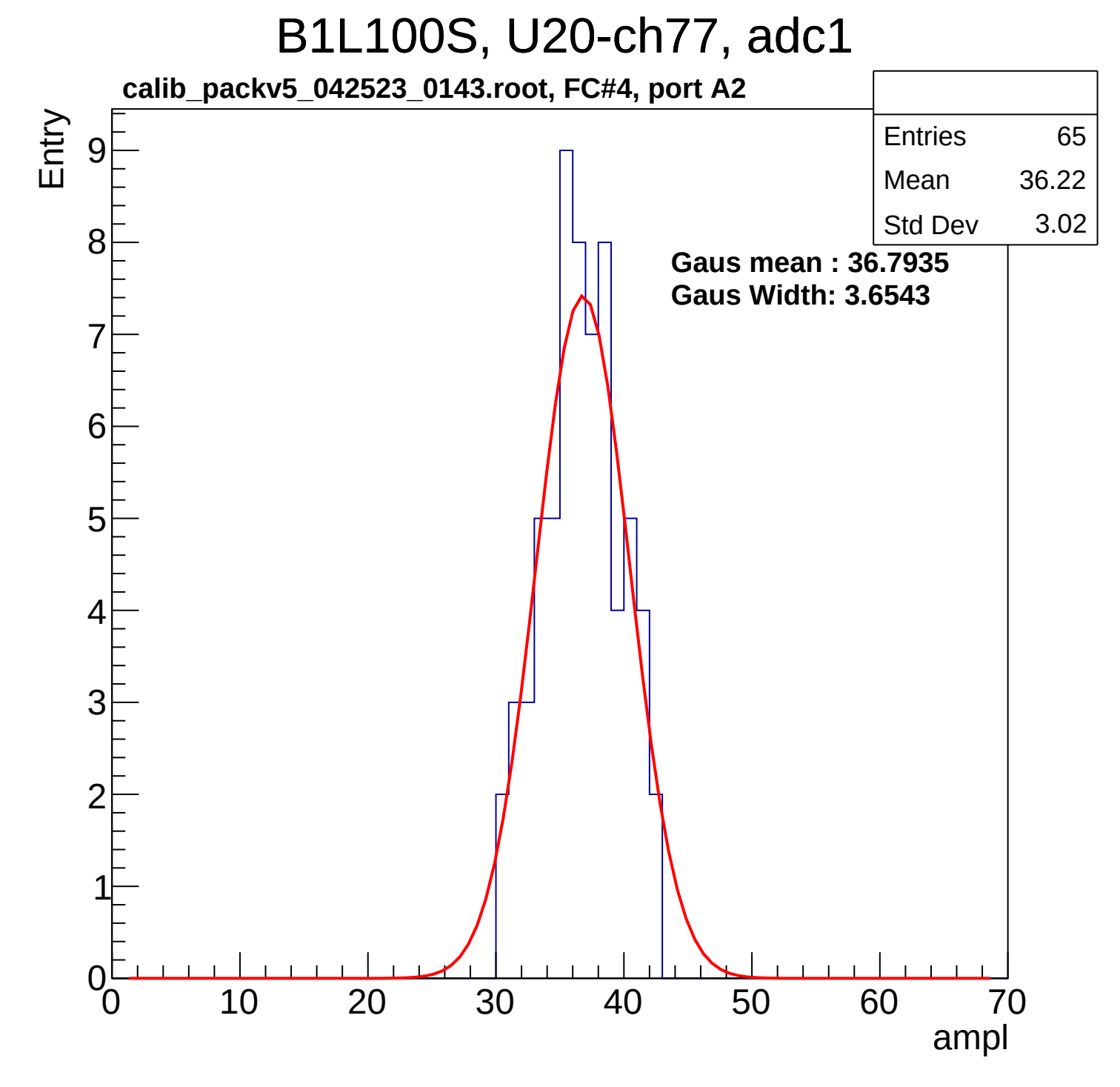
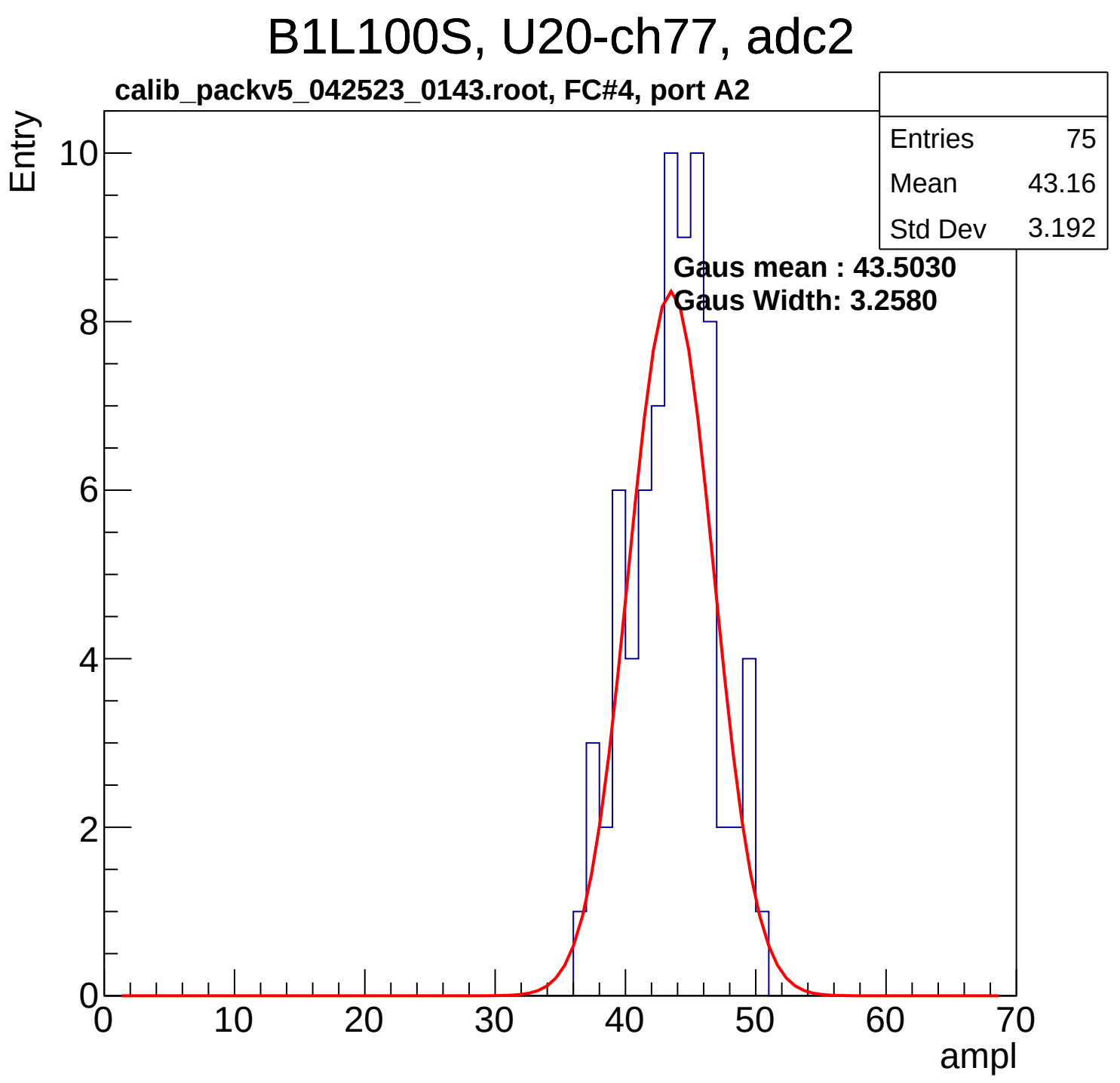

B1L100S, U20-ch77, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.22
Std Dev 3.02
Gaus mean : 36.7935
Gaus Width: 3.6543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.16
Std Dev 3.192
Gaus mean : 43.5030
Gaus Width: 3.2580
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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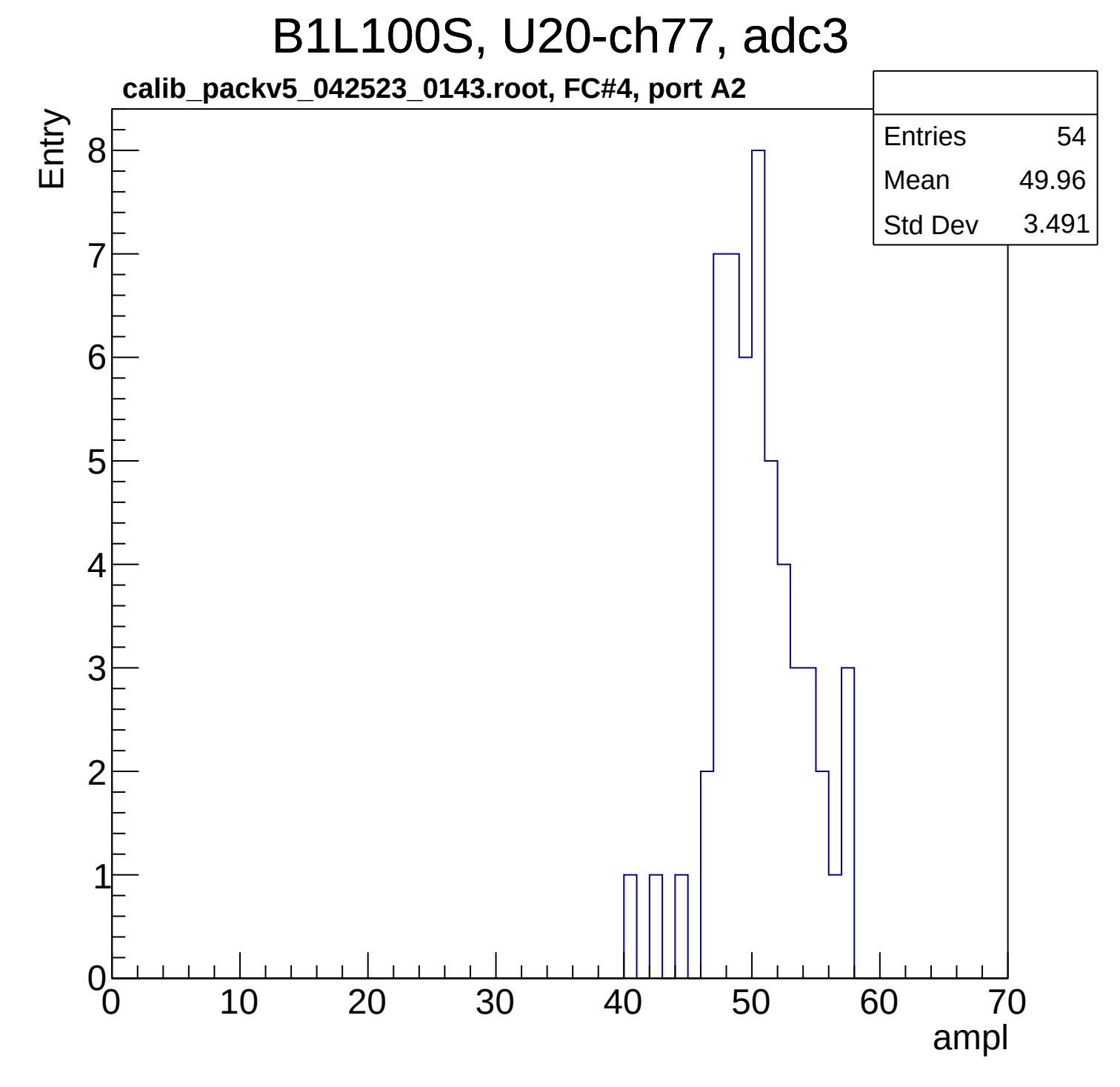
B1L100S, U20-ch77, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 49.96
Std Dev 3.491
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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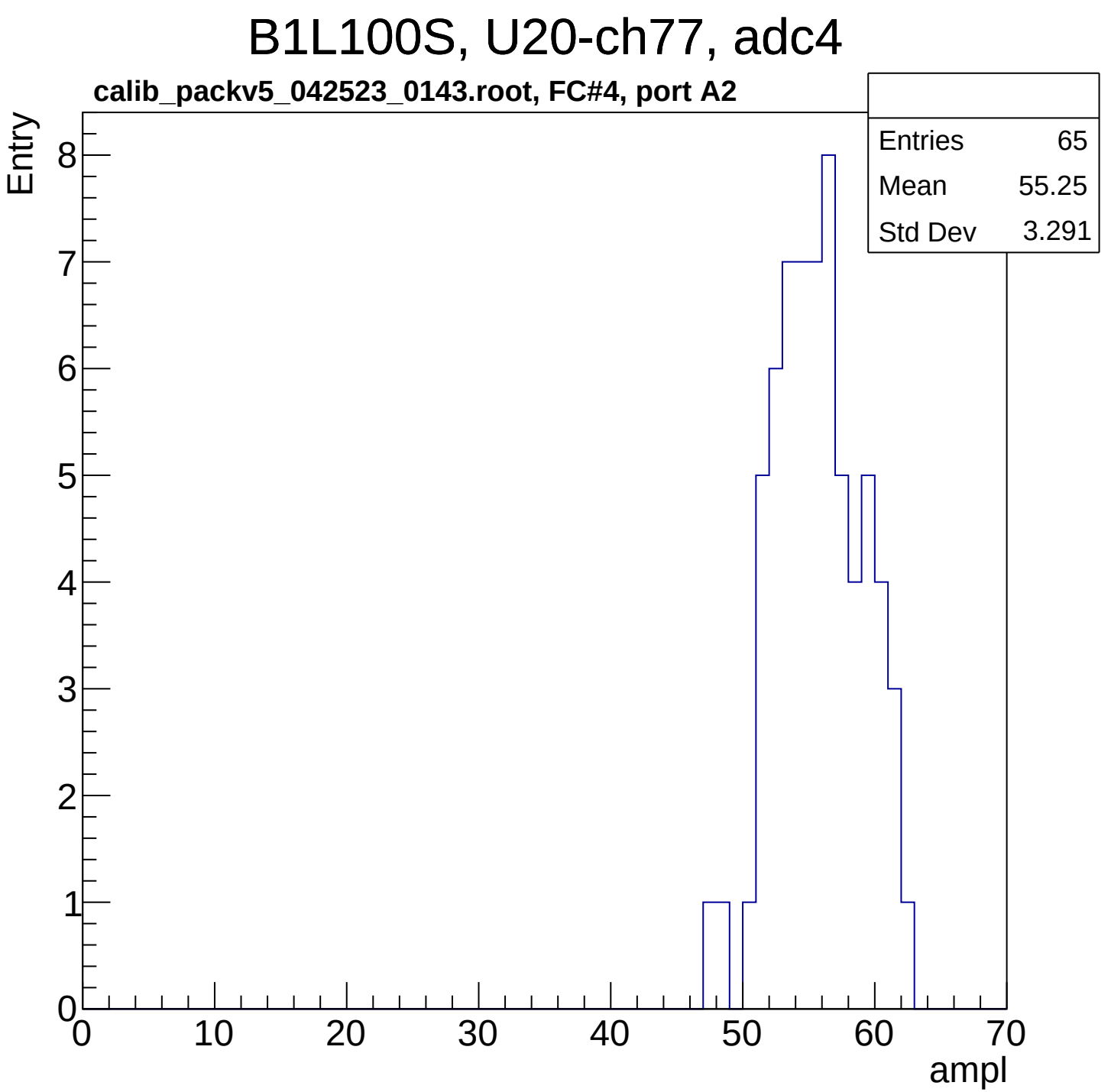
B1L100S, U20-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.25
Std Dev 3.291
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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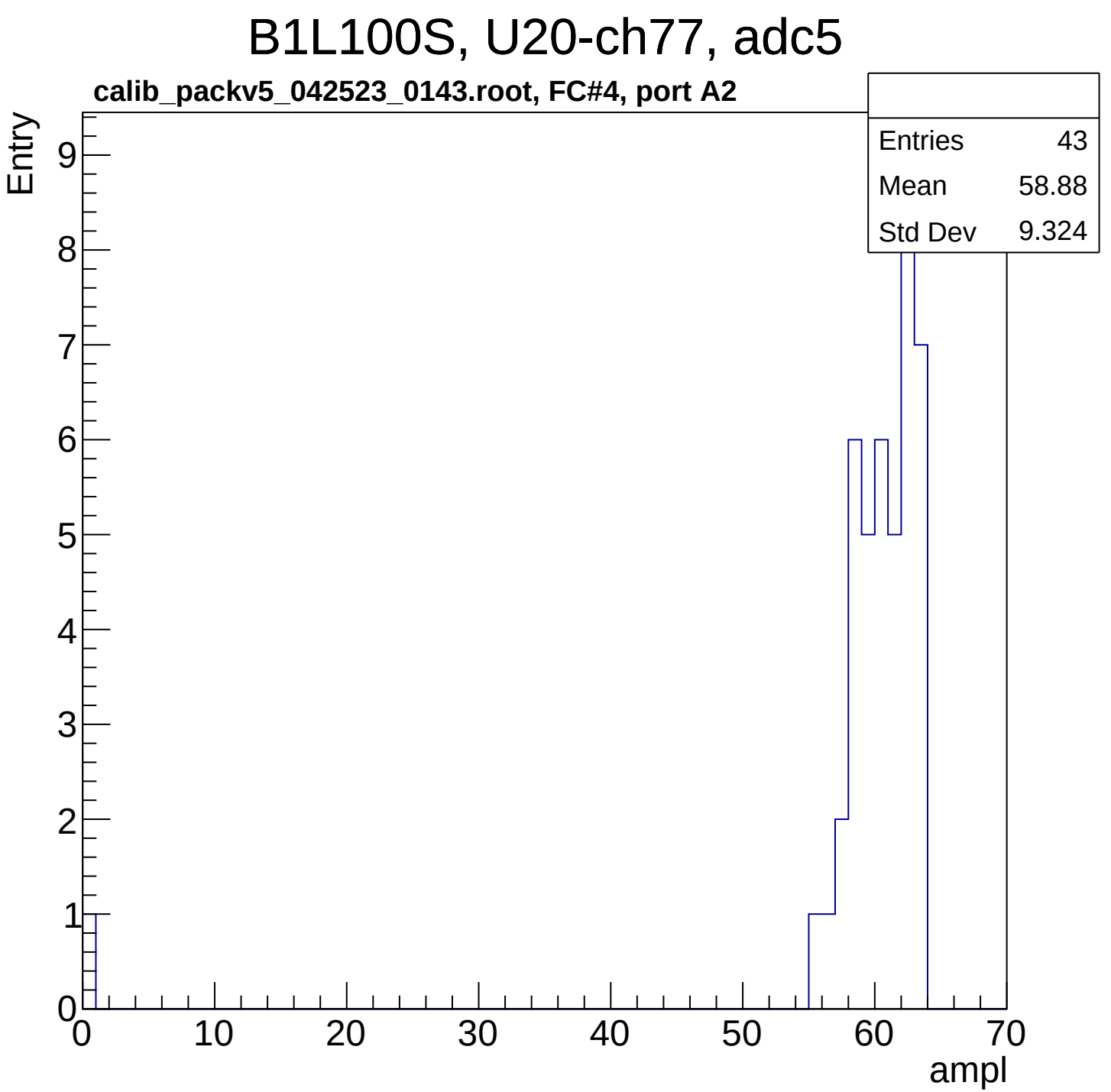
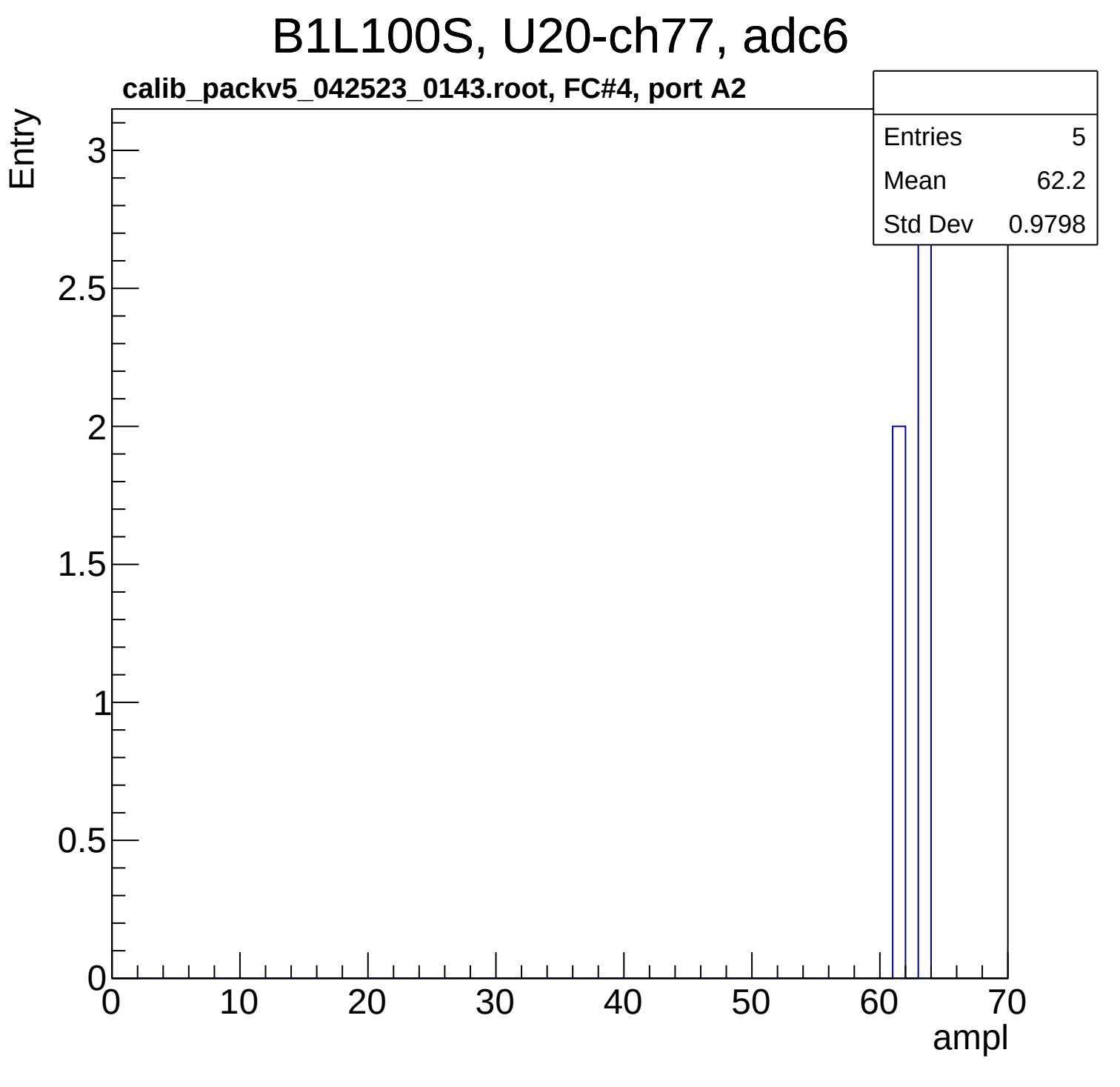
40
50
60
70


B1L100S, U20-ch77, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.88
Std Dev 9.324
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.2
Std Dev 0.9798
Entry
ampl
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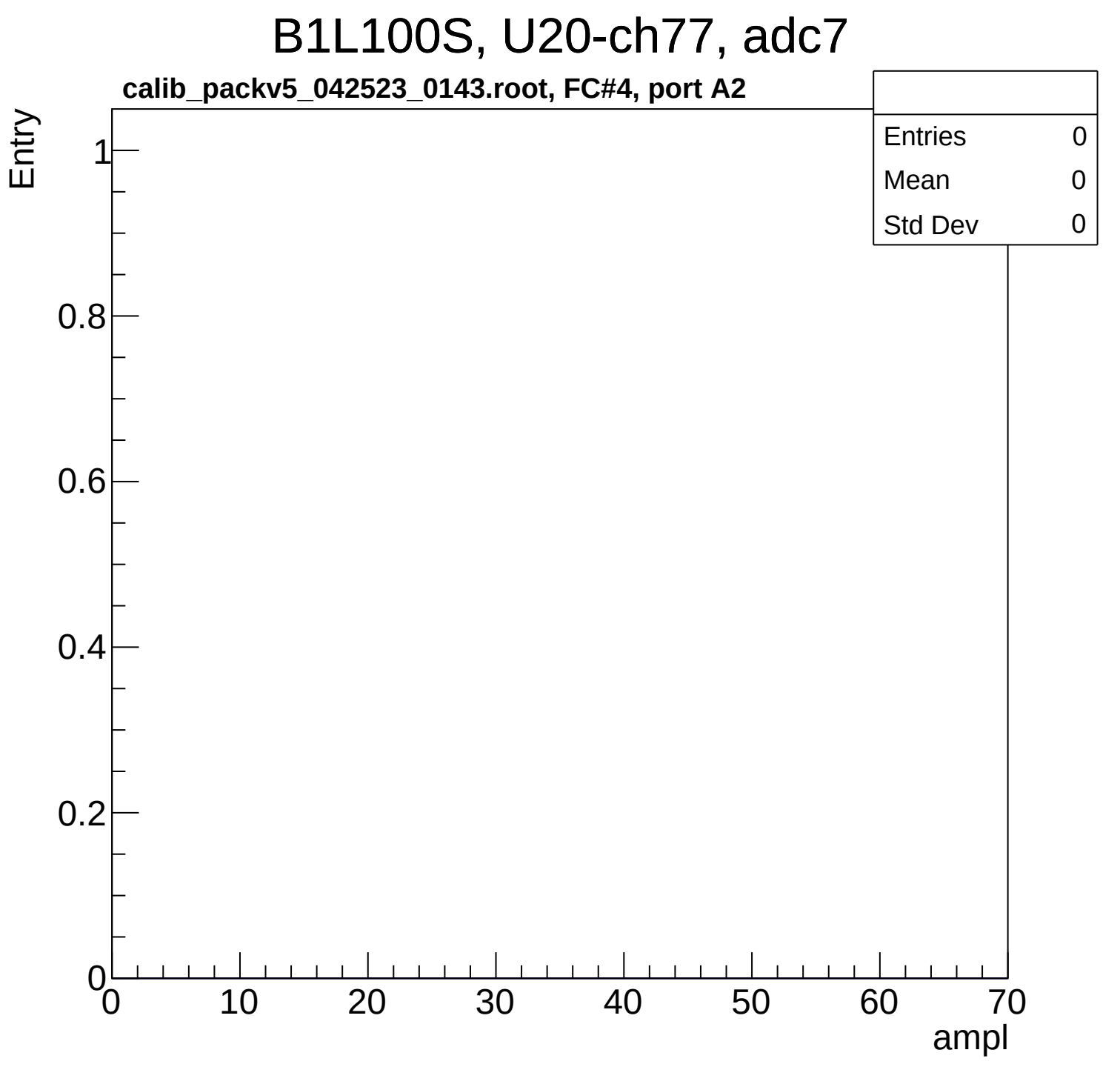
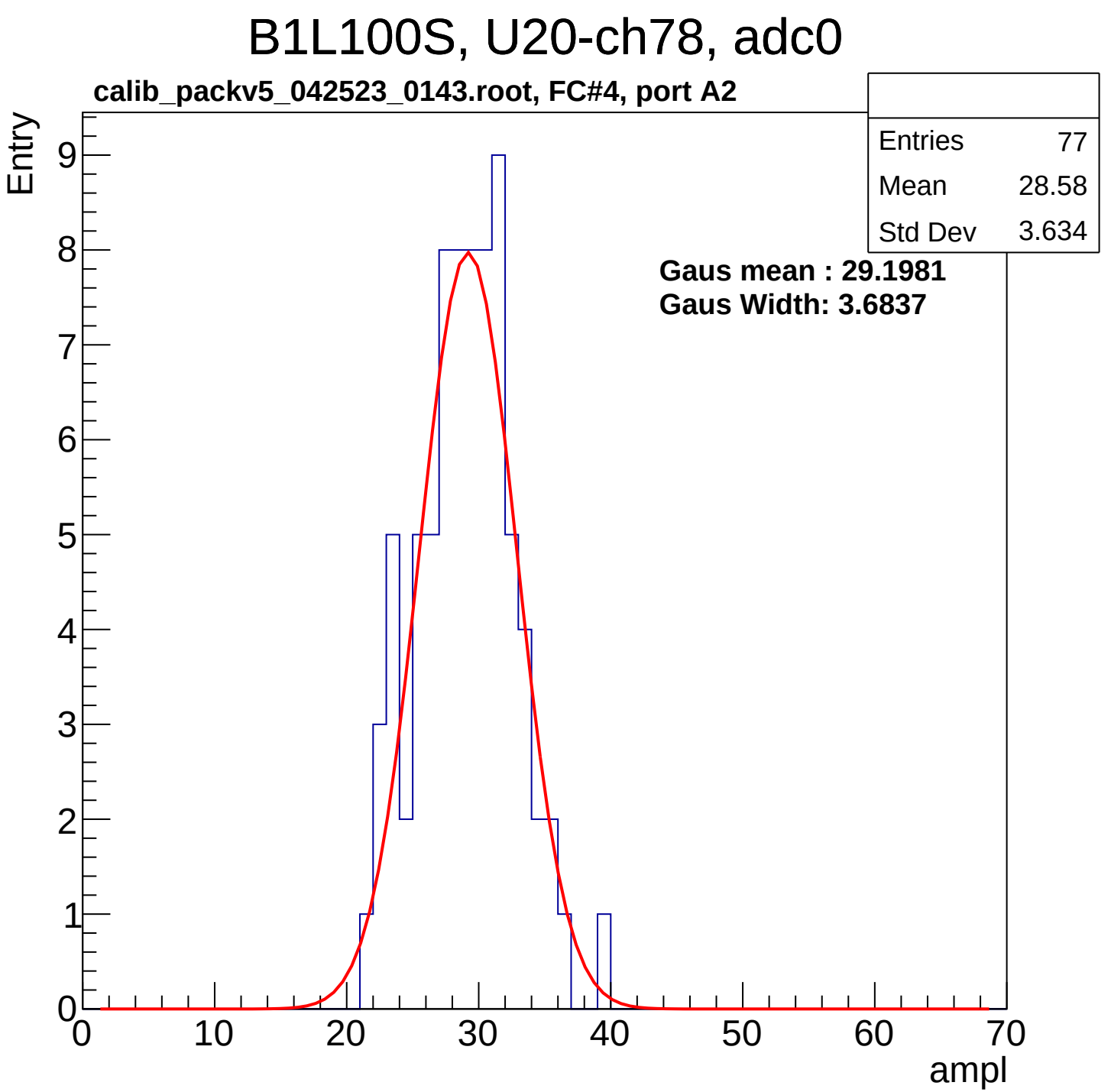
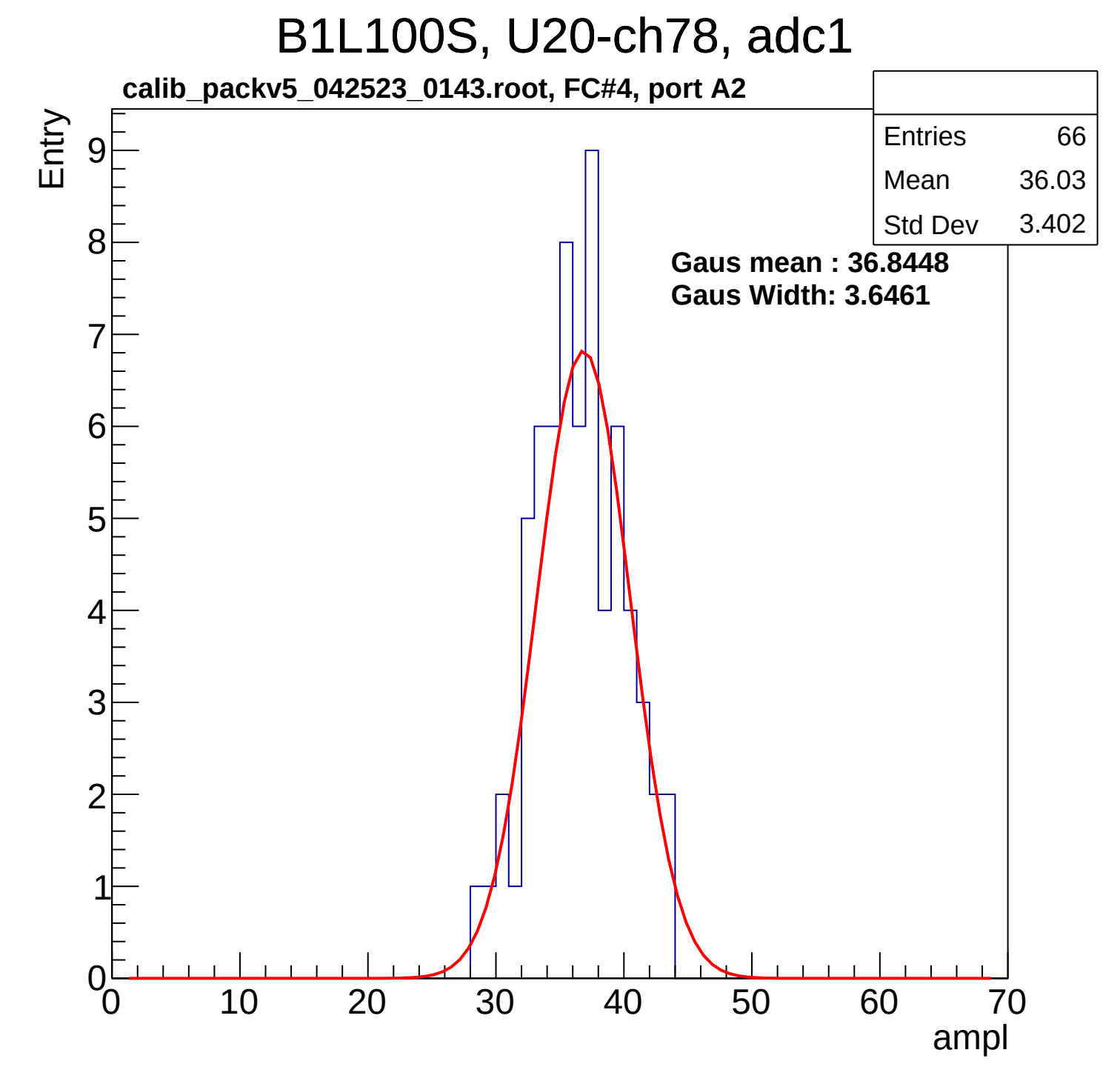
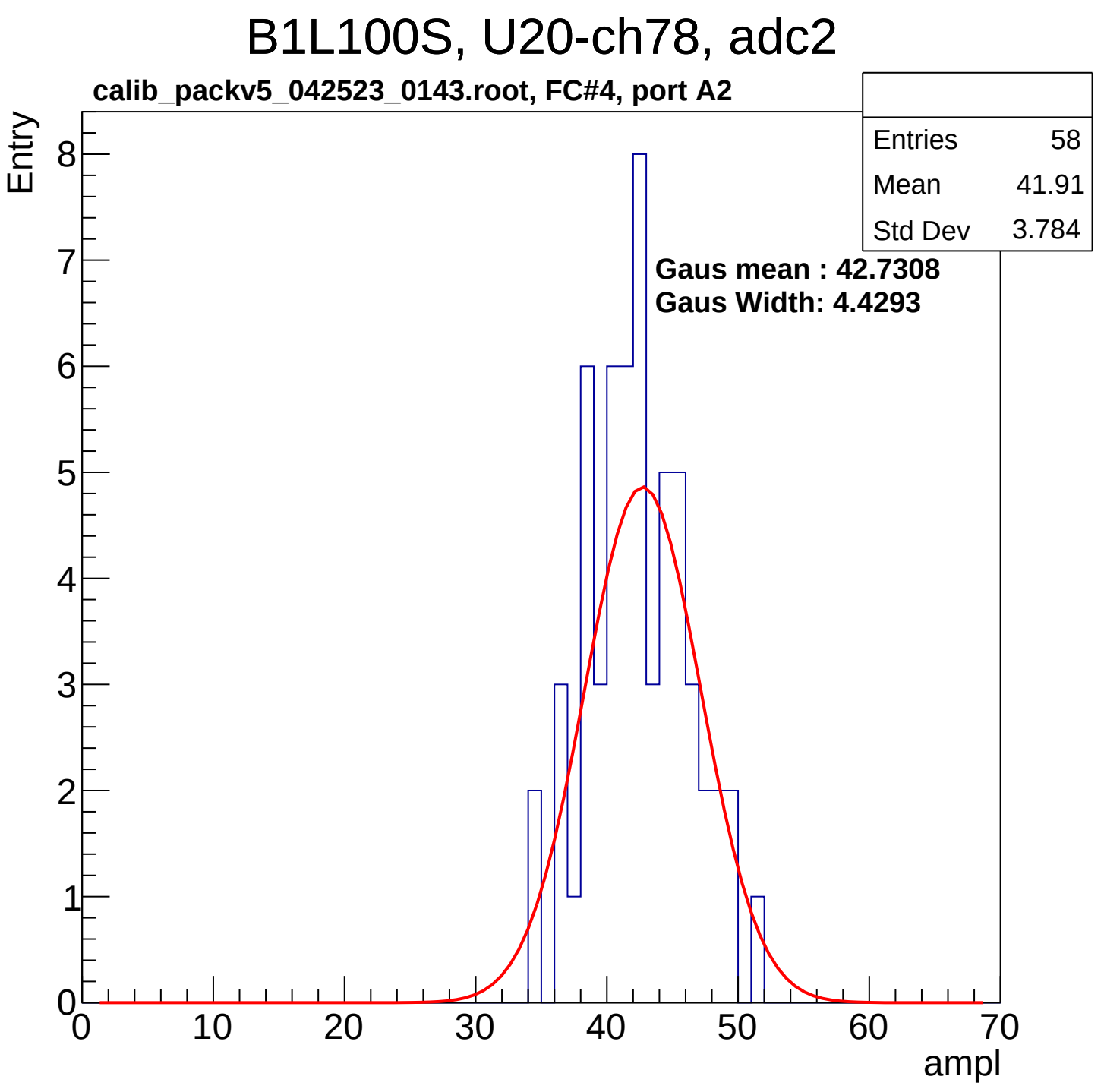
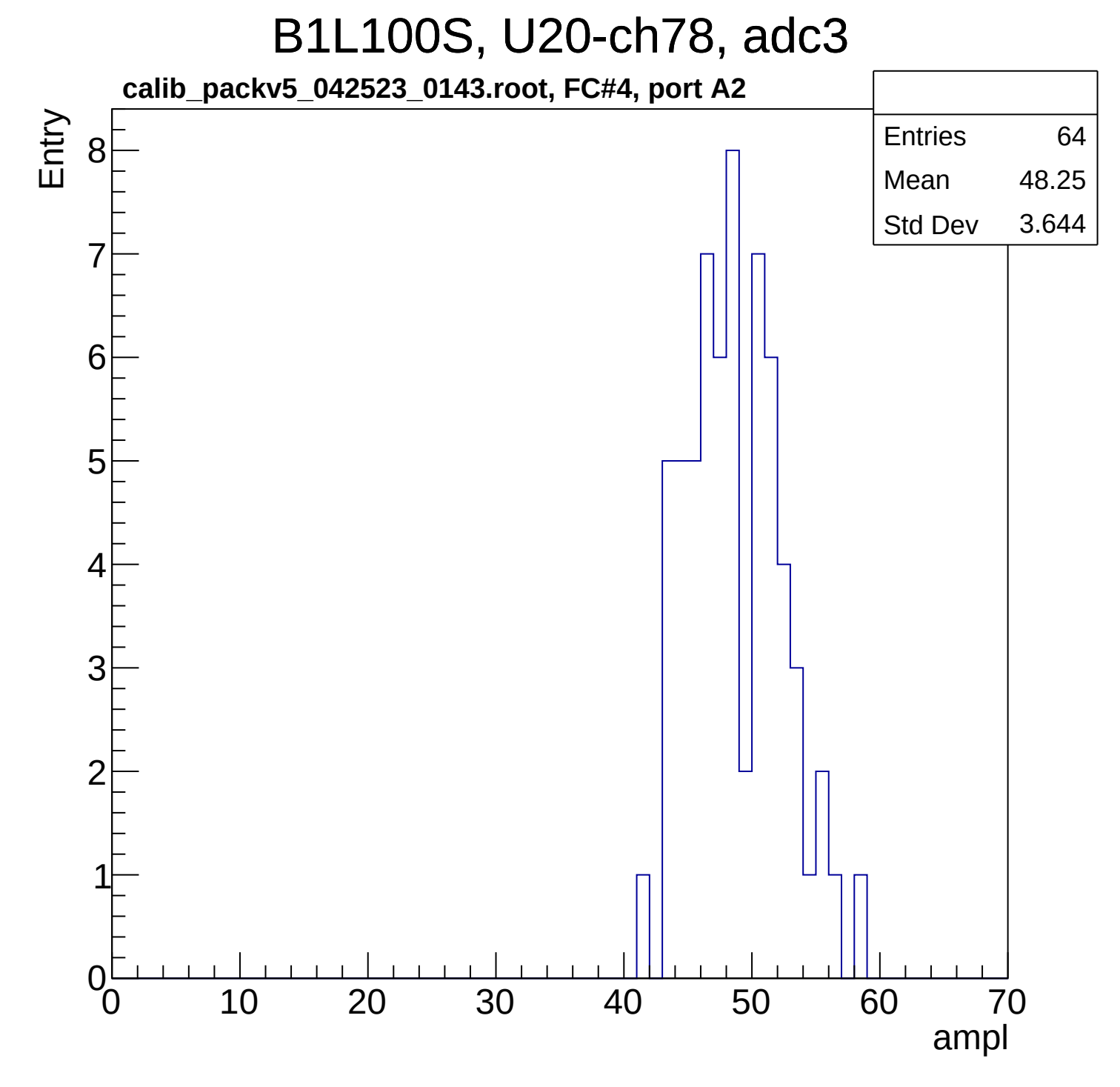
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

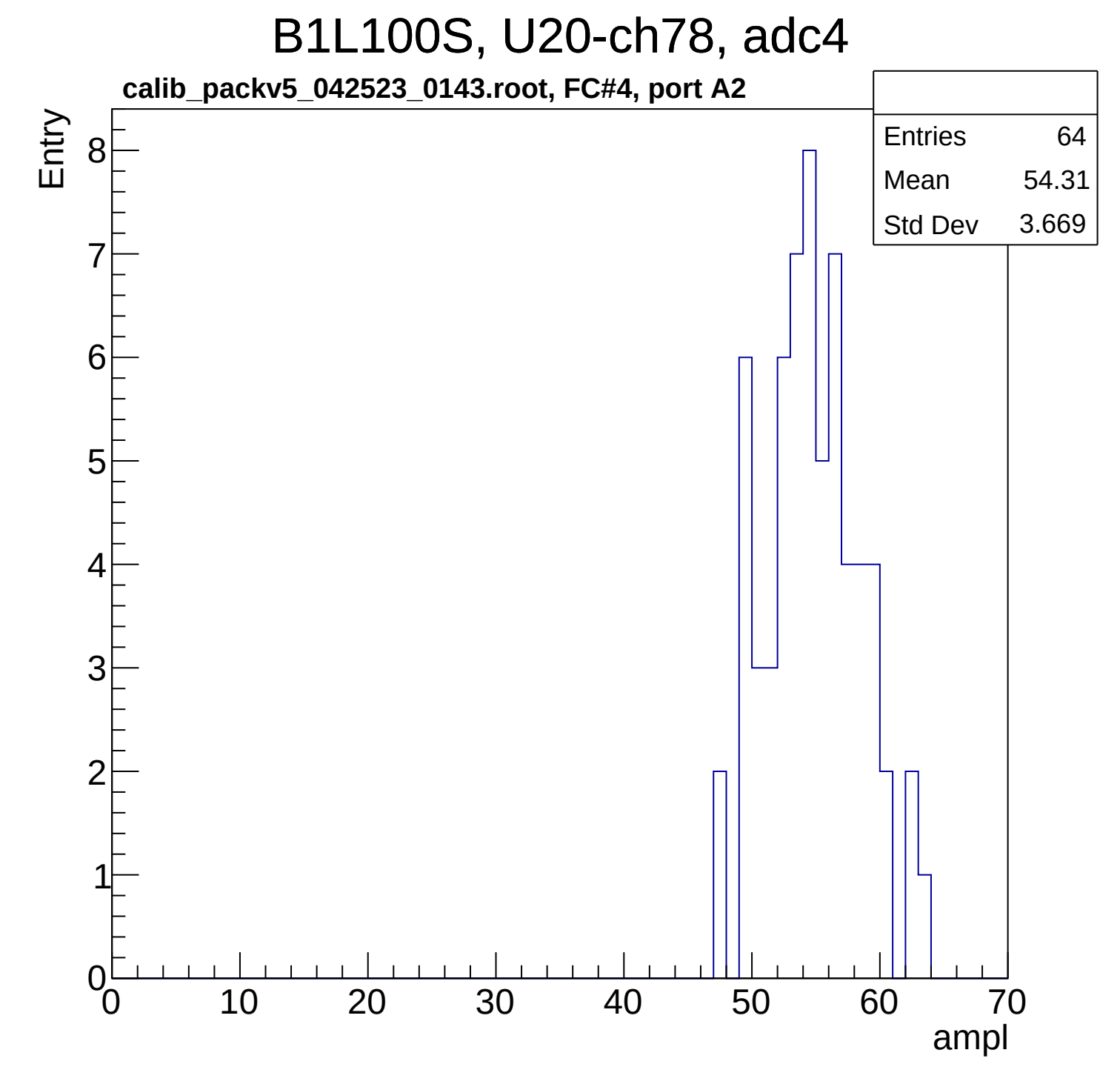

B1L100S, U20-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.58
Std Dev 3.634
Gaus mean : 29.1981
Gaus Width: 3.6837
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.03
Std Dev 3.402
Gaus mean : 36.8448
Gaus Width: 3.6461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 41.91
Std Dev 3.784
Gaus mean : 42.7308
Gaus Width: 4.4293
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch78, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 48.25
Std Dev 3.644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.31
Std Dev 3.669
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
50
60
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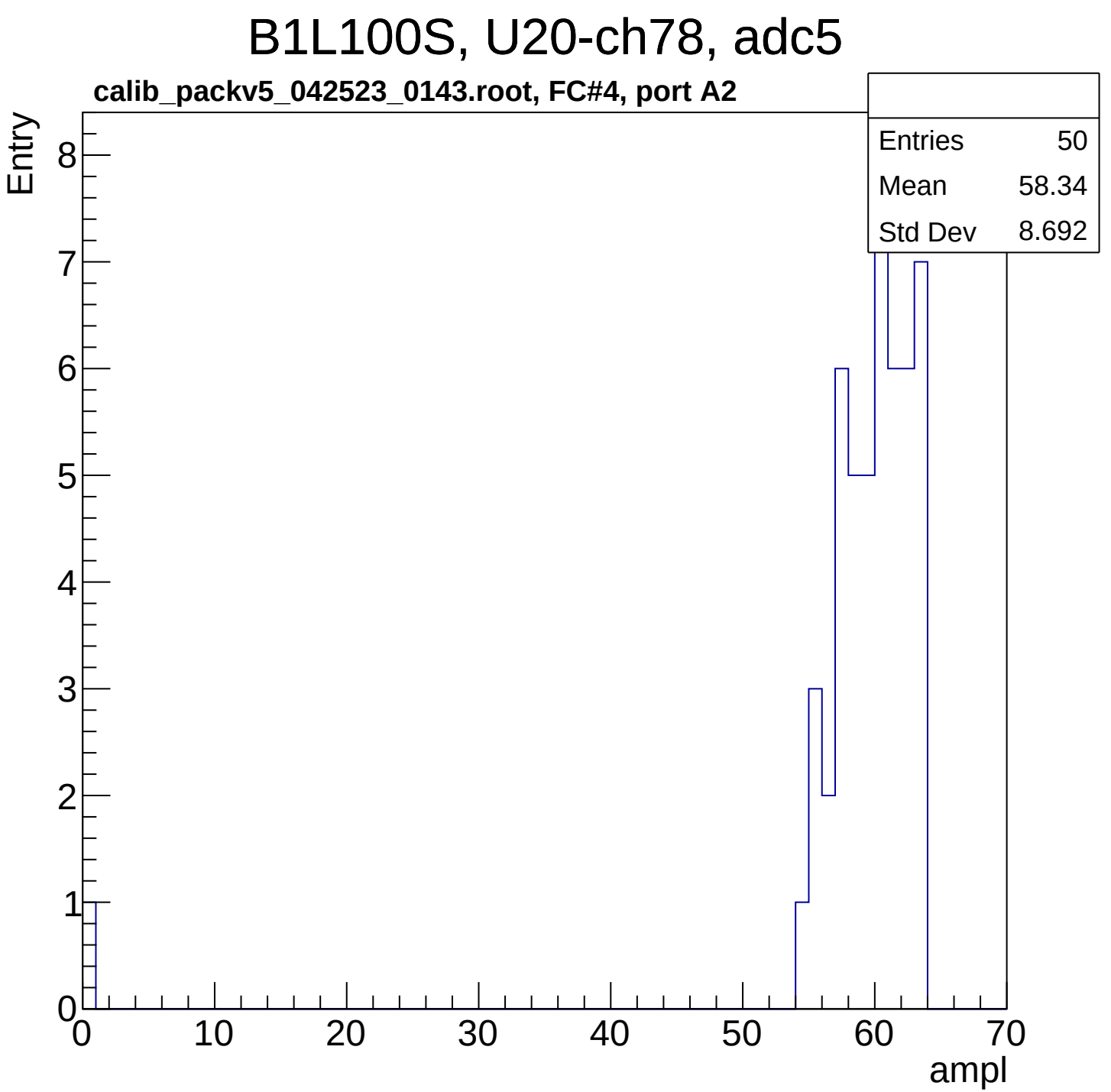
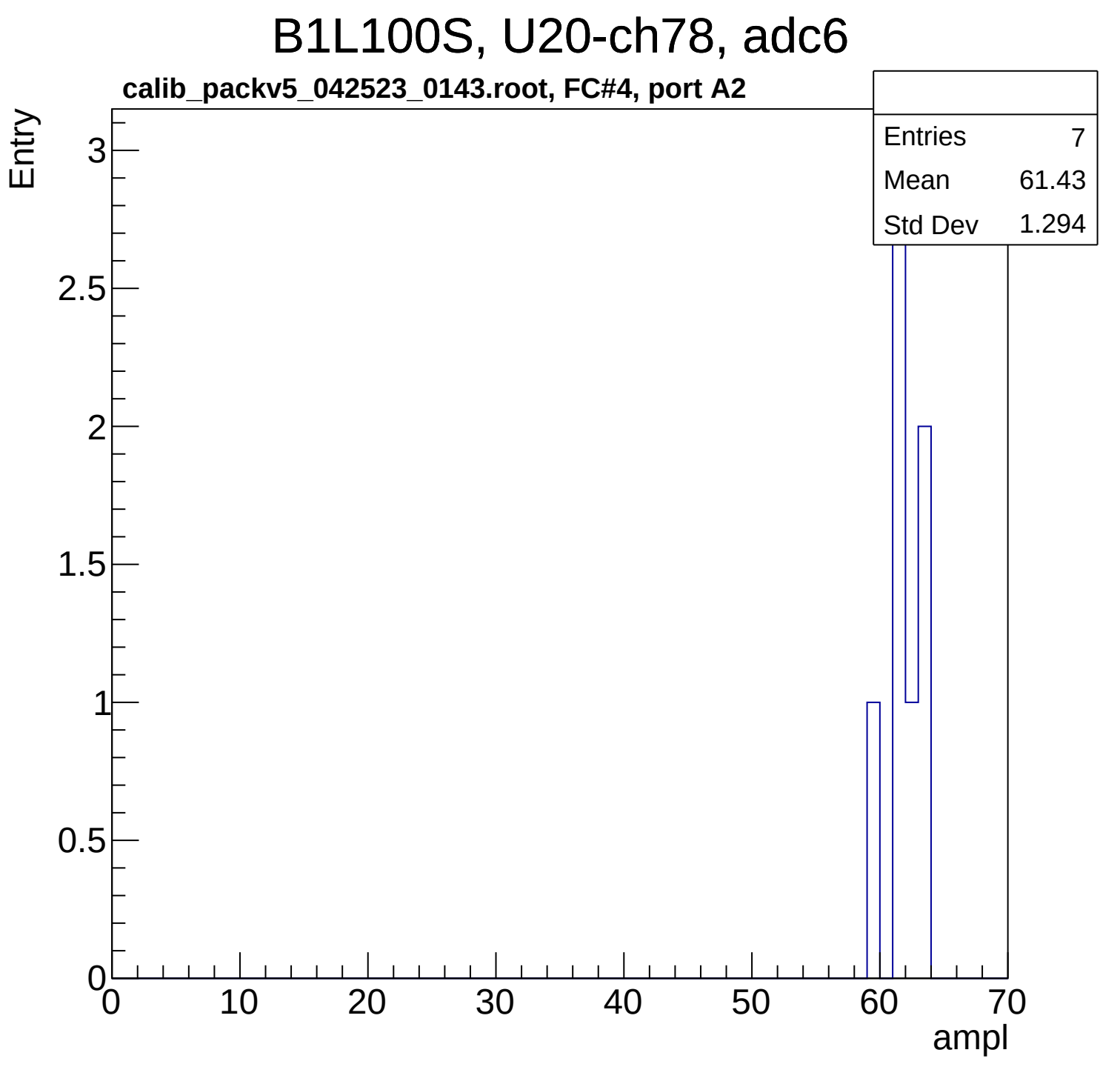

B1L100S, U20-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.34
Std Dev 8.692
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.43
Std Dev 1.294
Entry
ampl
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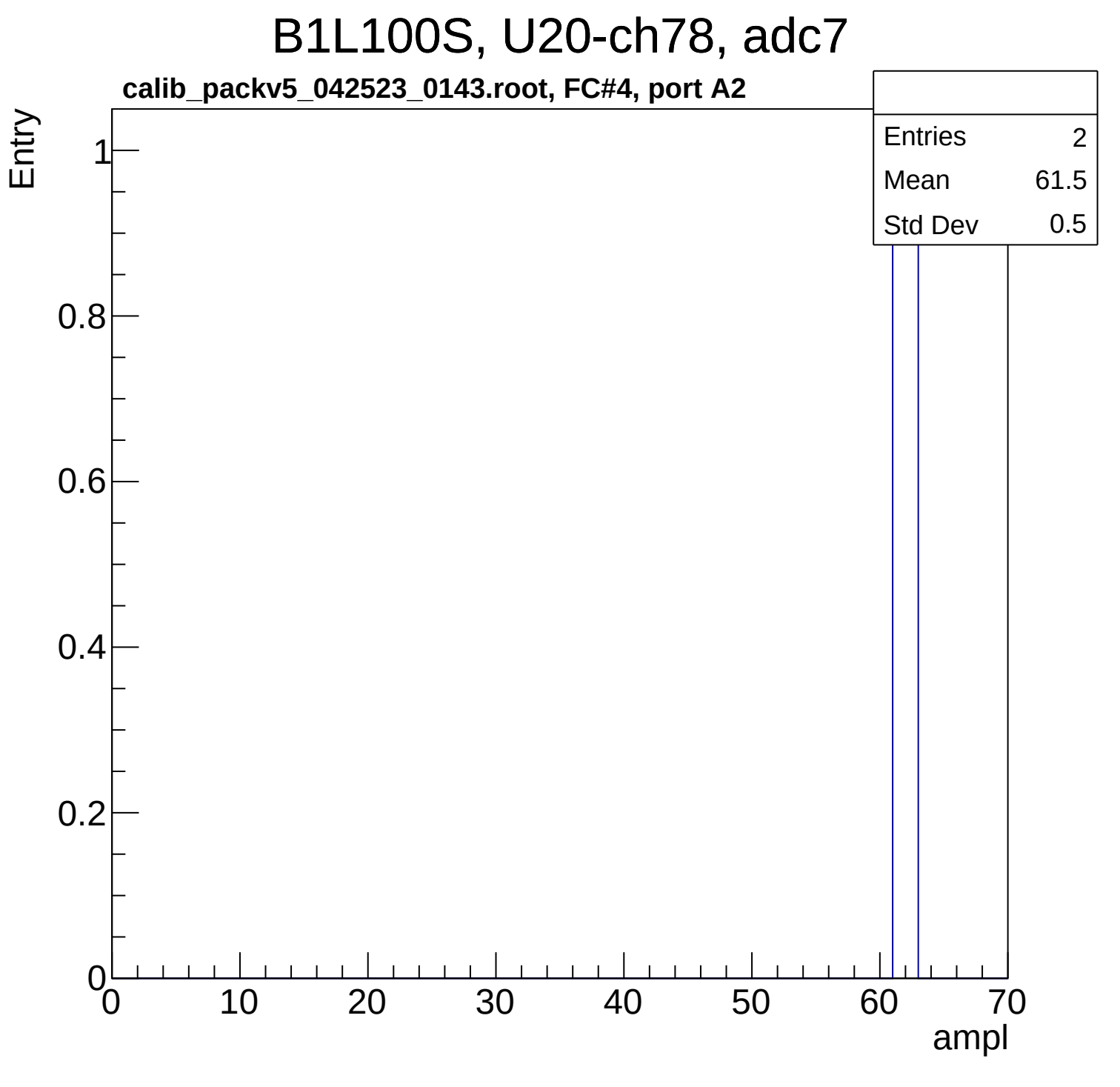
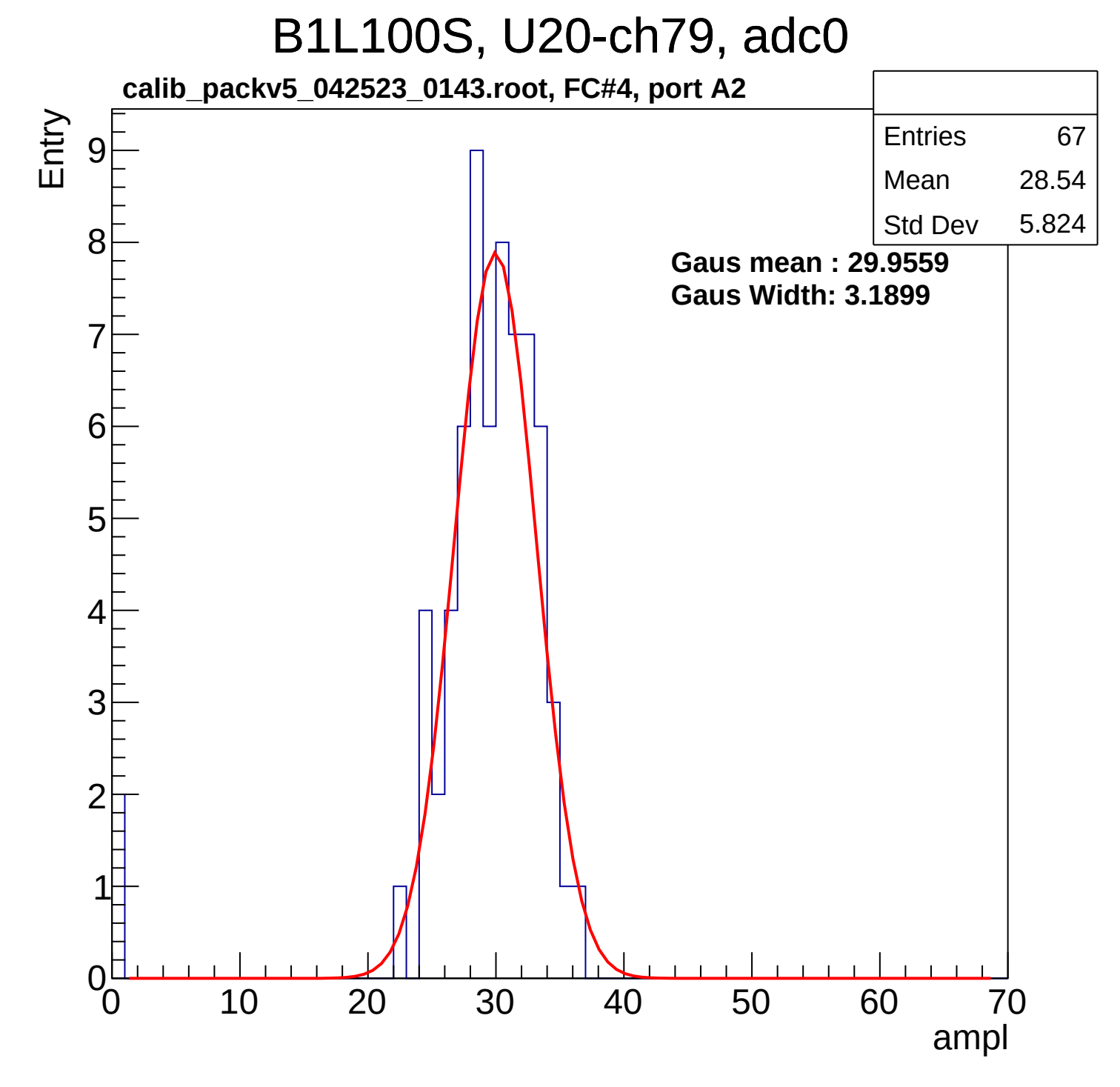
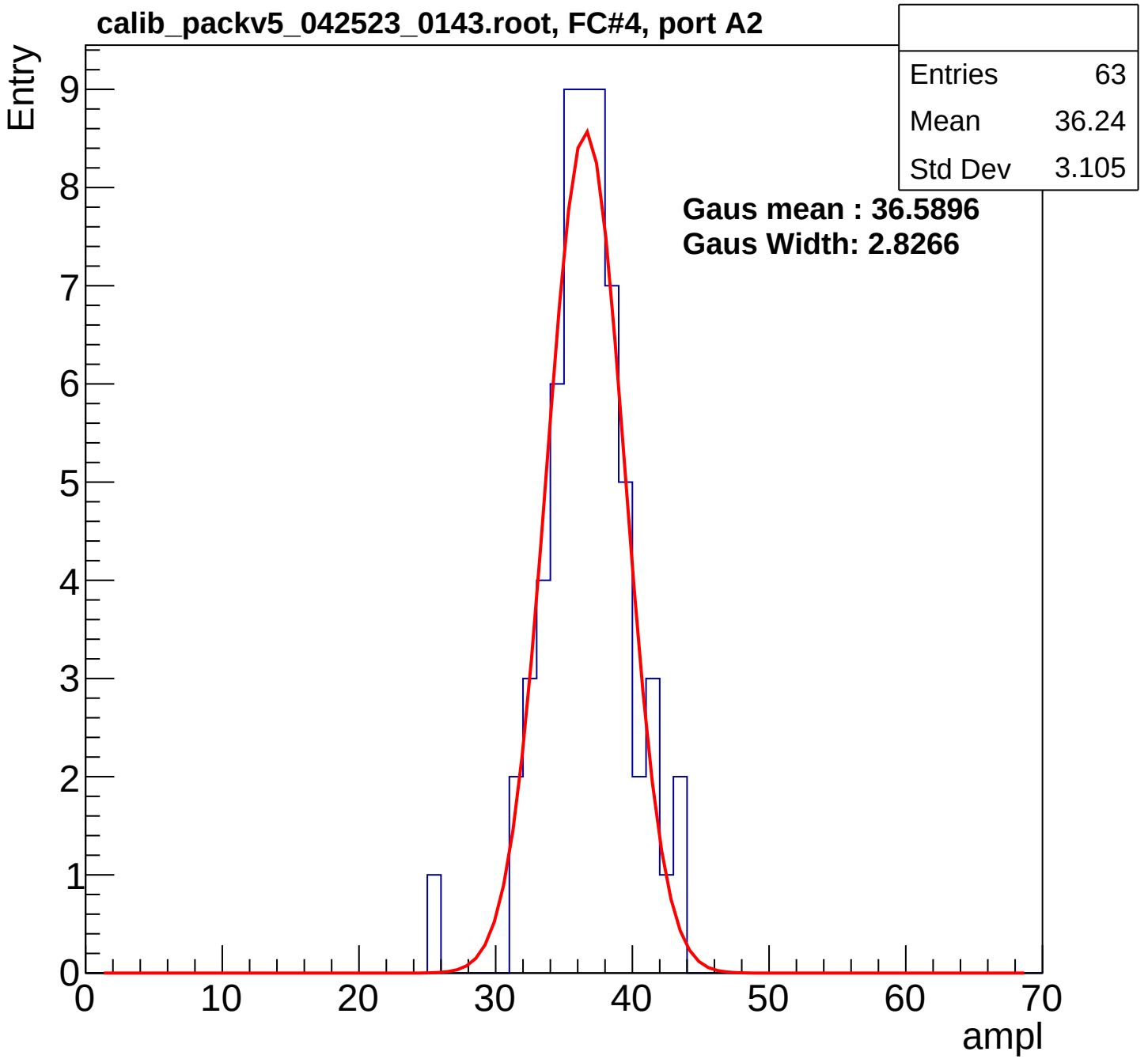
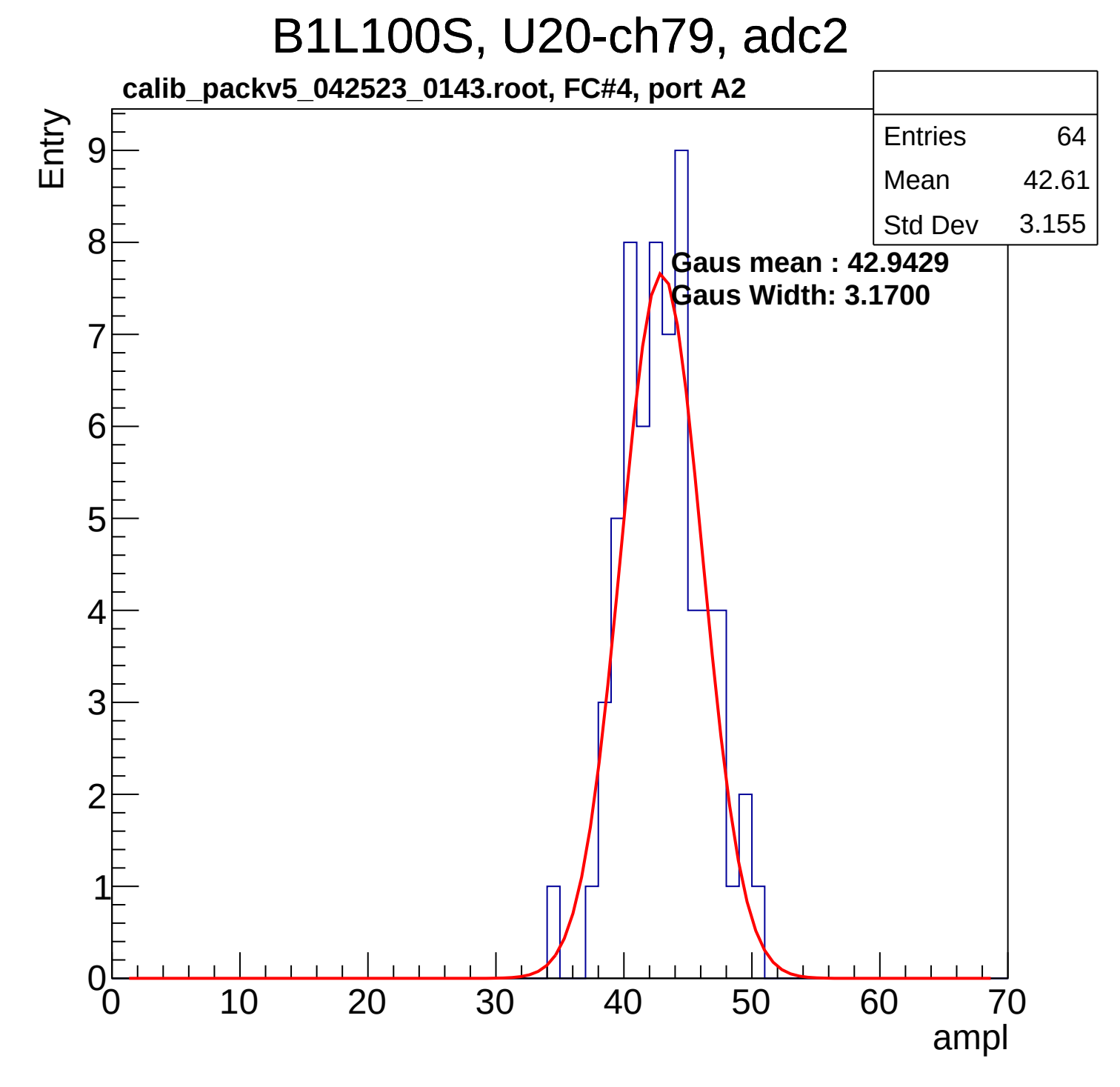
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.54
Std Dev 5.824
Gaus mean : 29.9559
Gaus Width: 3.1899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 36.24
Std Dev 3.105
Gaus mean : 36.5896
Gaus Width: 2.8266
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 42.61
Std Dev 3.155
Gaus mean : 42.9429
Gaus Width: 3.1700
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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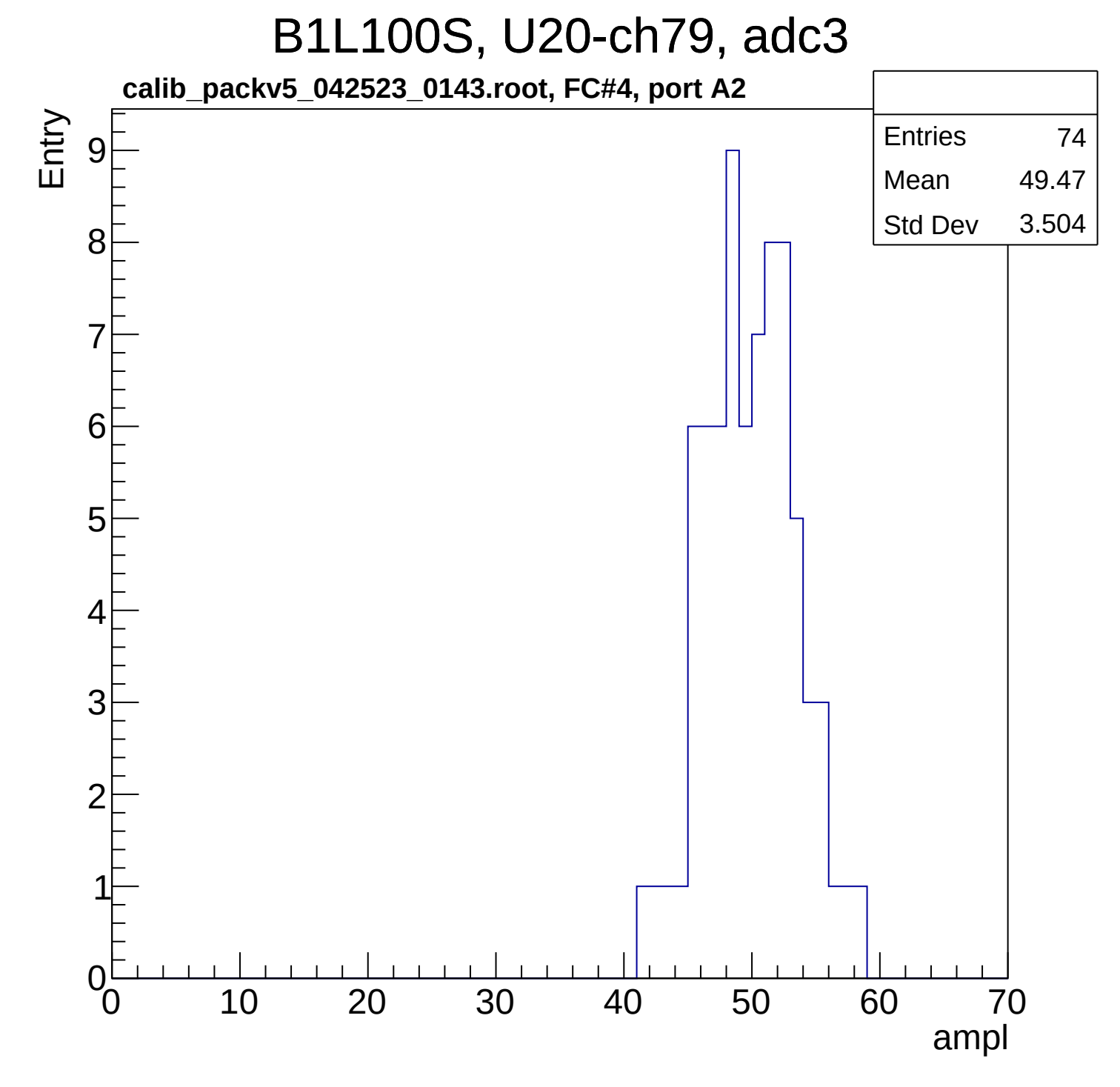
40
50
60
70


B1L100S, U20-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.47
Std Dev 3.504
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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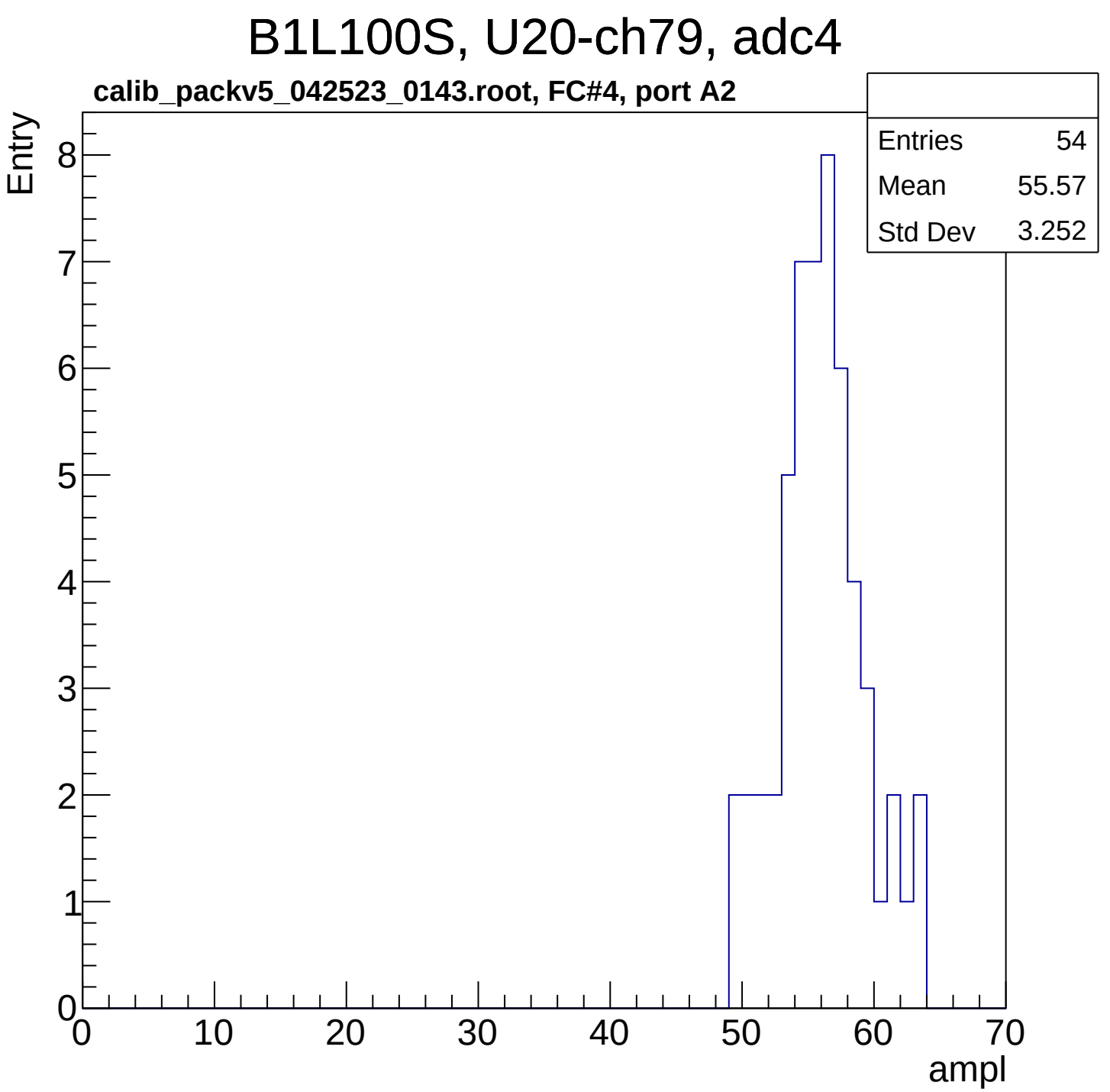
40
50
60
70


B1L100S, U20-ch79, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.57
Std Dev 3.252
Entry
ampl
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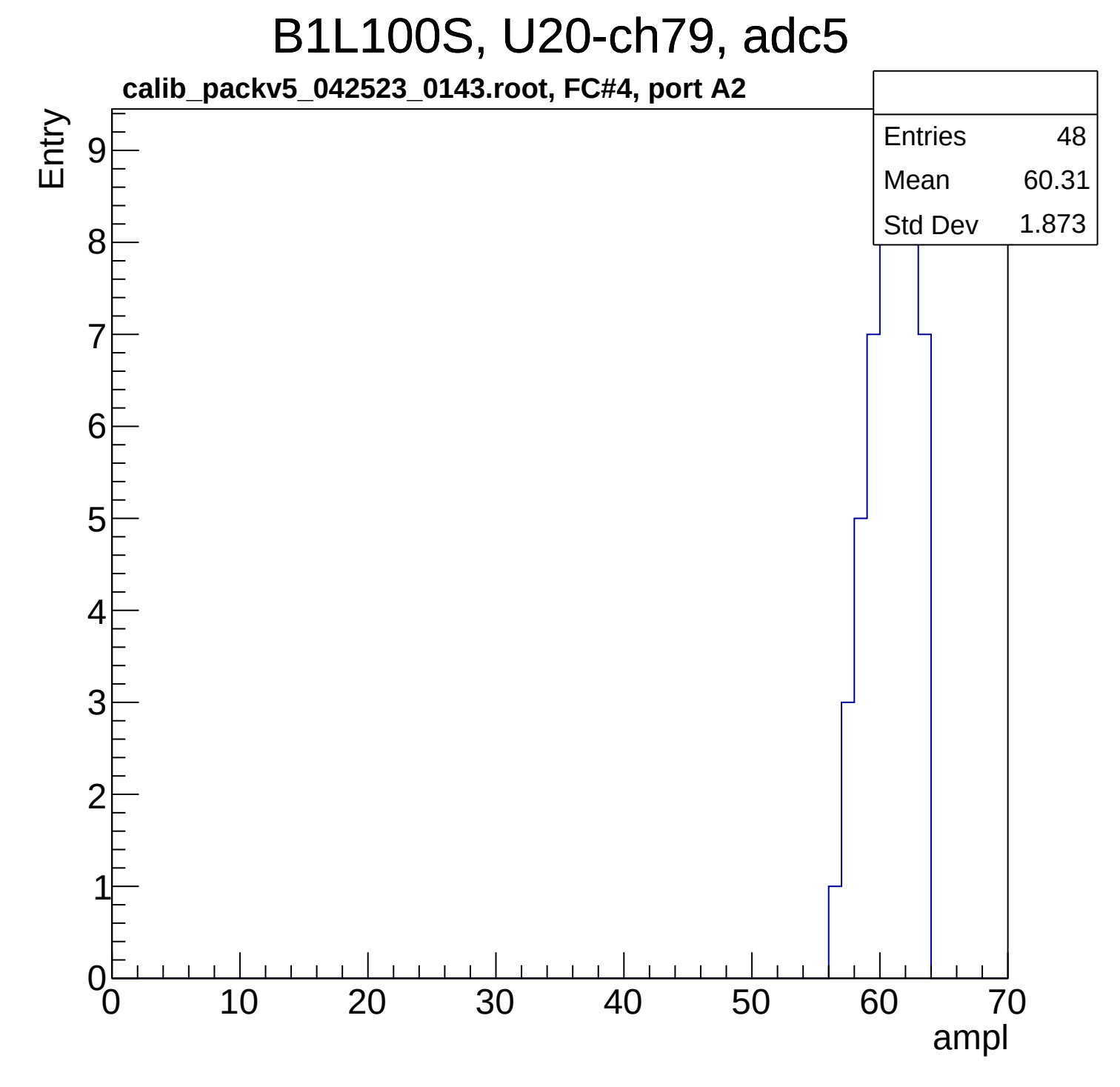
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 60.31
Std Dev 1.873
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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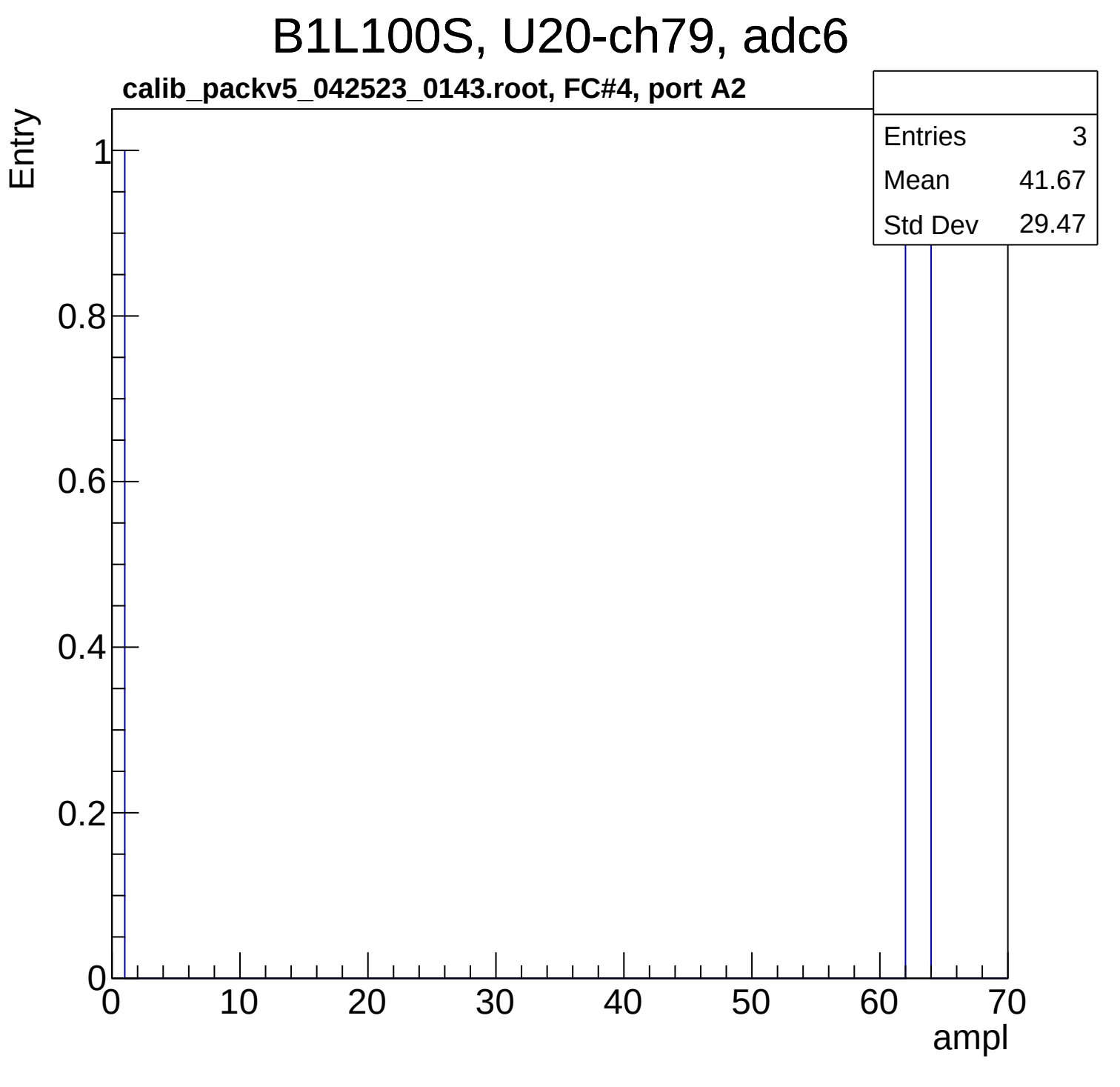
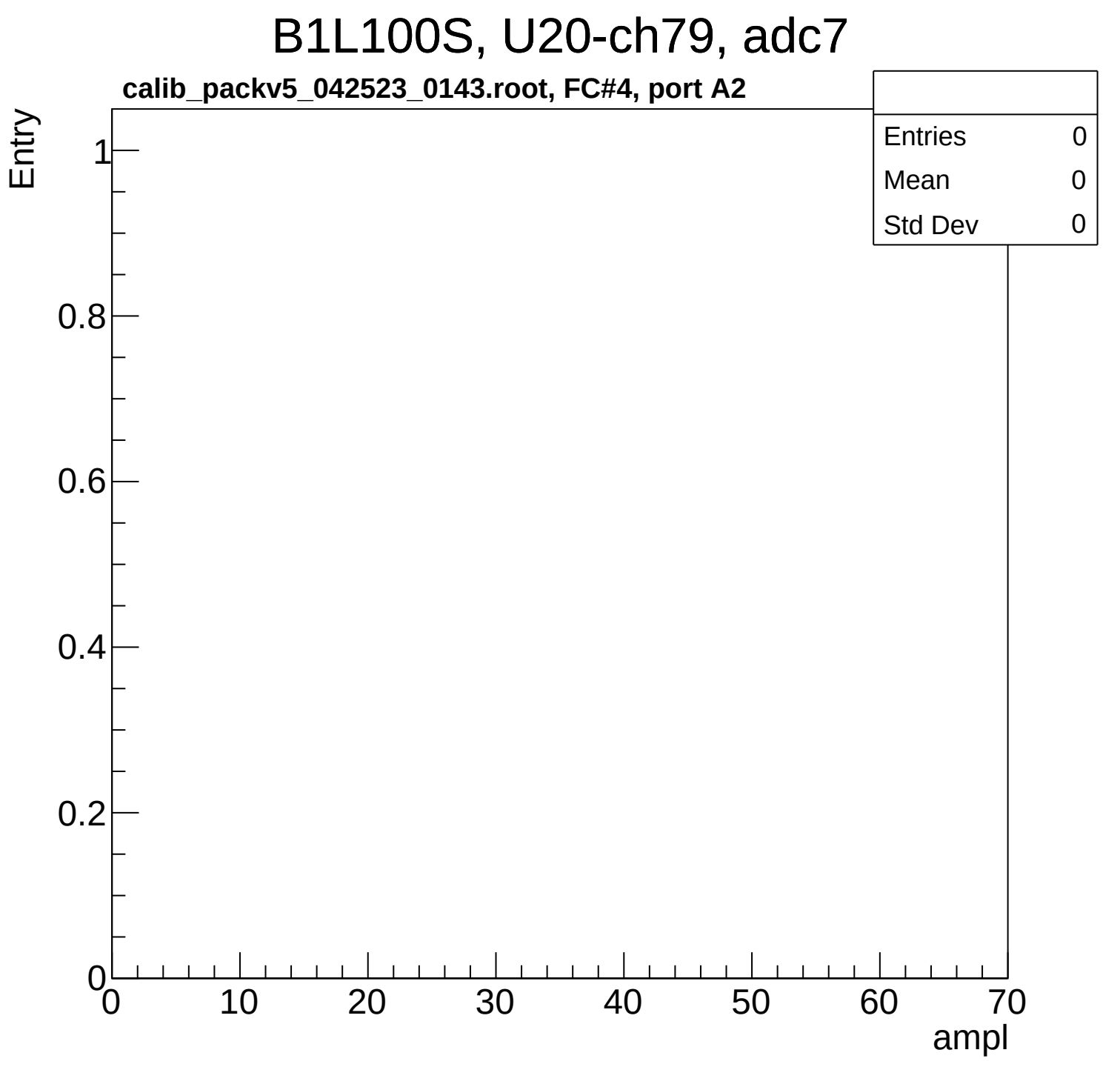
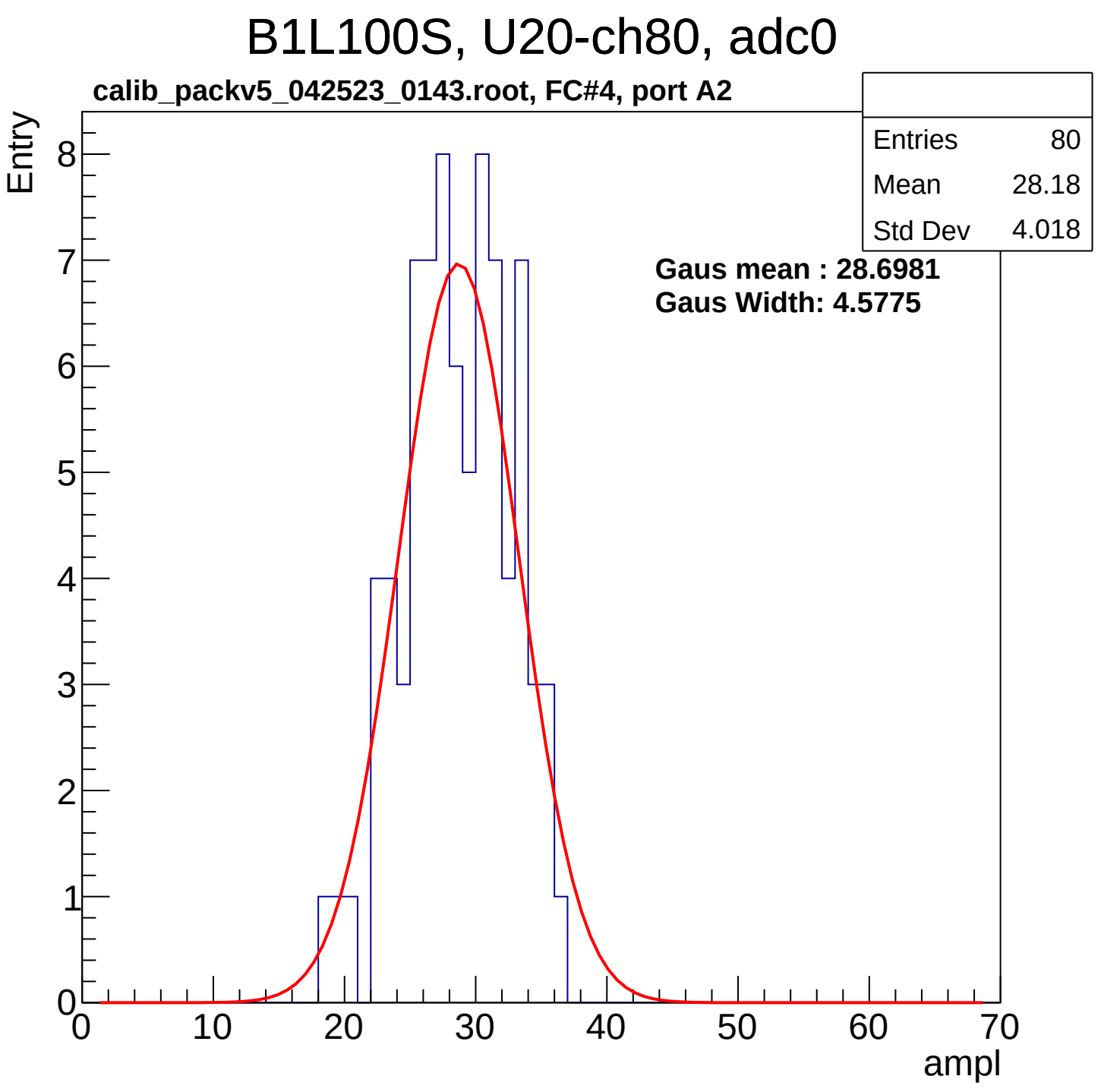

B1L100S, U20-ch79, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.18
Std Dev 4.018
Gaus mean : 28.6981
Gaus Width: 4.5775
Entry
ampl
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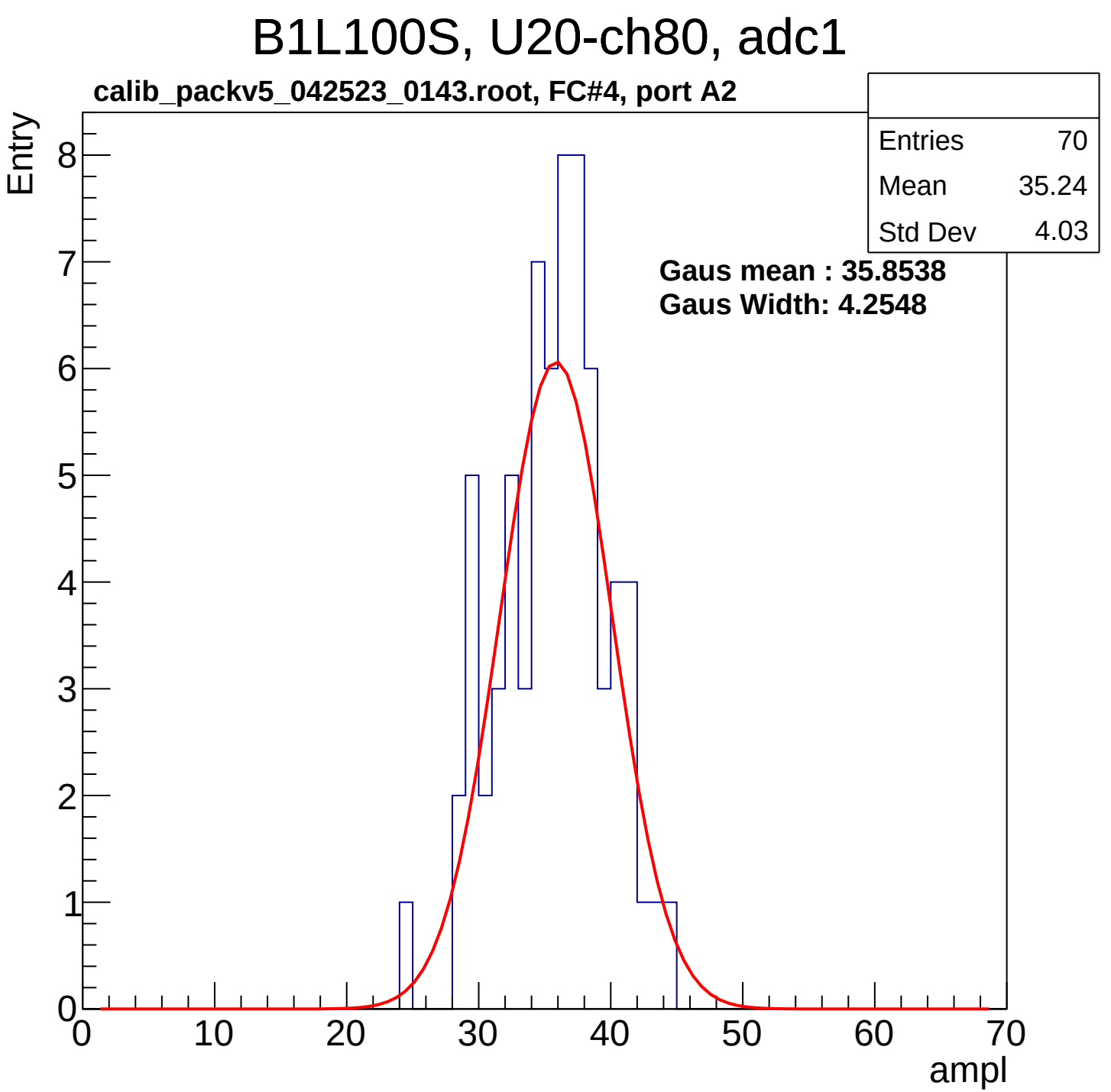
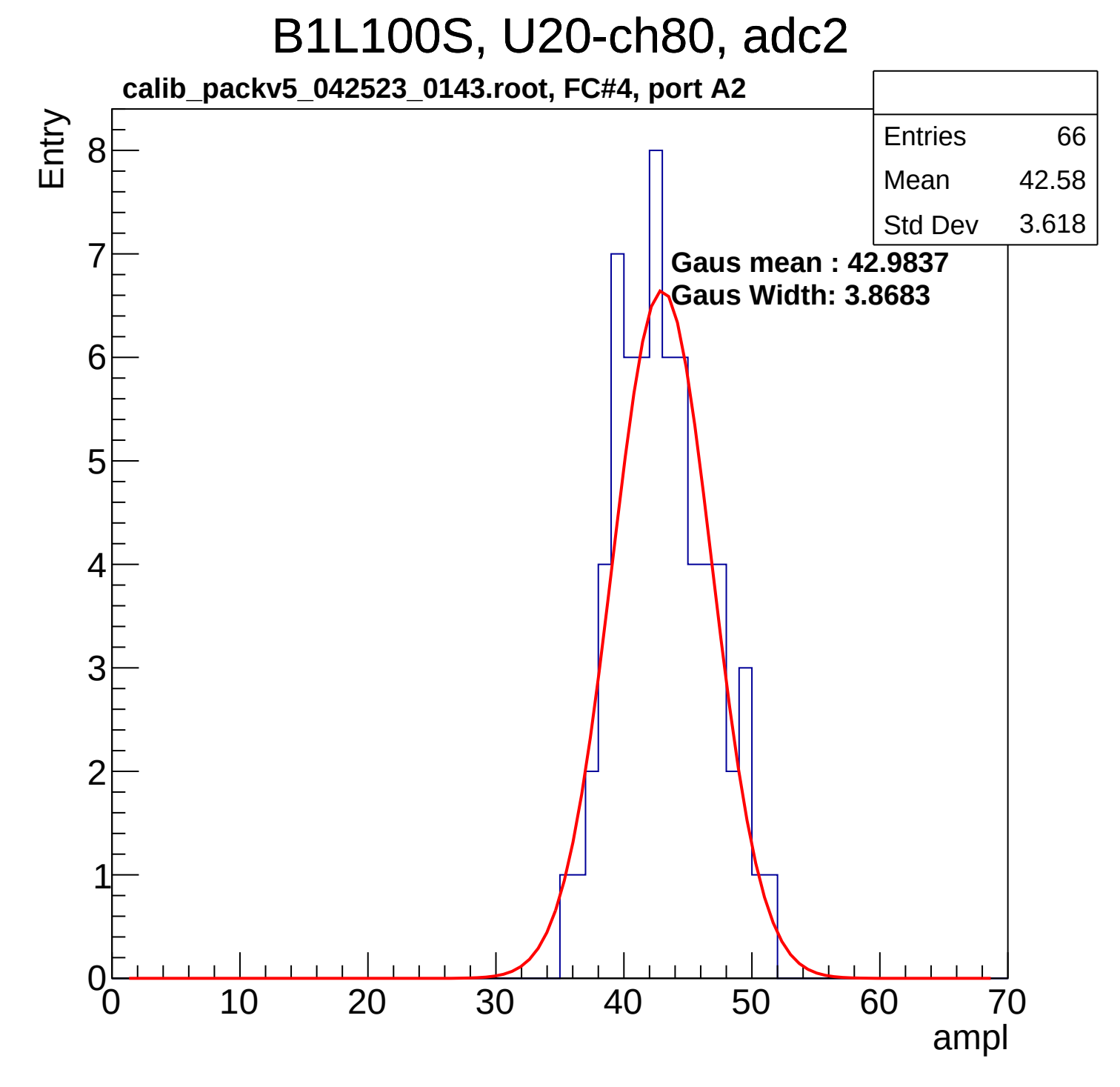
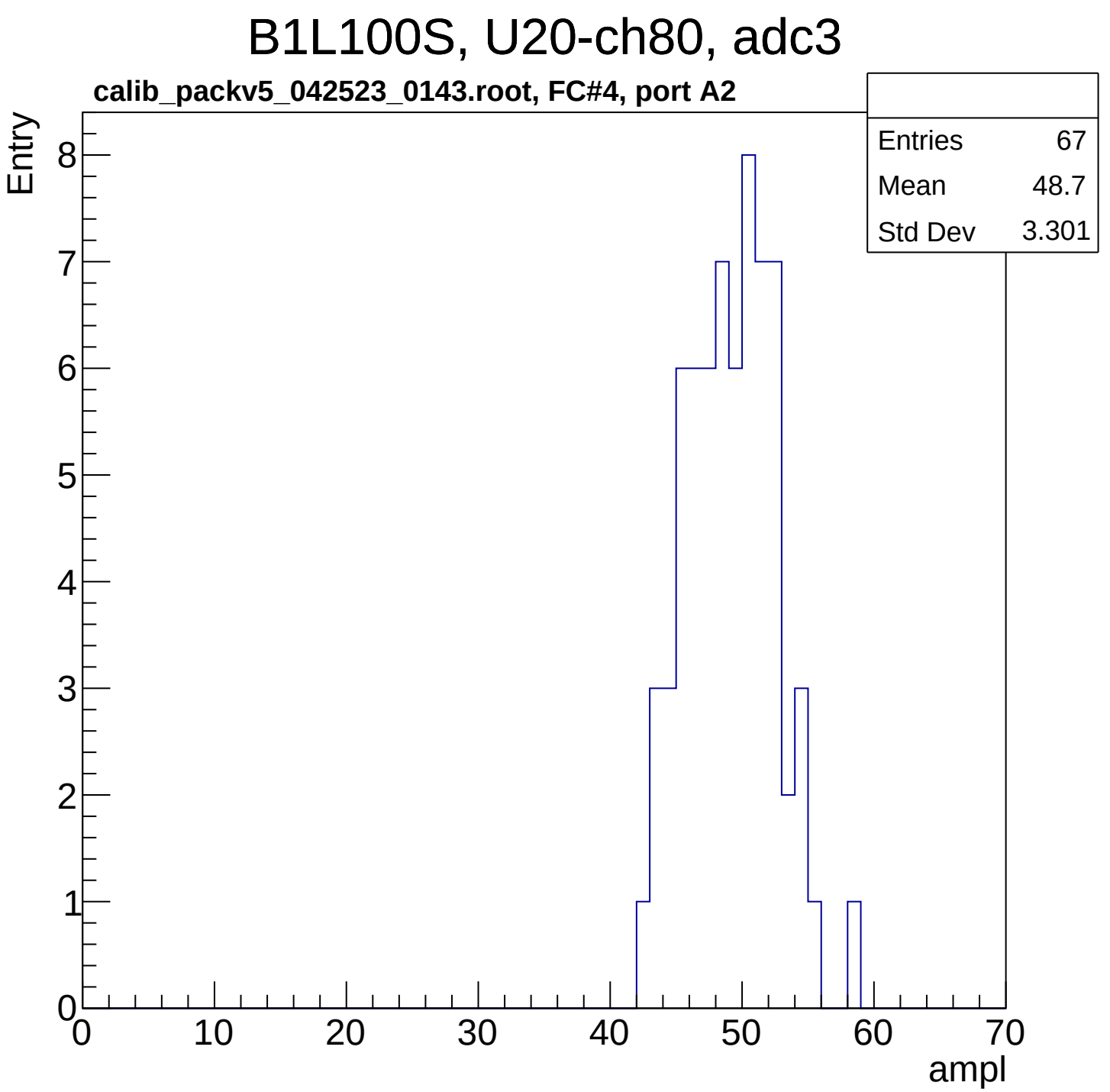
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.24
Std Dev 4.03
Gaus mean : 35.8538
Gaus Width: 4.2548
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.58
Std Dev 3.618
Gaus mean : 42.9837
Gaus Width: 3.8683
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 48.7
Std Dev 3.301
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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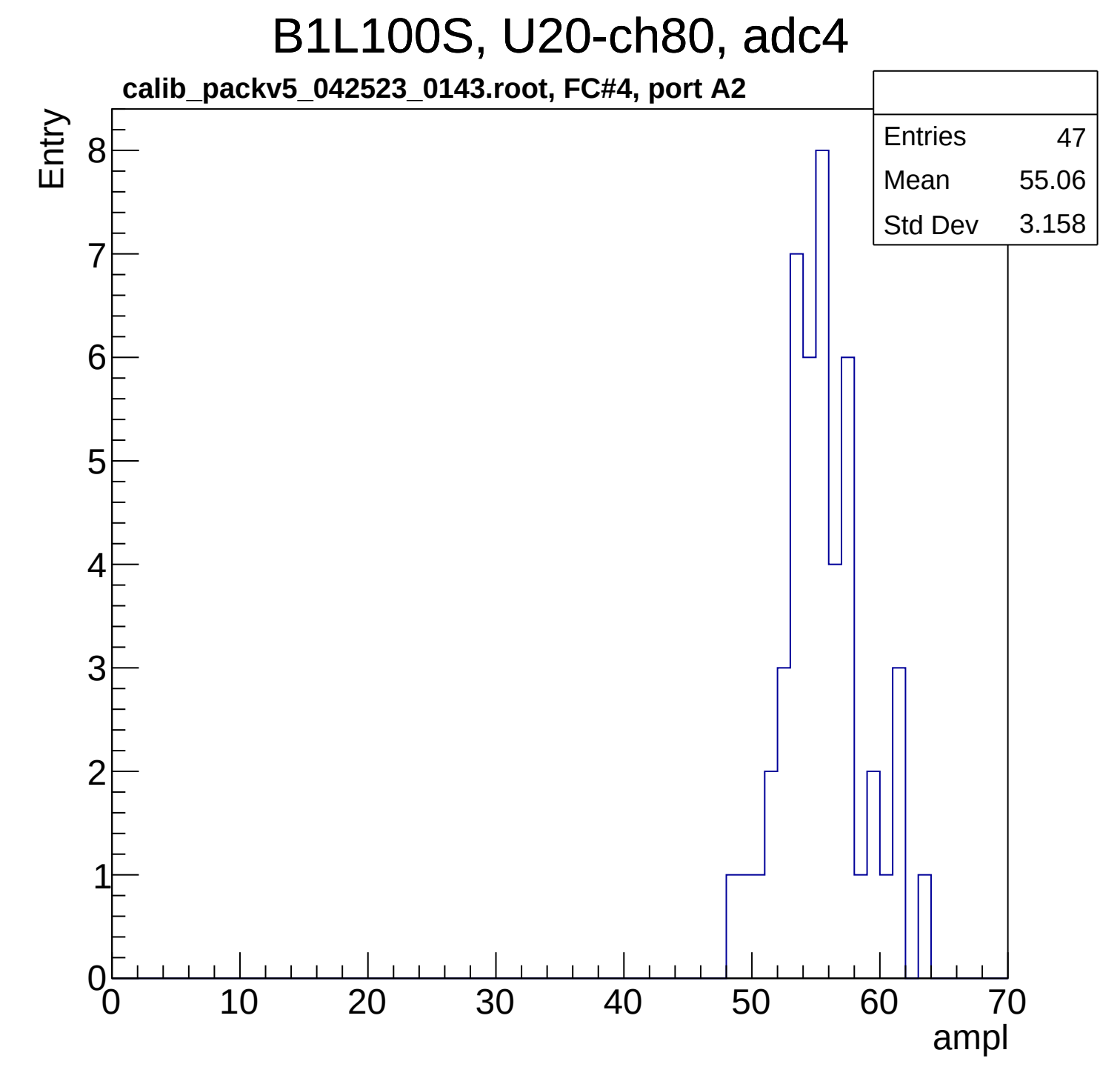
B1L100S, U20-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.06
Std Dev 3.158
Entry
ampl
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2
3
4
5
6
7
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0
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40
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60
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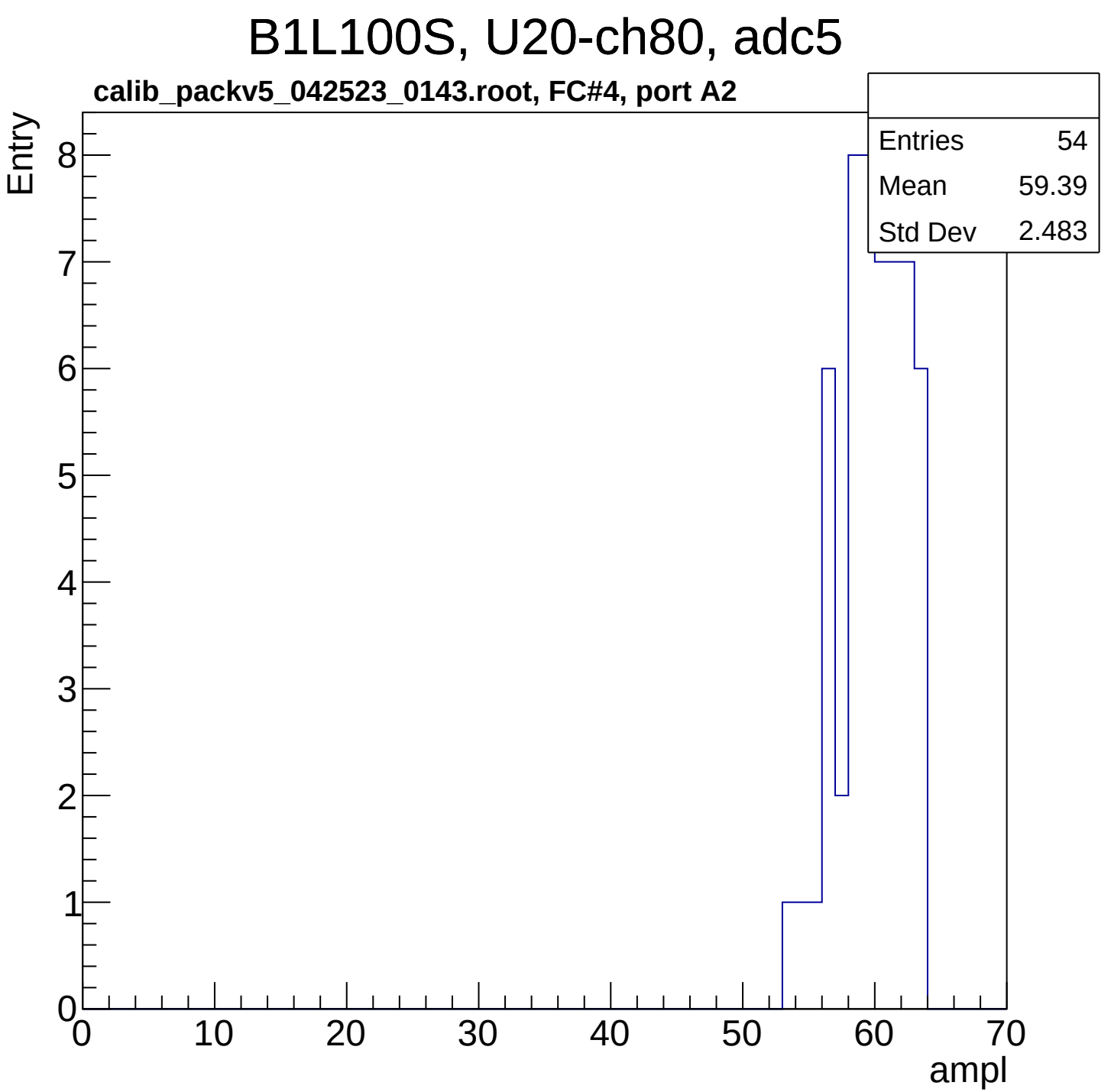
B1L100S, U20-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 59.39
Std Dev 2.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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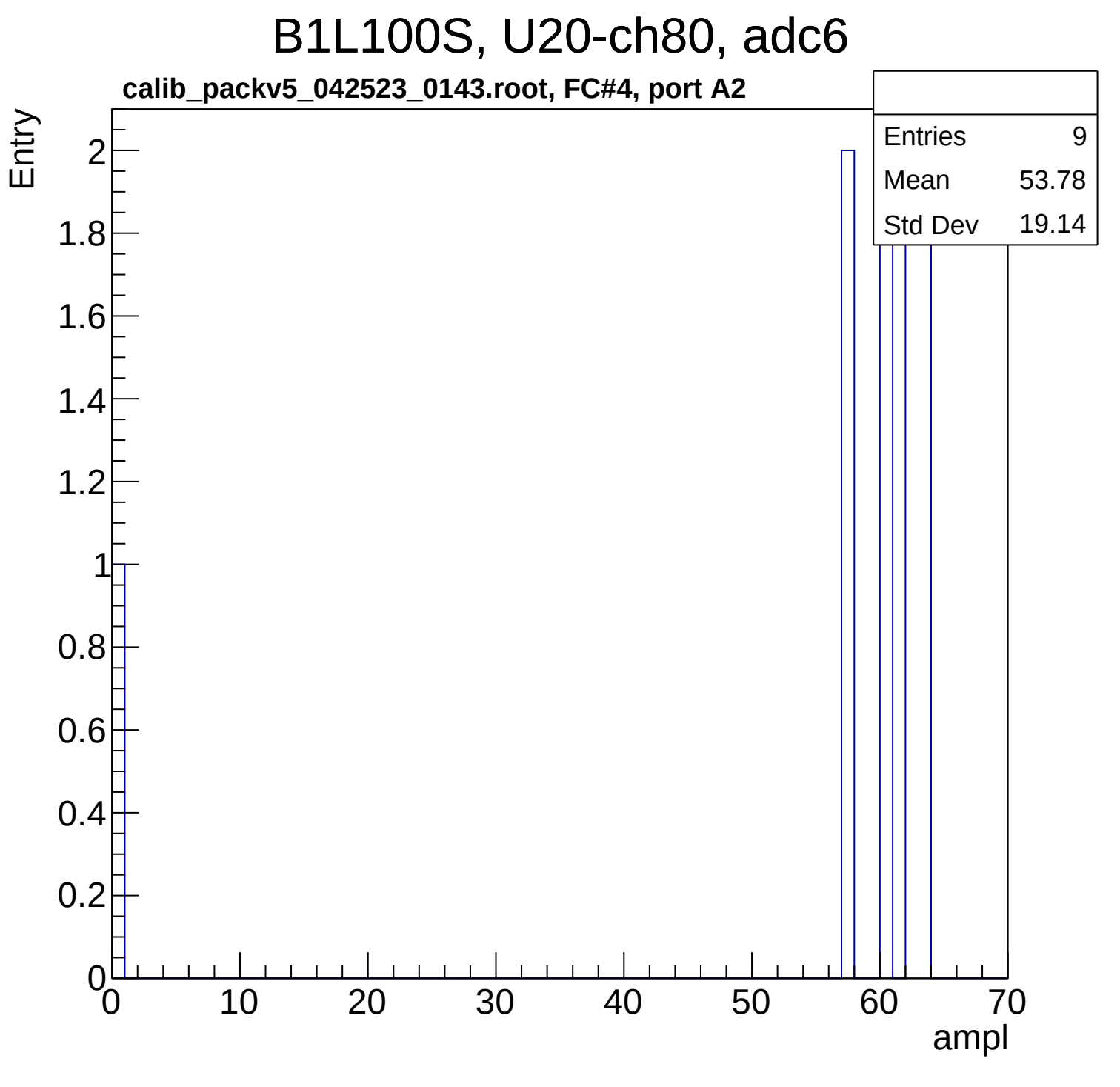
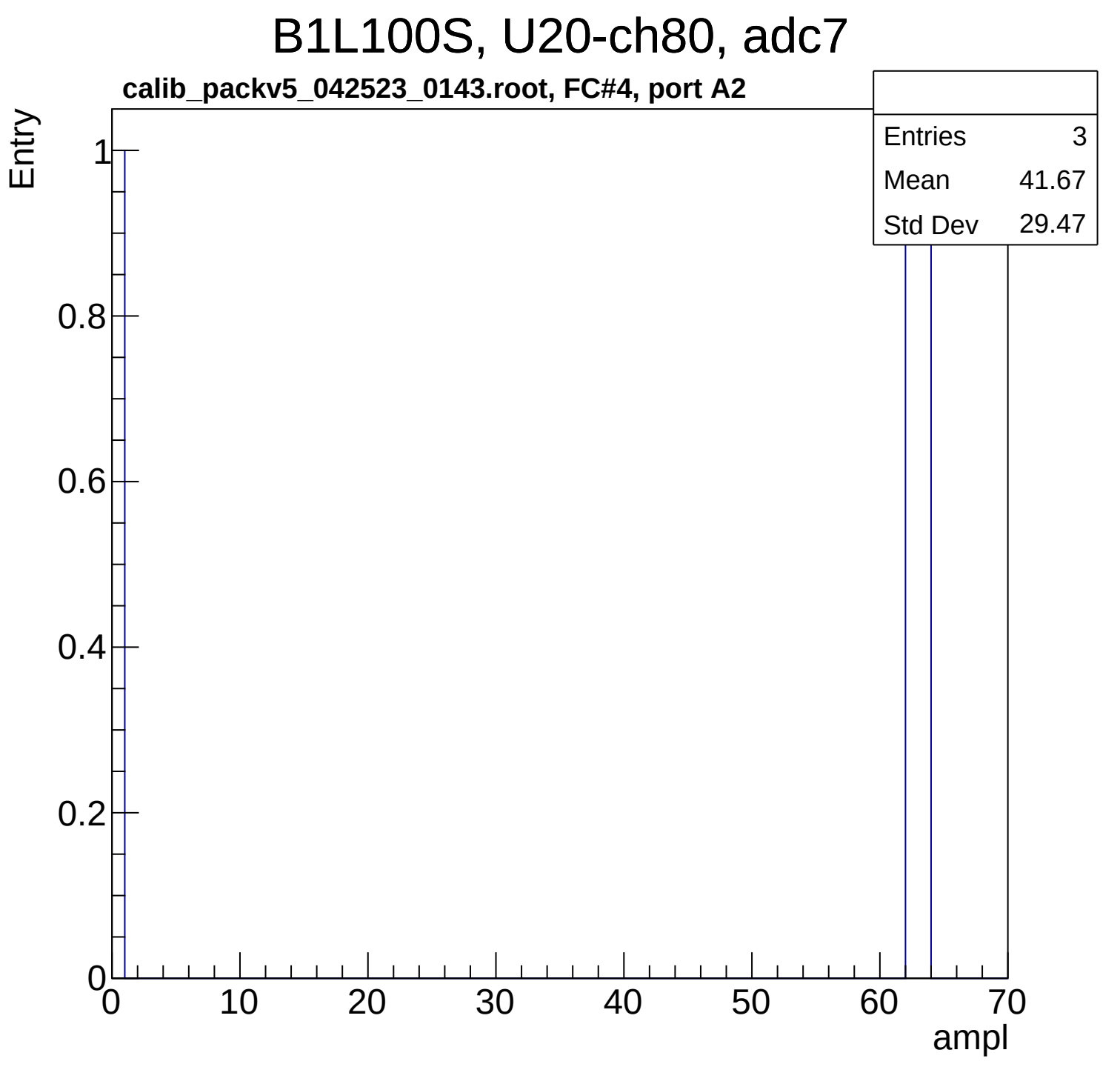

B1L100S, U20-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 53.78
Std Dev 19.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch80, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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0.6
0.4
0.2
0
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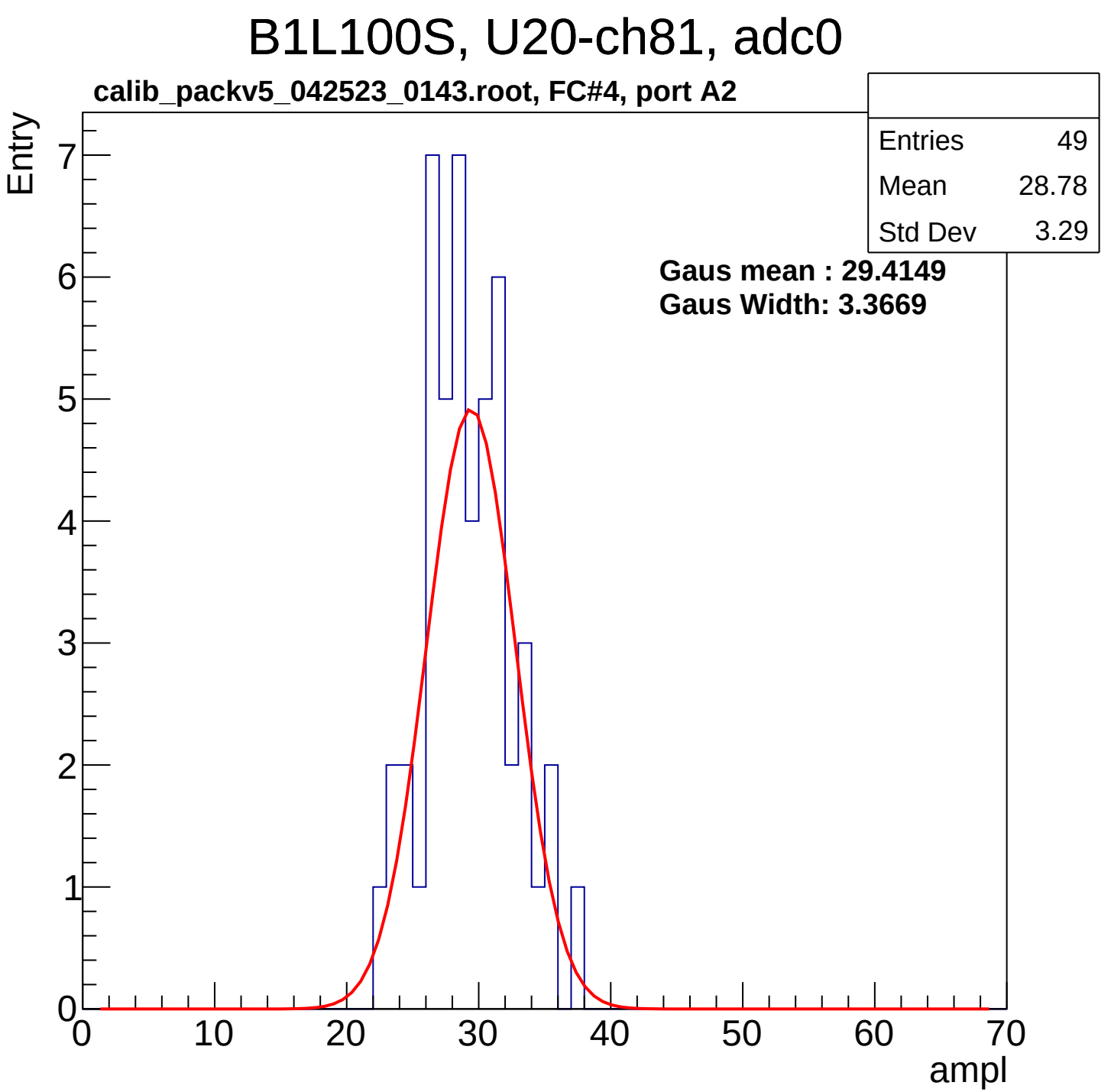
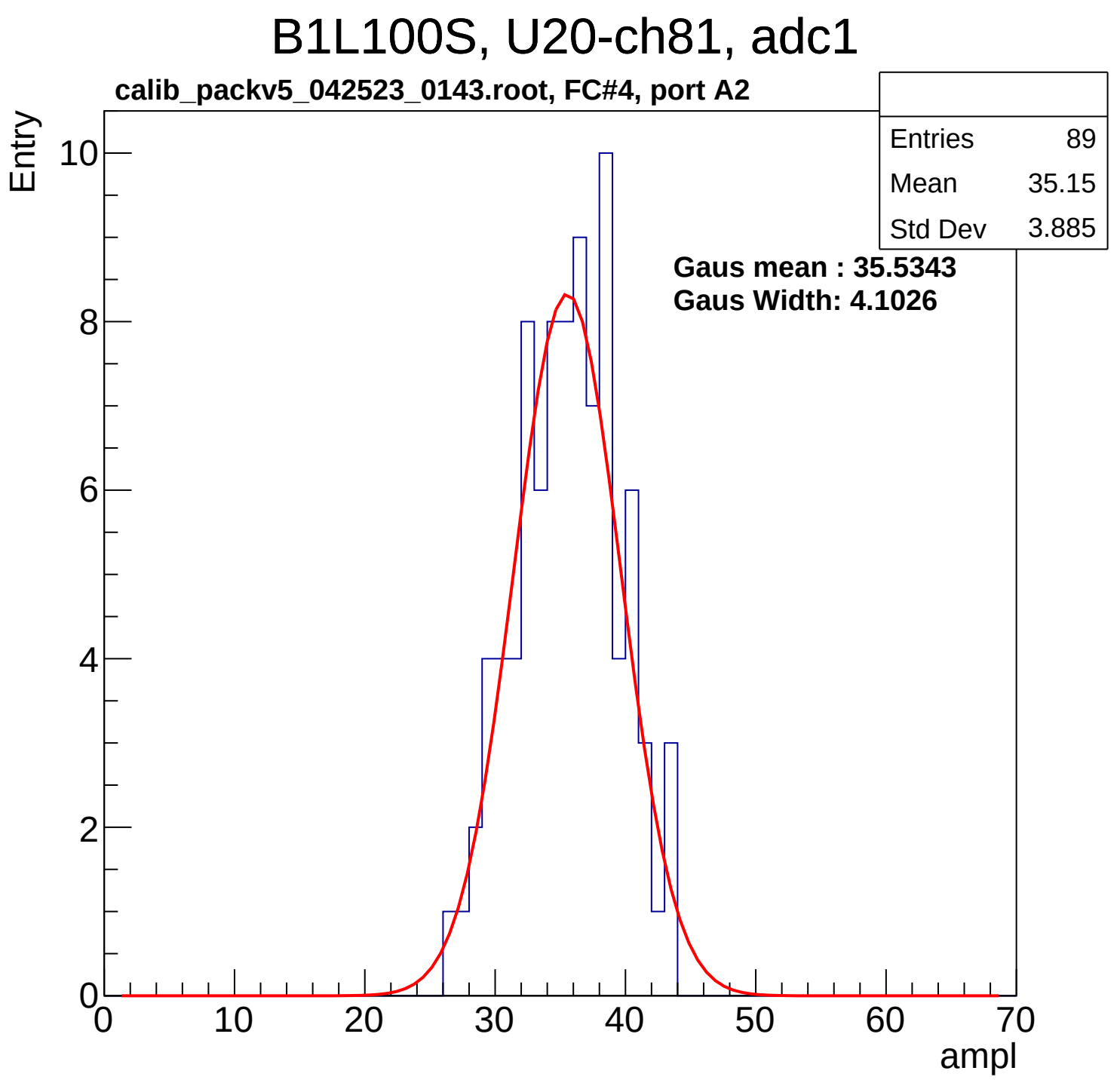
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 28.78
Std Dev 3.29
Gaus mean : 29.4149
Gaus Width: 3.3669
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 35.15
Std Dev 3.885
Gaus mean : 35.5343
Gaus Width: 4.1026
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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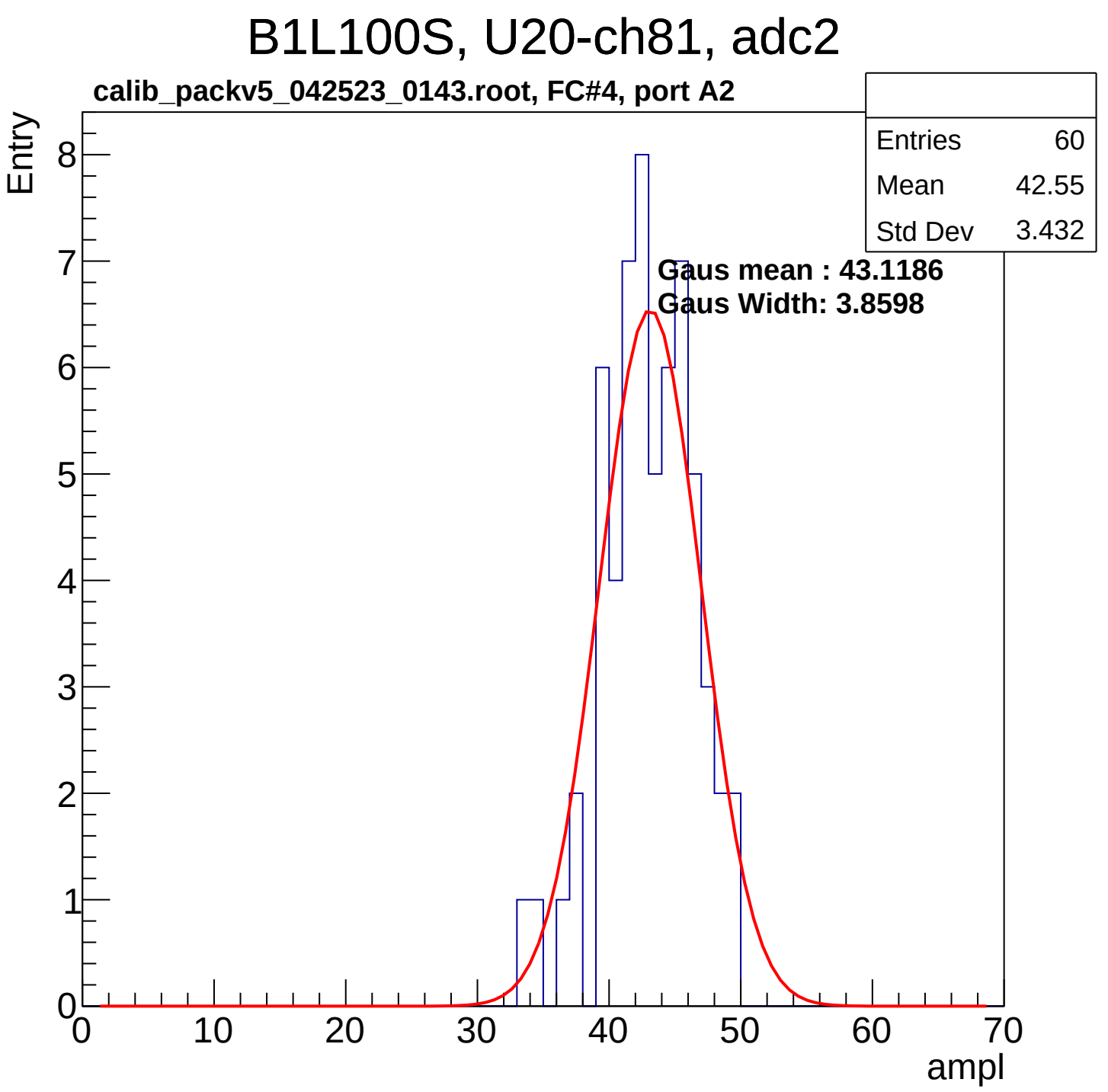
B1L100S, U20-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.55
Std Dev 3.432
Gaus mean : 43.1186
Gaus Width: 3.8598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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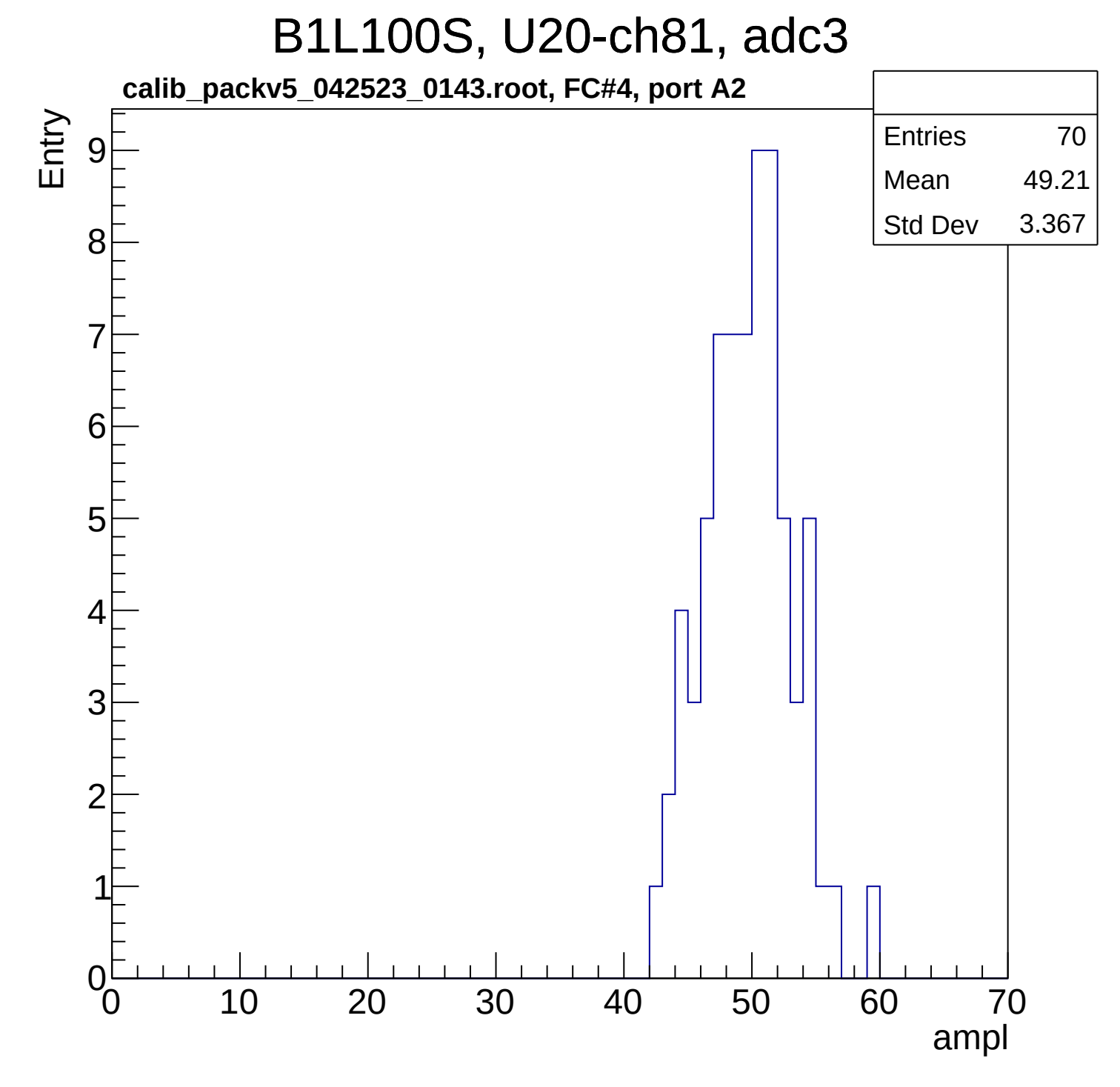
B1L100S, U20-ch81, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.21
Std Dev 3.367
Entry
ampl
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2
3
4
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6
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0
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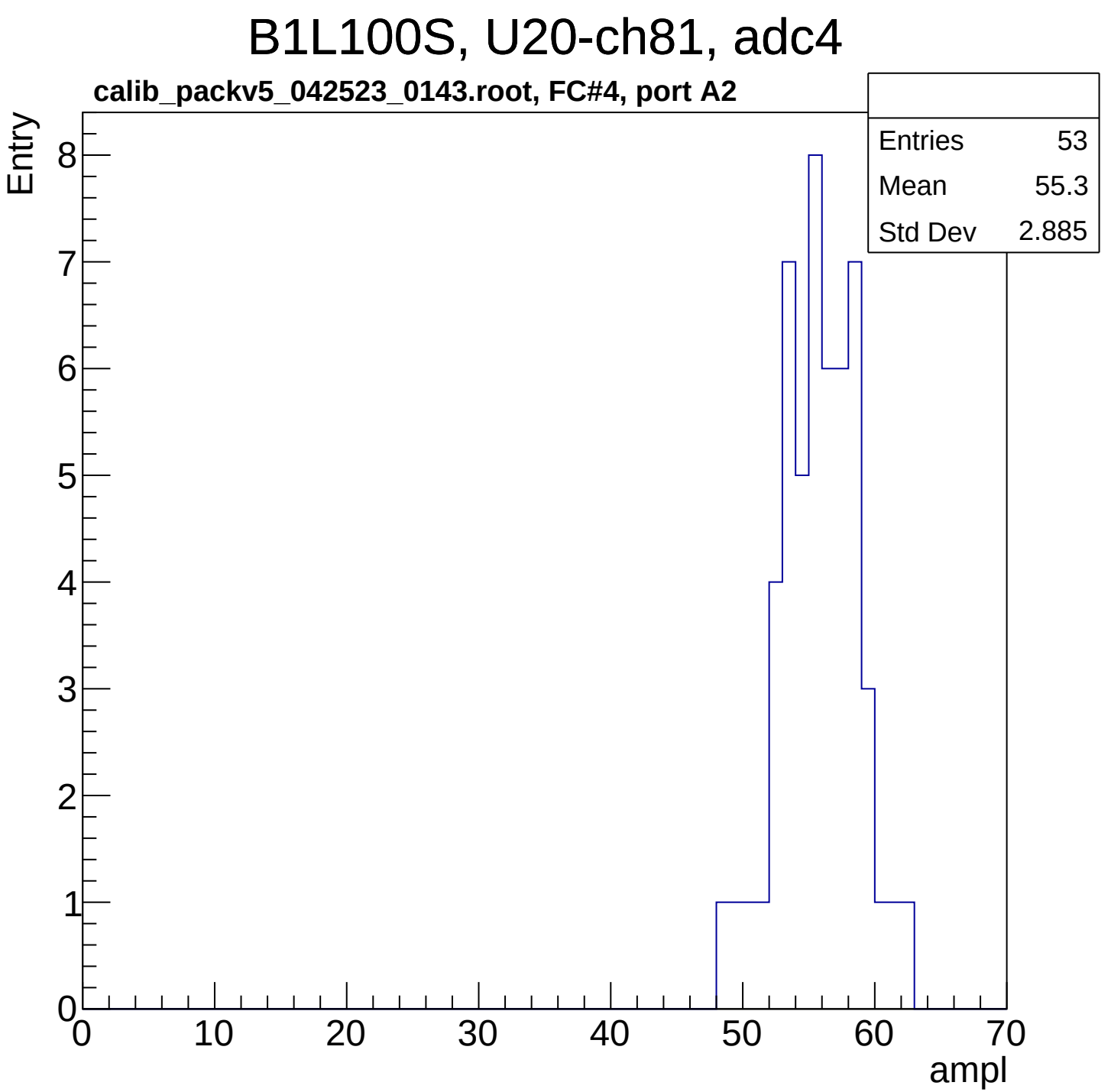
B1L100S, U20-ch81, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.3
Std Dev 2.885
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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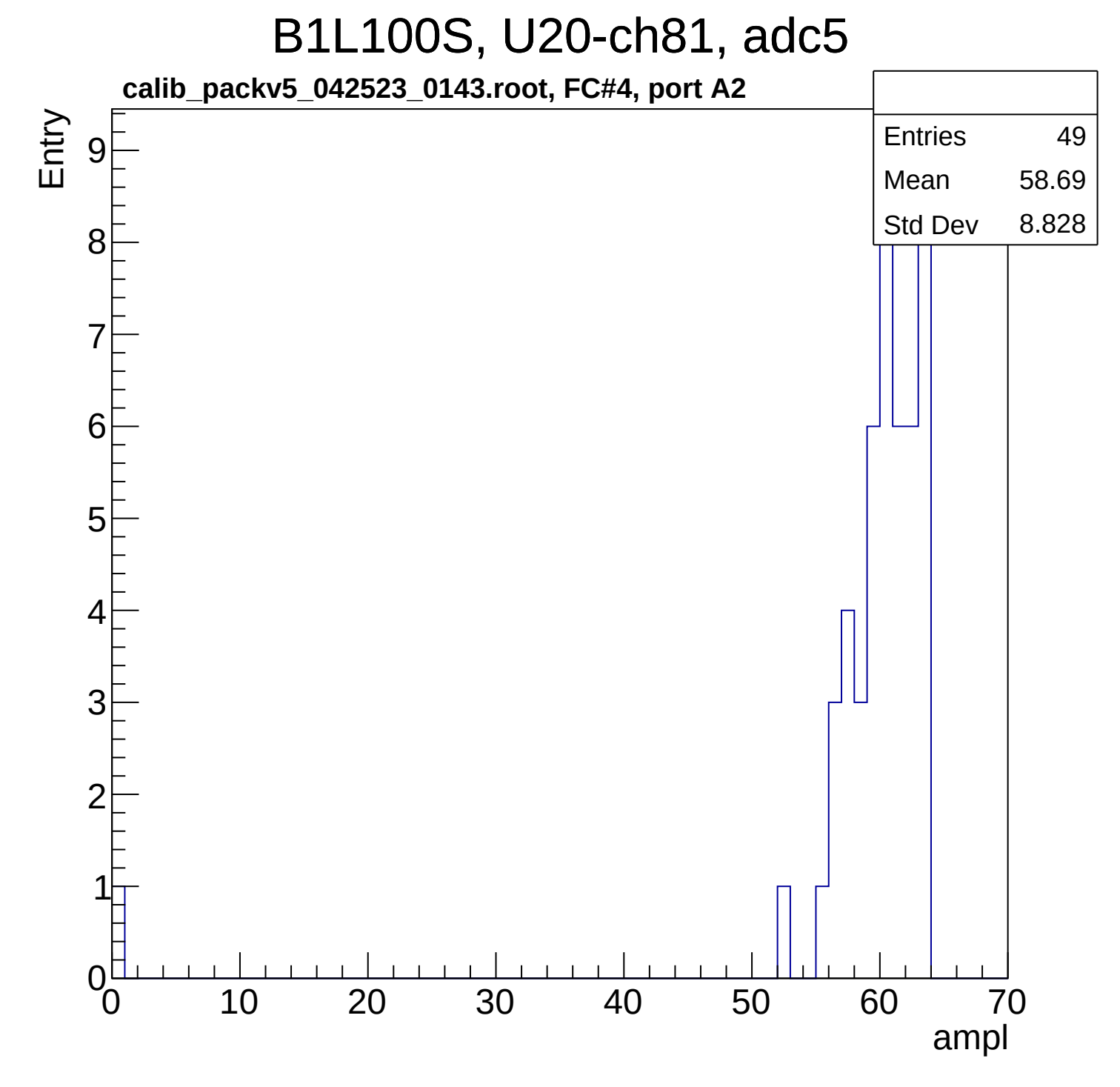
B1L100S, U20-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.69
Std Dev 8.828
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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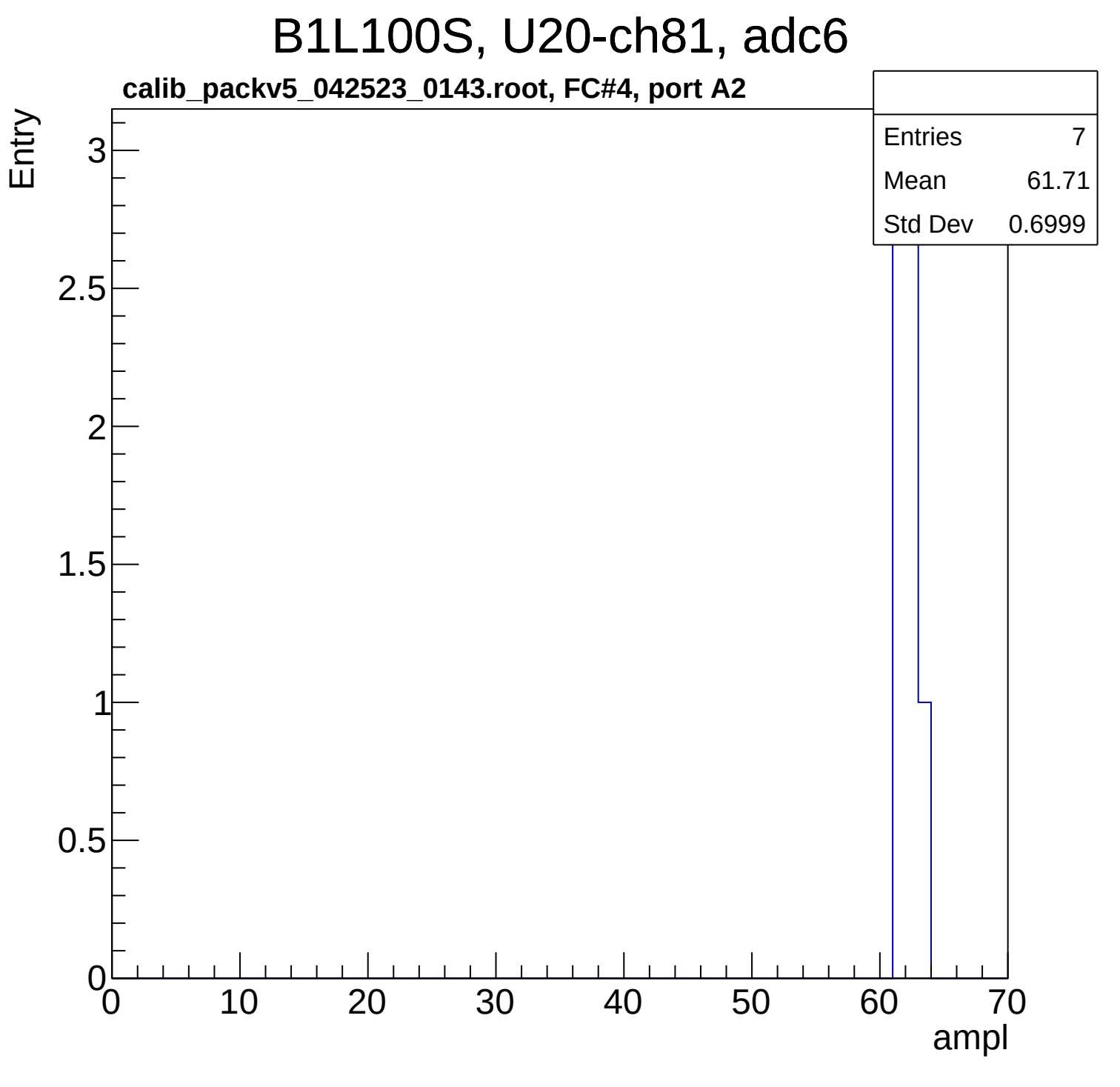
B1L100S, U20-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.71
Std Dev 0.6999
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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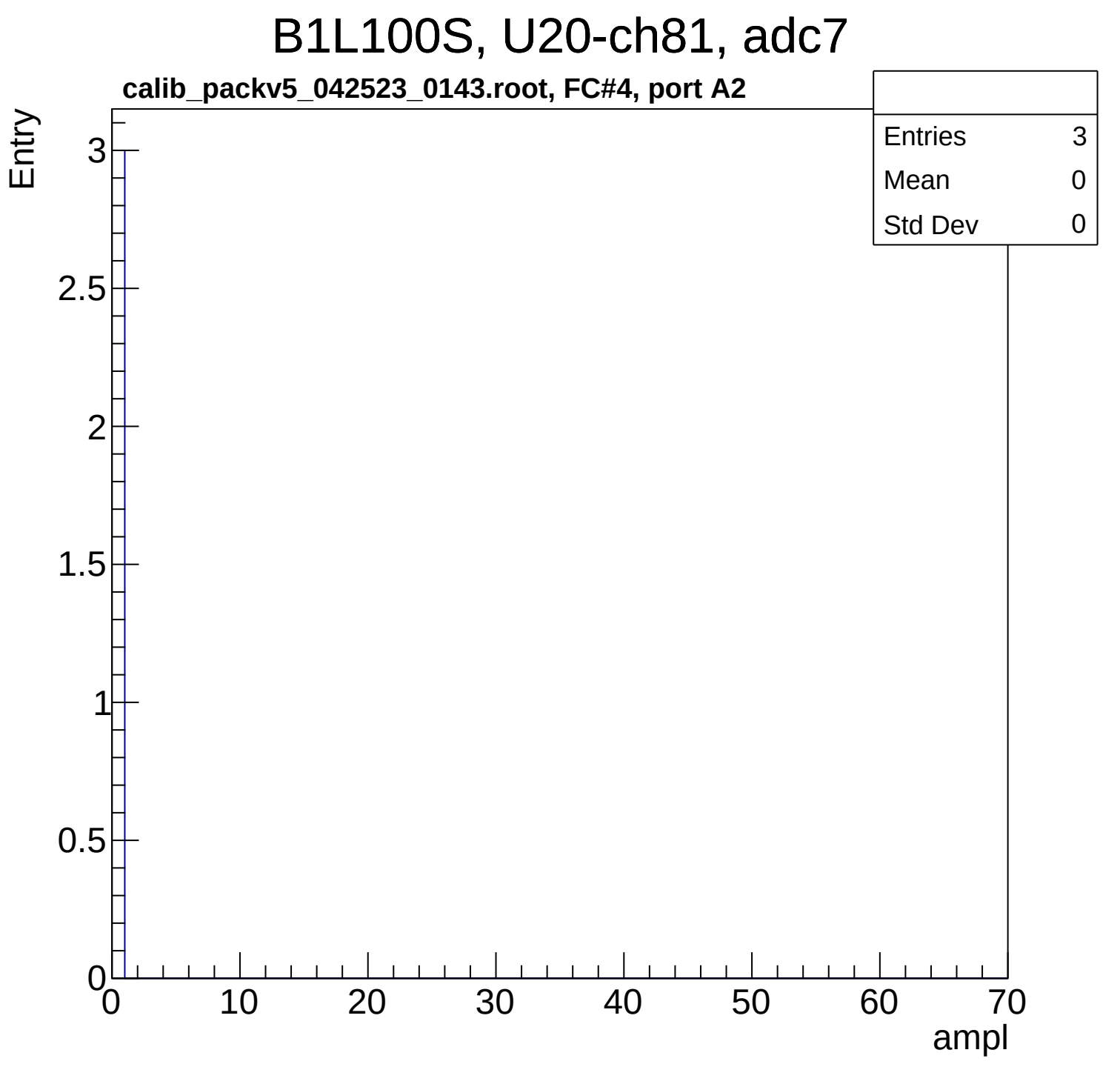
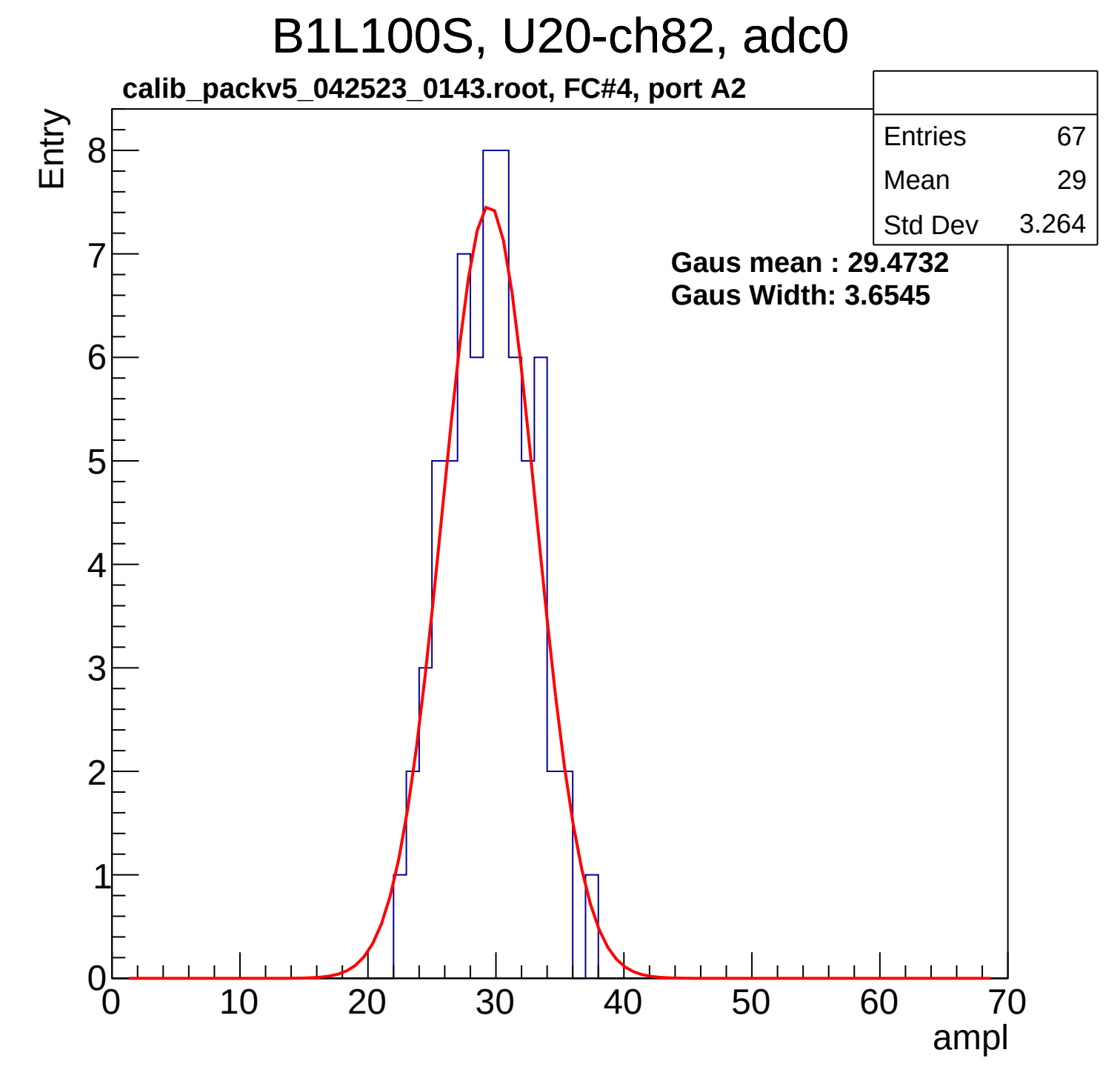

B1L100S, U20-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29
Std Dev 3.264
Gaus mean : 29.4732
Gaus Width: 3.6545
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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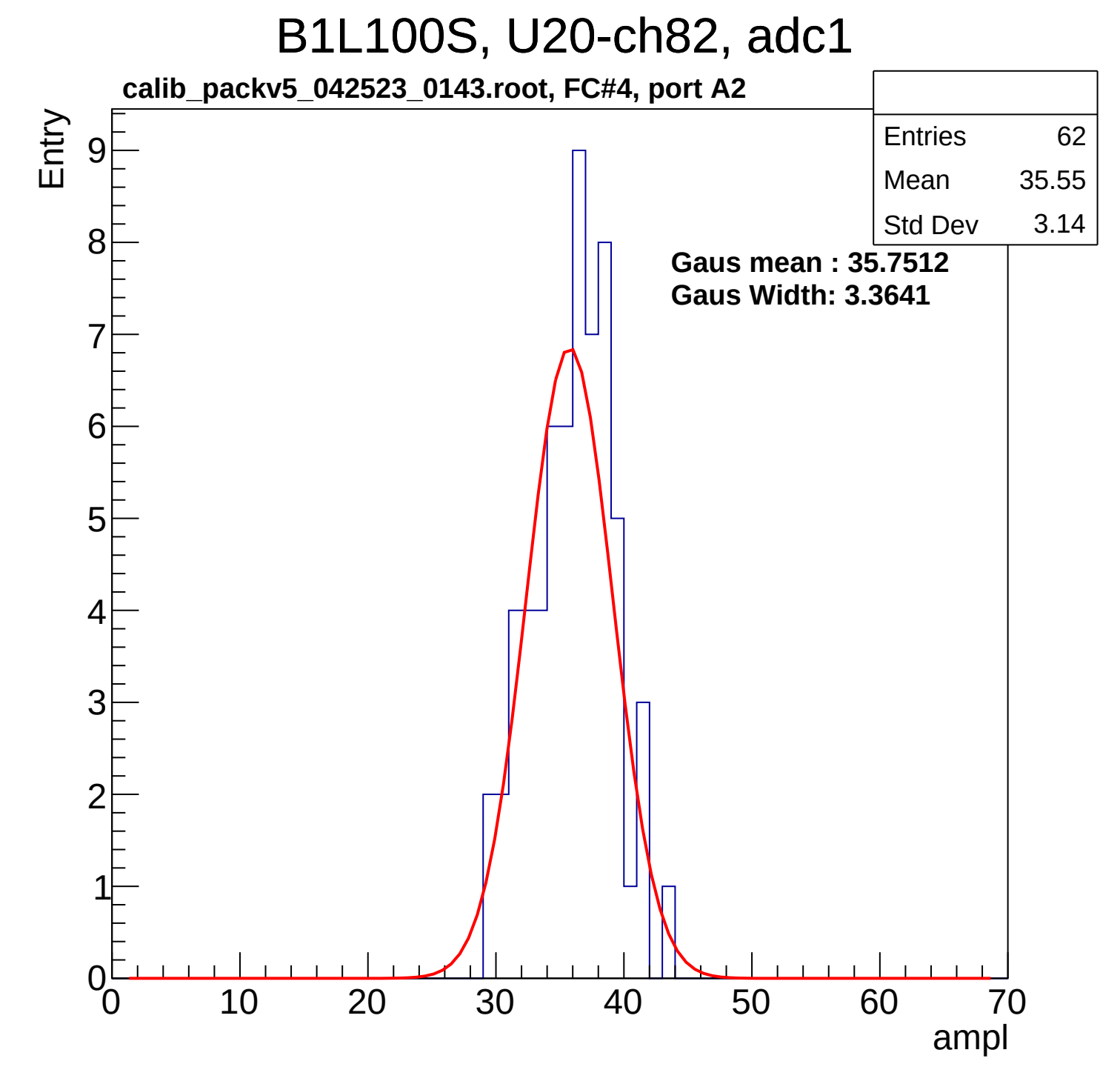
B1L100S, U20-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 35.55
Std Dev 3.14
Gaus mean : 35.7512
Gaus Width: 3.3641
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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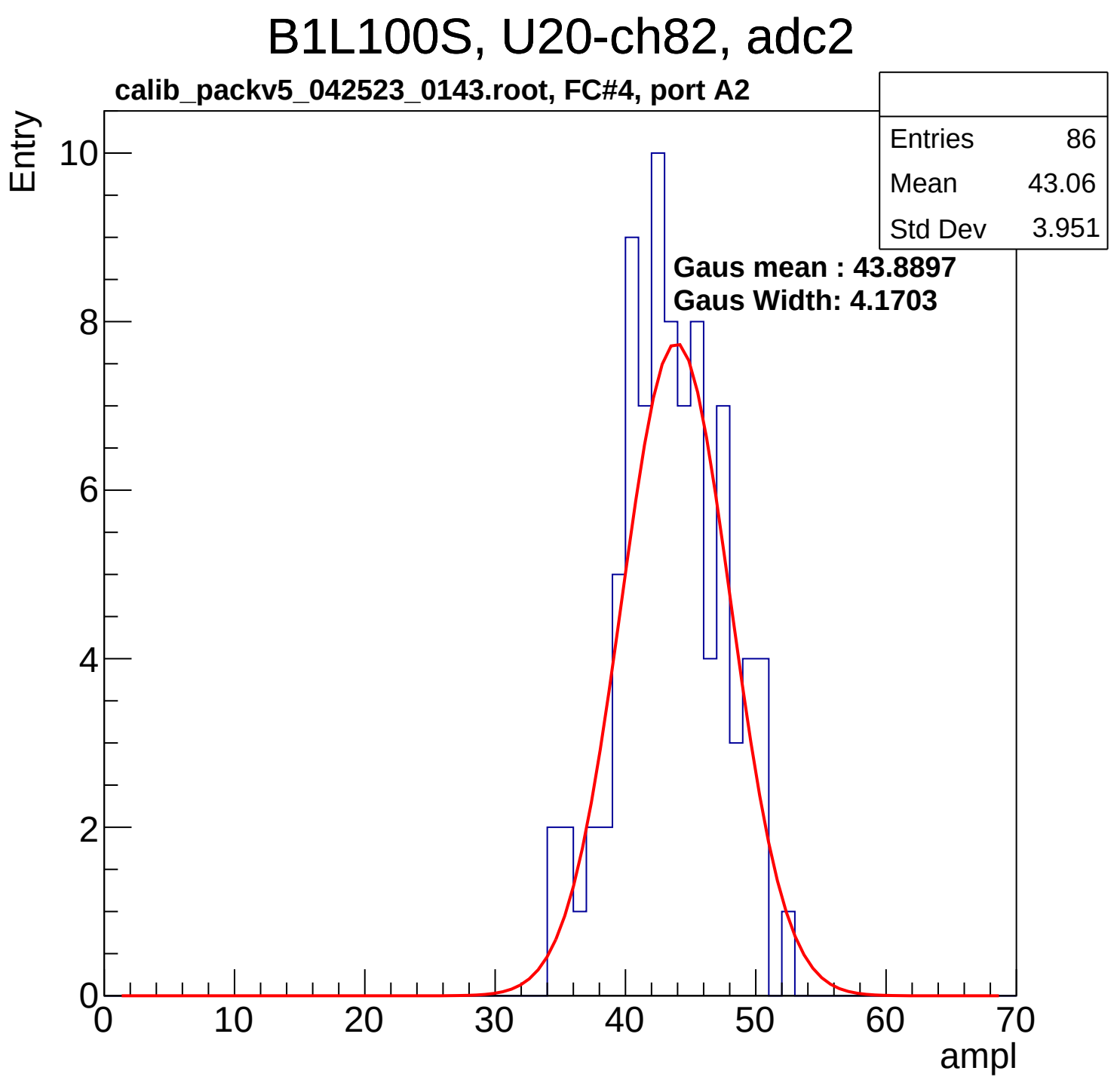
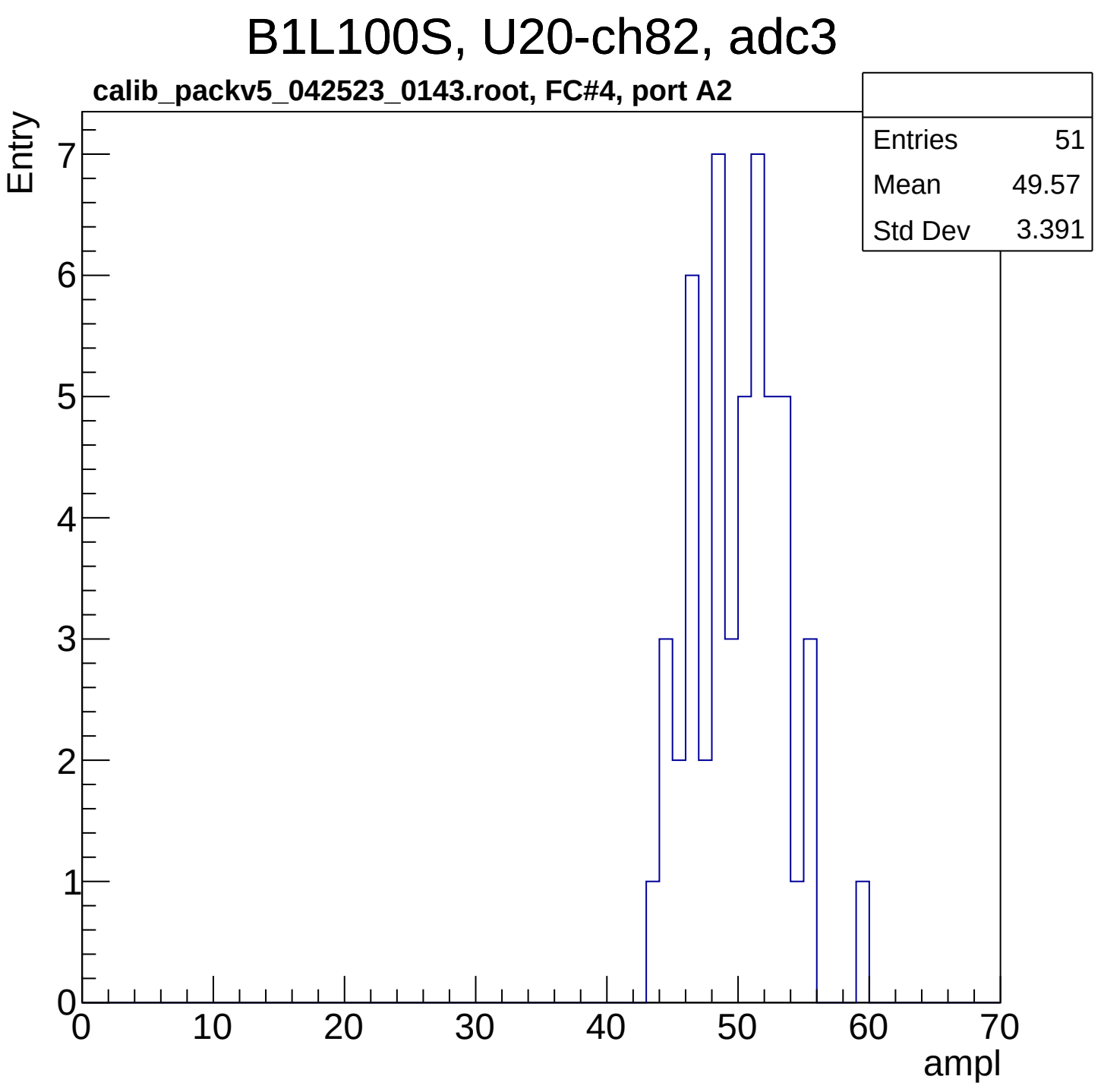

B1L100S, U20-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 43.06
Std Dev 3.951
Gaus mean : 43.8897
Gaus Width: 4.1703
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 49.57
Std Dev 3.391
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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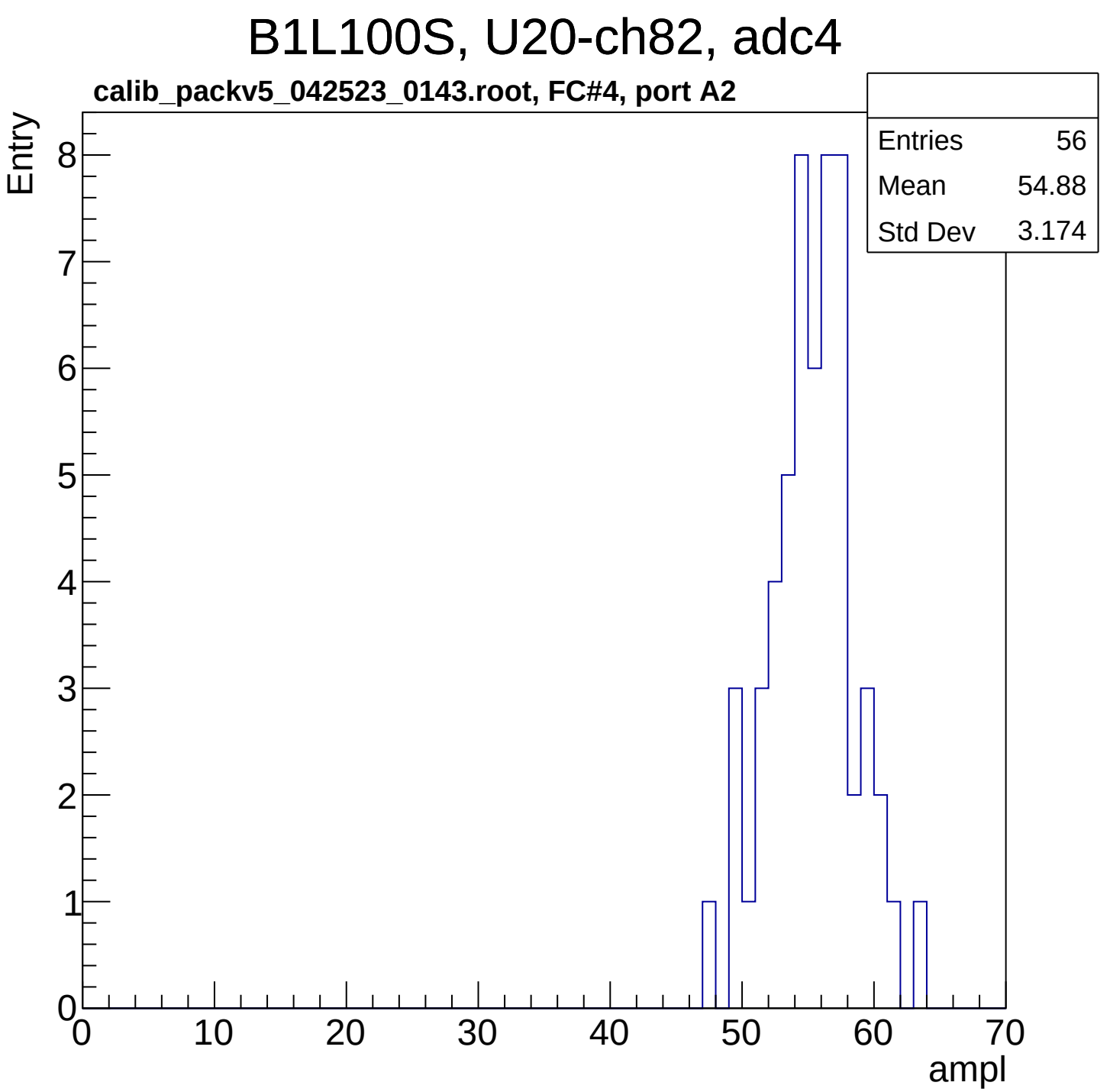
B1L100S, U20-ch82, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 54.88
Std Dev 3.174
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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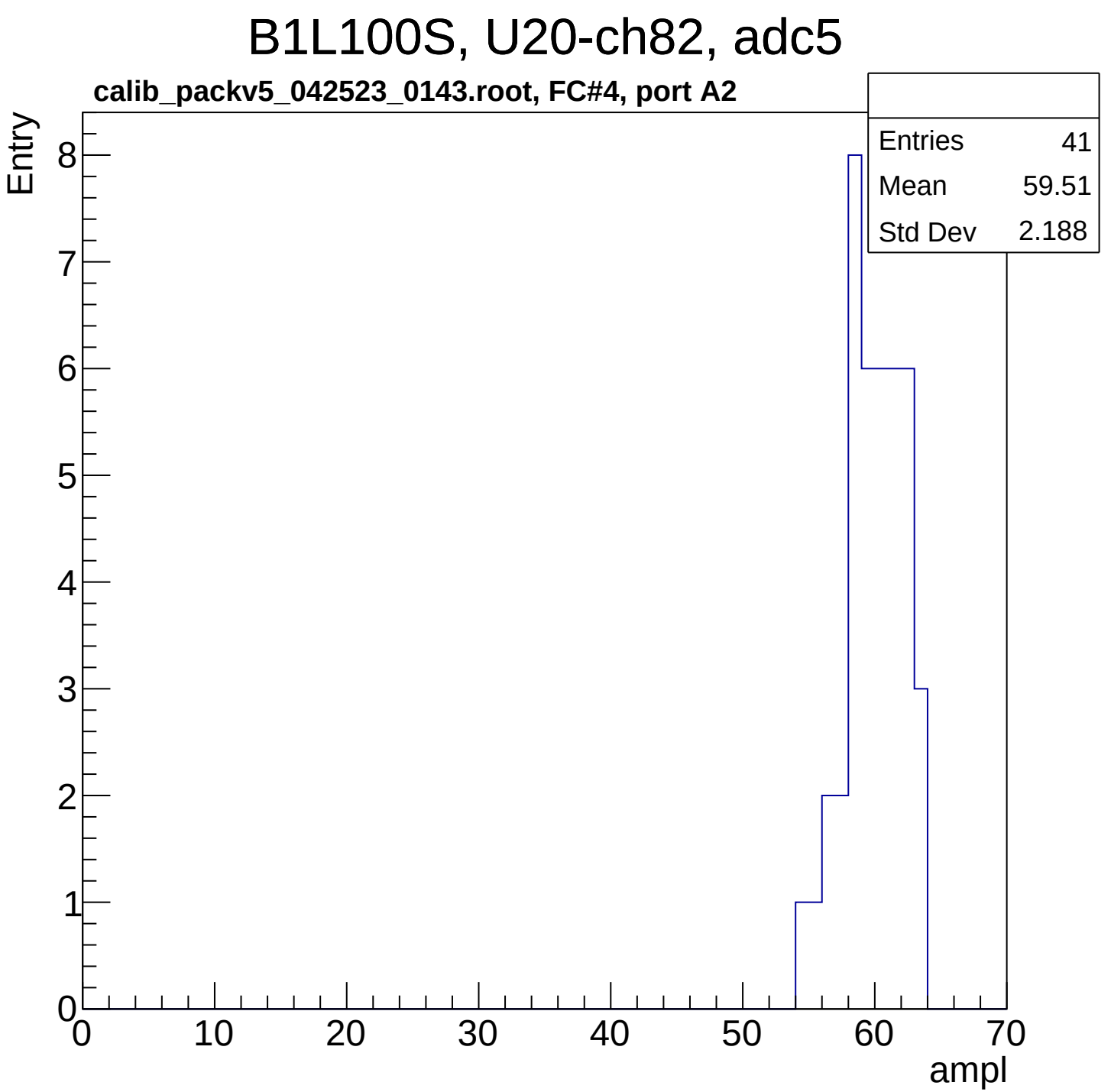
B1L100S, U20-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.51
Std Dev 2.188
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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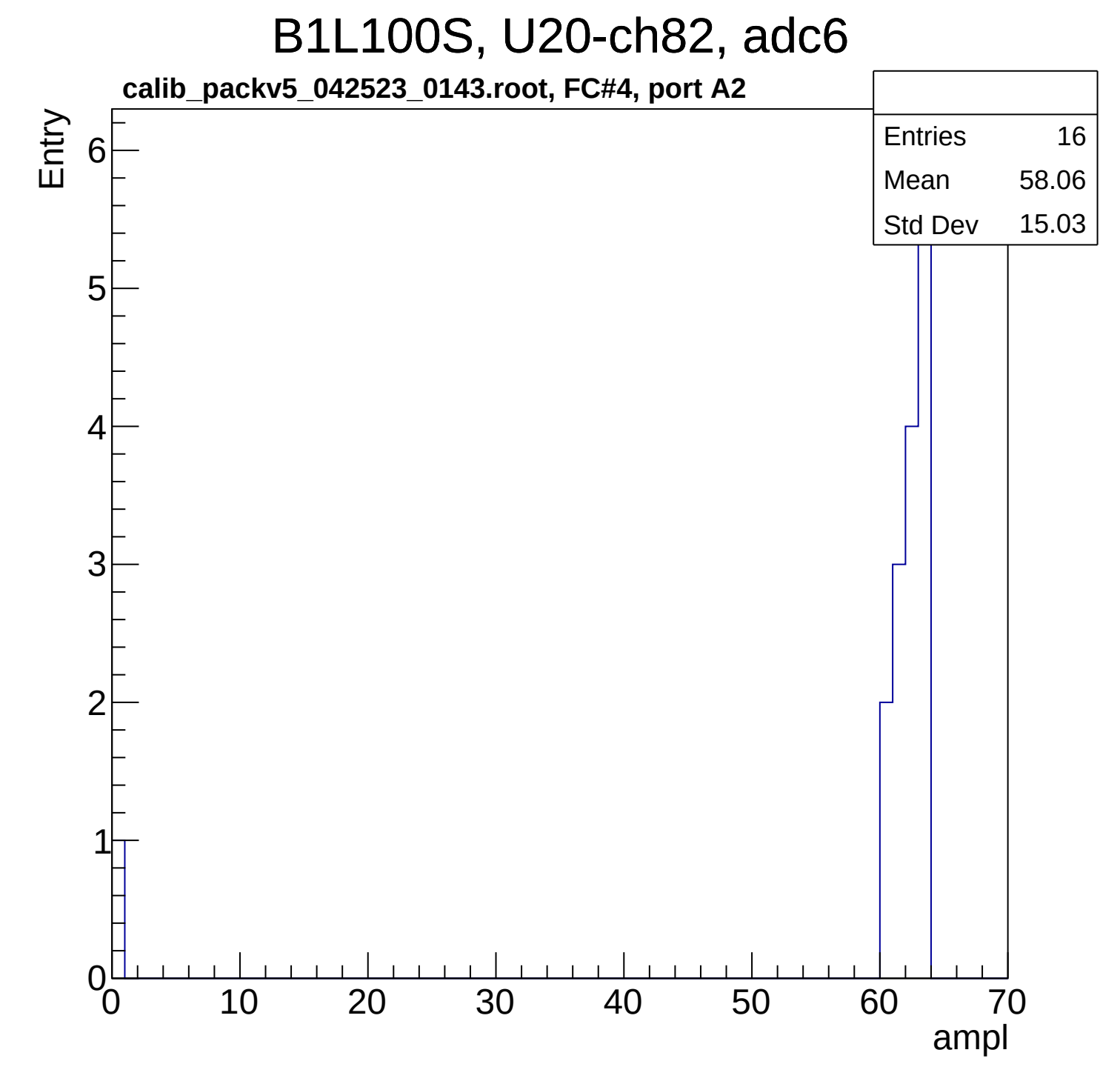
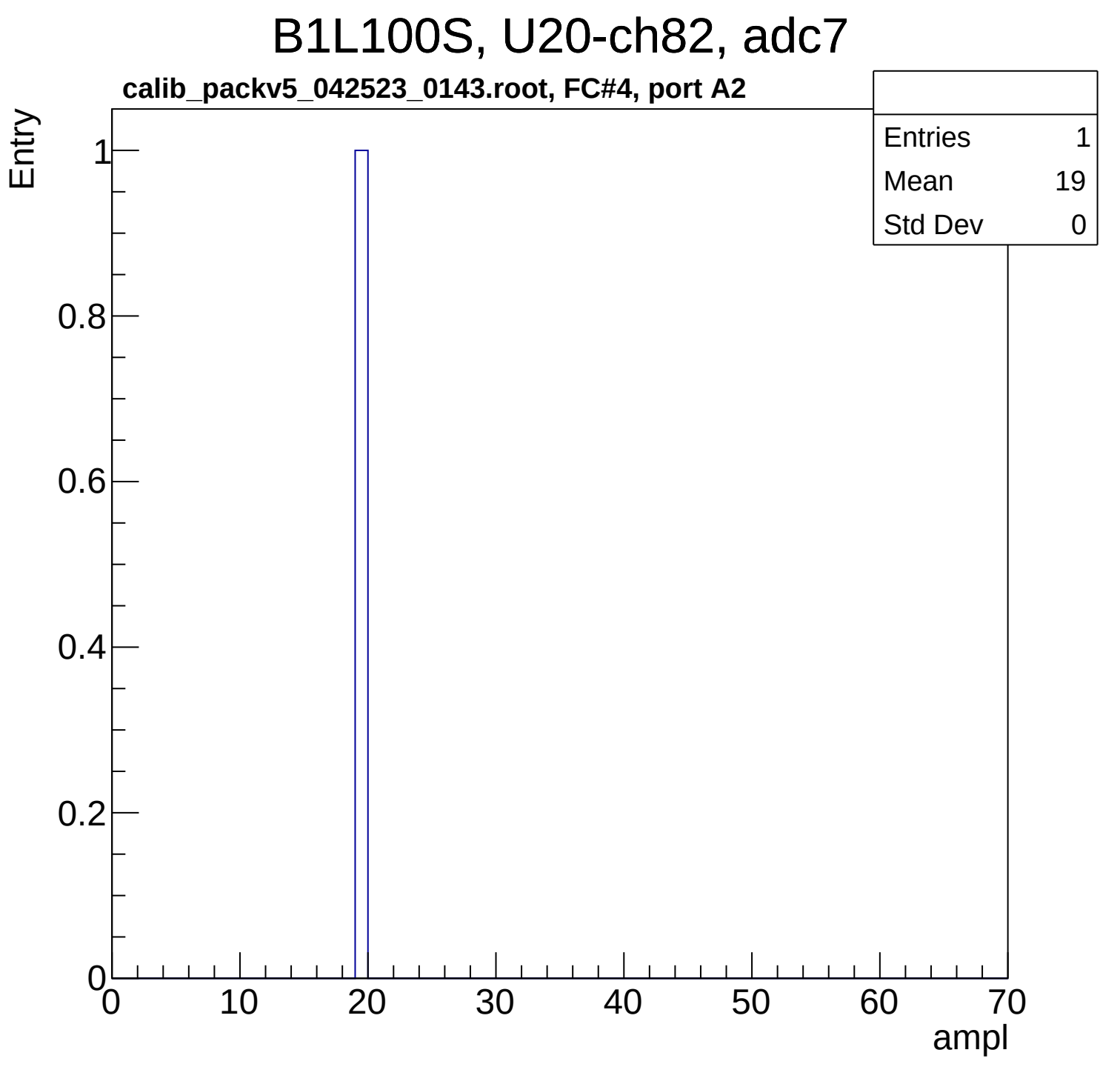
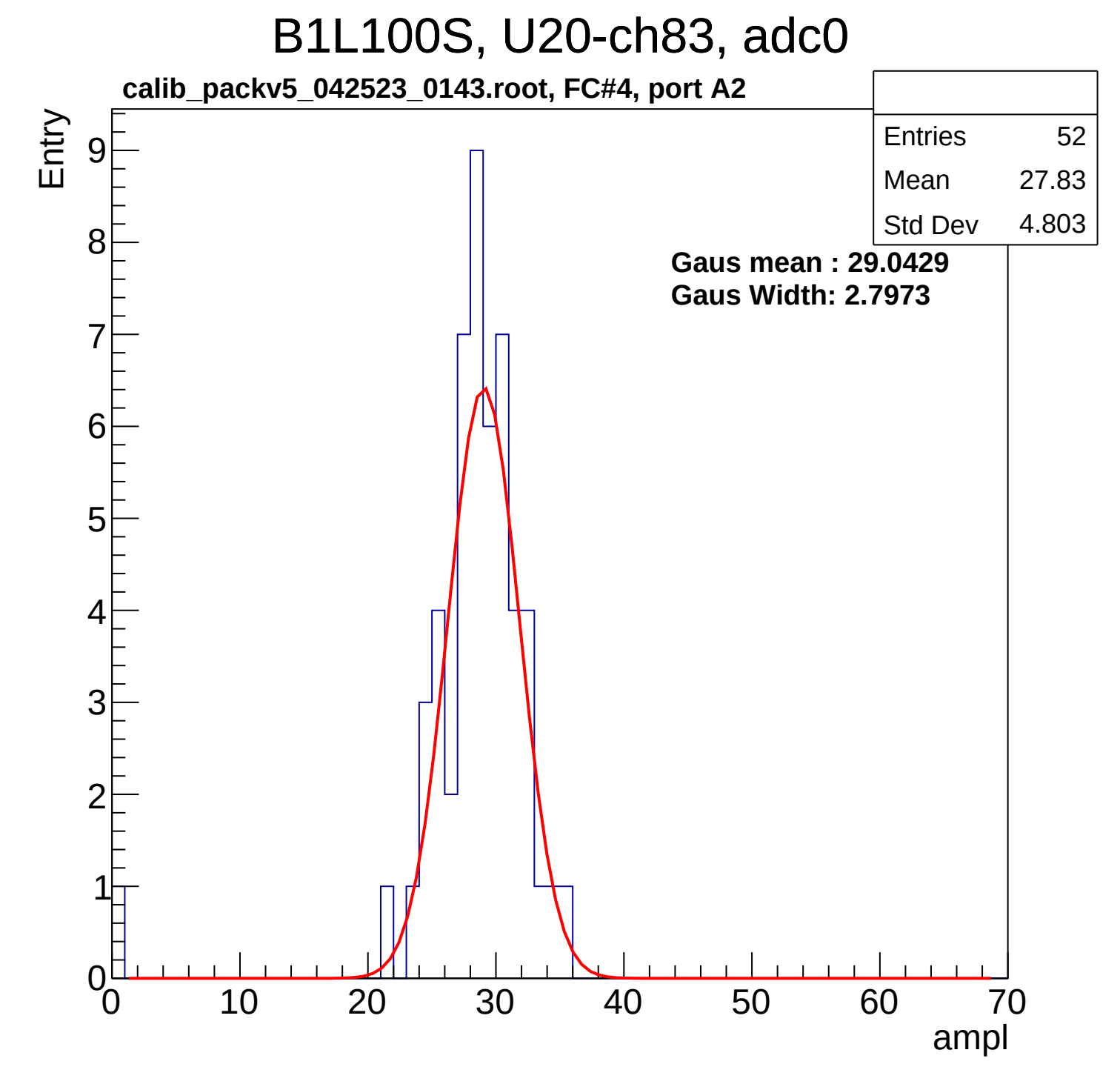
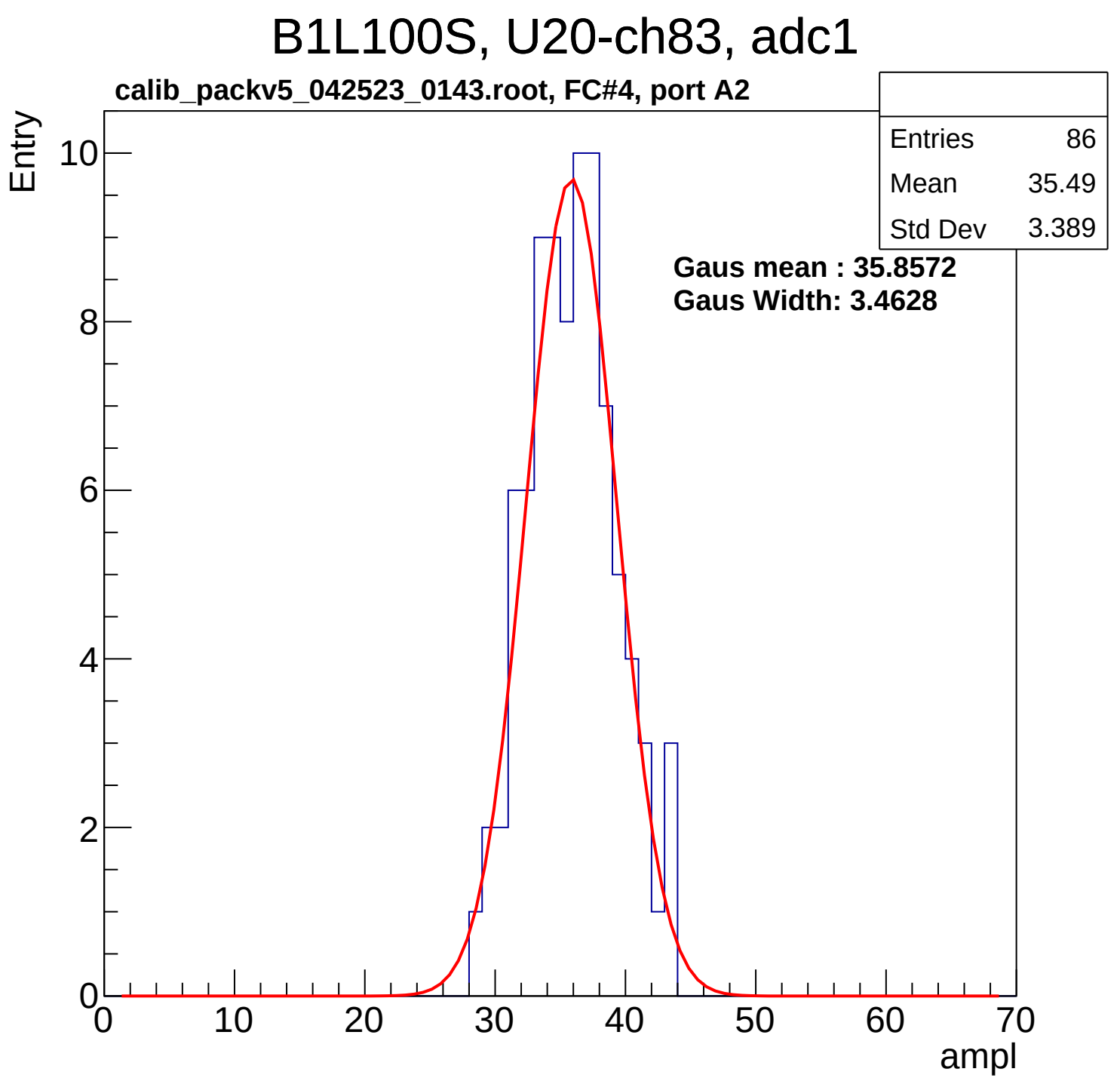

B1L100S, U20-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 58.06
Std Dev 15.03
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch82, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 19
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 27.83
Std Dev 4.803
Gaus mean : 29.0429
Gaus Width: 2.7973
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch83, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 35.49
Std Dev 3.389
Gaus mean : 35.8572
Gaus Width: 3.4628
Entry
ampl
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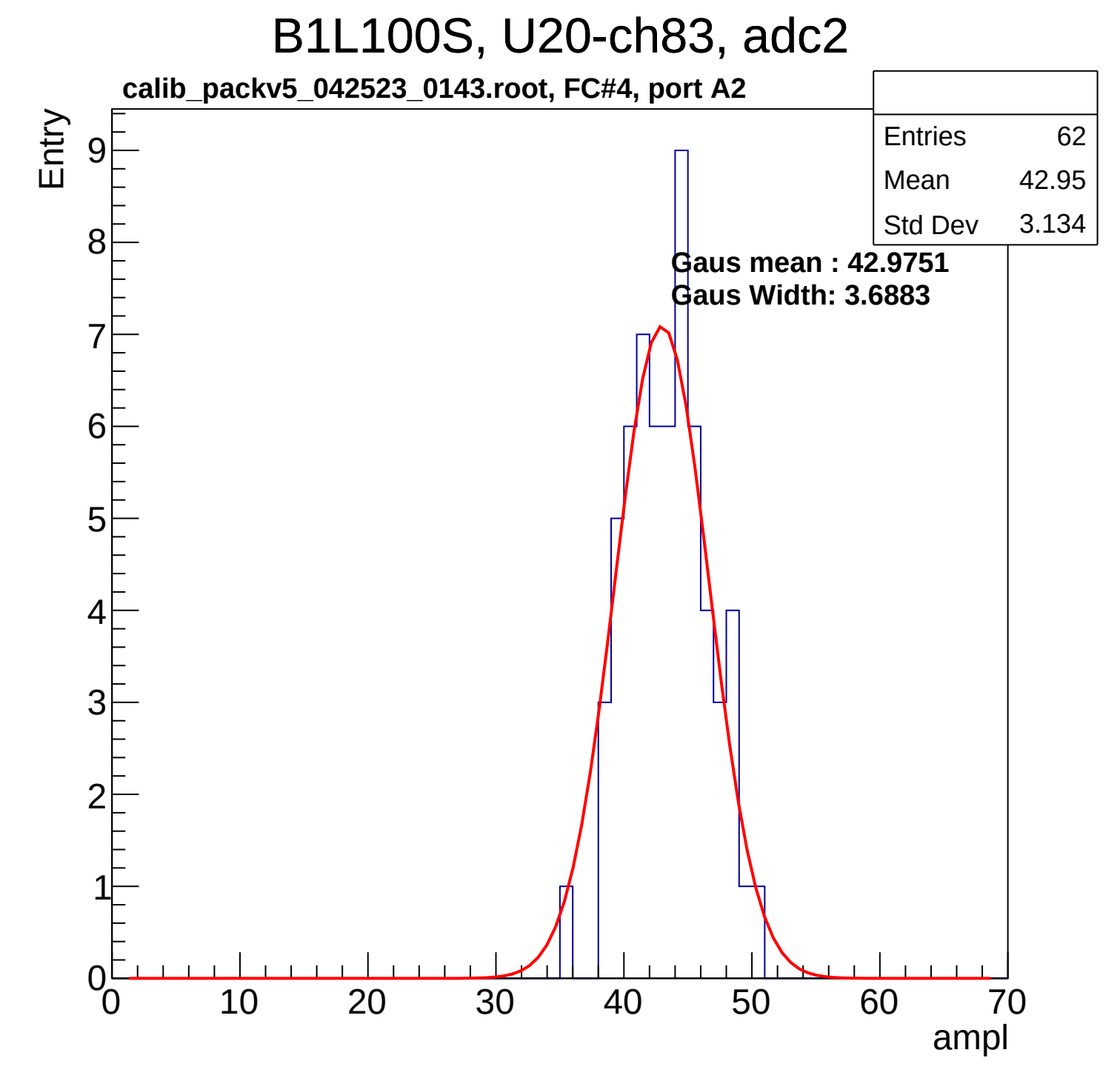
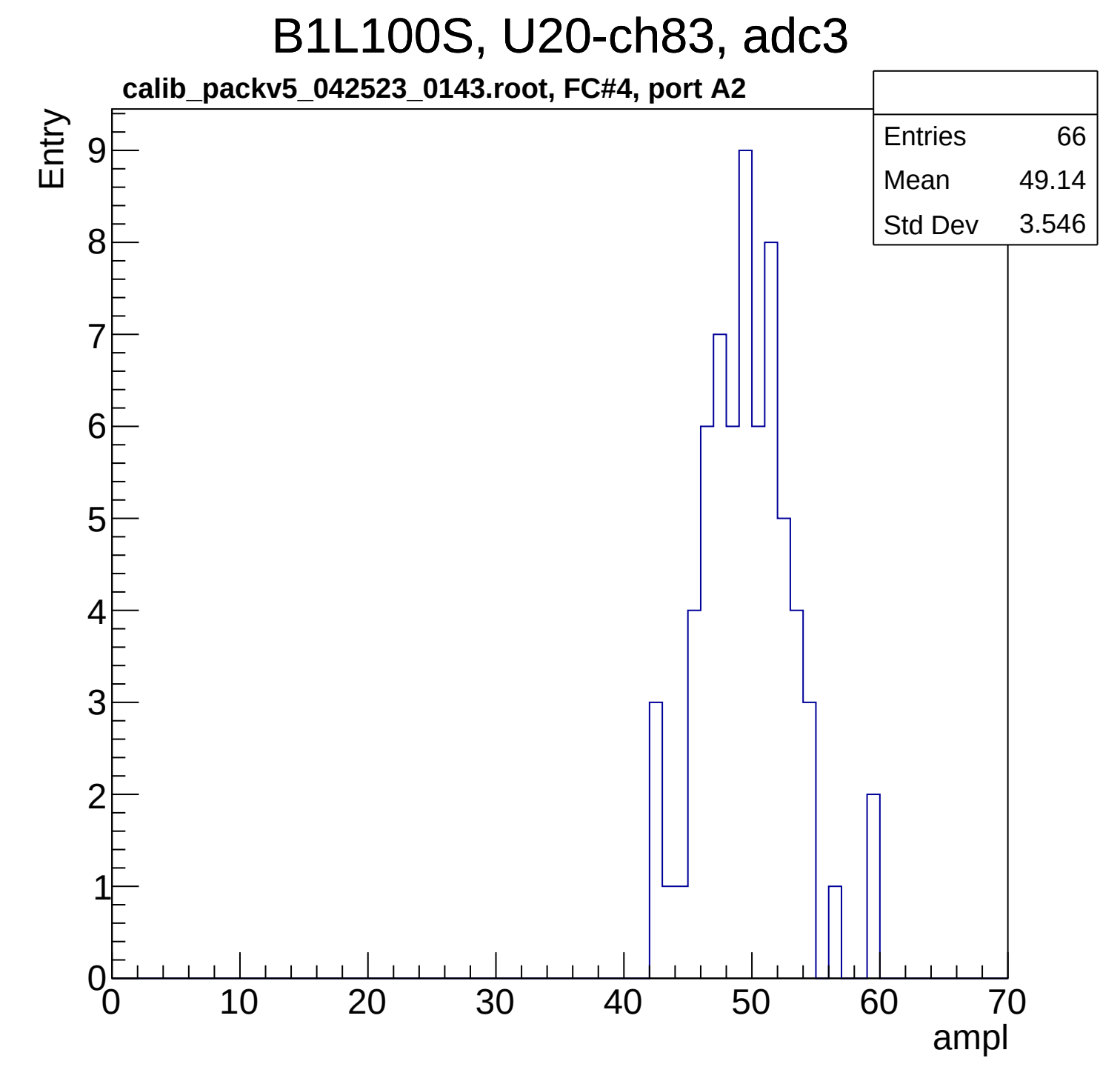
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 42.95
Std Dev 3.134
Gaus mean : 42.9751
Gaus Width: 3.6883
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U20-ch83, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.14
Std Dev 3.546
Entry
ampl
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6
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8
9
0
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60
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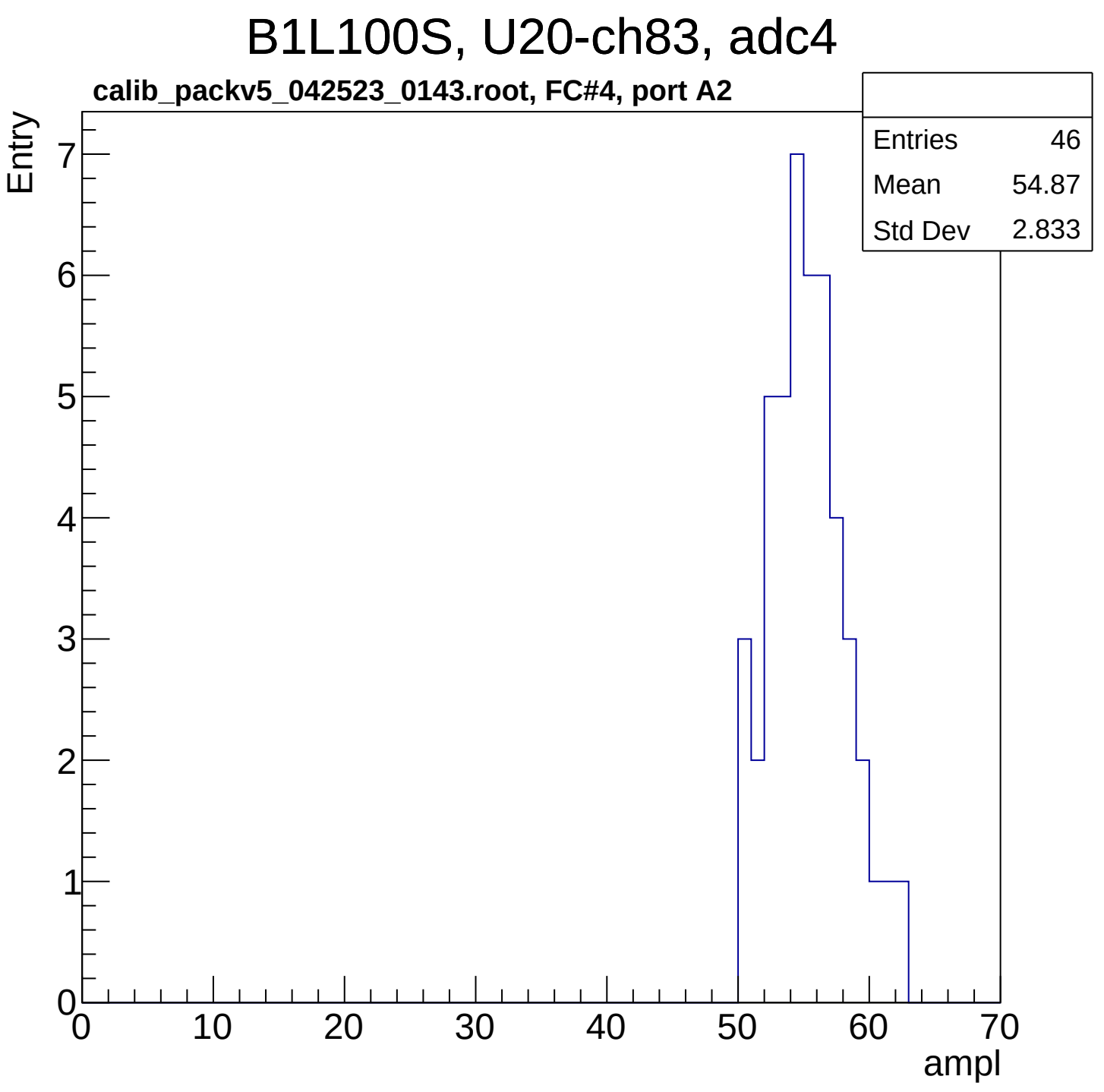
B1L100S, U20-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 54.87
Std Dev 2.833
Entry
ampl
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0
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40
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60
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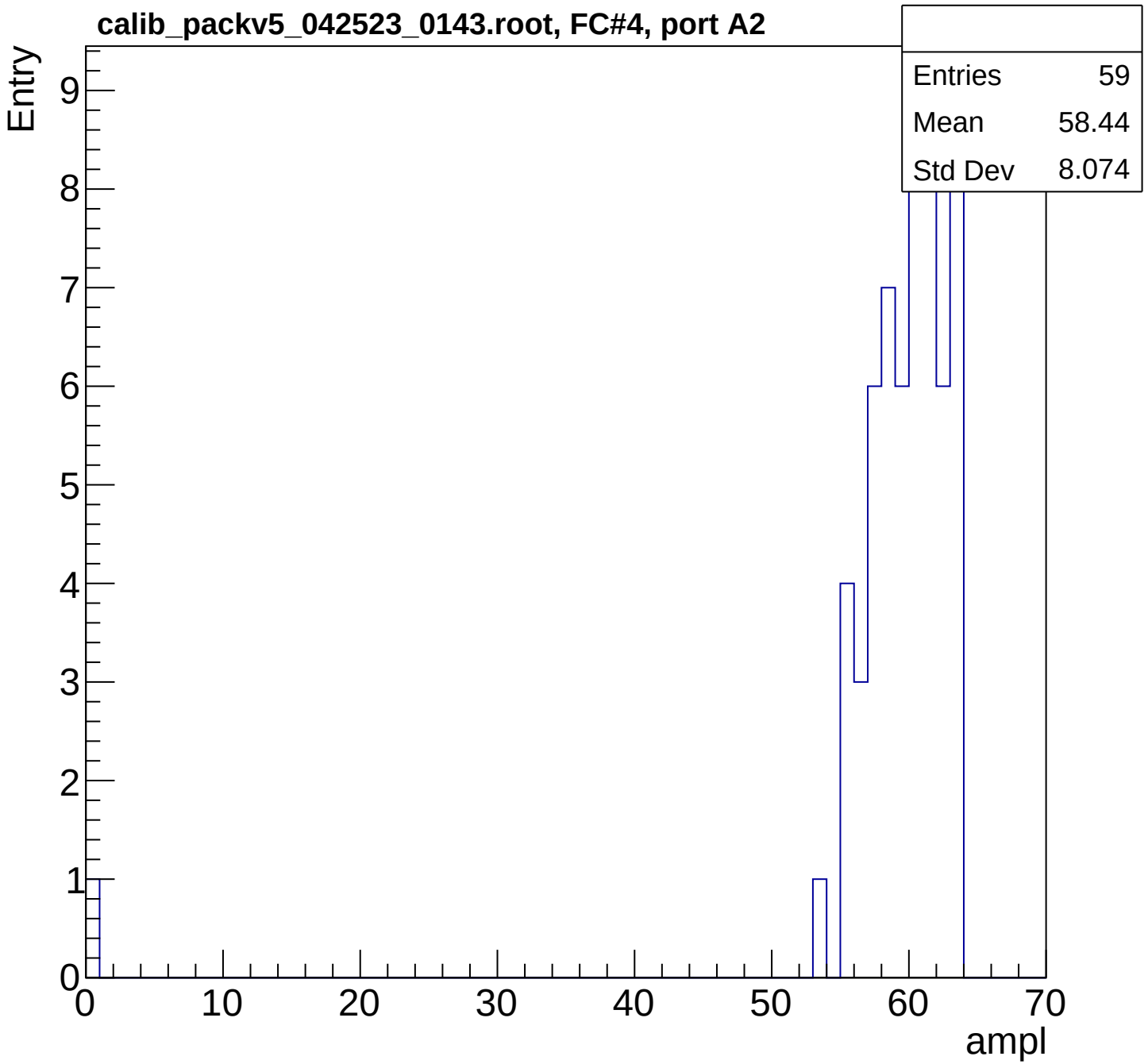
B1L100S, U20-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.44
Std Dev 8.074
Entry
ampl
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0
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60
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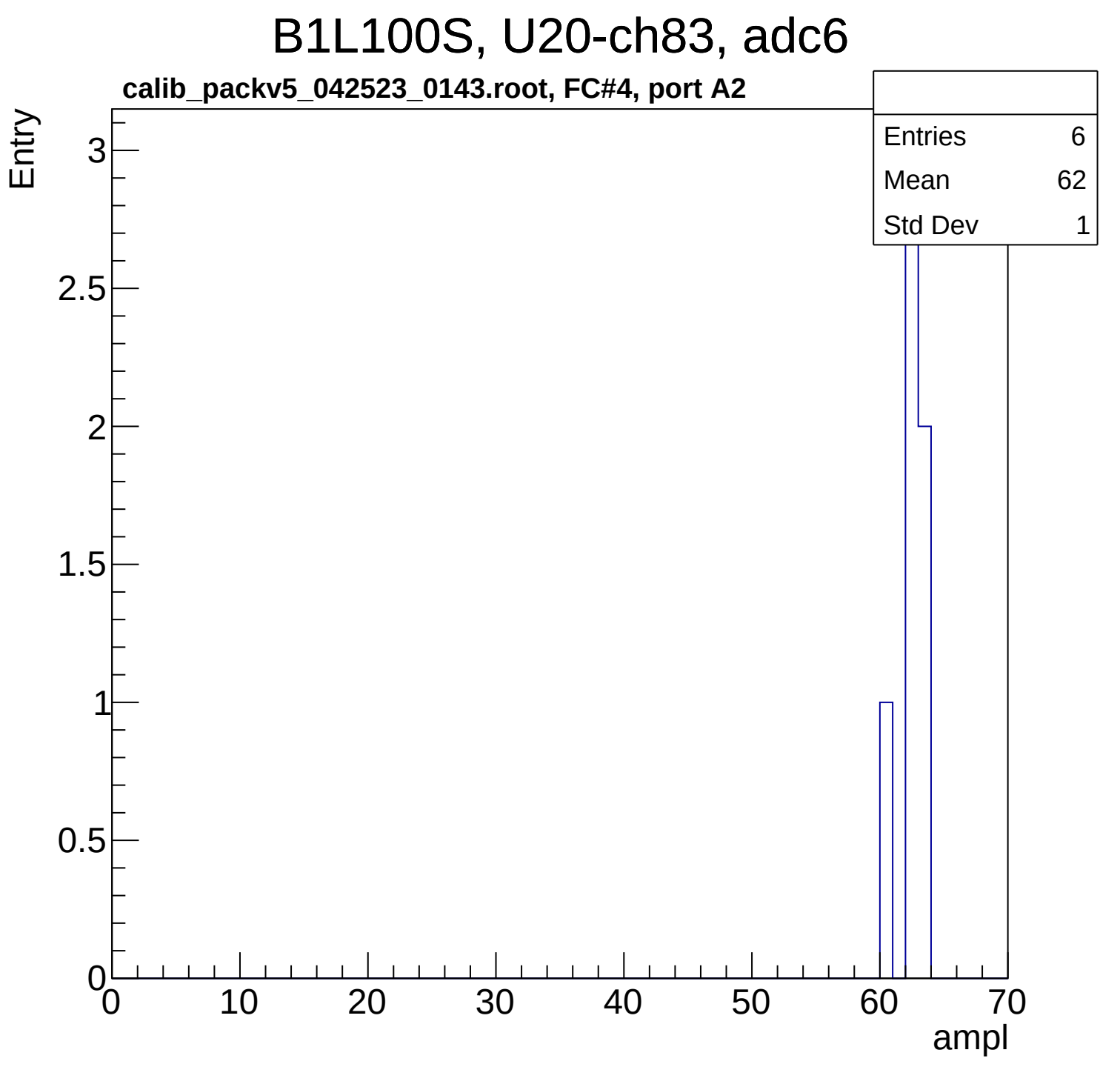
B1L100S, U20-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62
Std Dev 1
Entry
ampl
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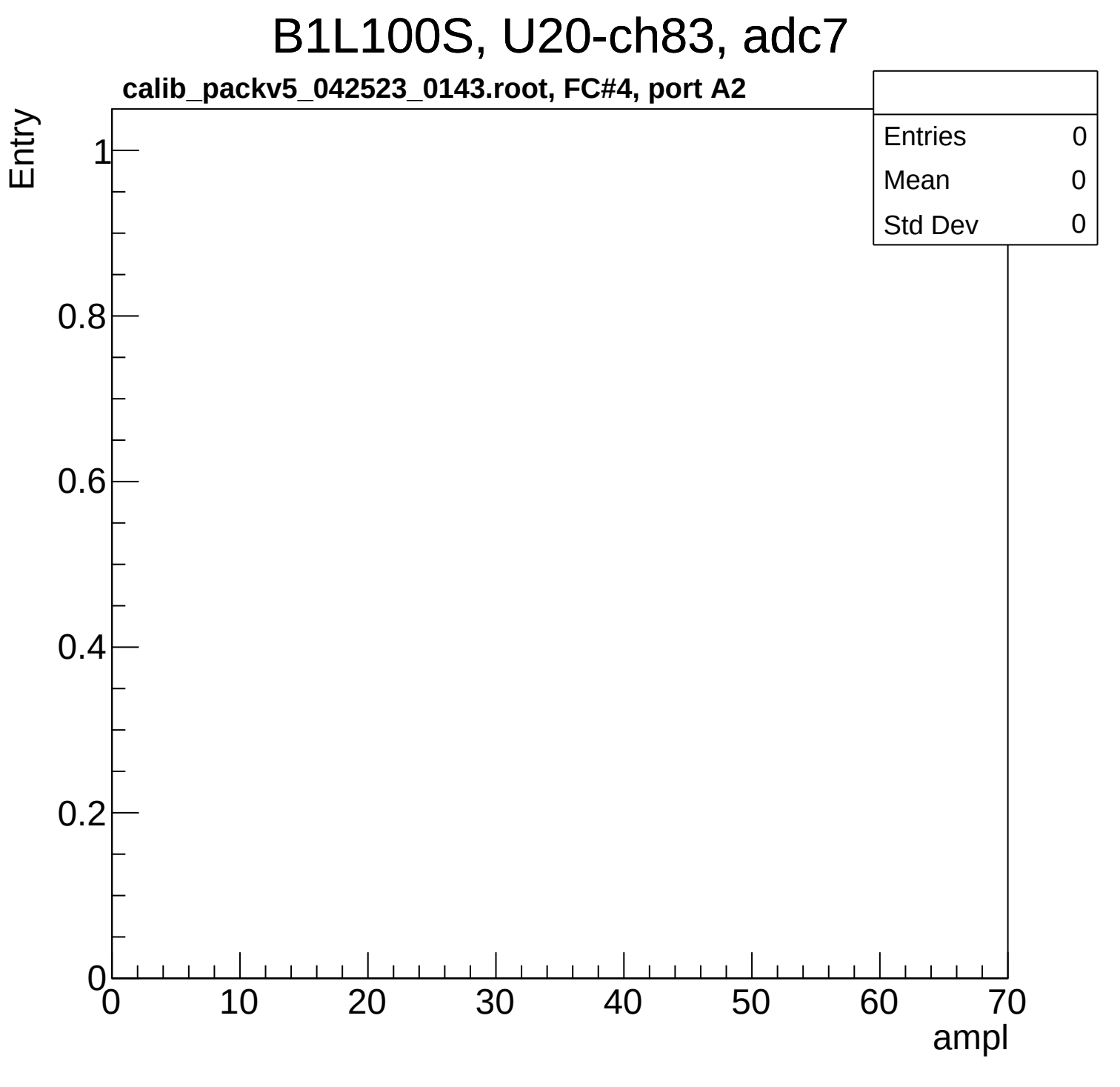
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch83, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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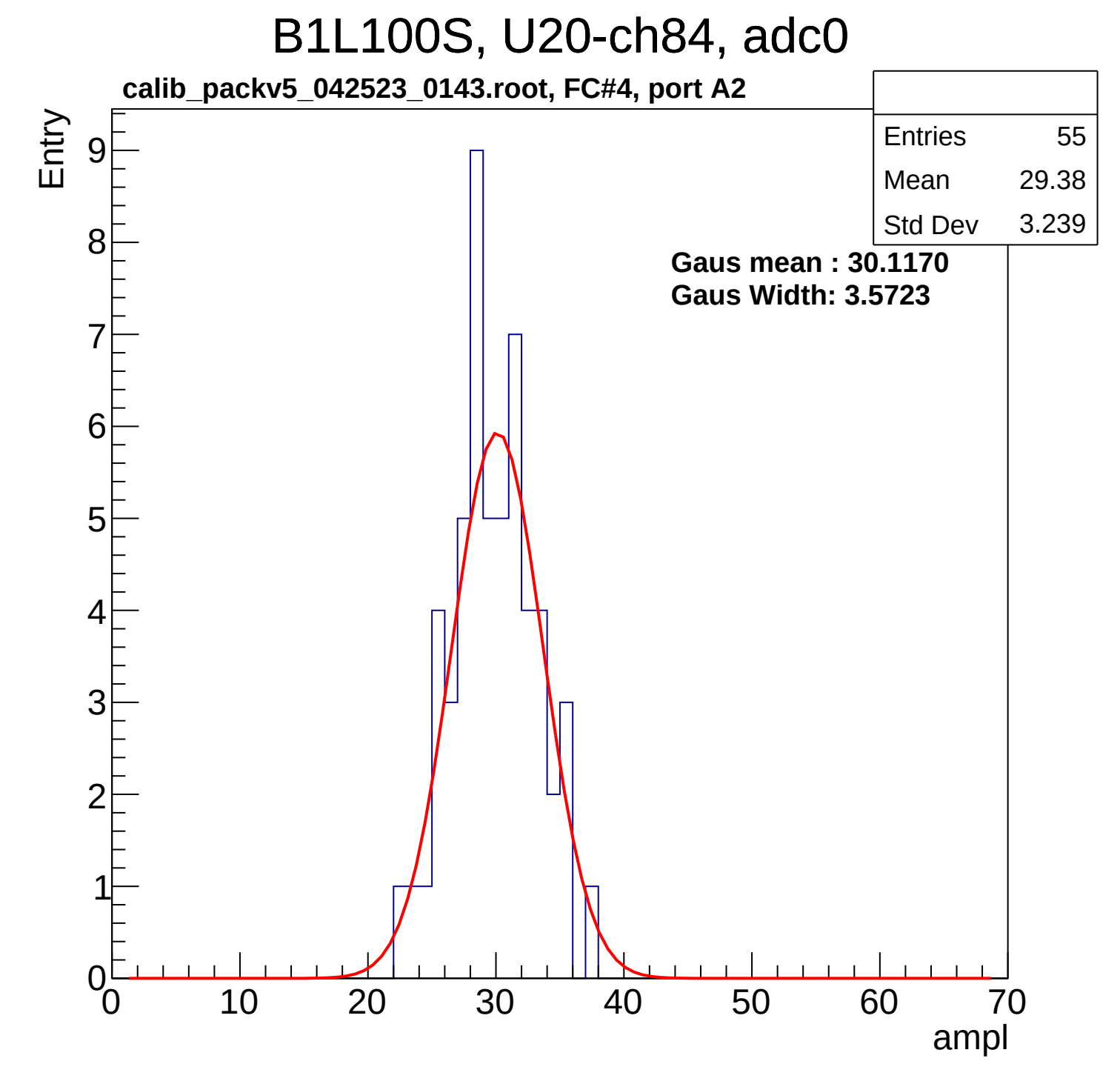
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 29.38
Std Dev 3.239
Gaus mean : 30.1170
Gaus Width: 3.5723
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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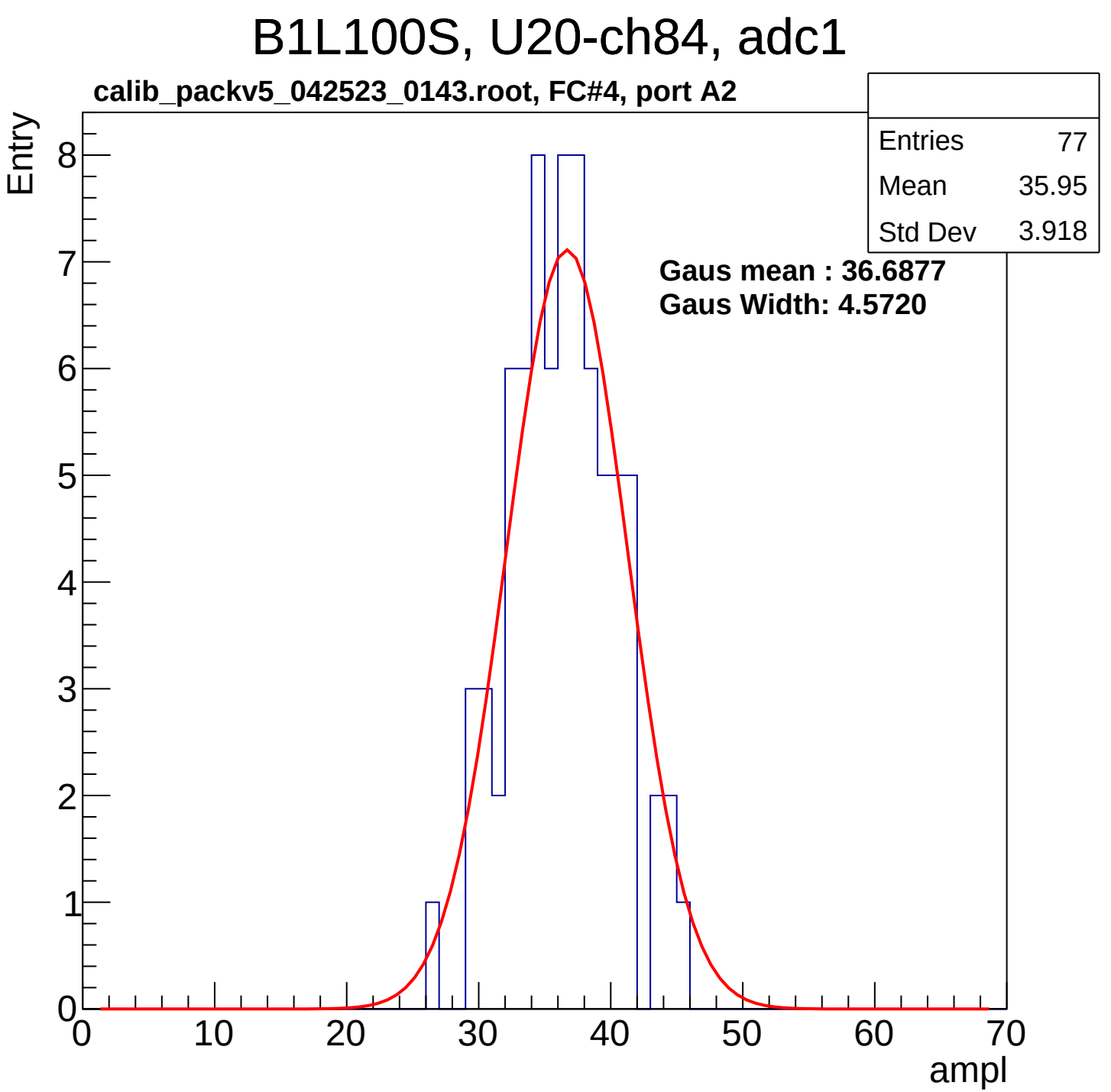
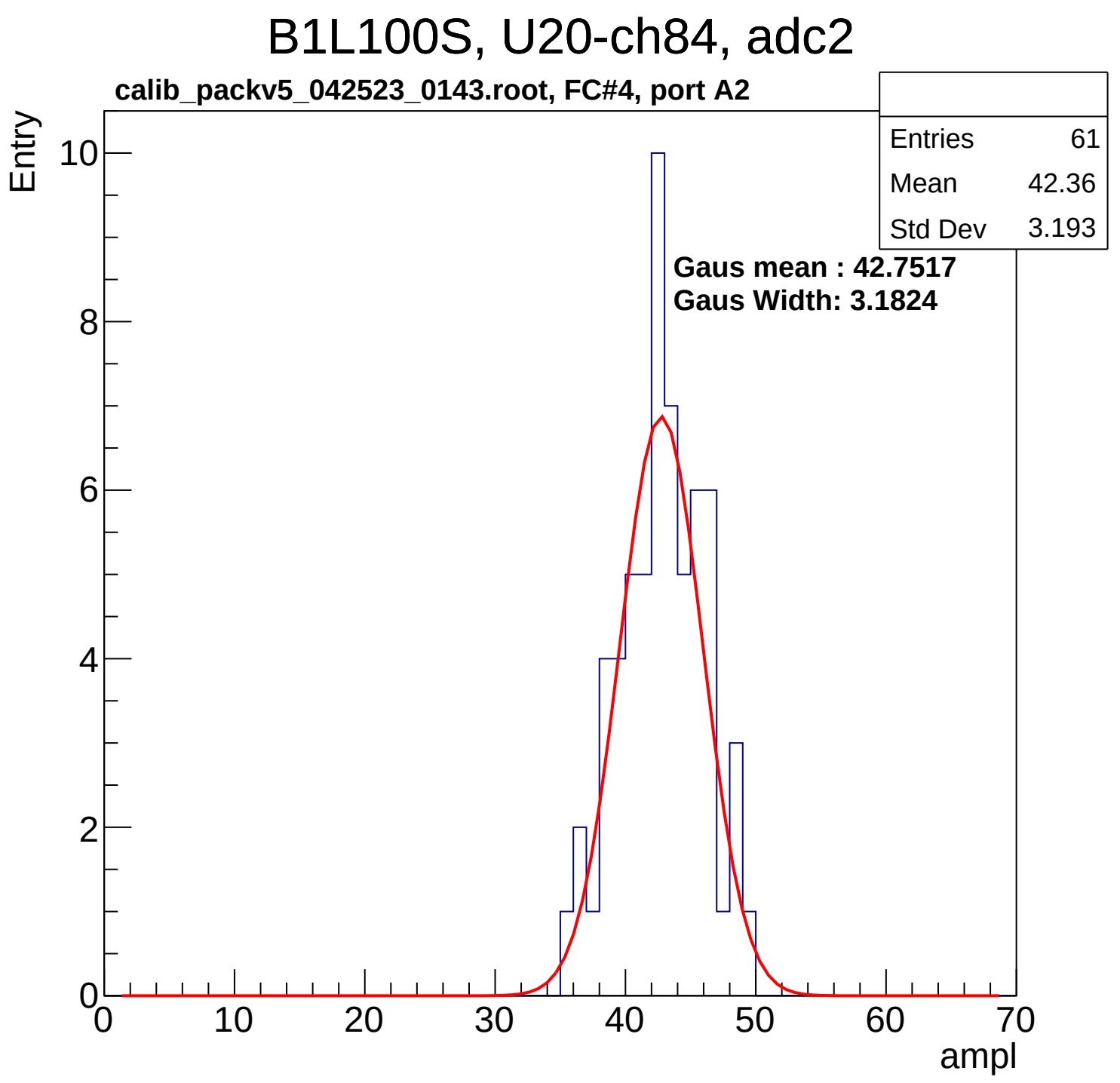
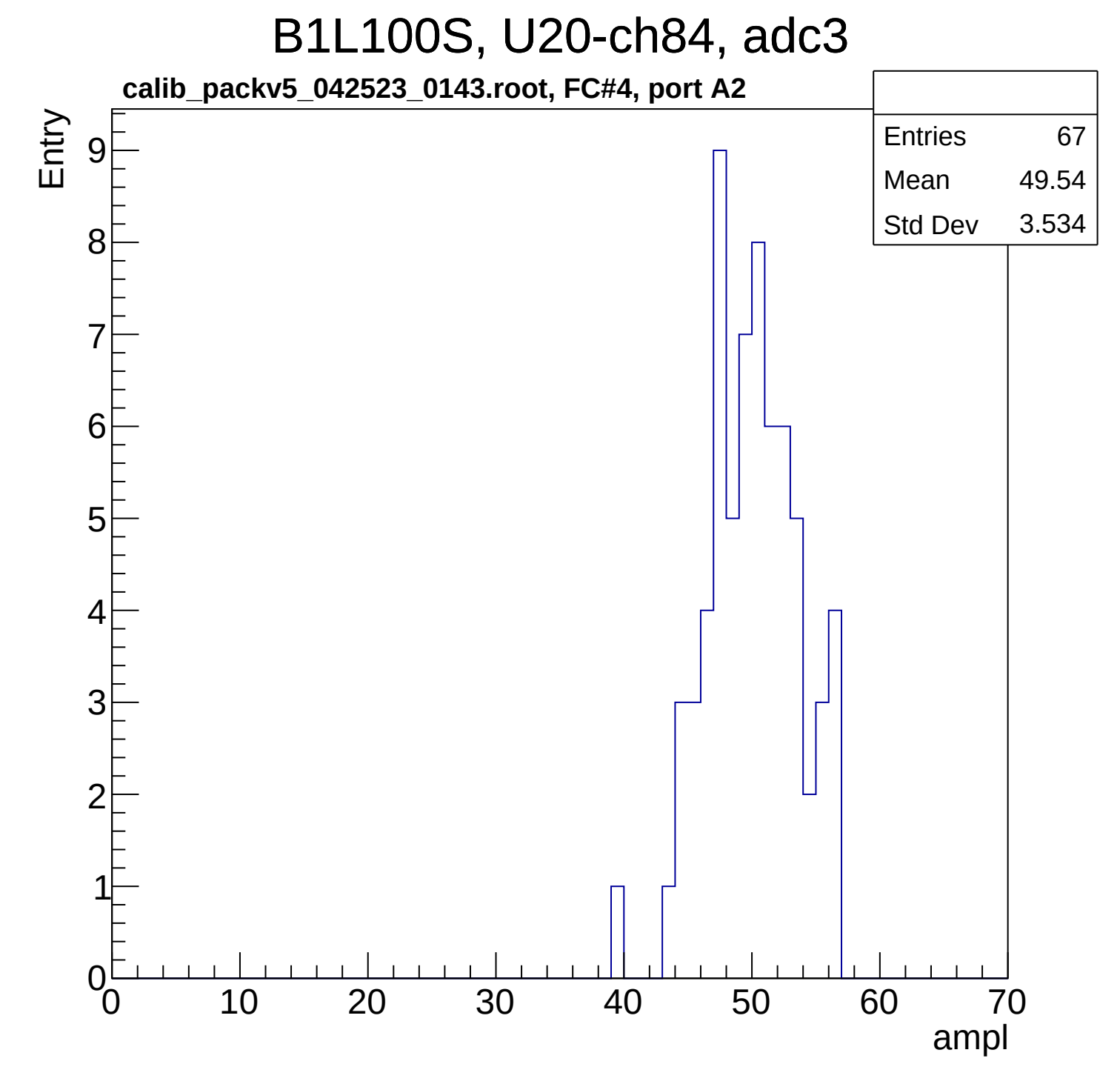
20
30
40
50
60
70


B1L100S, U20-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 35.95
Std Dev 3.918
Gaus mean : 36.6877
Gaus Width: 4.5720
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch84, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.36
Std Dev 3.193
Gaus mean : 42.7517
Gaus Width: 3.1824
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch84, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.54
Std Dev 3.534
Entry
ampl
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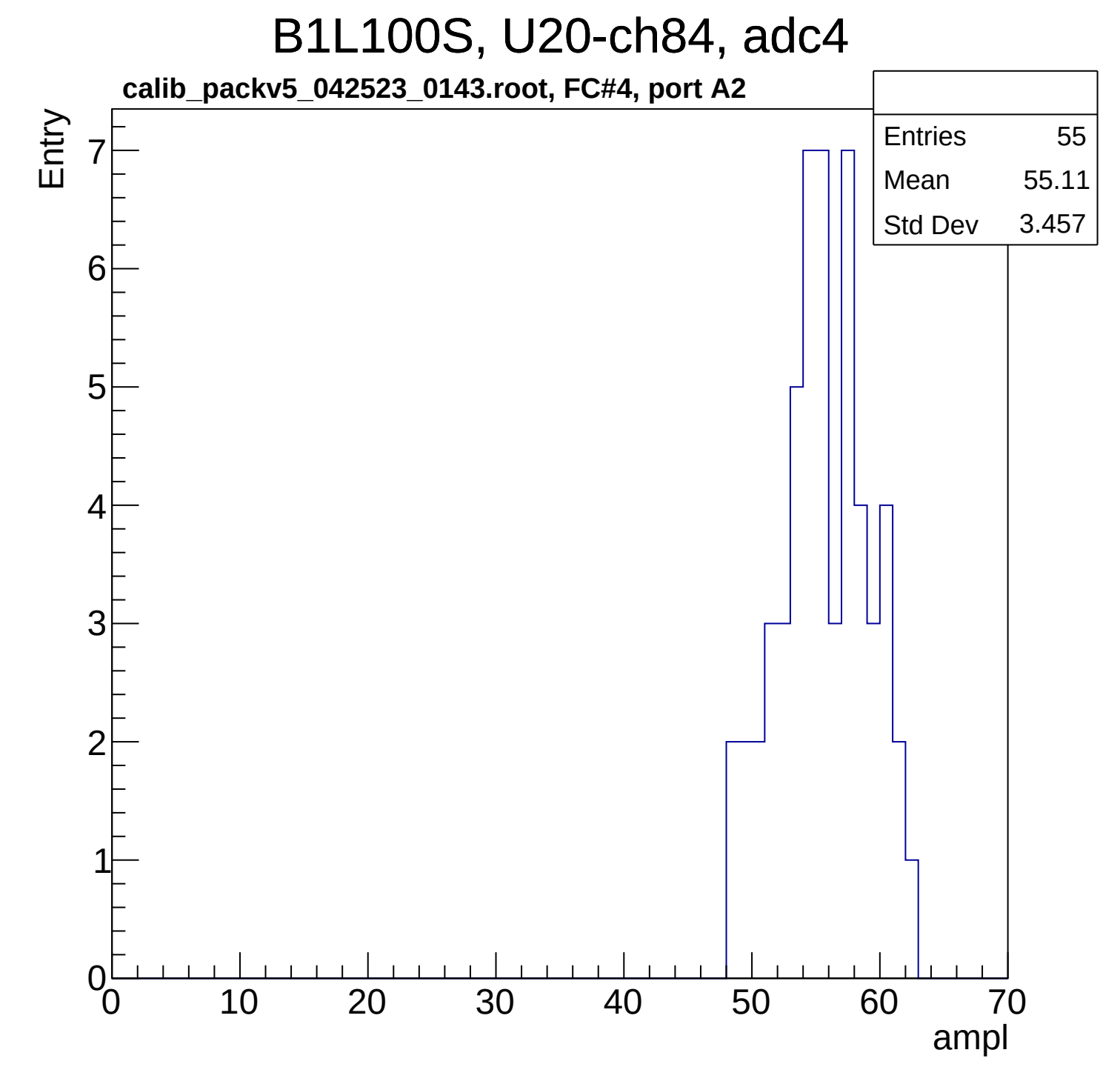
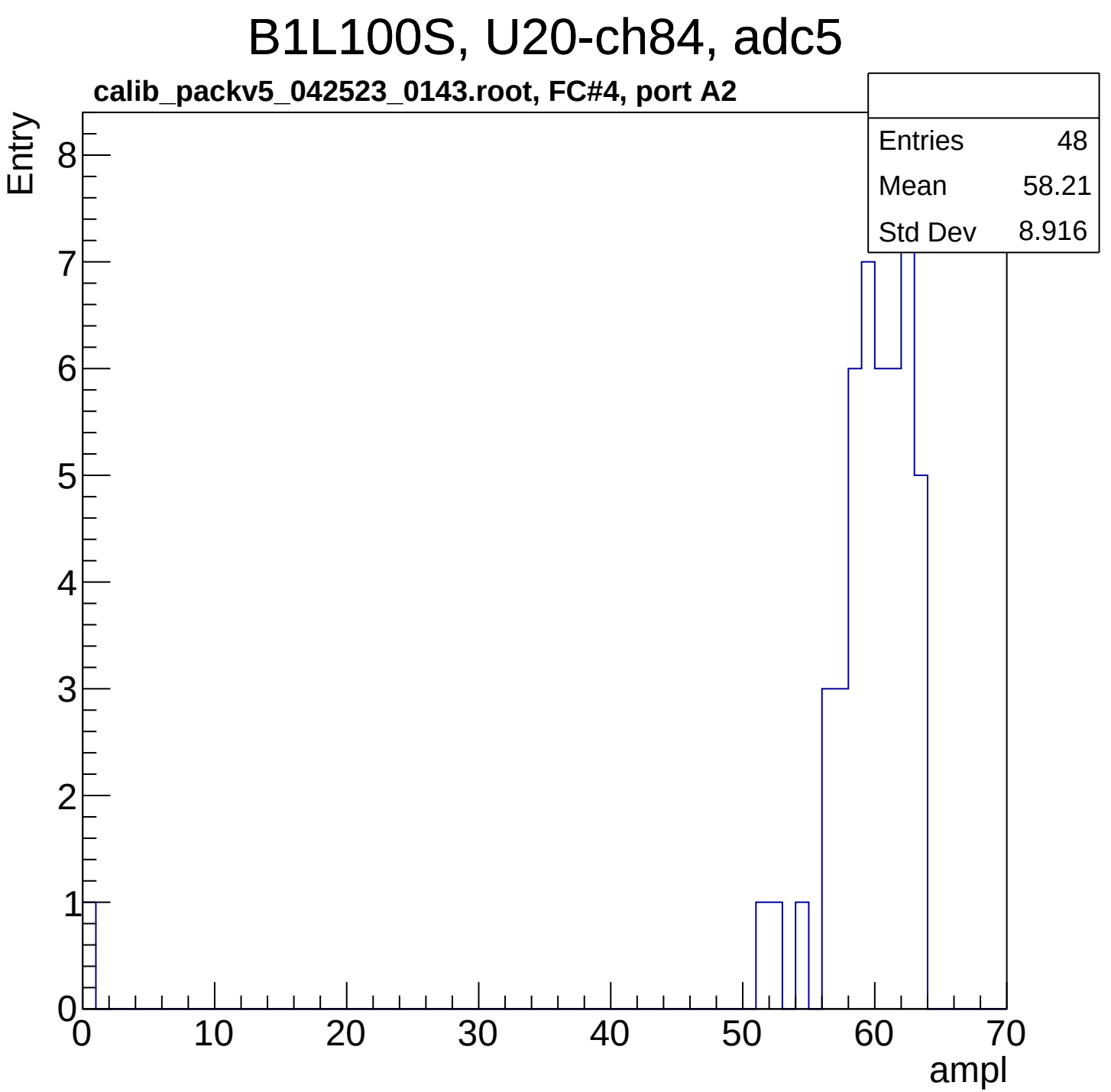
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch84, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.11
Std Dev 3.457
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.21
Std Dev 8.916
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
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60
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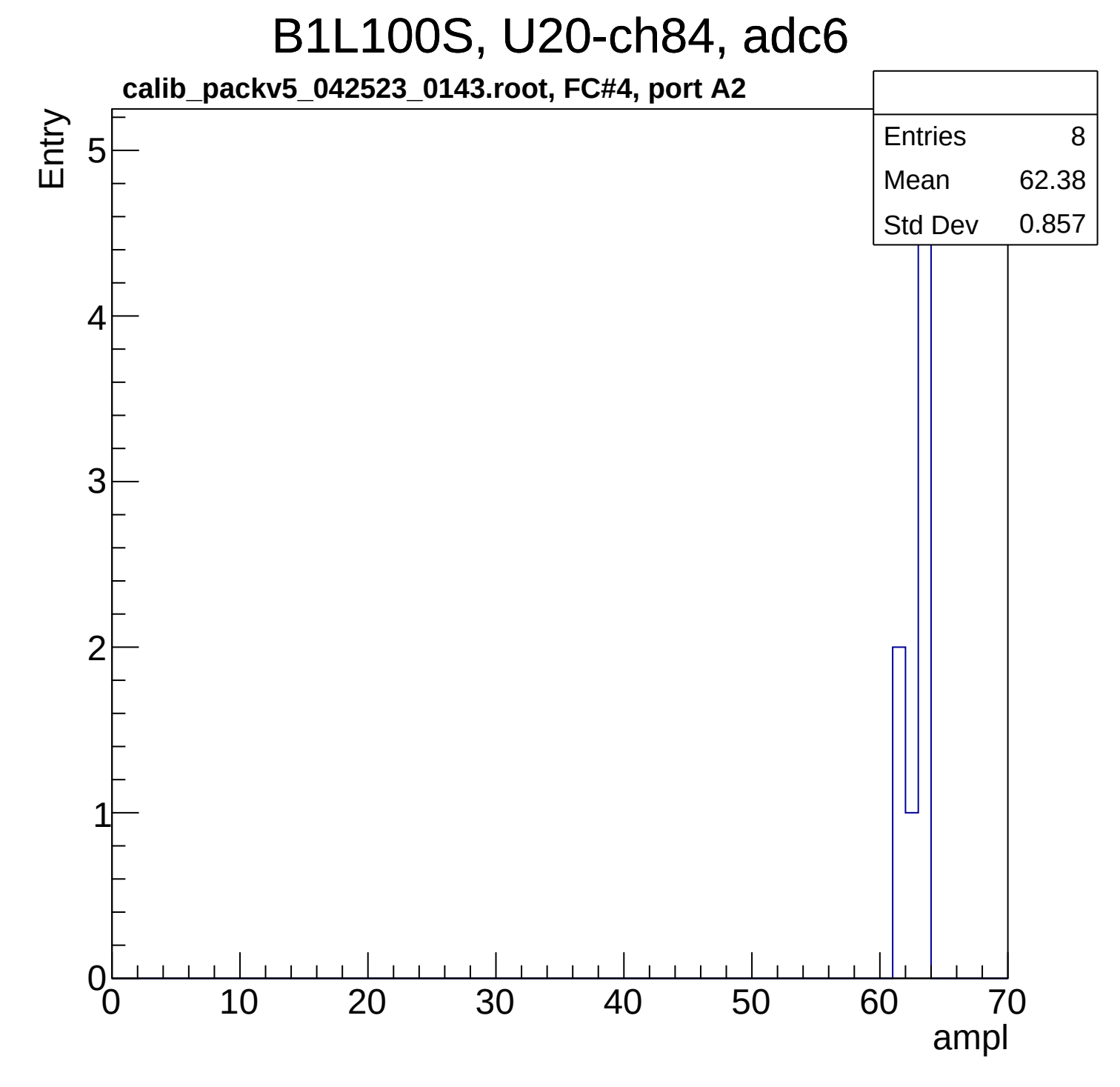
B1L100S, U20-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.38
Std Dev 0.857
Entry
ampl
0
1
2
3
4
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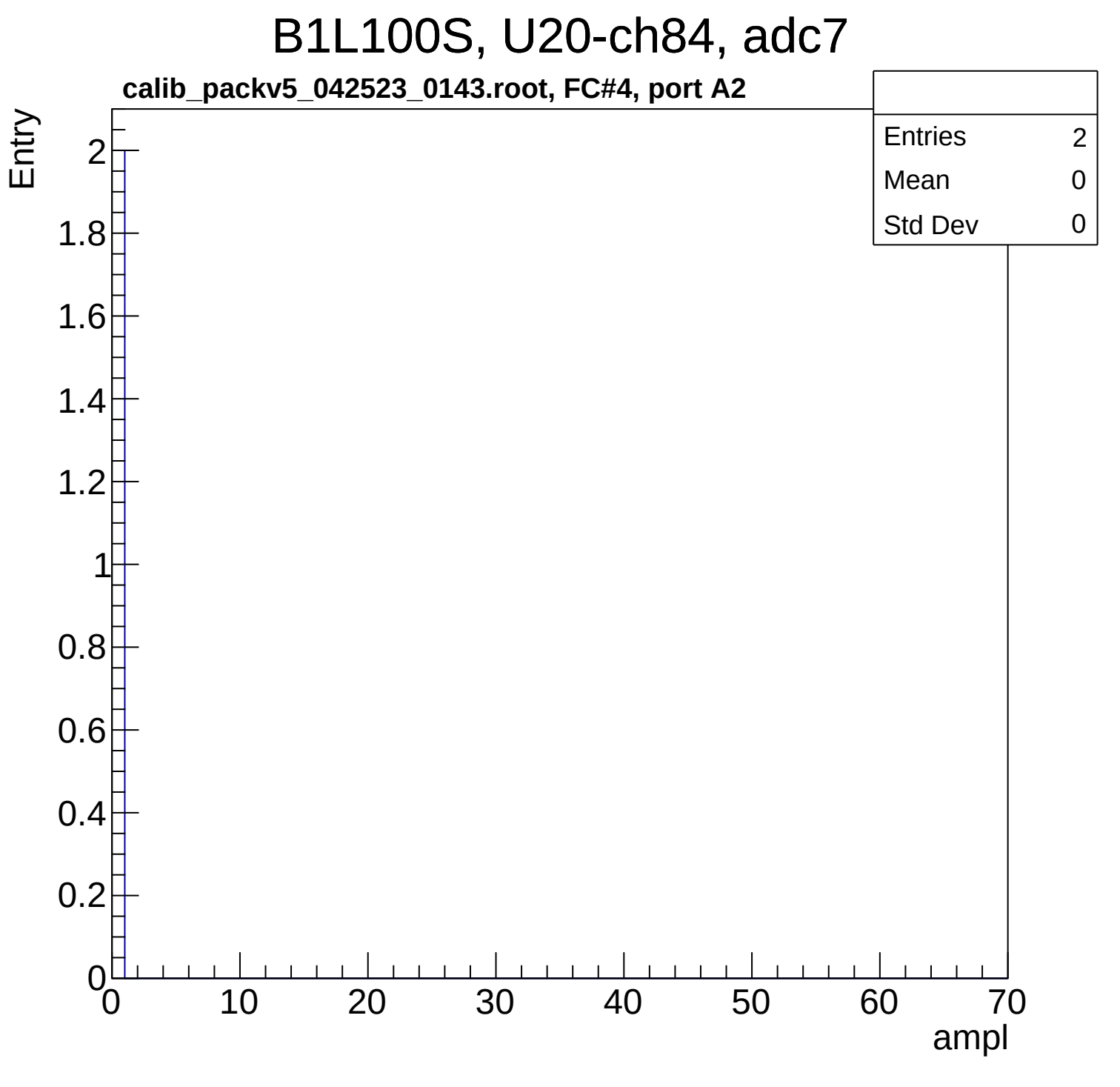
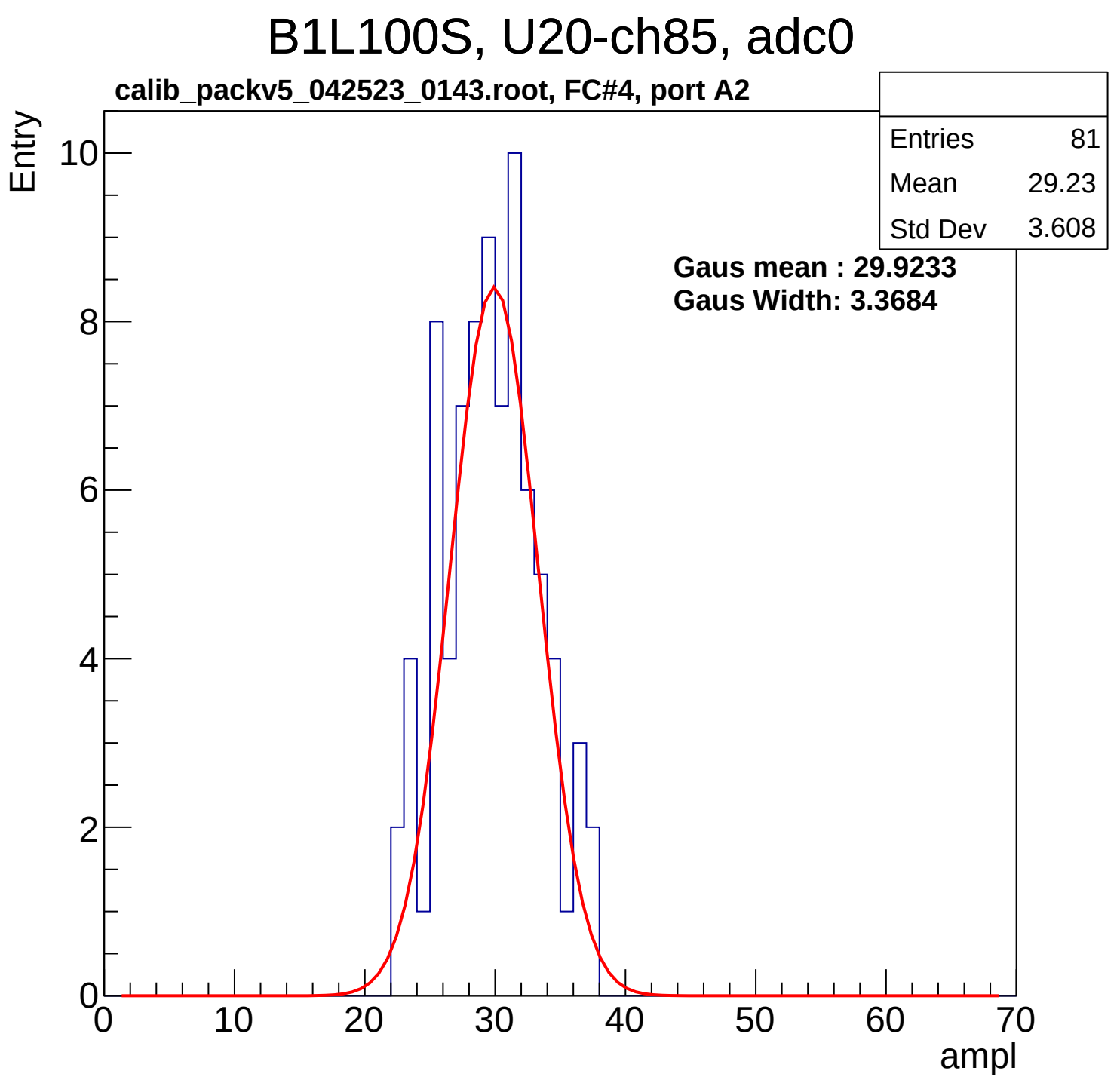
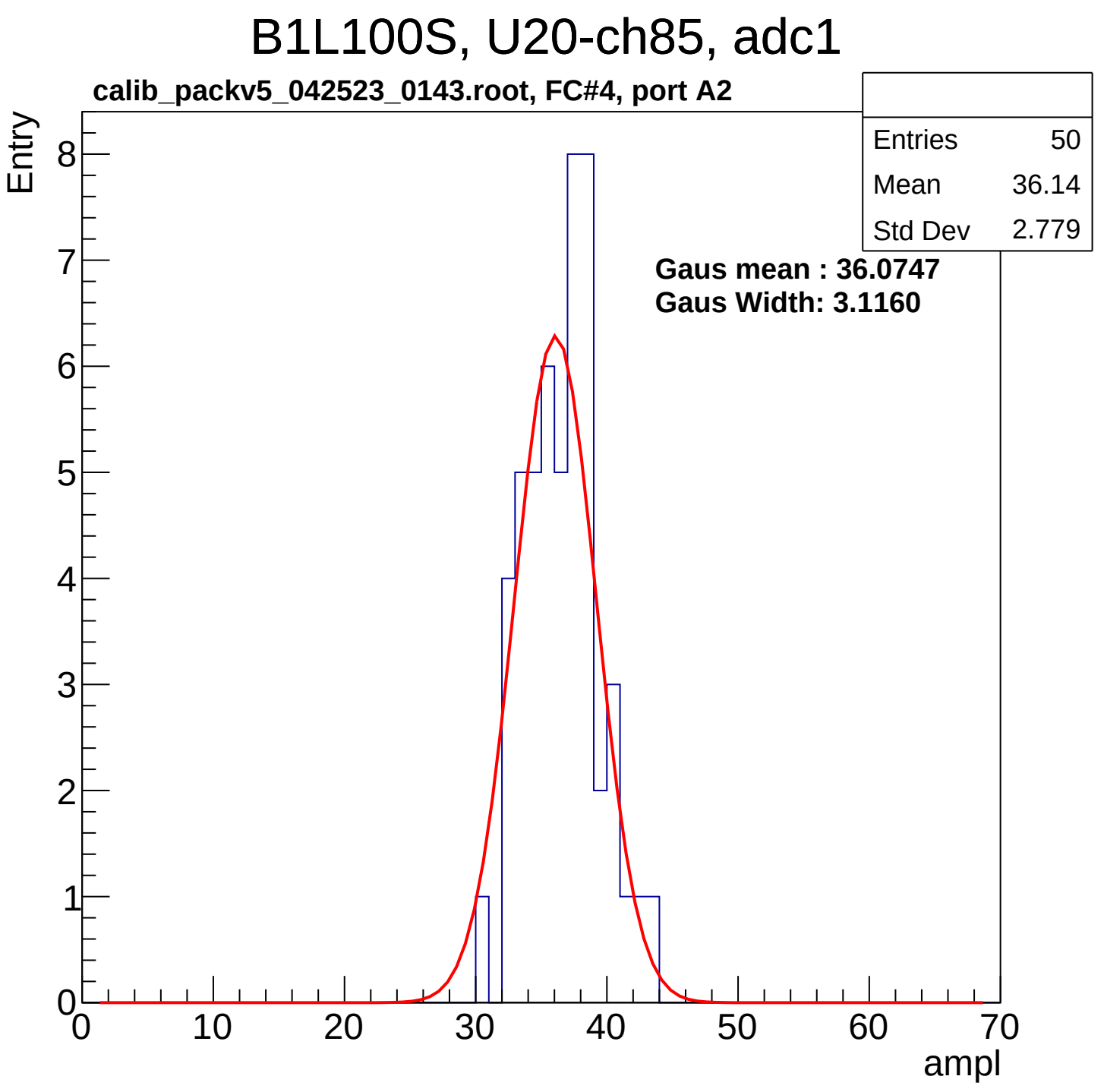
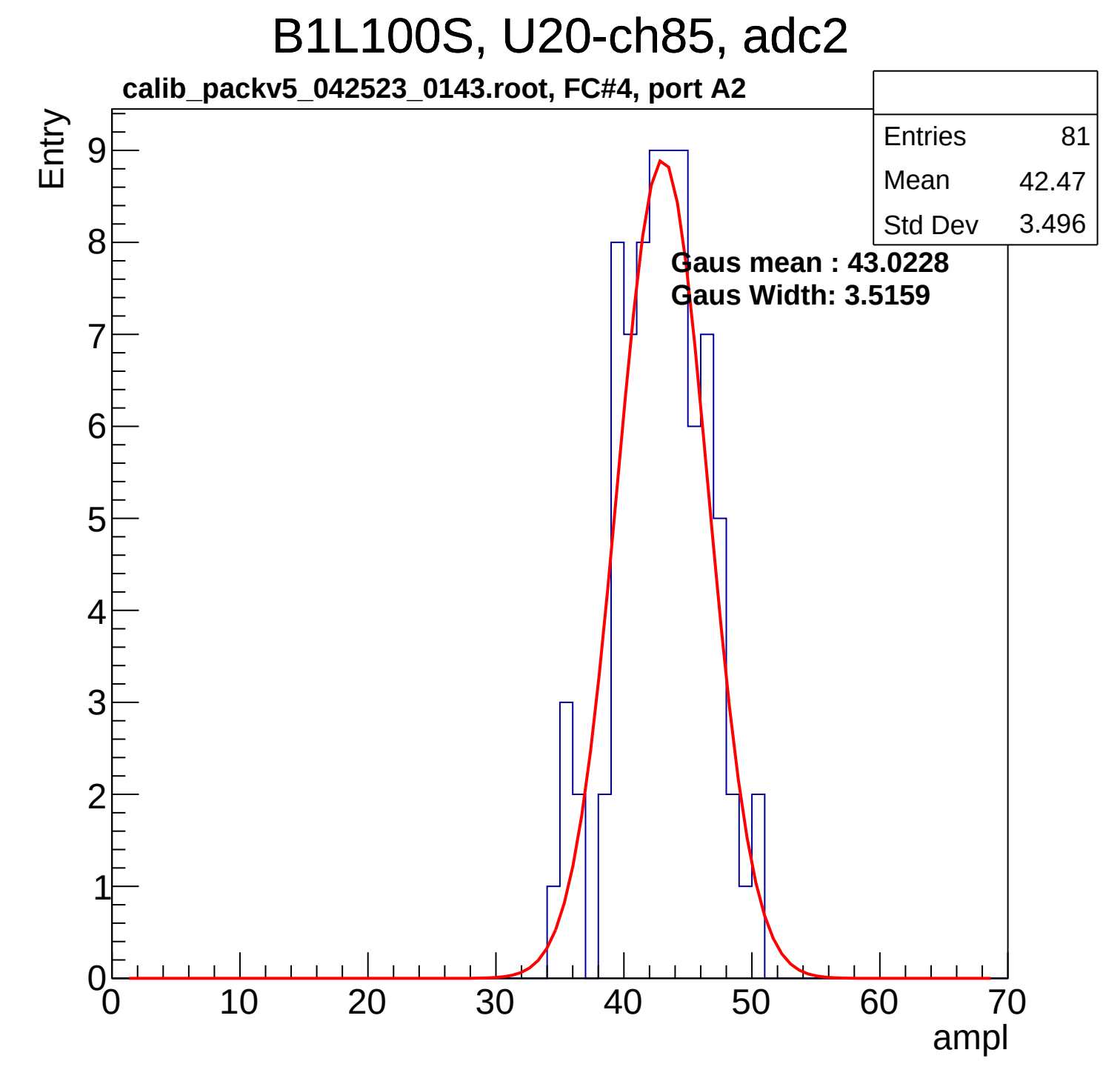
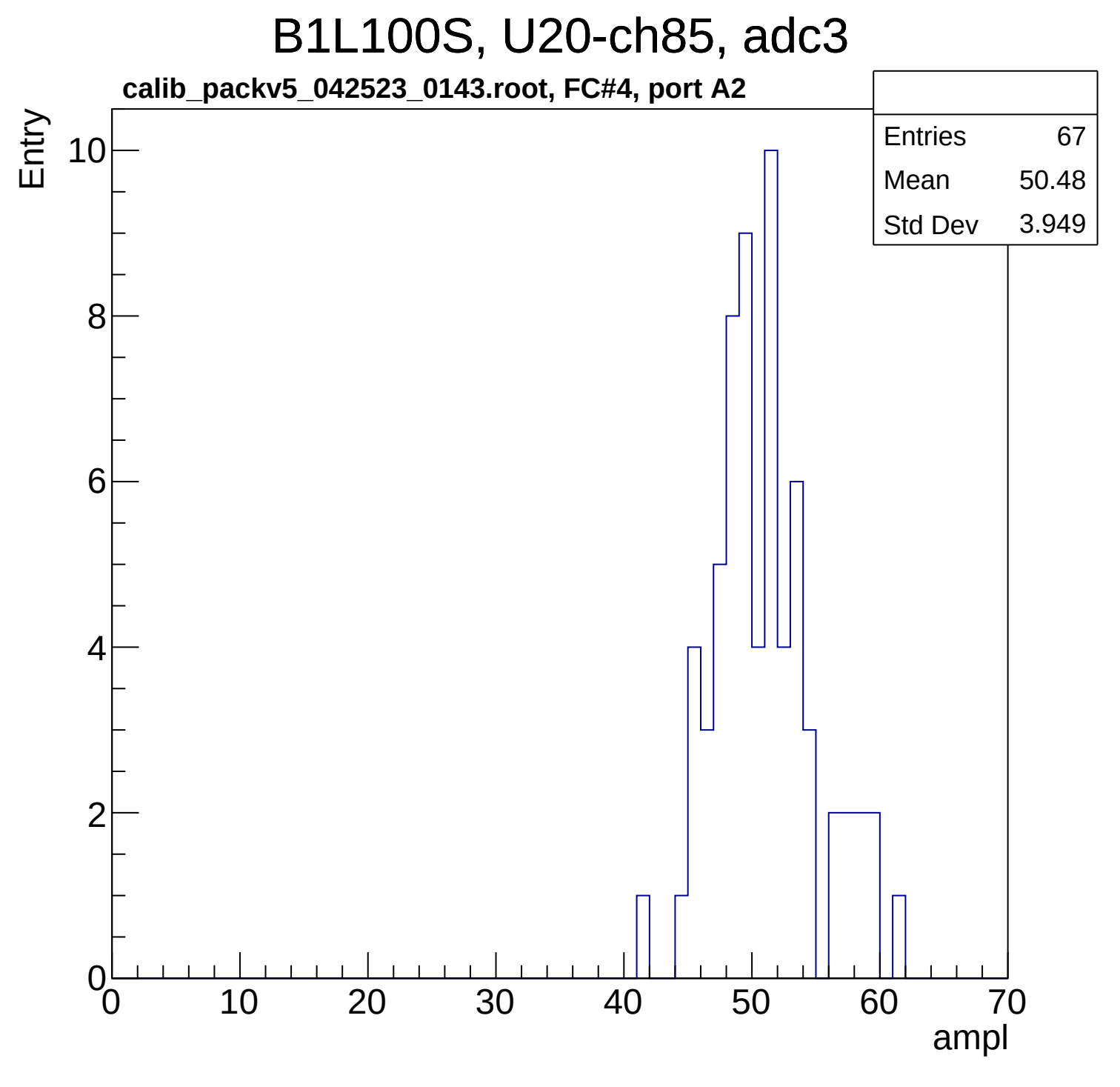
0
10
20
30
40
50
60
70

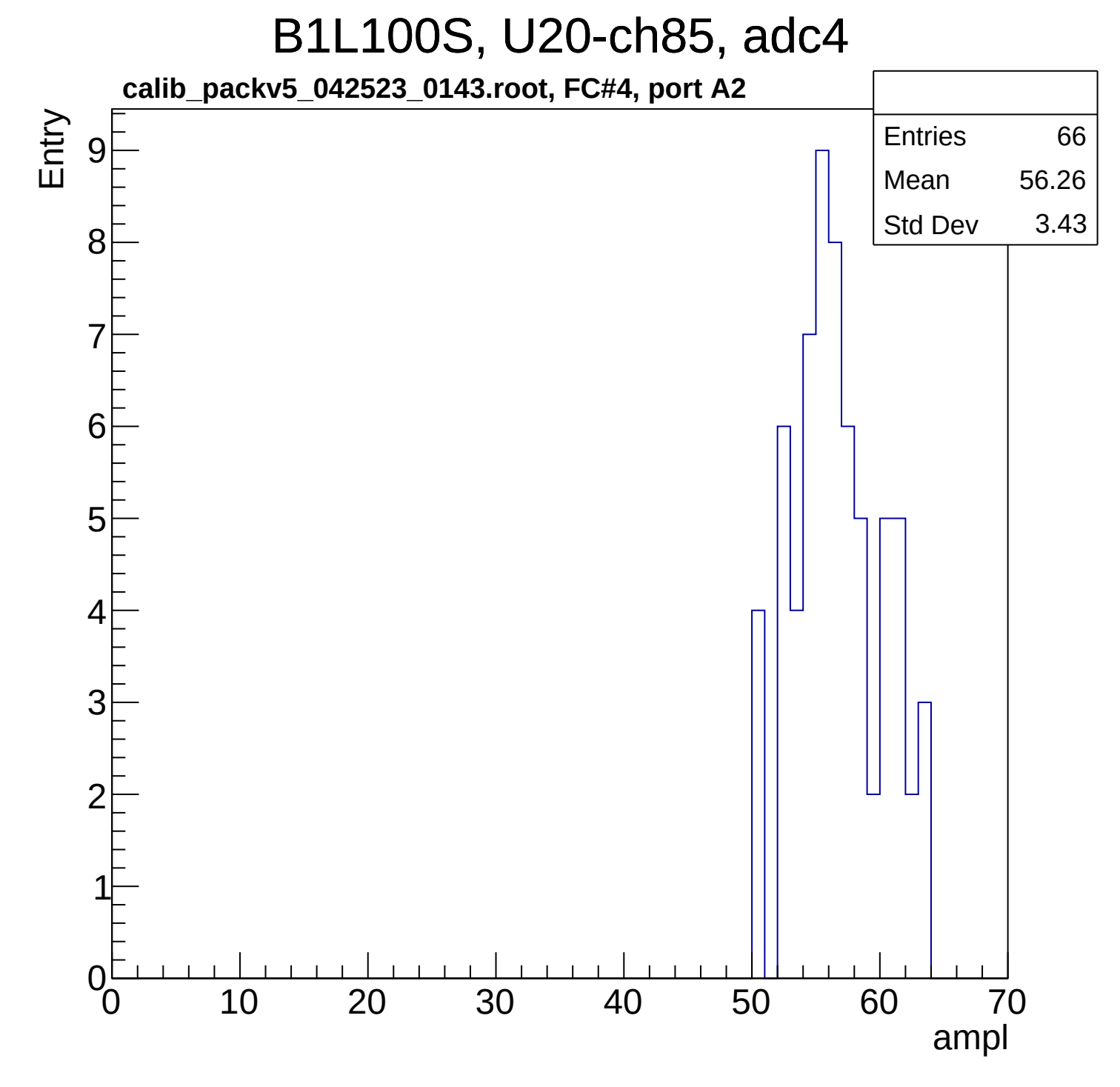

B1L100S, U20-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

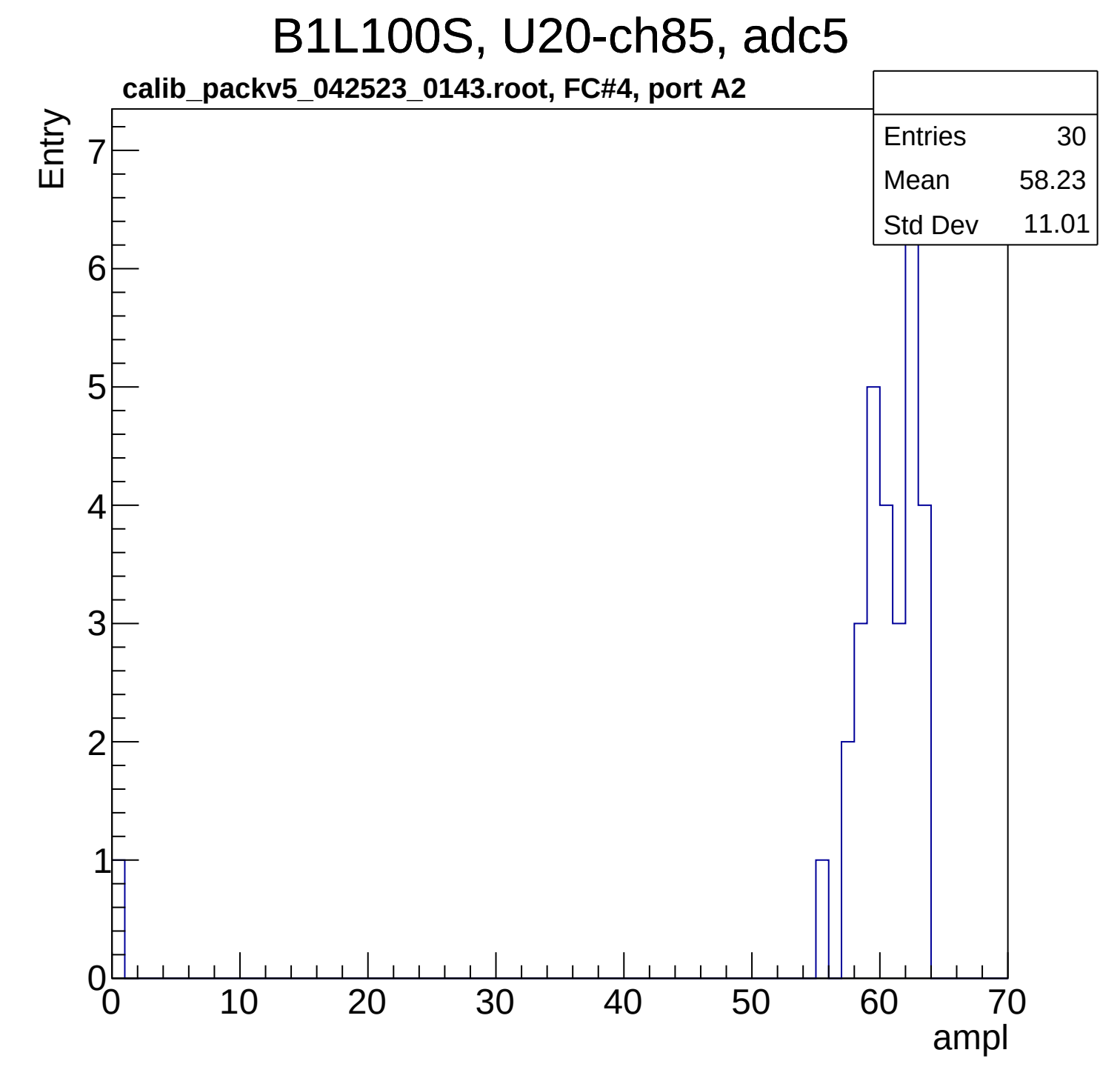

B1L100S, U20-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.23
Std Dev 3.608
Gaus mean : 29.9233
Gaus Width: 3.3684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

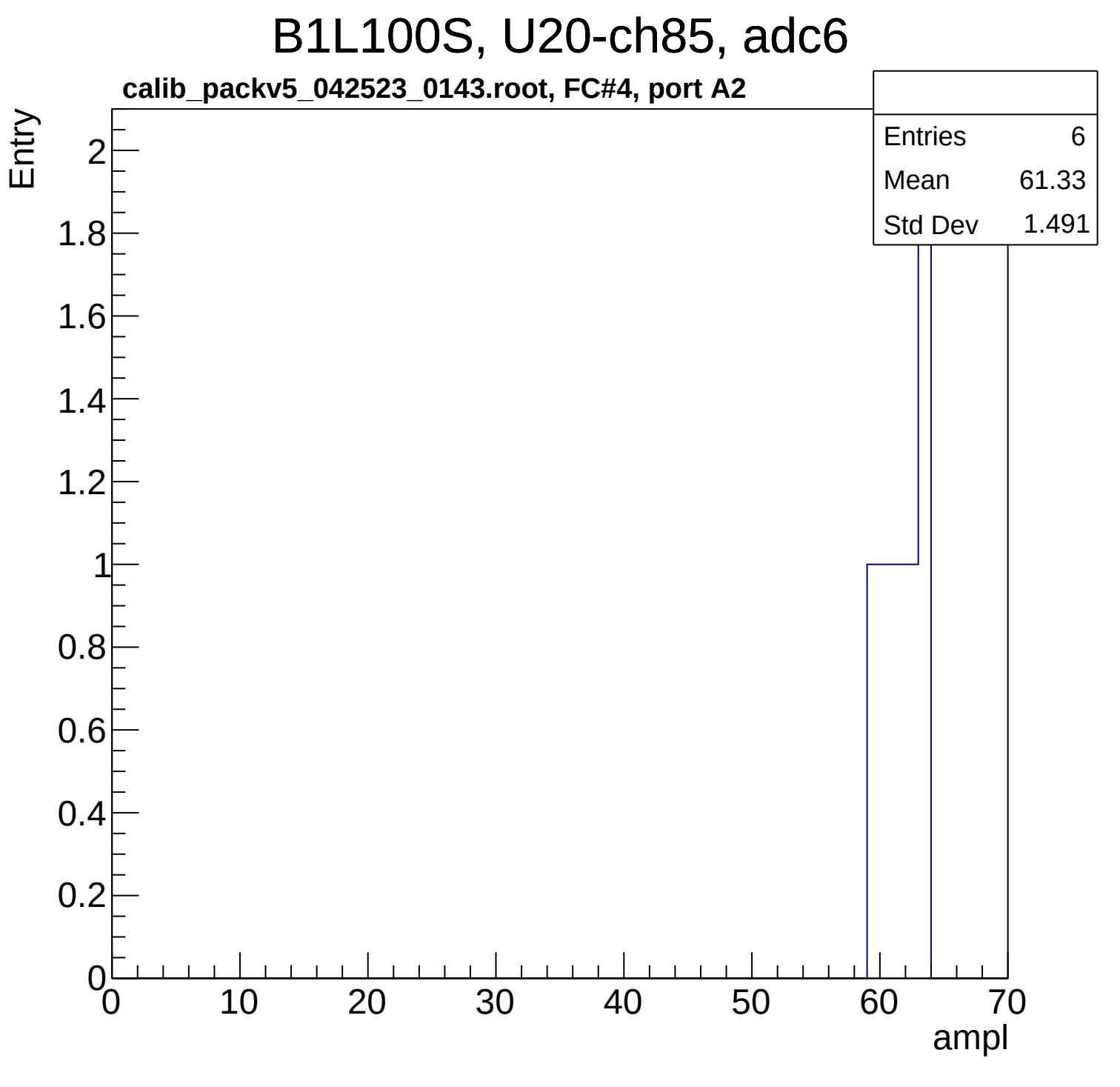

B1L100S, U20-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 36.14
Std Dev 2.779
Gaus mean : 36.0747
Gaus Width: 3.1160
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

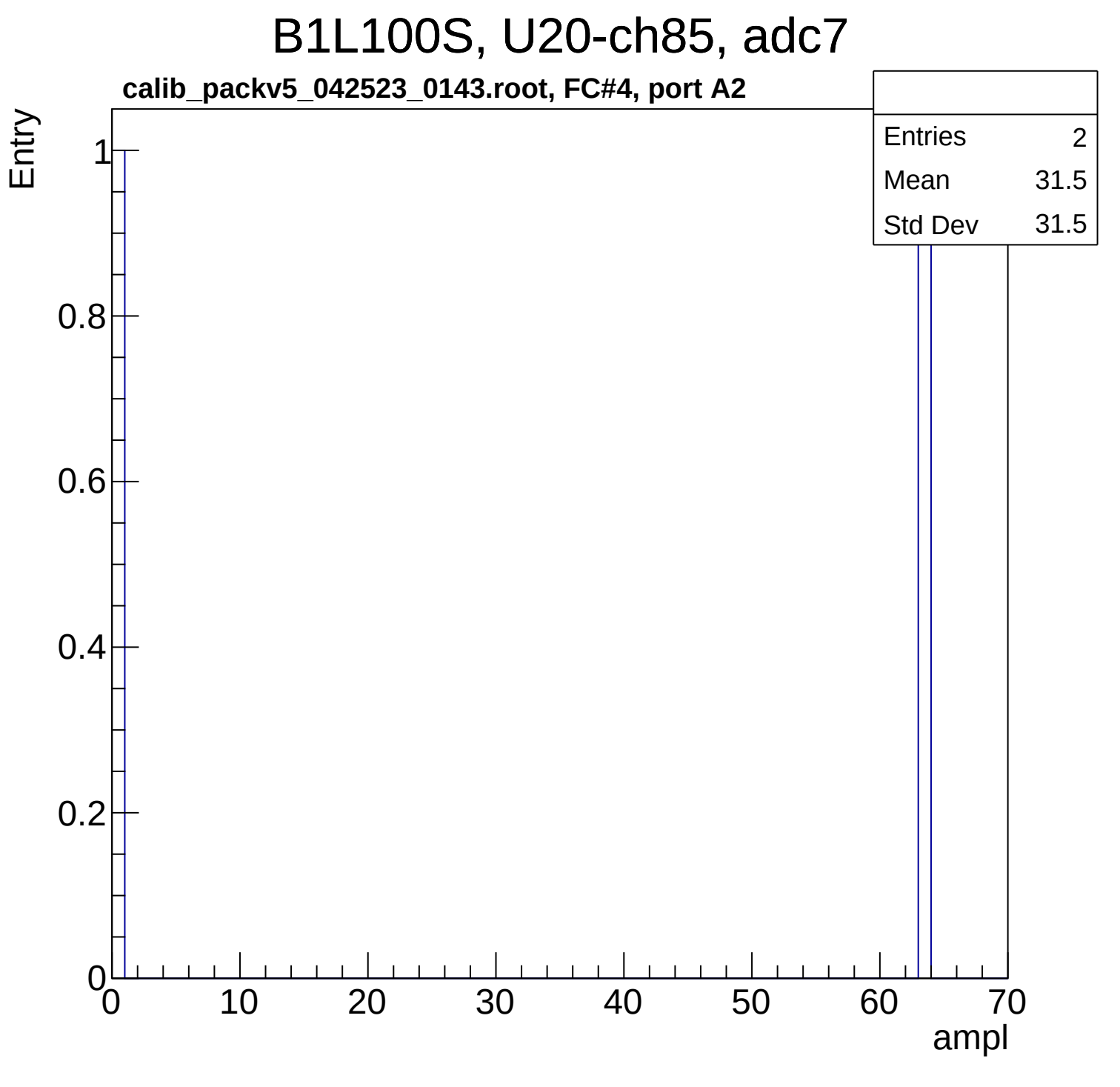

B1L100S, U20-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 42.47
Std Dev 3.496
Gaus mean : 43.0228
Gaus Width: 3.5159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

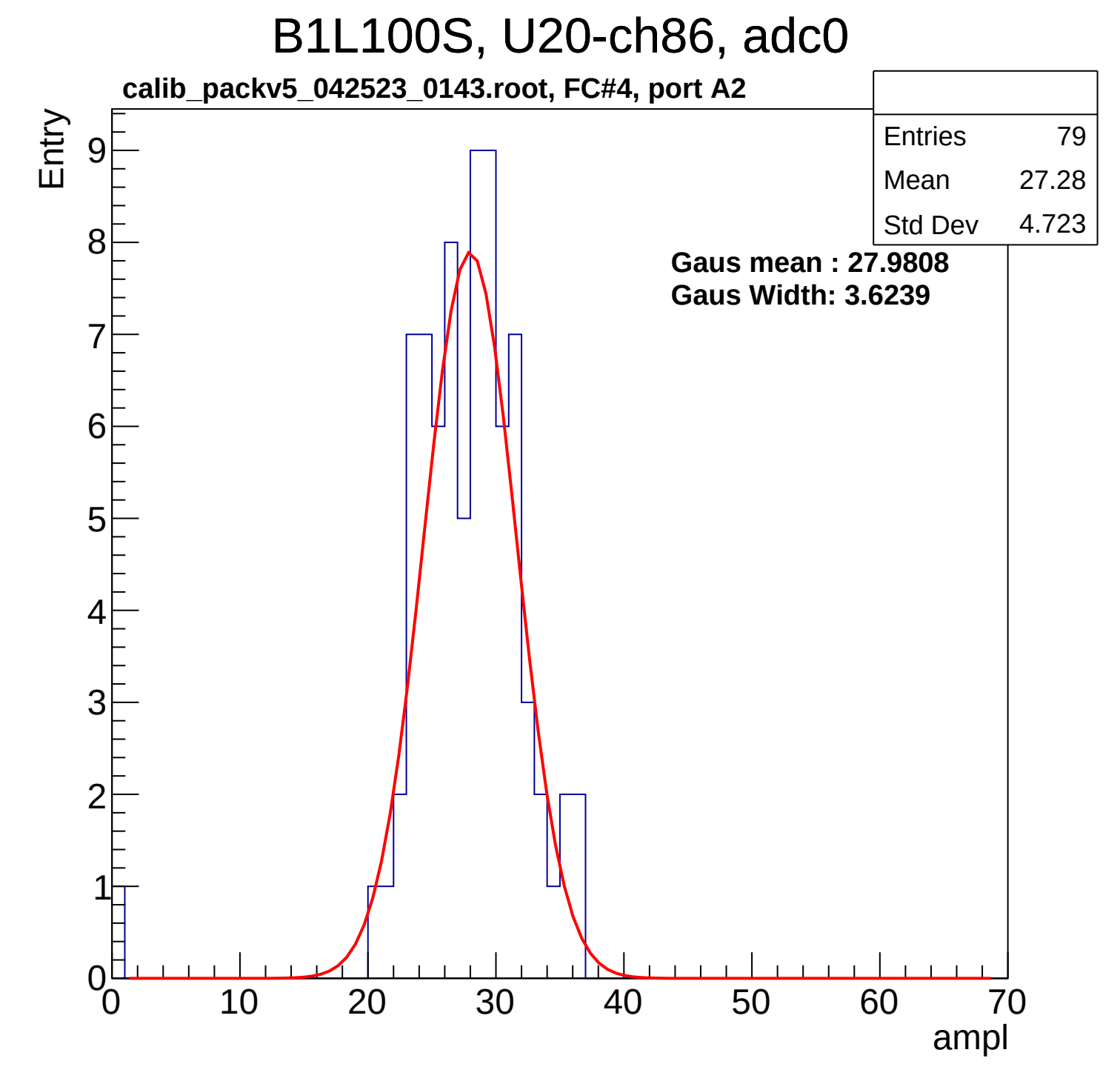

B1L100S, U20-ch85, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.48
Std Dev 3.949
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

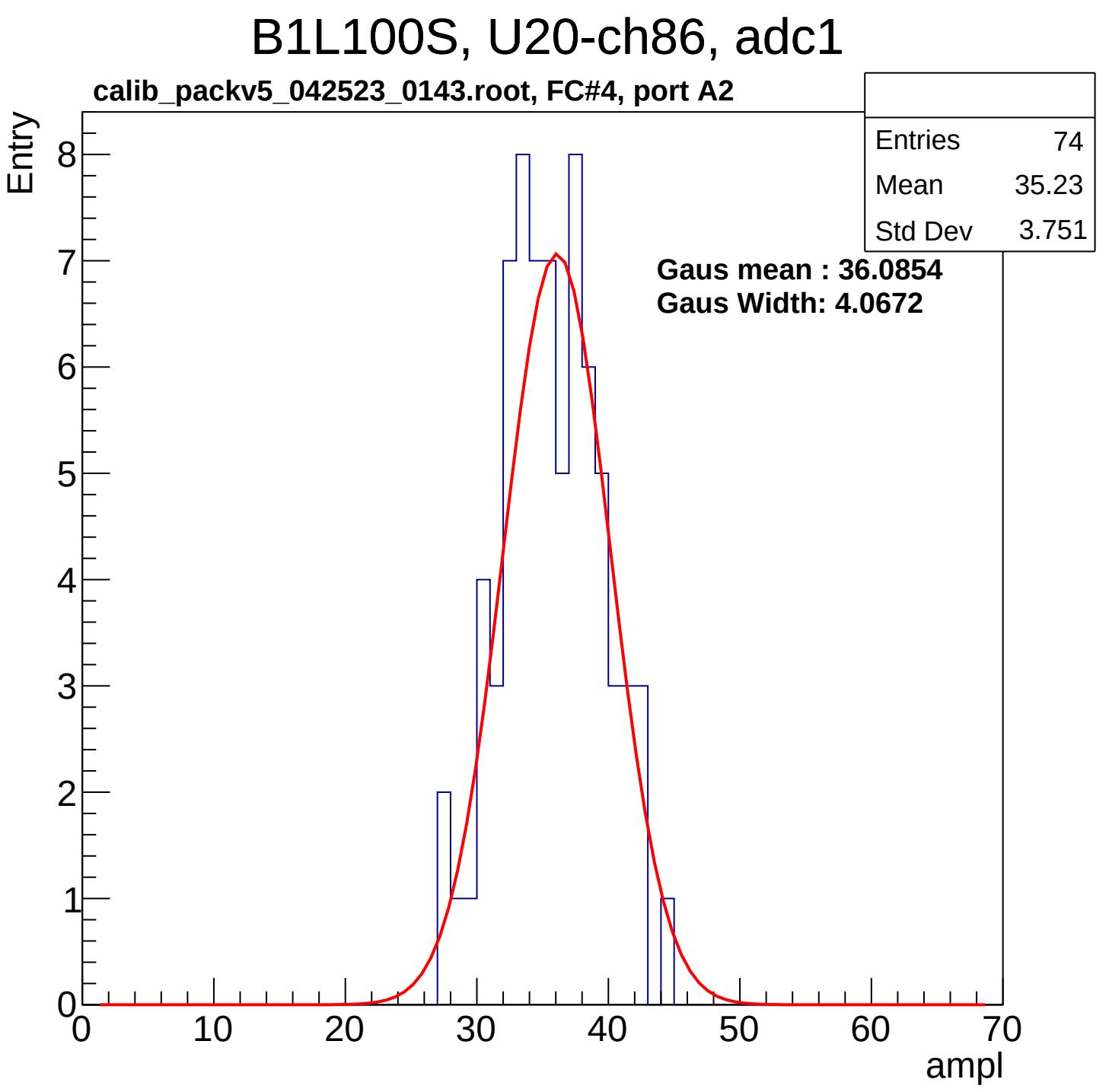

B1L100S, U20-ch85, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 56.26
Std Dev 3.43
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

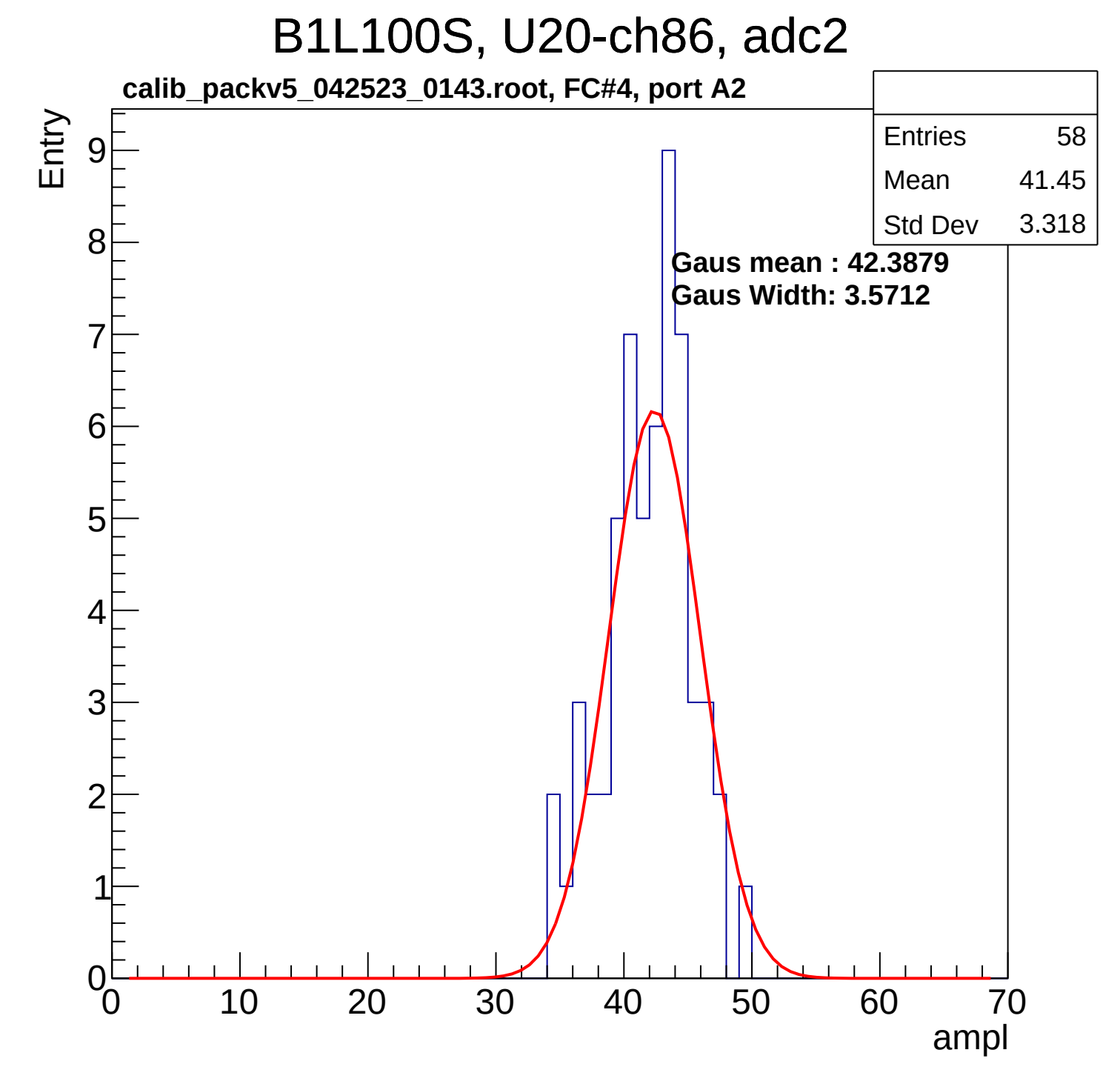

B1L100S, U20-ch85, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 58.23
Std Dev 11.01
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch85, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.33
Std Dev 1.491
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 27.28
Std Dev 4.723
Gaus mean : 27.9808
Gaus Width: 3.6239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 35.23
Std Dev 3.751
Gaus mean : 36.0854
Gaus Width: 4.0672
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 41.45
Std Dev 3.318
Gaus mean : 42.3879
Gaus Width: 3.5712
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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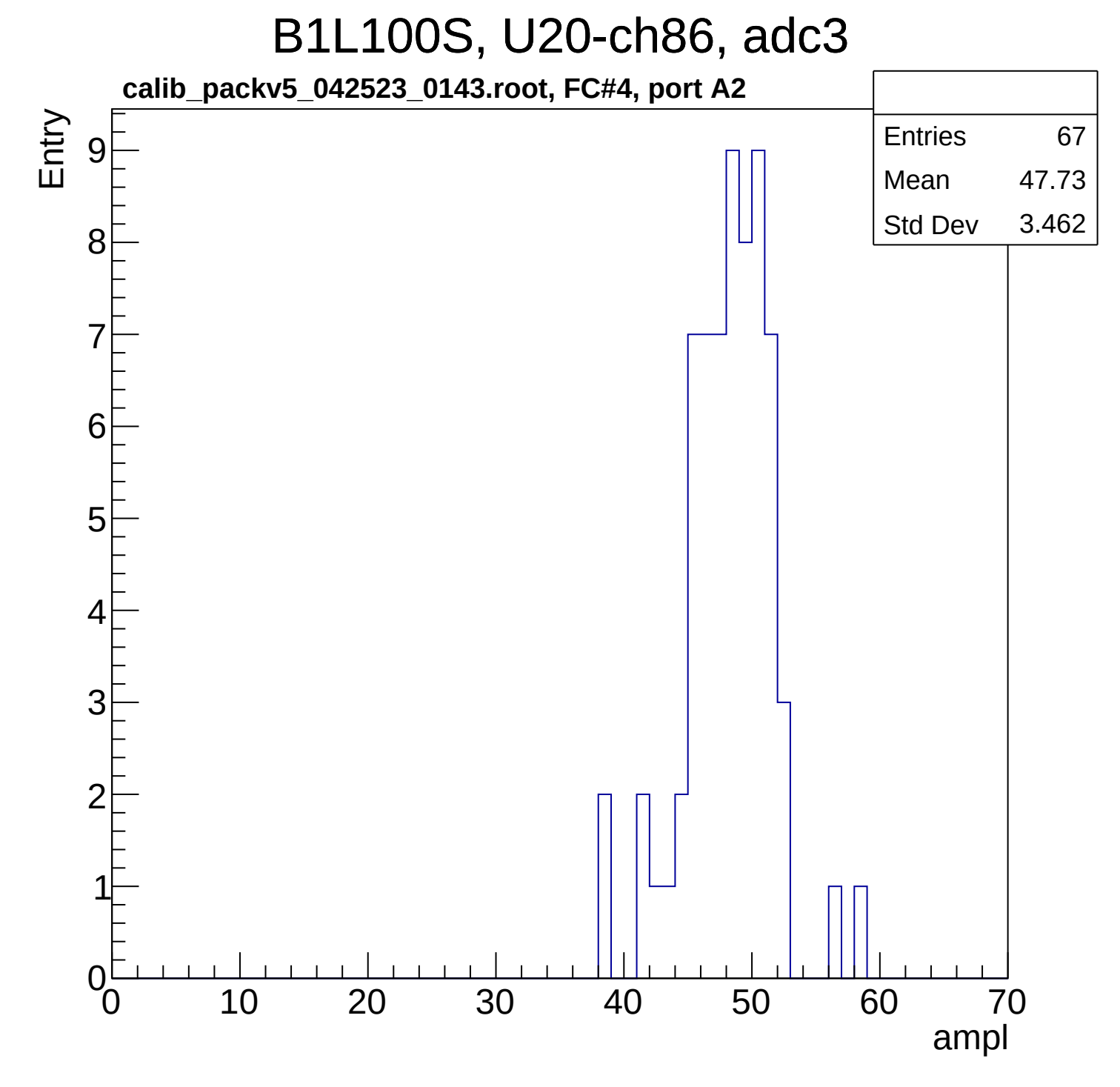
B1L100S, U20-ch86, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 47.73
Std Dev 3.462
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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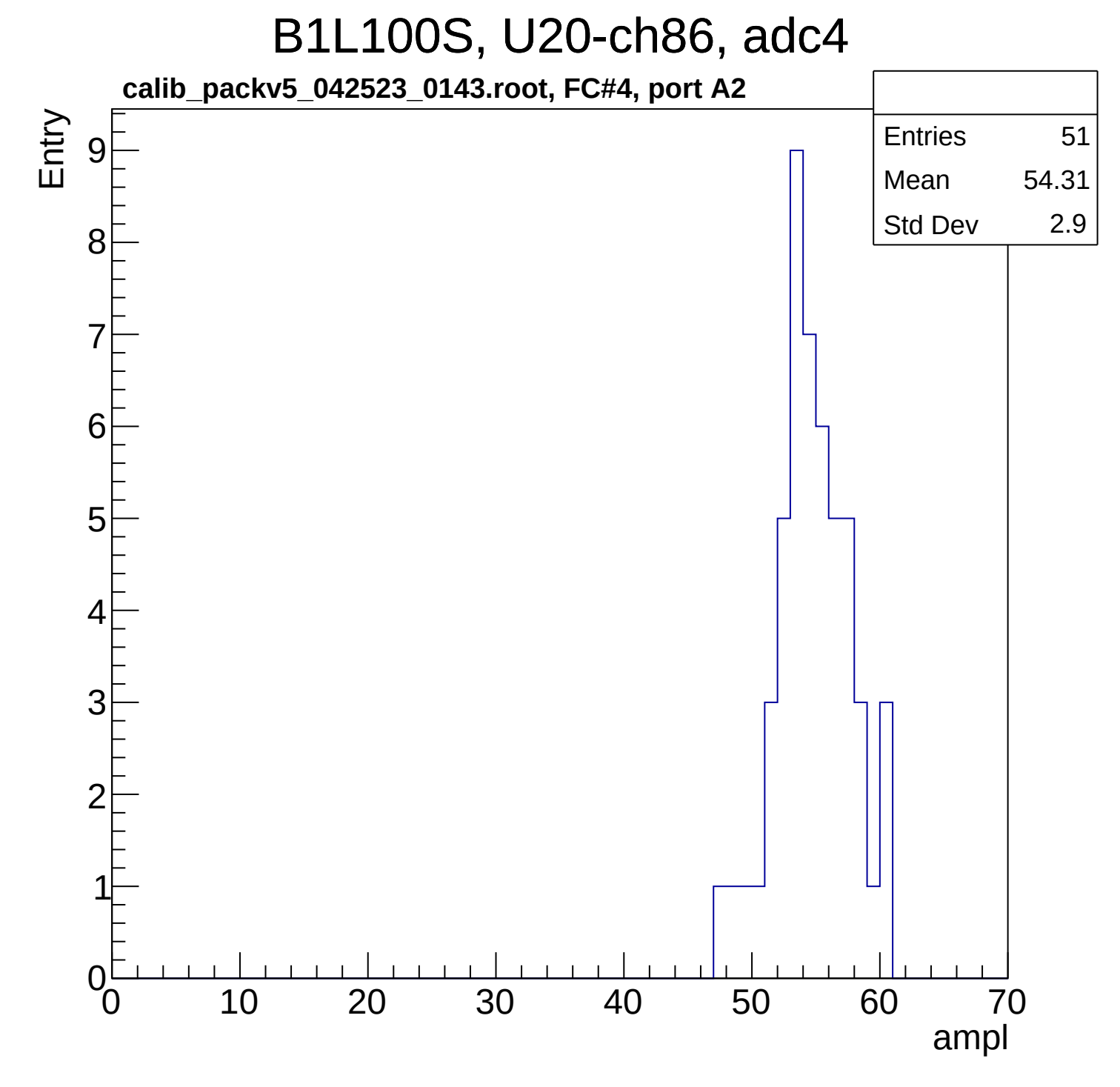
B1L100S, U20-ch86, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.31
Std Dev 2.9
Entry
ampl
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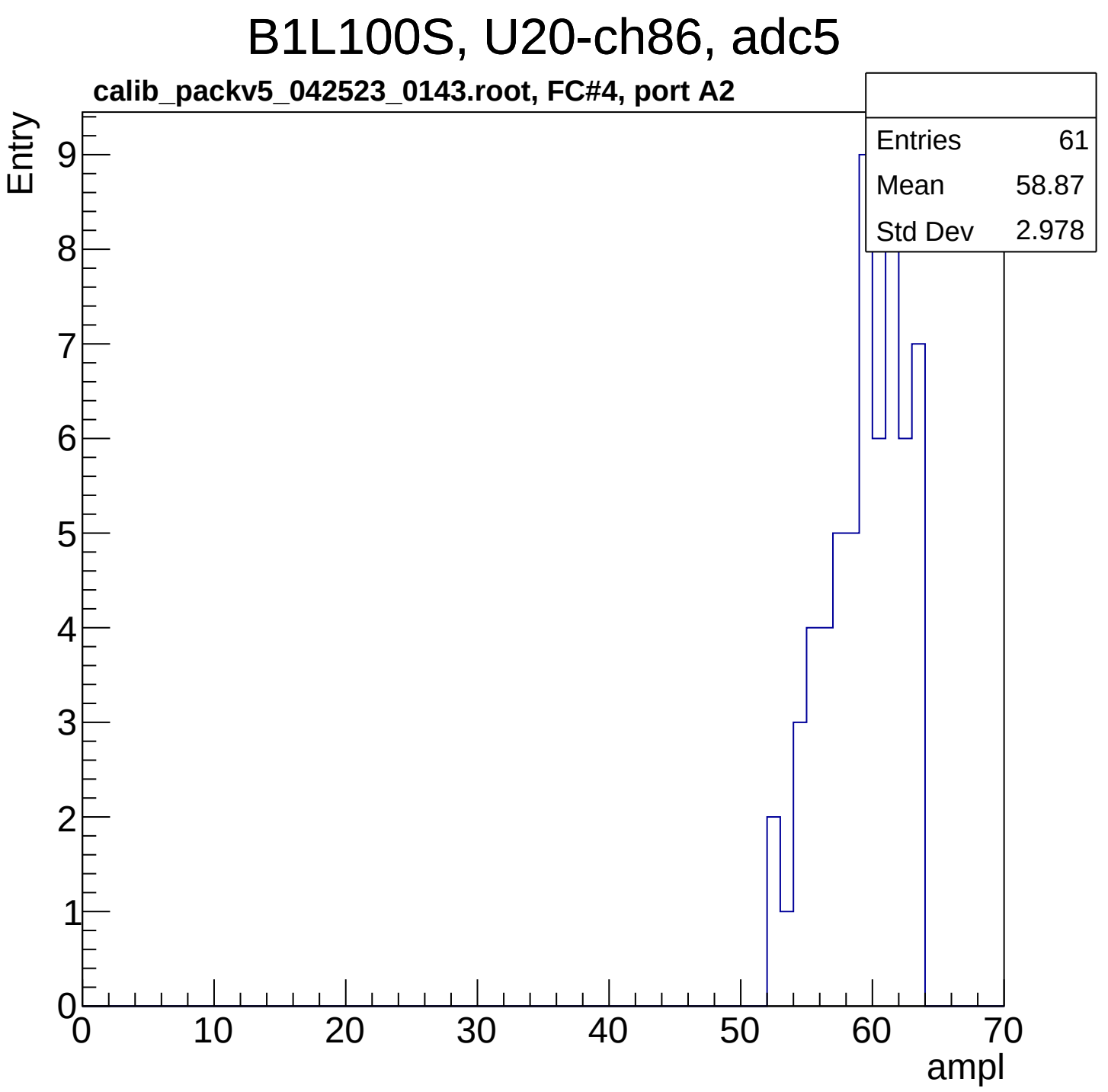
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch86, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 58.87
Std Dev 2.978
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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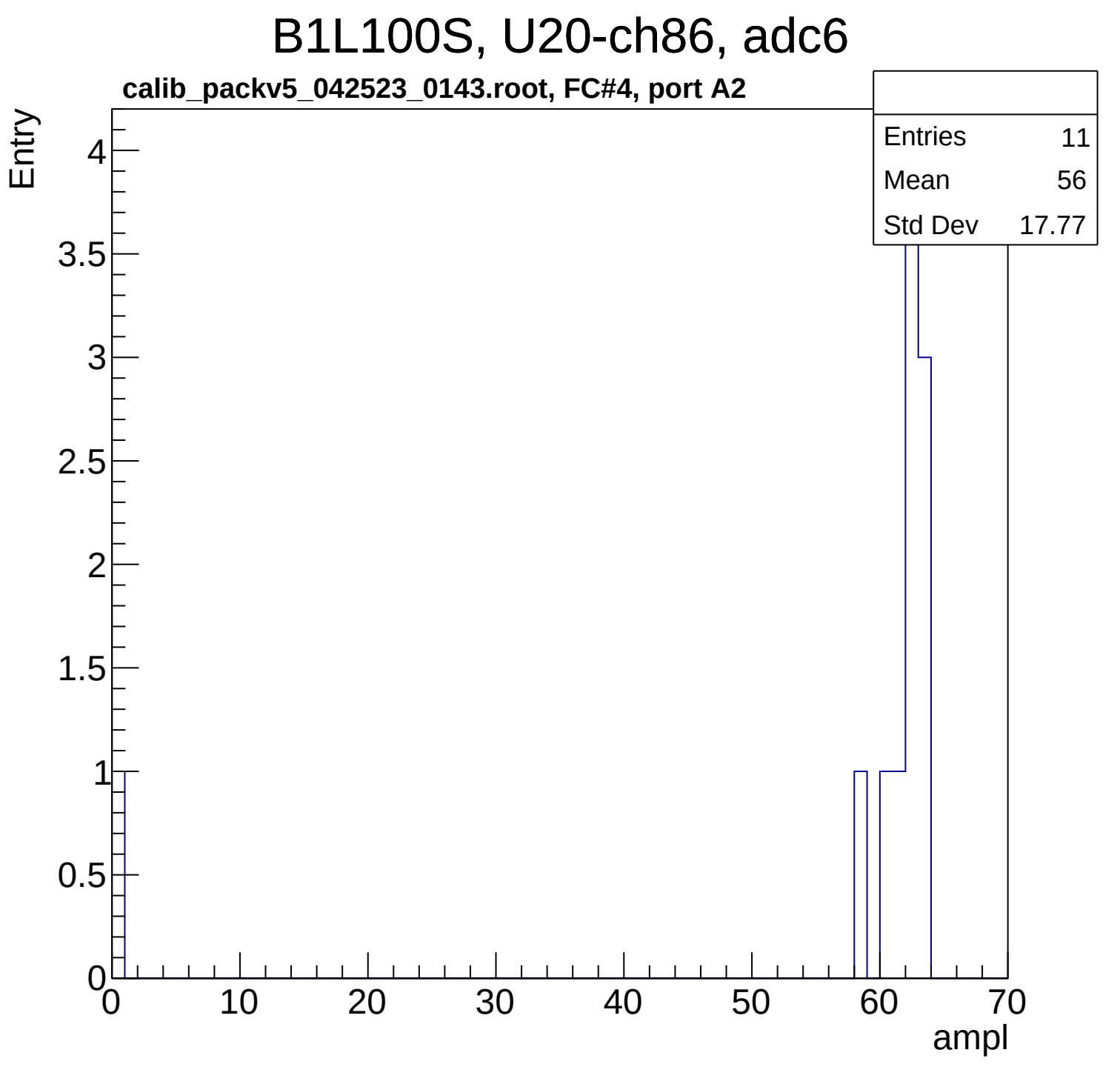
B1L100S, U20-ch86, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 56
Std Dev 17.77
Entry
ampl
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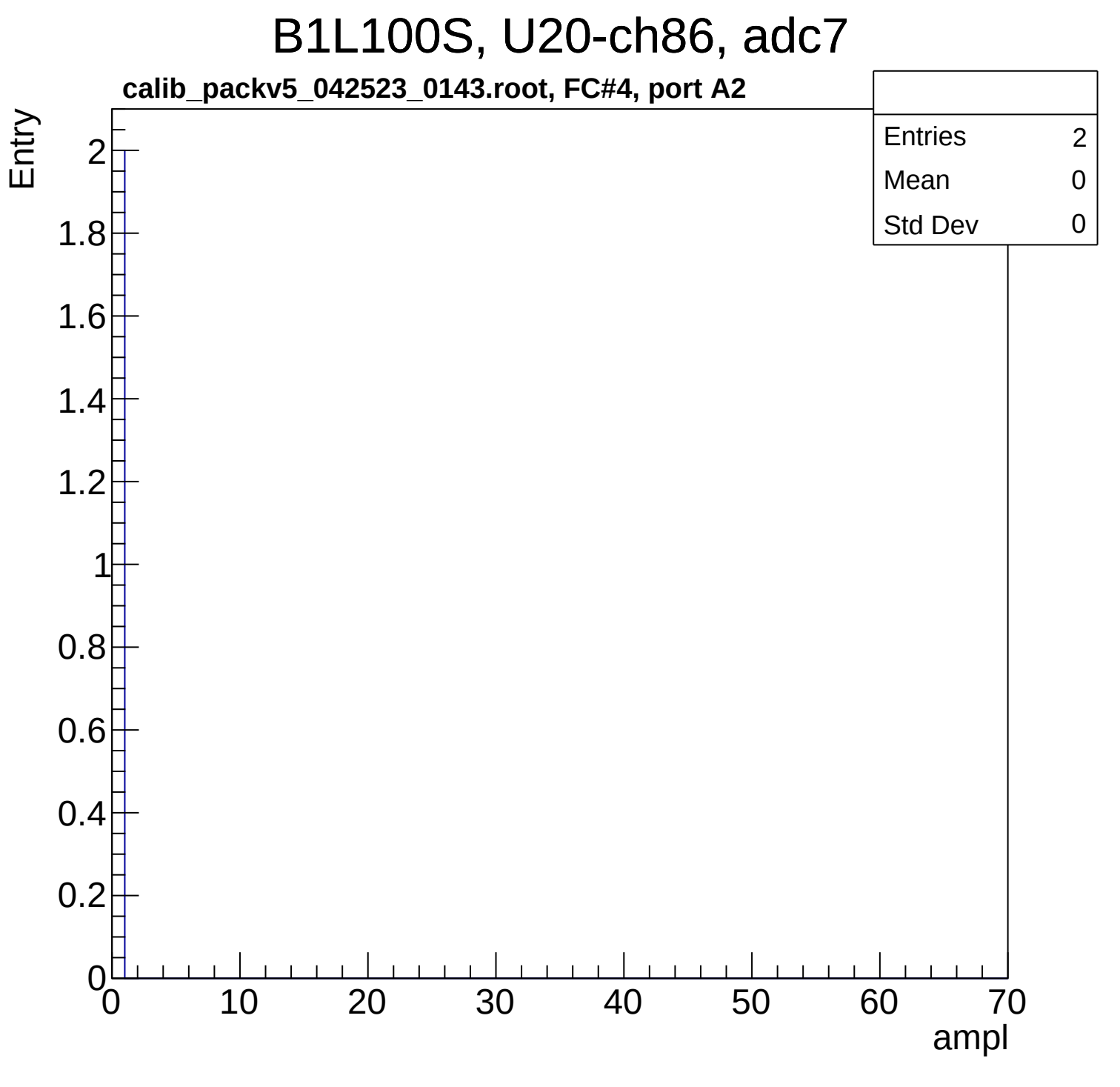
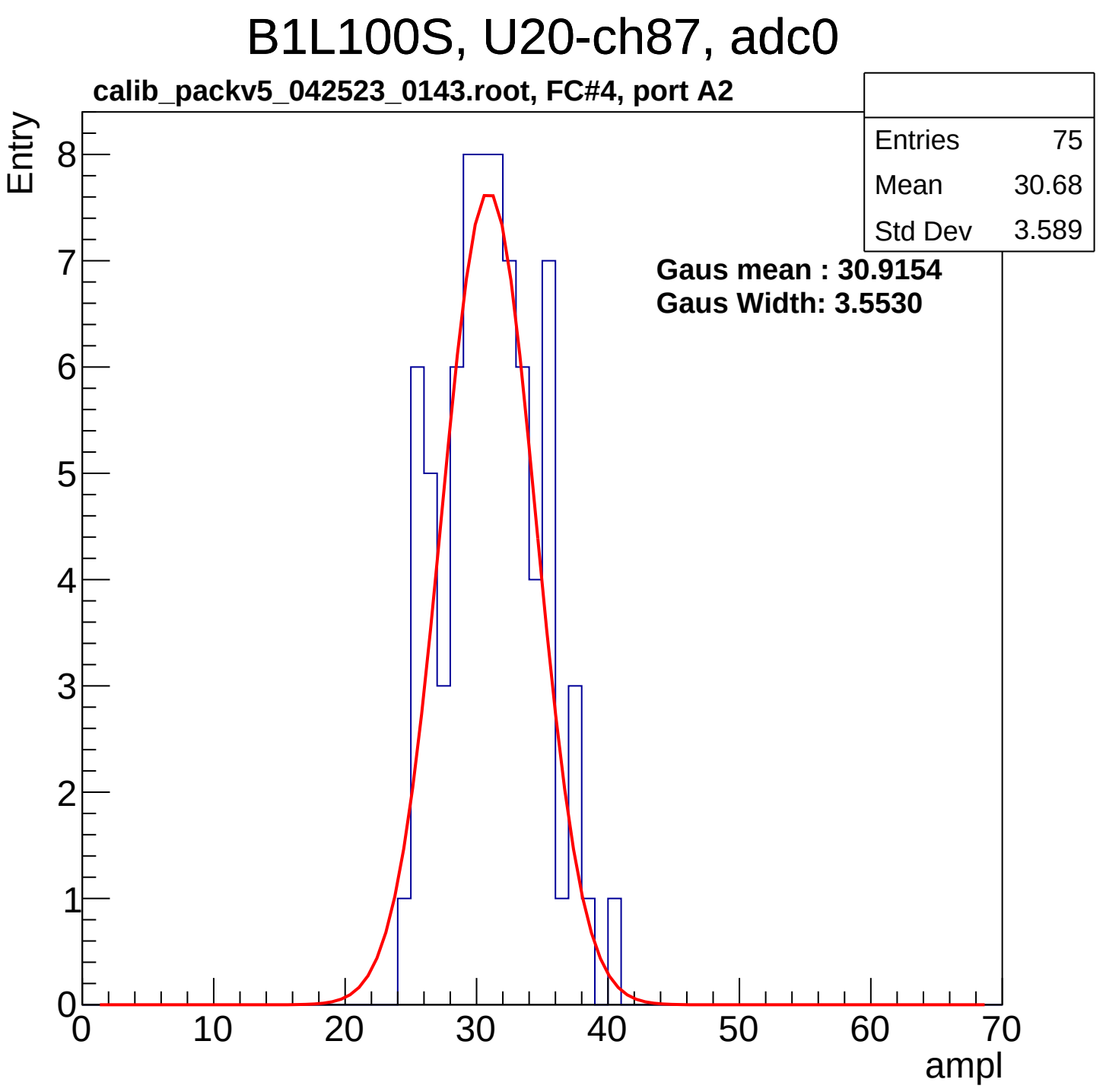
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch86, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 30.68
Std Dev 3.589
Gaus mean : 30.9154
Gaus Width: 3.5530
Entry
ampl
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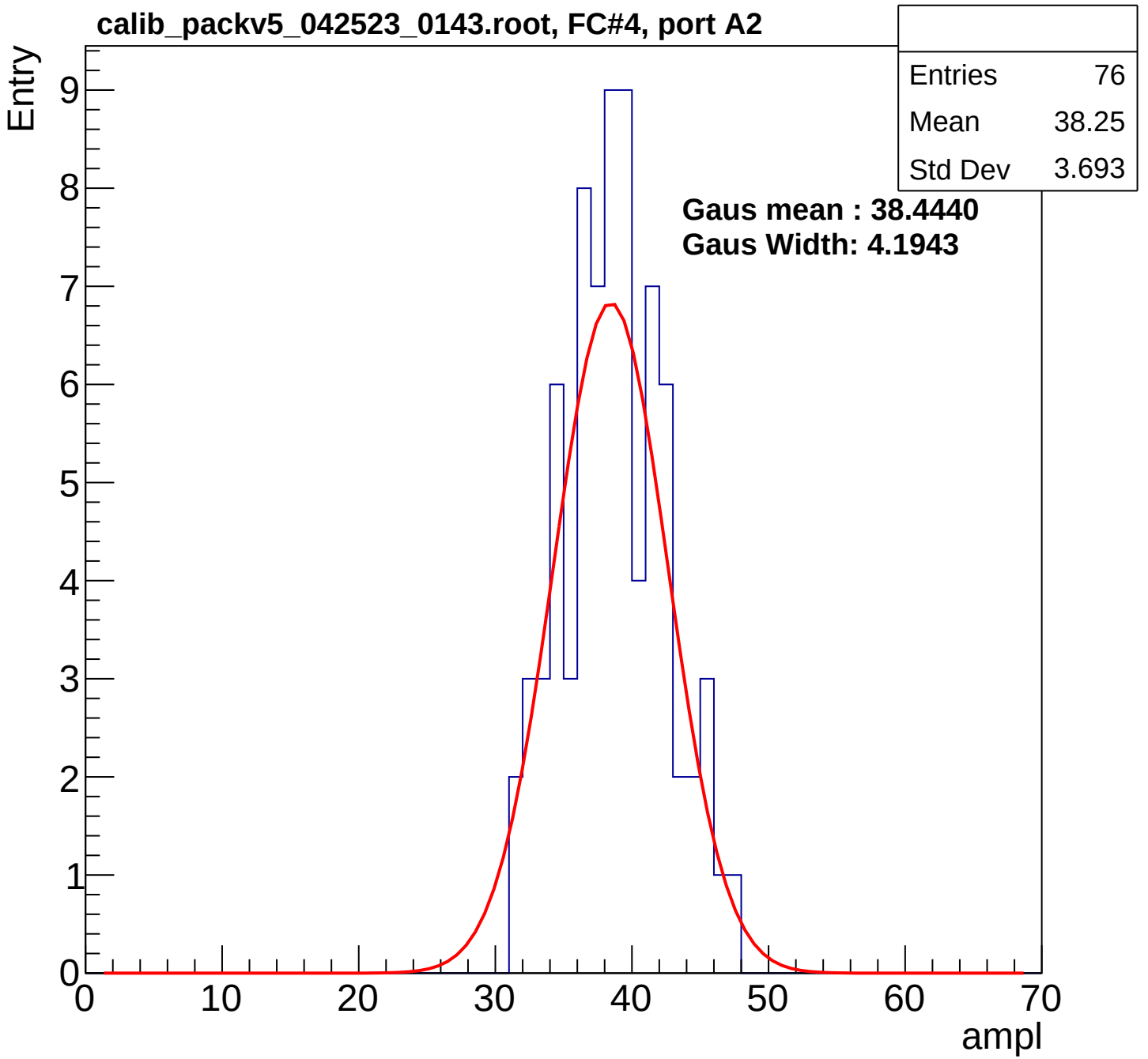
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 38.25
Std Dev 3.693
Gaus mean : 38.4440
Gaus Width: 4.1943
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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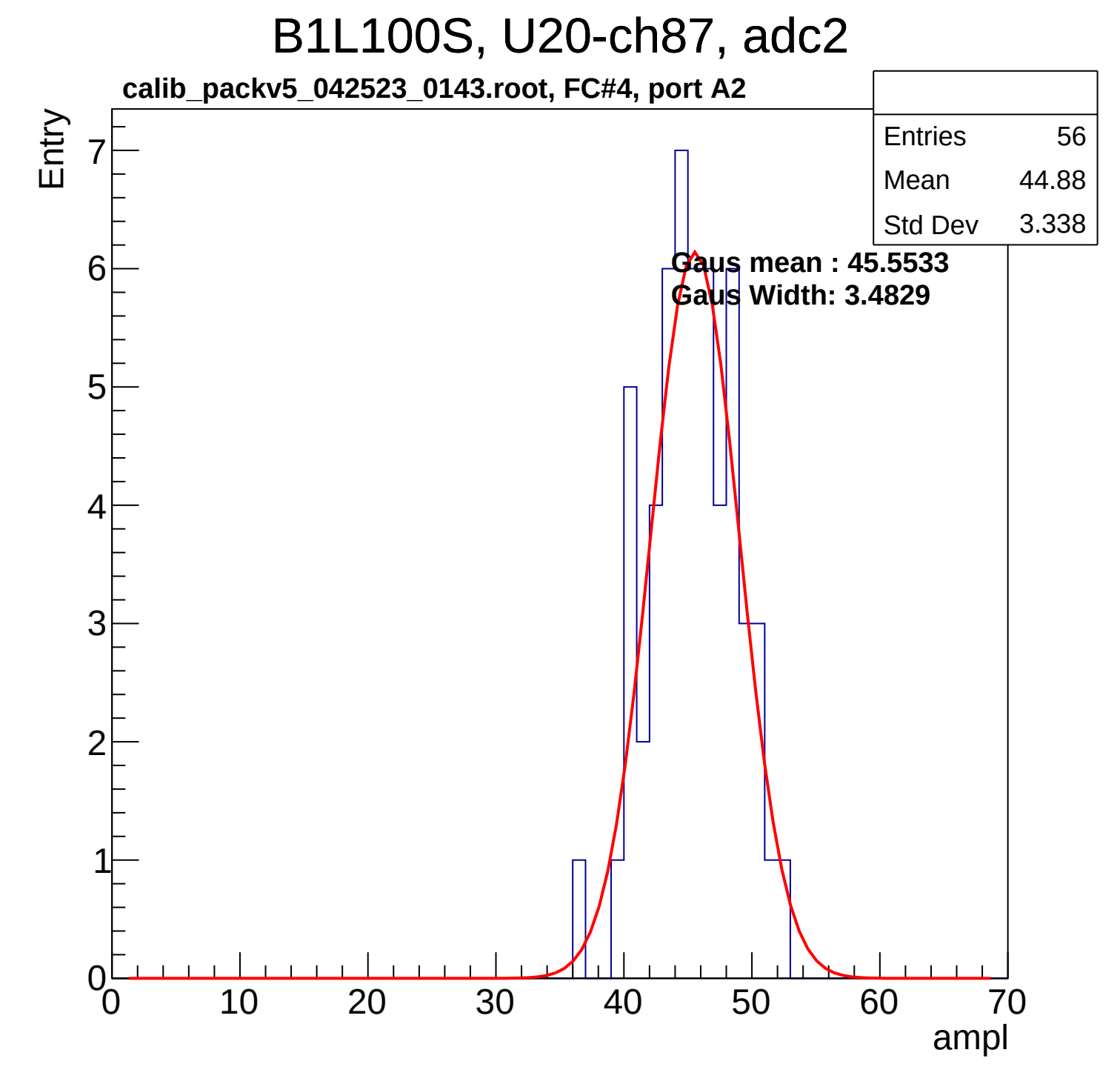
B1L100S, U20-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 44.88
Std Dev 3.338
Gaus mean : 45.5533
Gaus Width: 3.4829
Entry
ampl
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1
2
3
4
5
6
7
0
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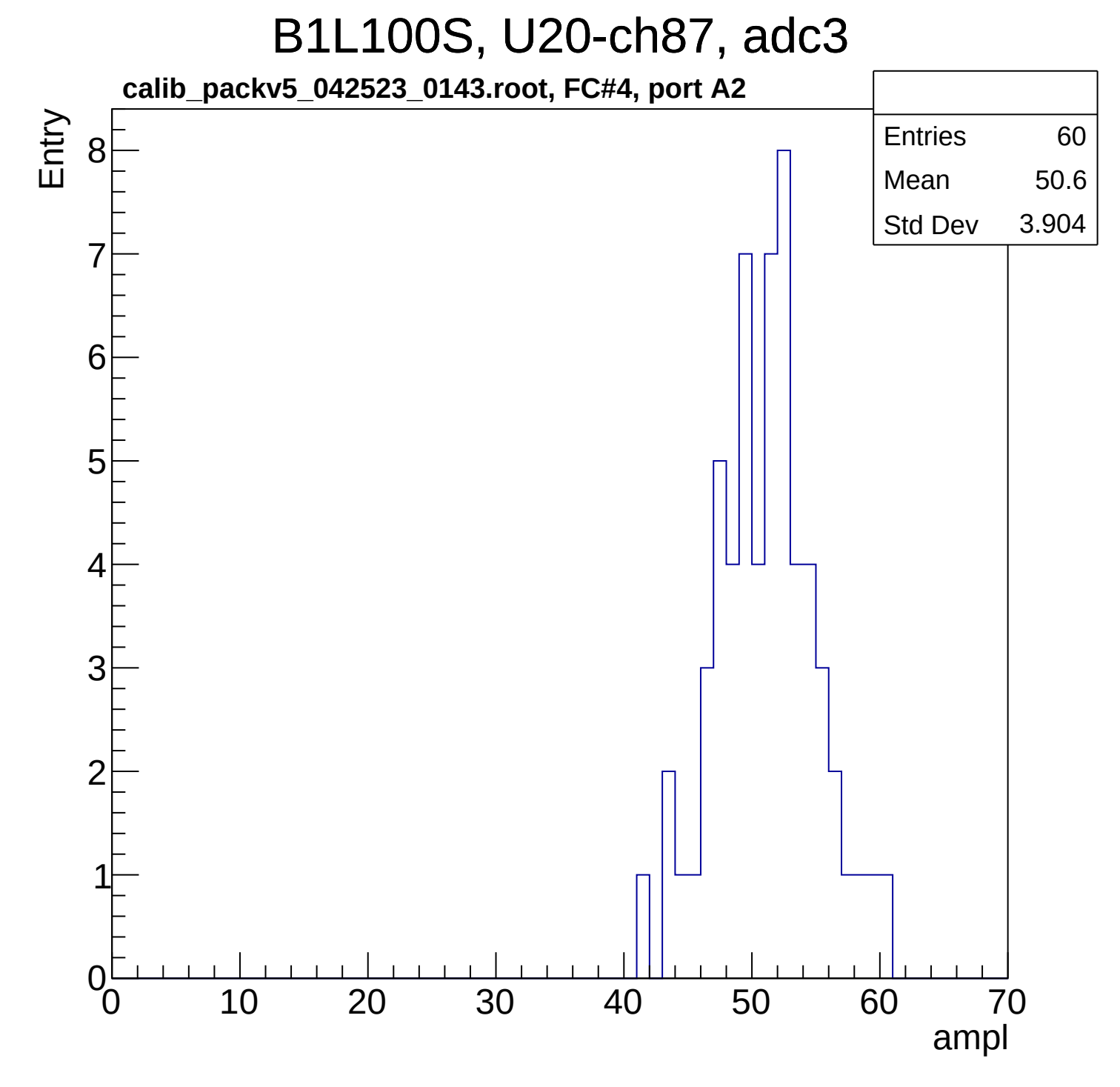
B1L100S, U20-ch87, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 50.6
Std Dev 3.904
Entry
ampl
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5
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7
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60
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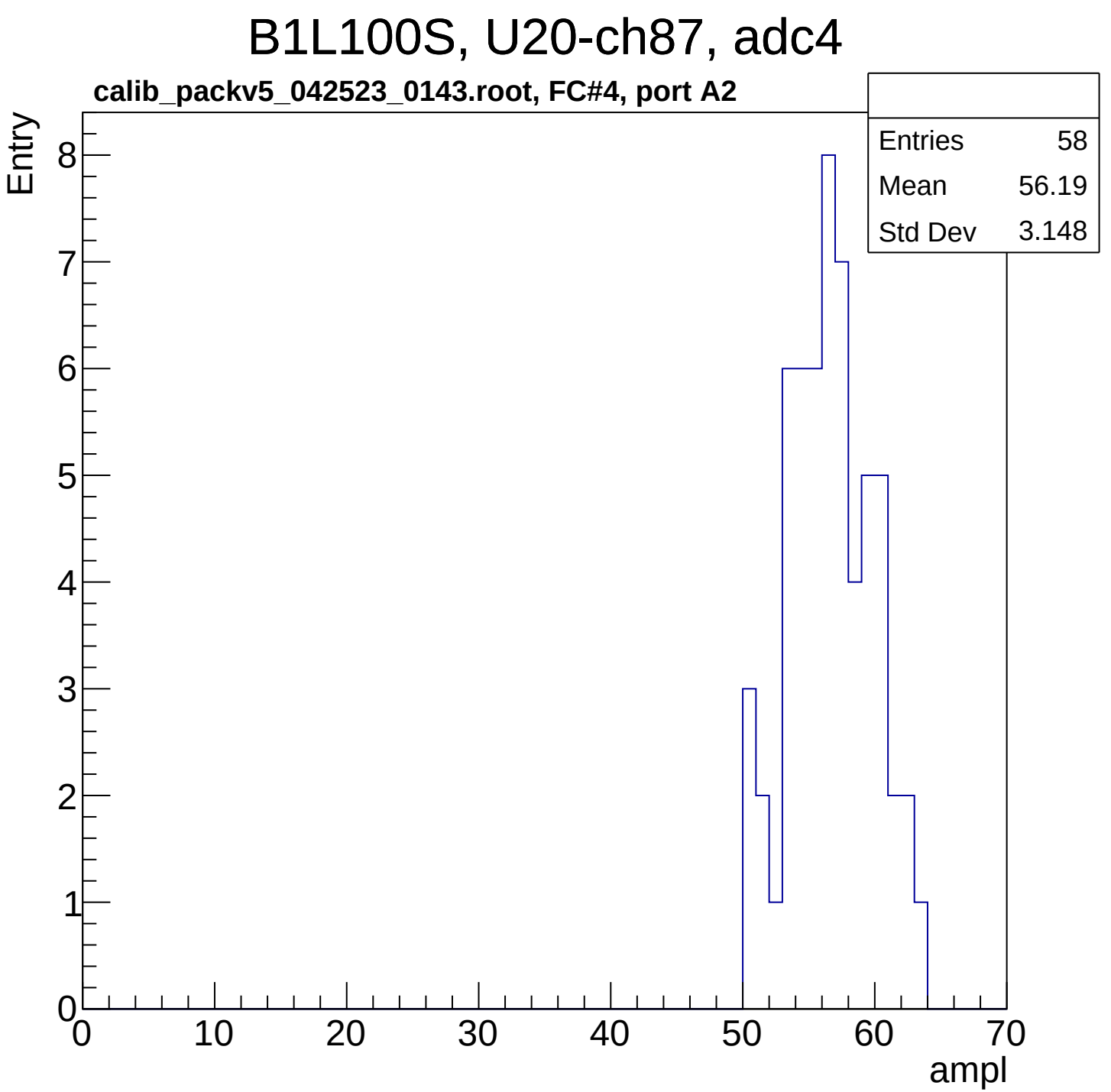
B1L100S, U20-ch87, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.19
Std Dev 3.148
Entry
ampl
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2
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5
6
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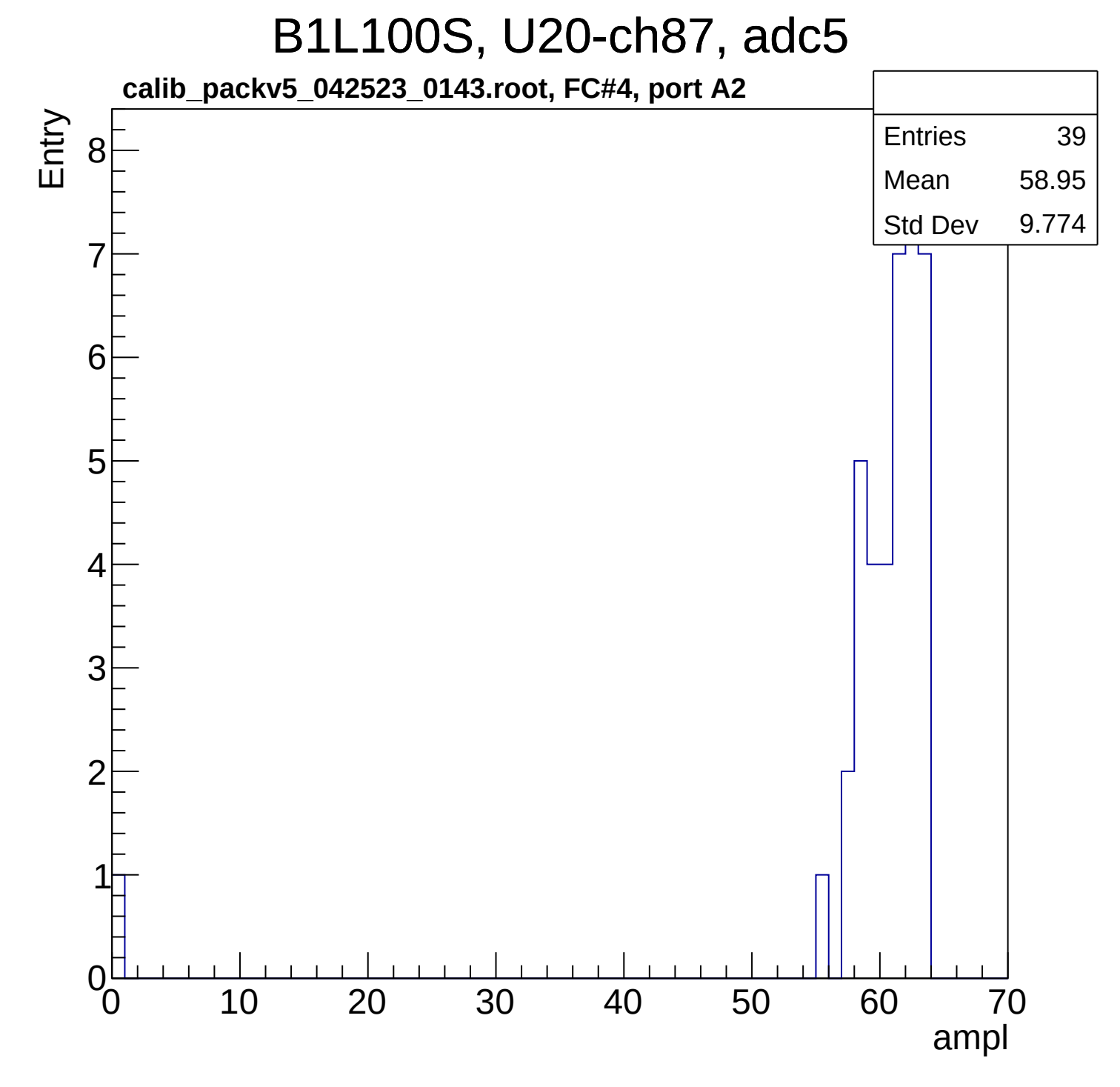
60
70


B1L100S, U20-ch87, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.95
Std Dev 9.774
Entry
ampl
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2
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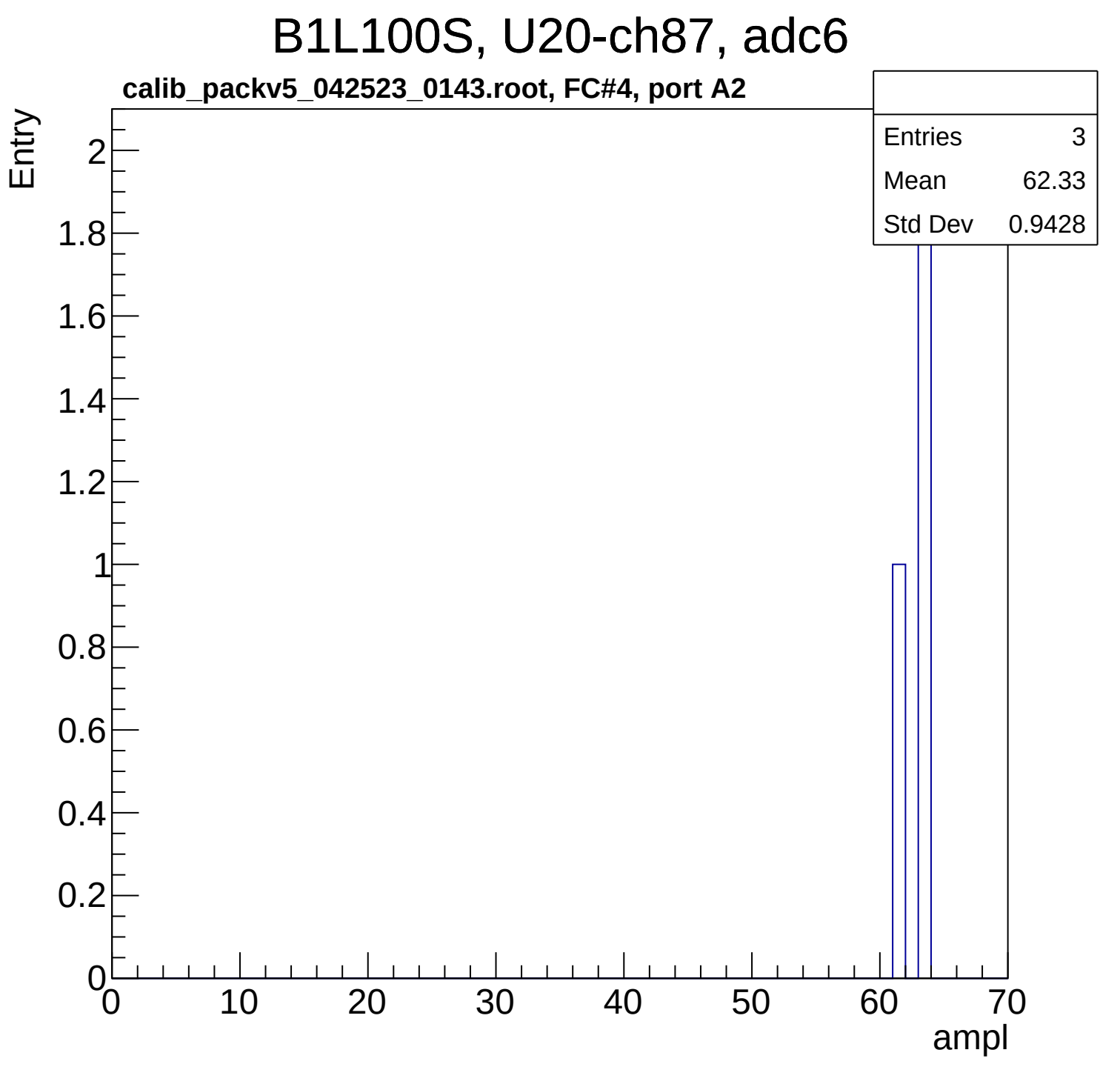
40
50
60
70


B1L100S, U20-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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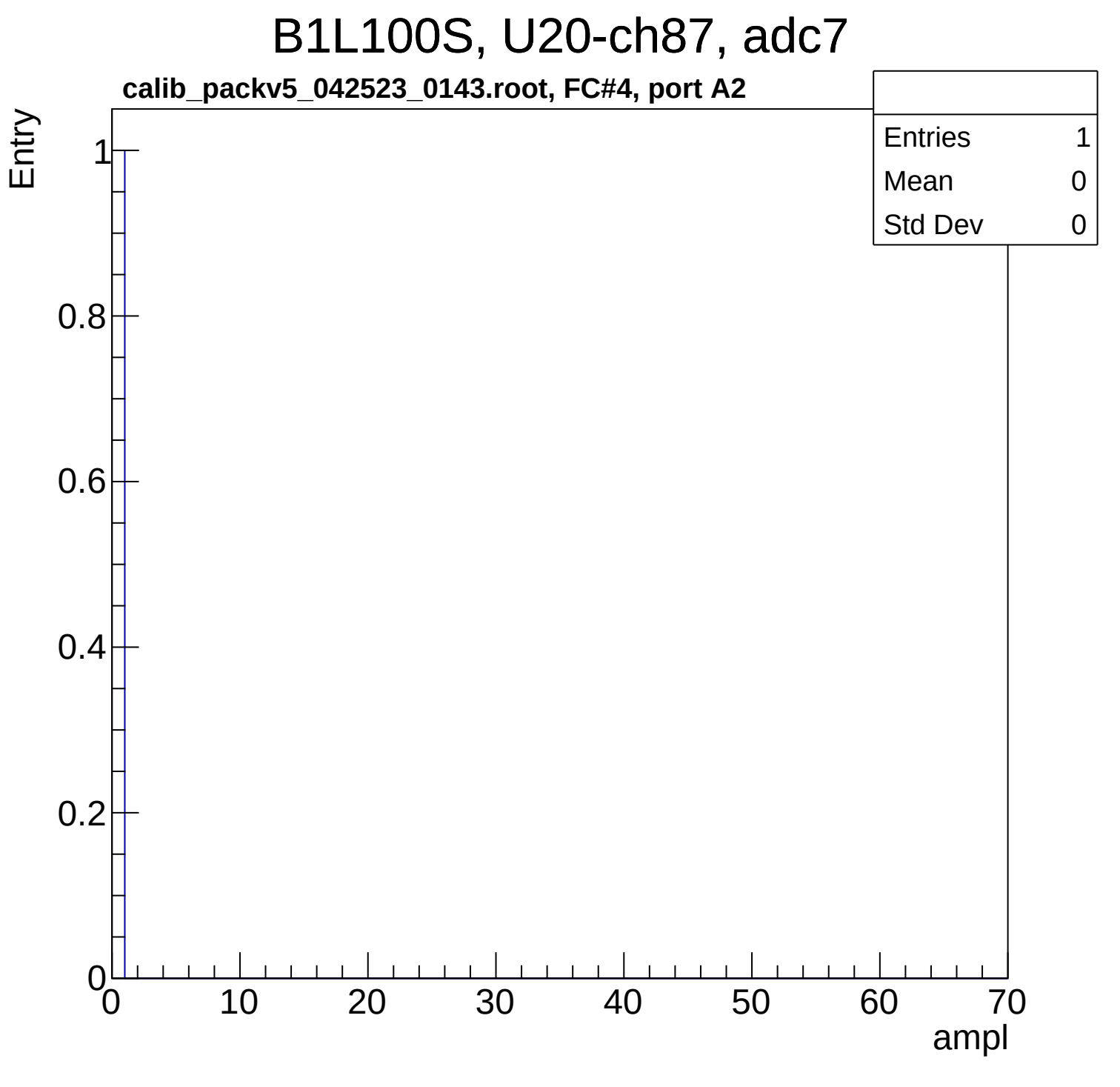
20
30
40
50
60
70


B1L100S, U20-ch87, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
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0
0
10
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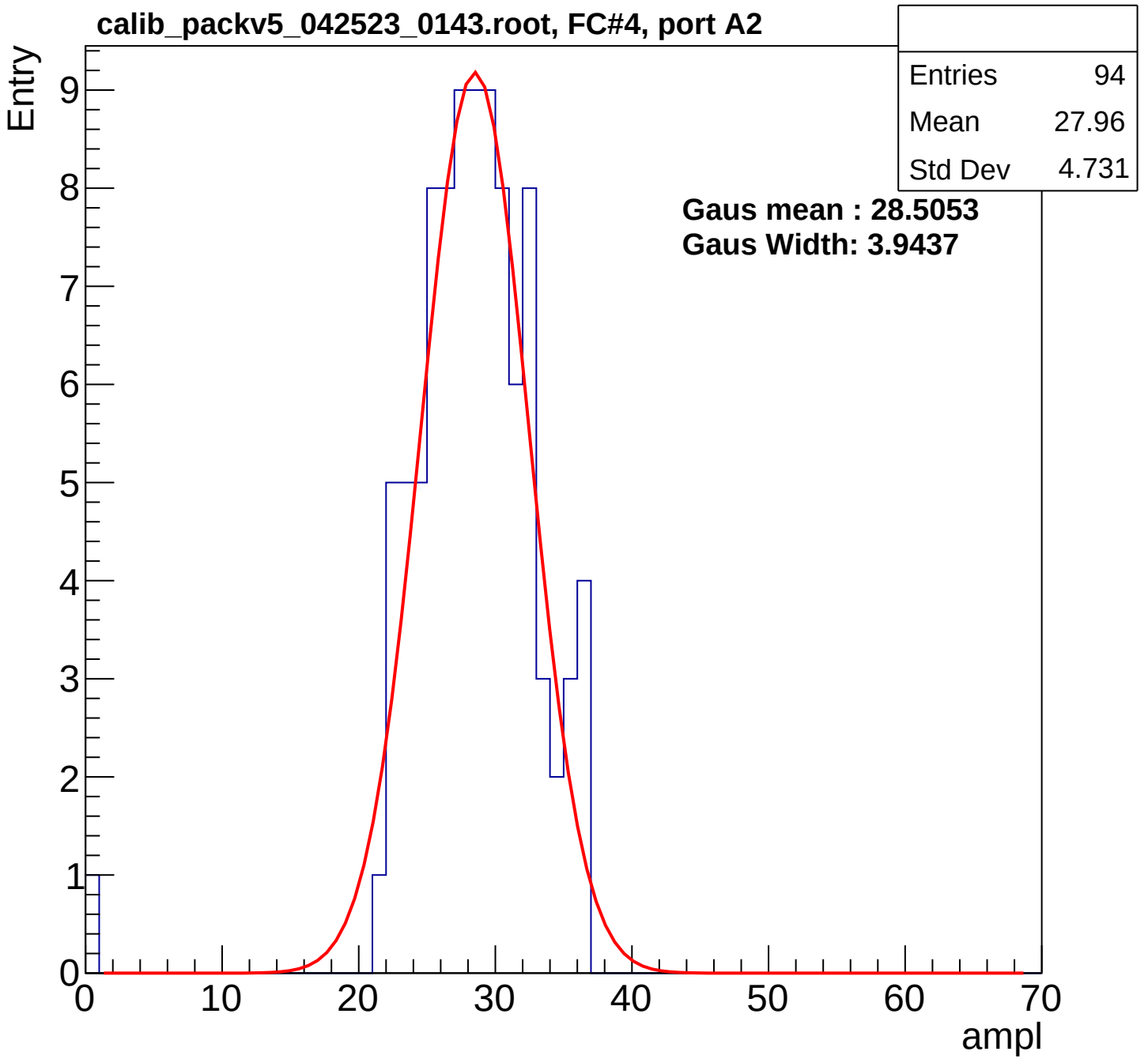
50
60
70
ampl


B1L100S, U20-ch88, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 27.96
Std Dev 4.731
Gaus mean : 28.5053
Gaus Width: 3.9437
Entry
ampl
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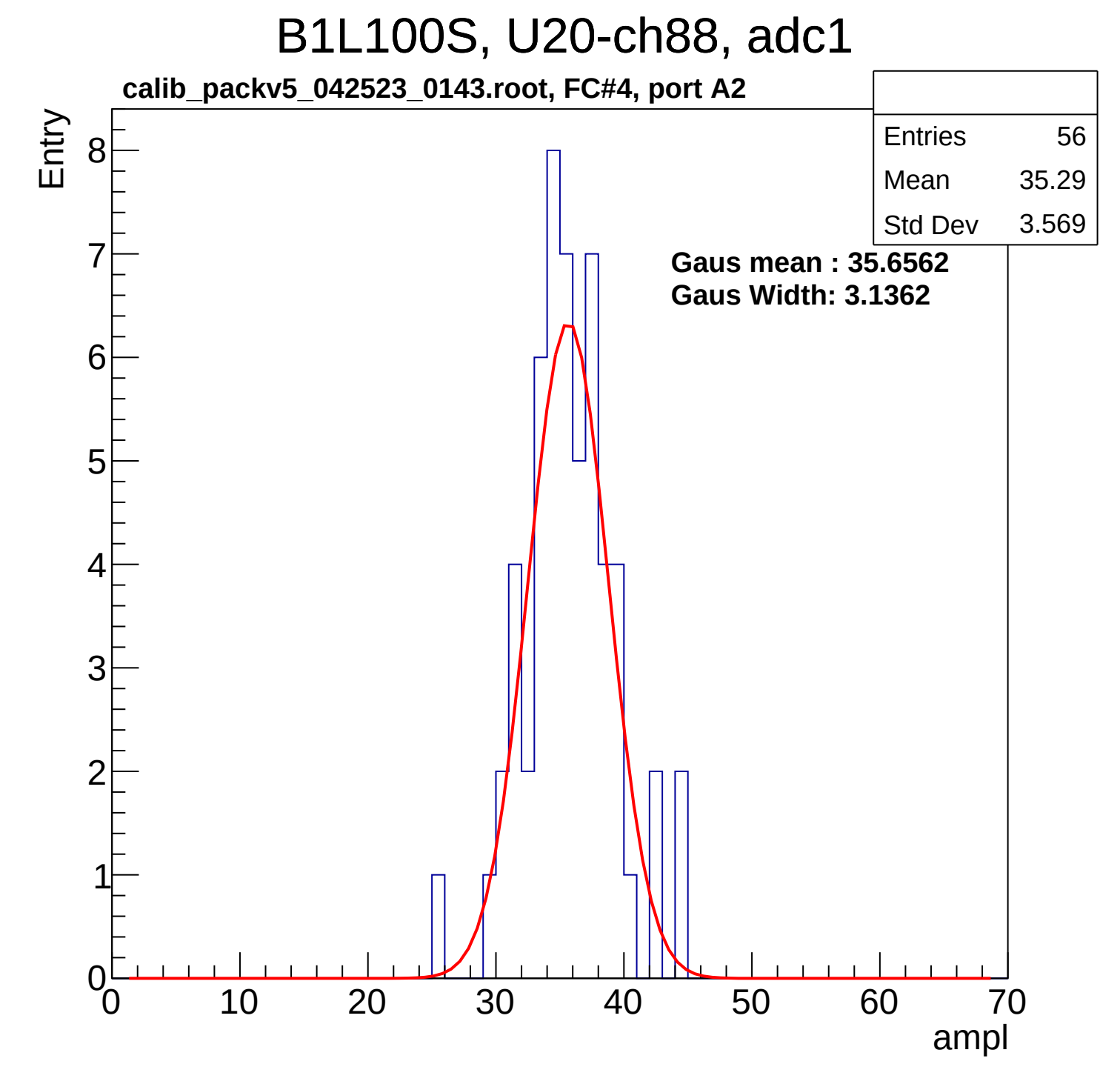
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch88, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 35.29
Std Dev 3.569
Gaus mean : 35.6562
Gaus Width: 3.1362
Entry
ampl
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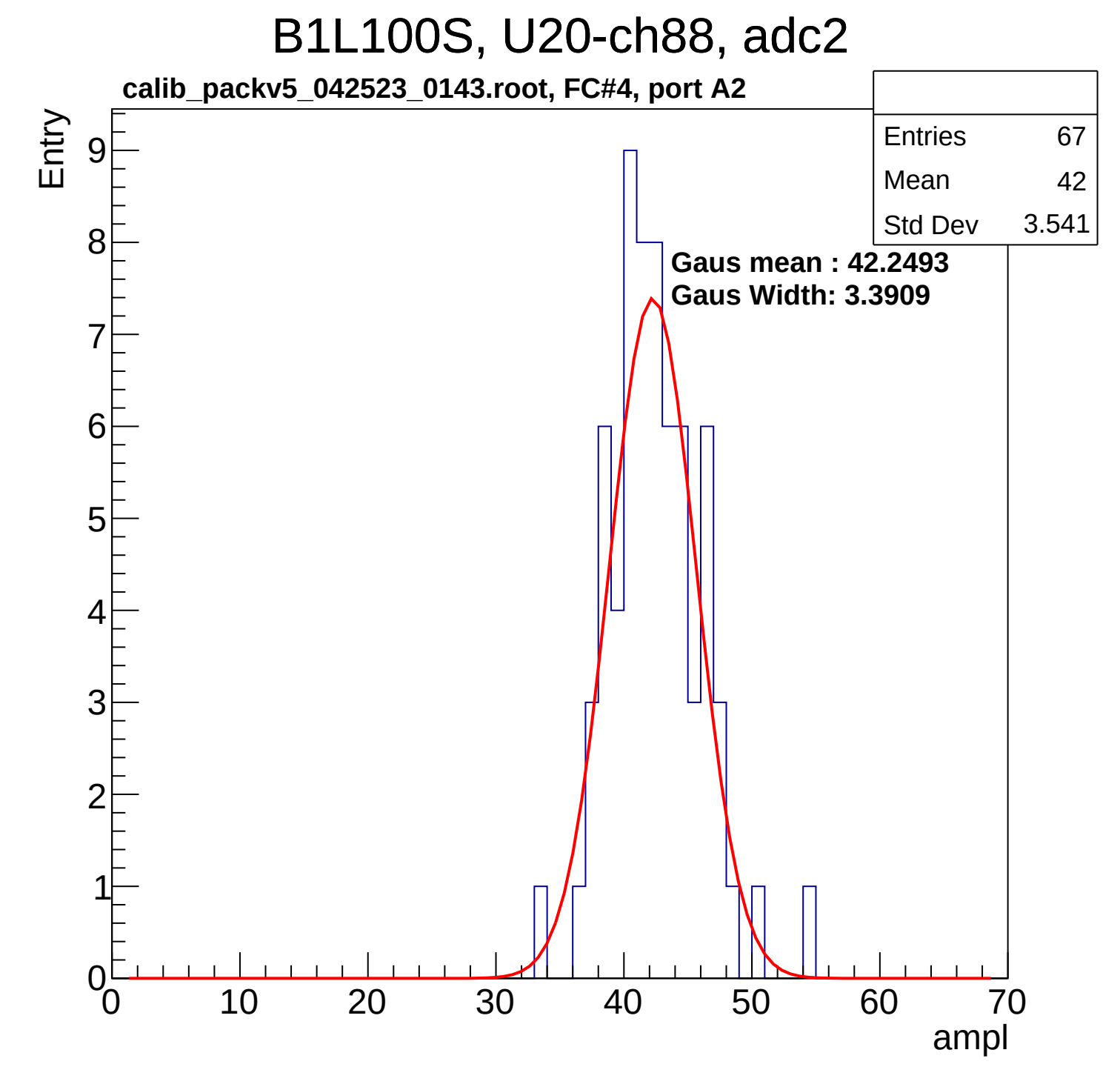
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 42
Std Dev 3.541
Gaus mean : 42.2493
Gaus Width: 3.3909
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
50
60
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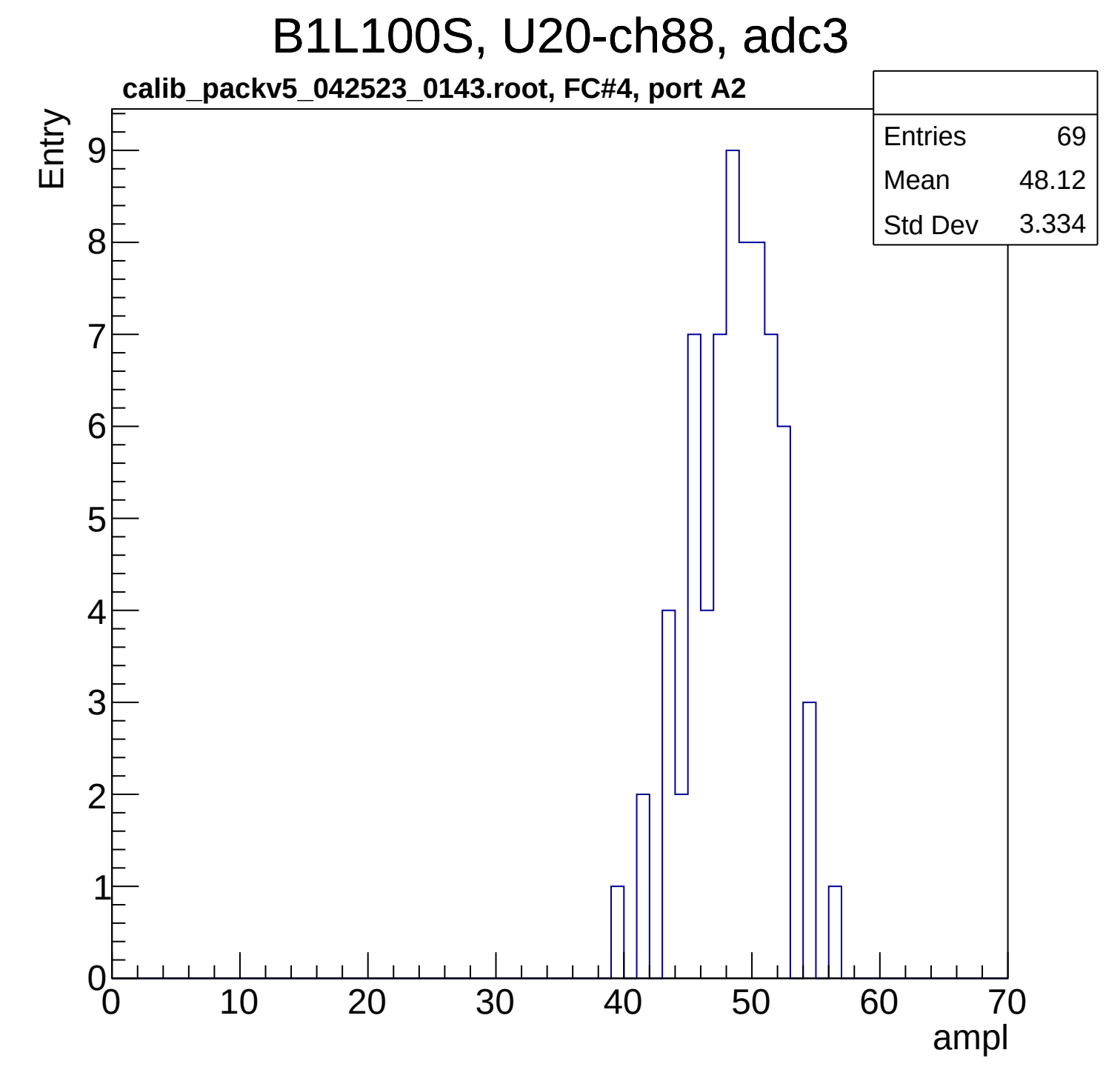
B1L100S, U20-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 48.12
Std Dev 3.334
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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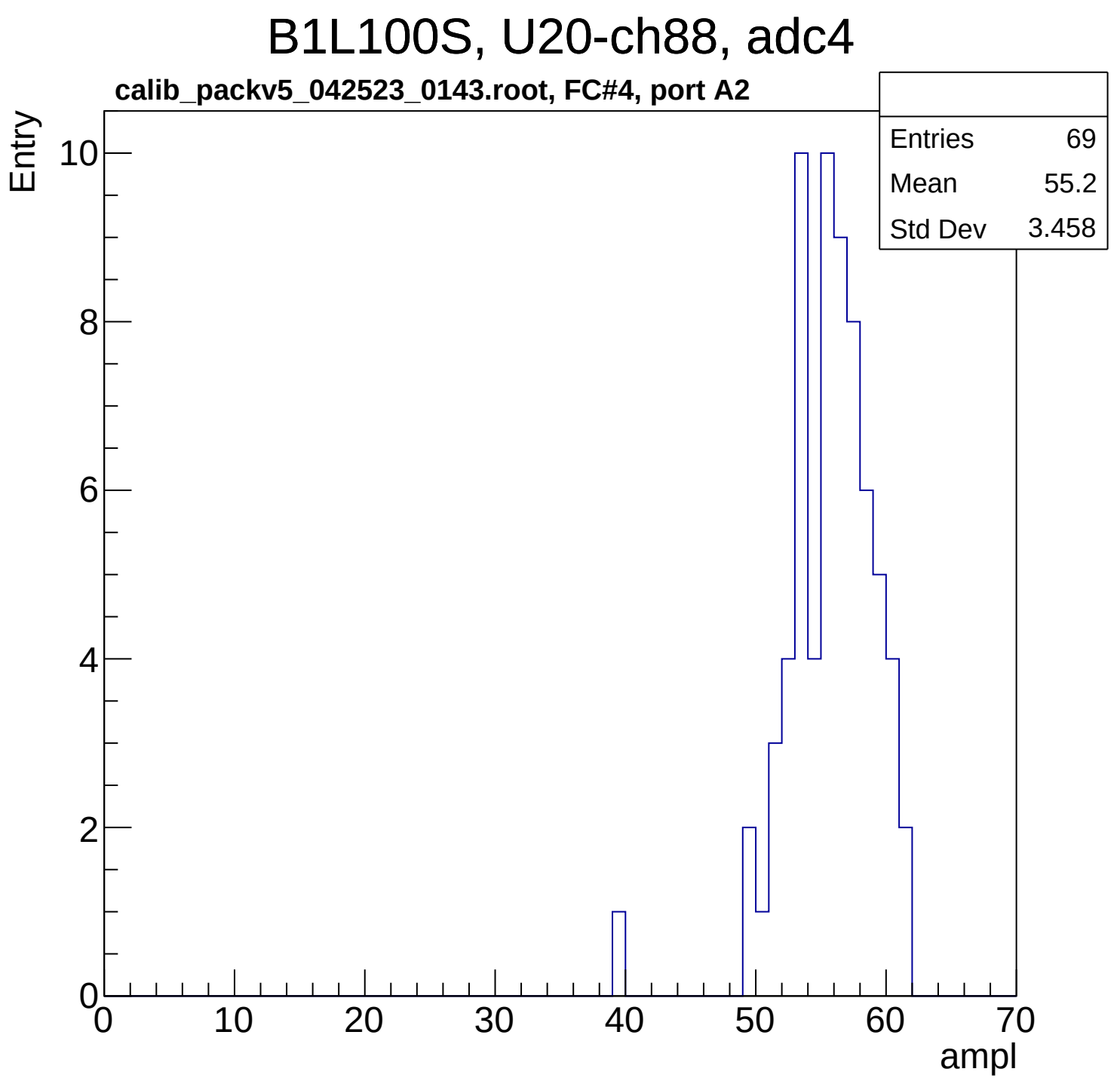
B1L100S, U20-ch88, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.2
Std Dev 3.458
Entry
ampl
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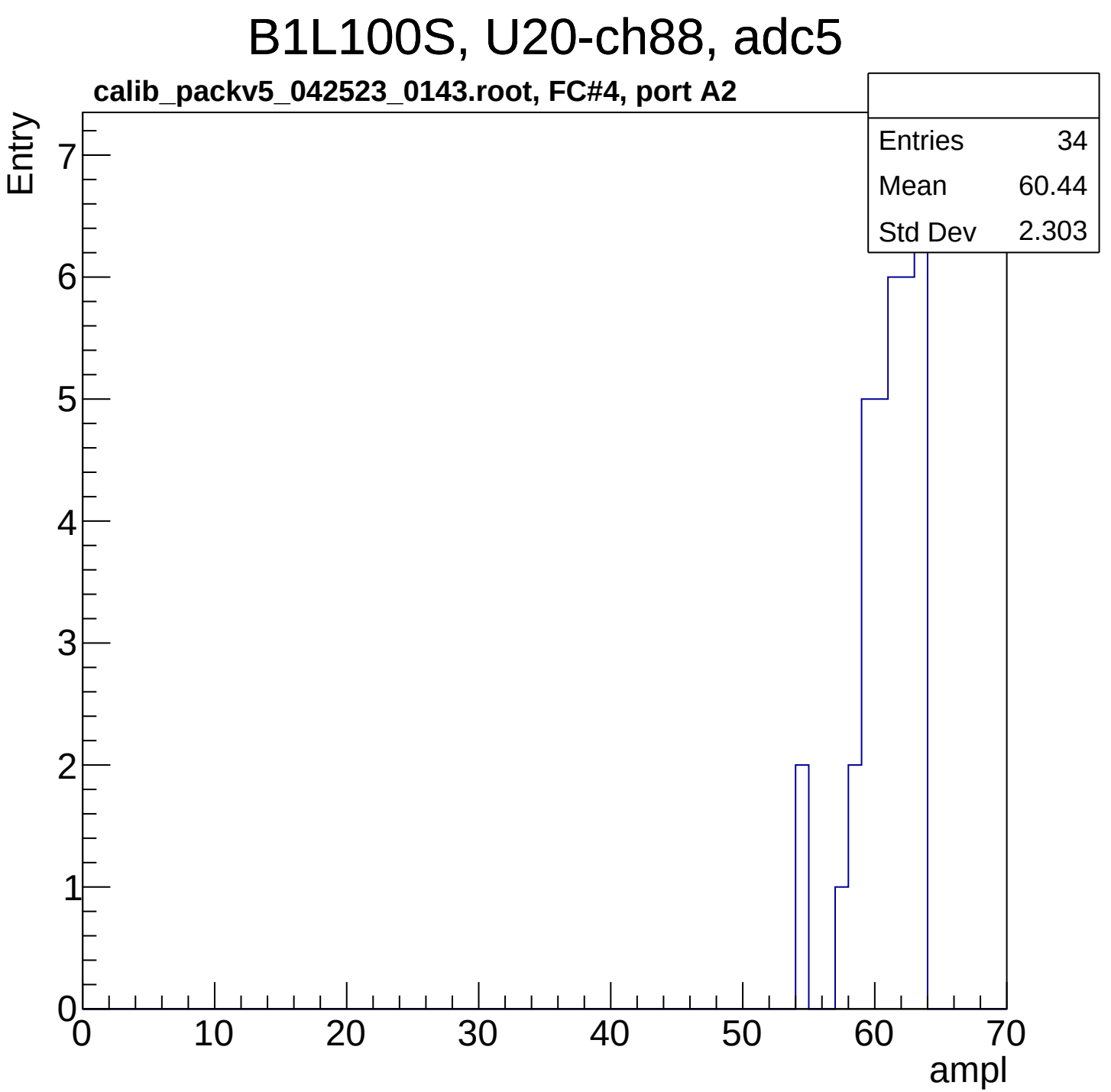
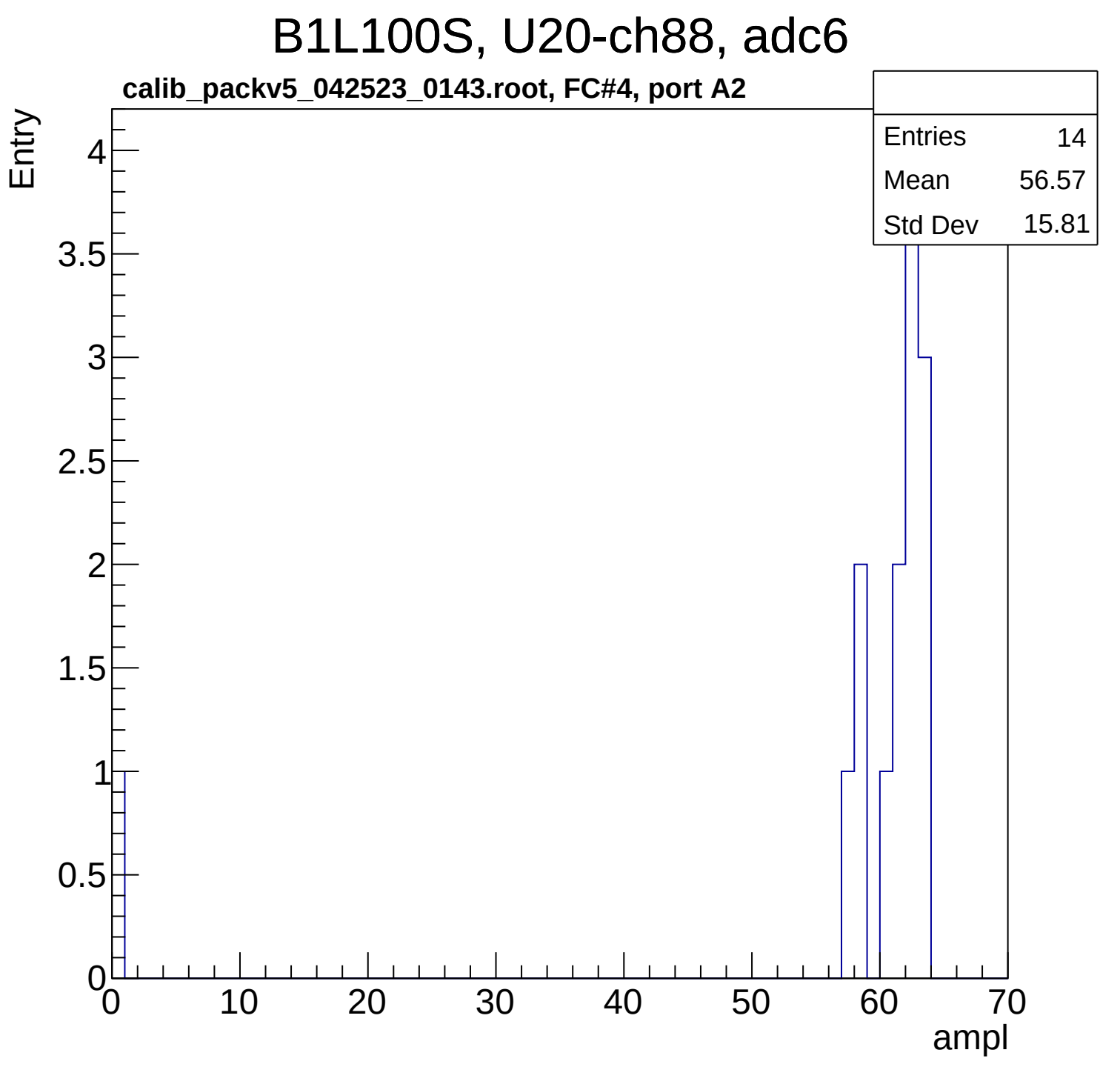
10
20
30
40
50
60
70


B1L100S, U20-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 60.44
Std Dev 2.303
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch88, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 56.57
Std Dev 15.81
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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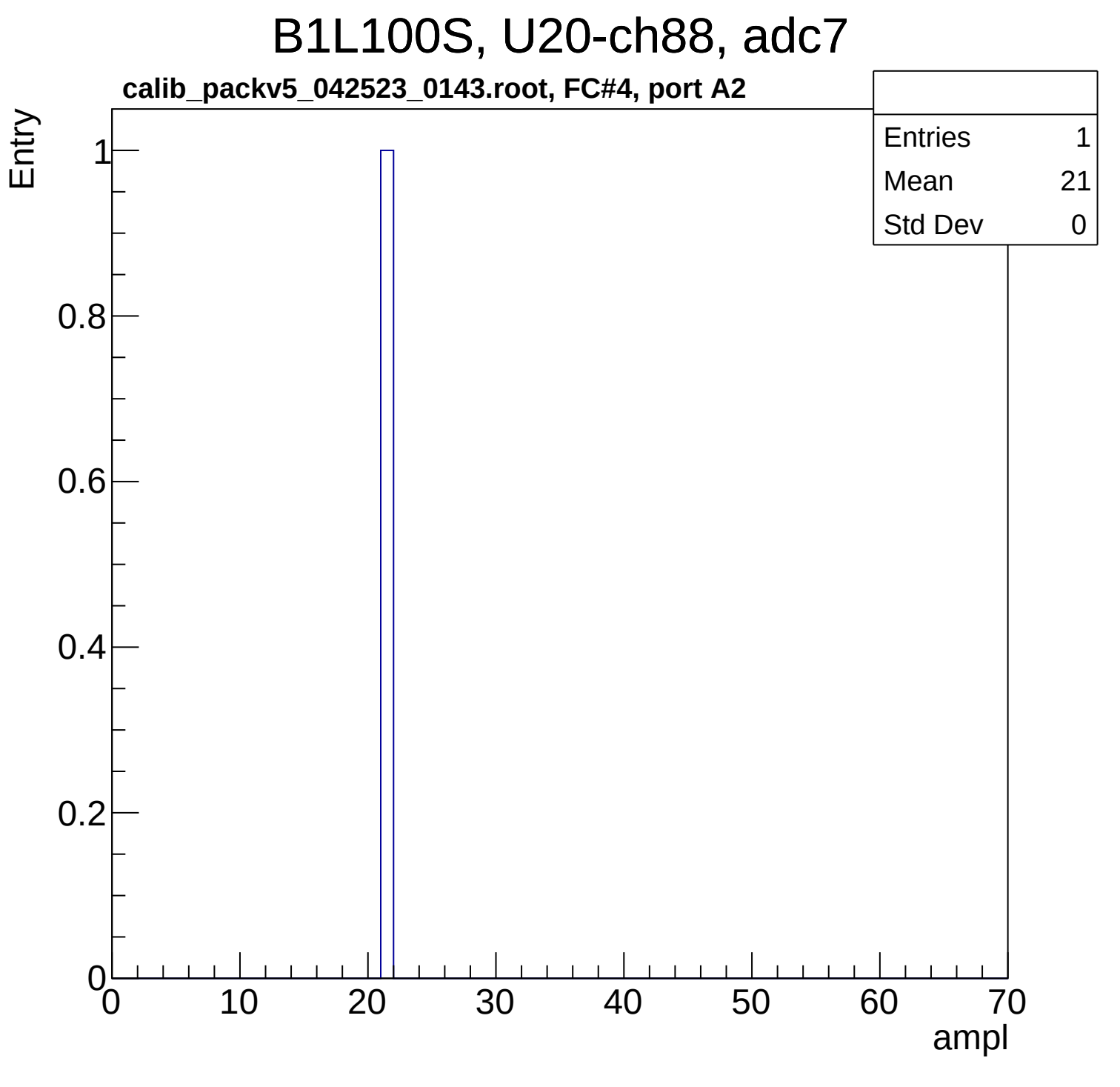
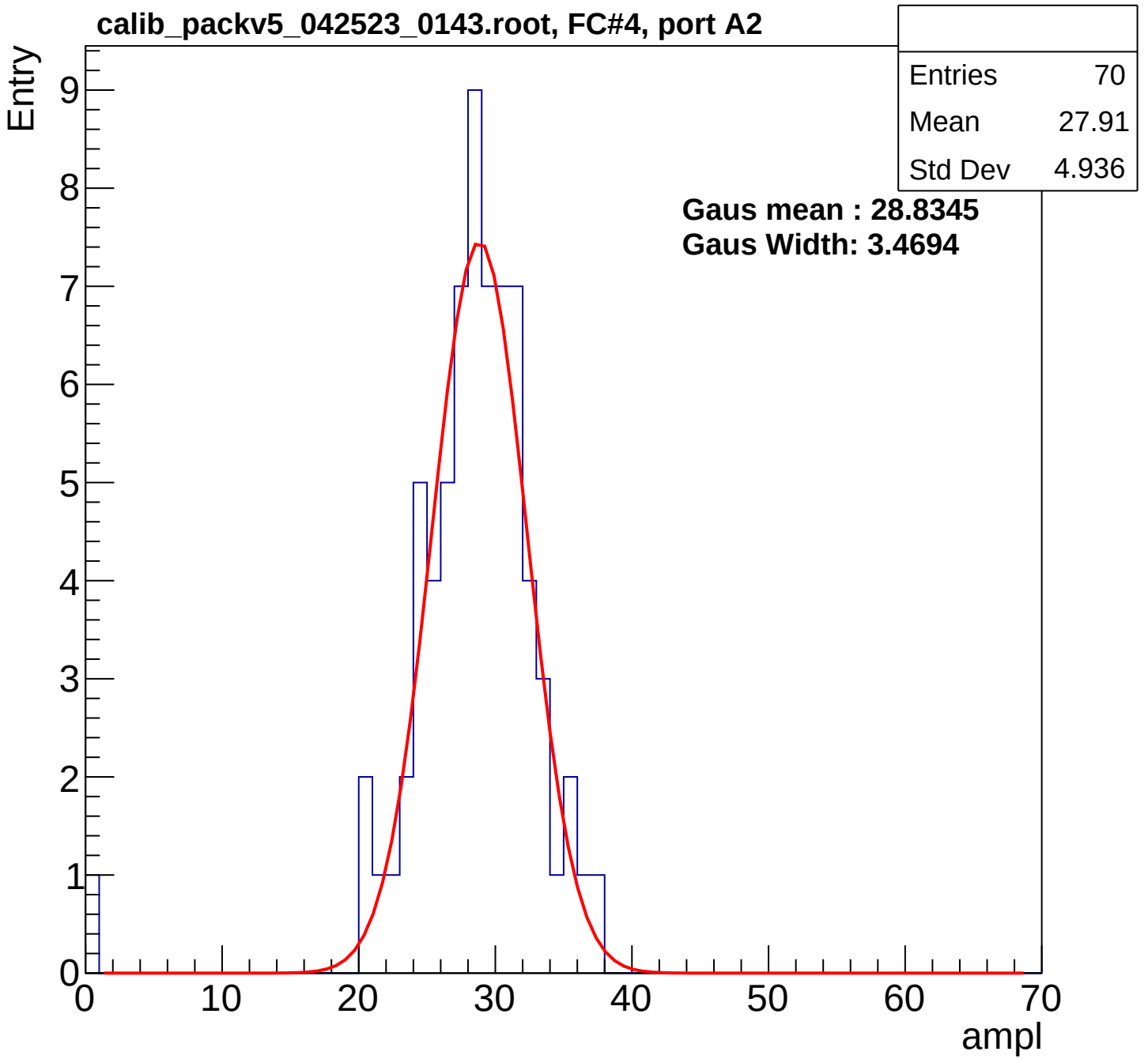
60
70


B1L100S, U20-ch88, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 27.91
Std Dev 4.936
Gaus mean : 28.8345
Gaus Width: 3.4694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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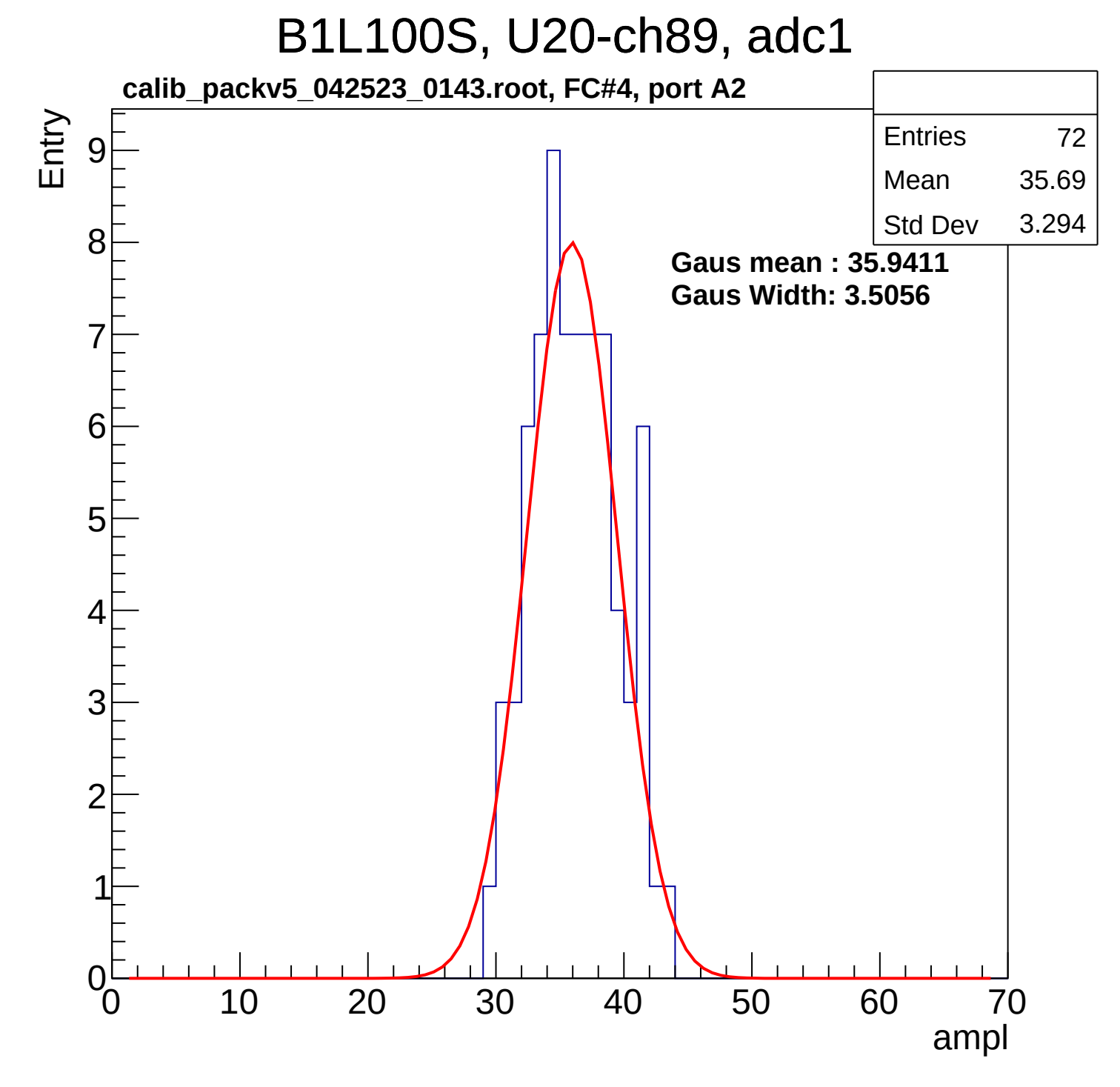
B1L100S, U20-ch89, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 35.69
Std Dev 3.294
Gaus mean : 35.9411
Gaus Width: 3.5056
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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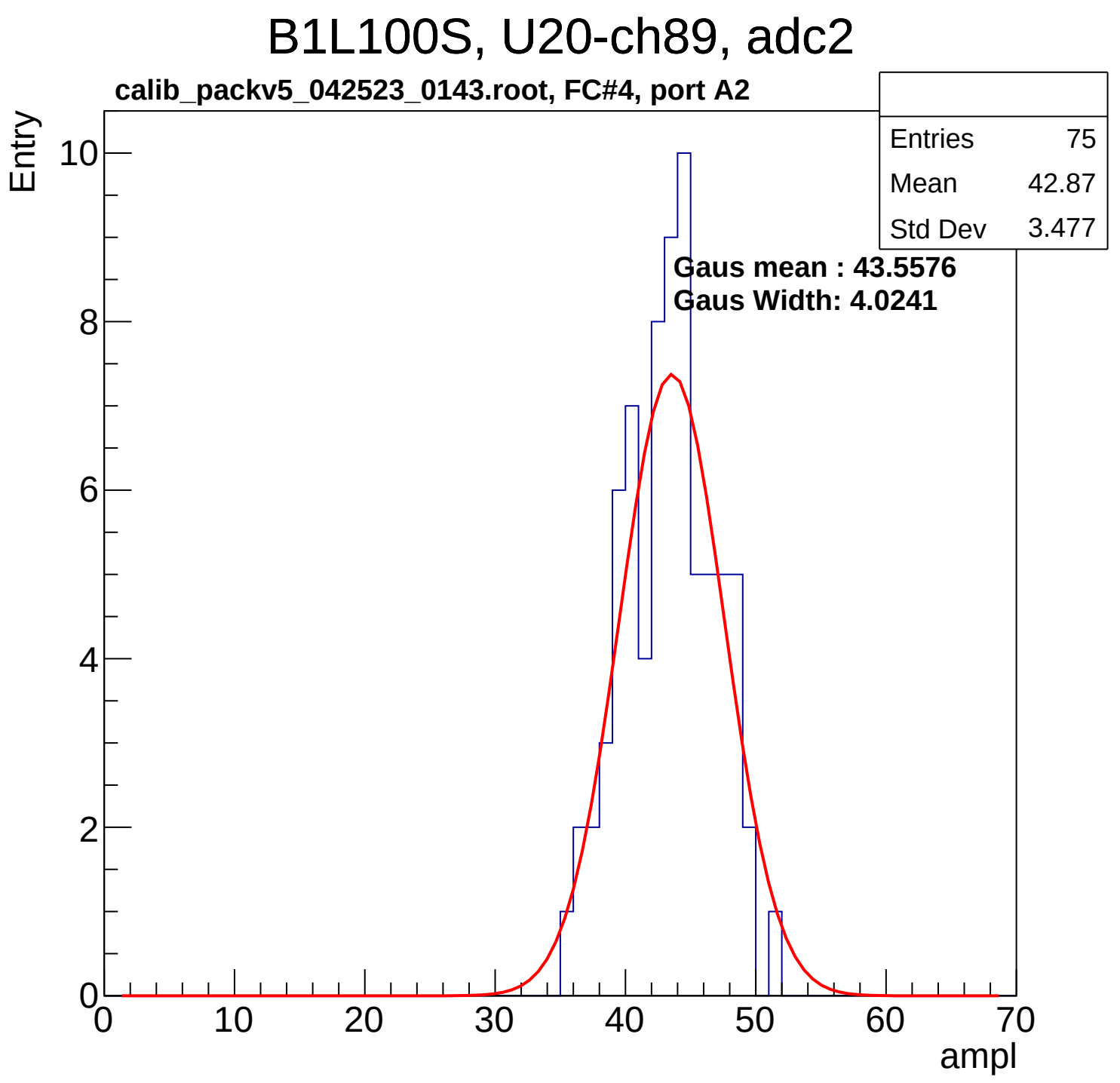
B1L100S, U20-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 42.87
Std Dev 3.477
Gaus mean : 43.5576
Gaus Width: 4.0241
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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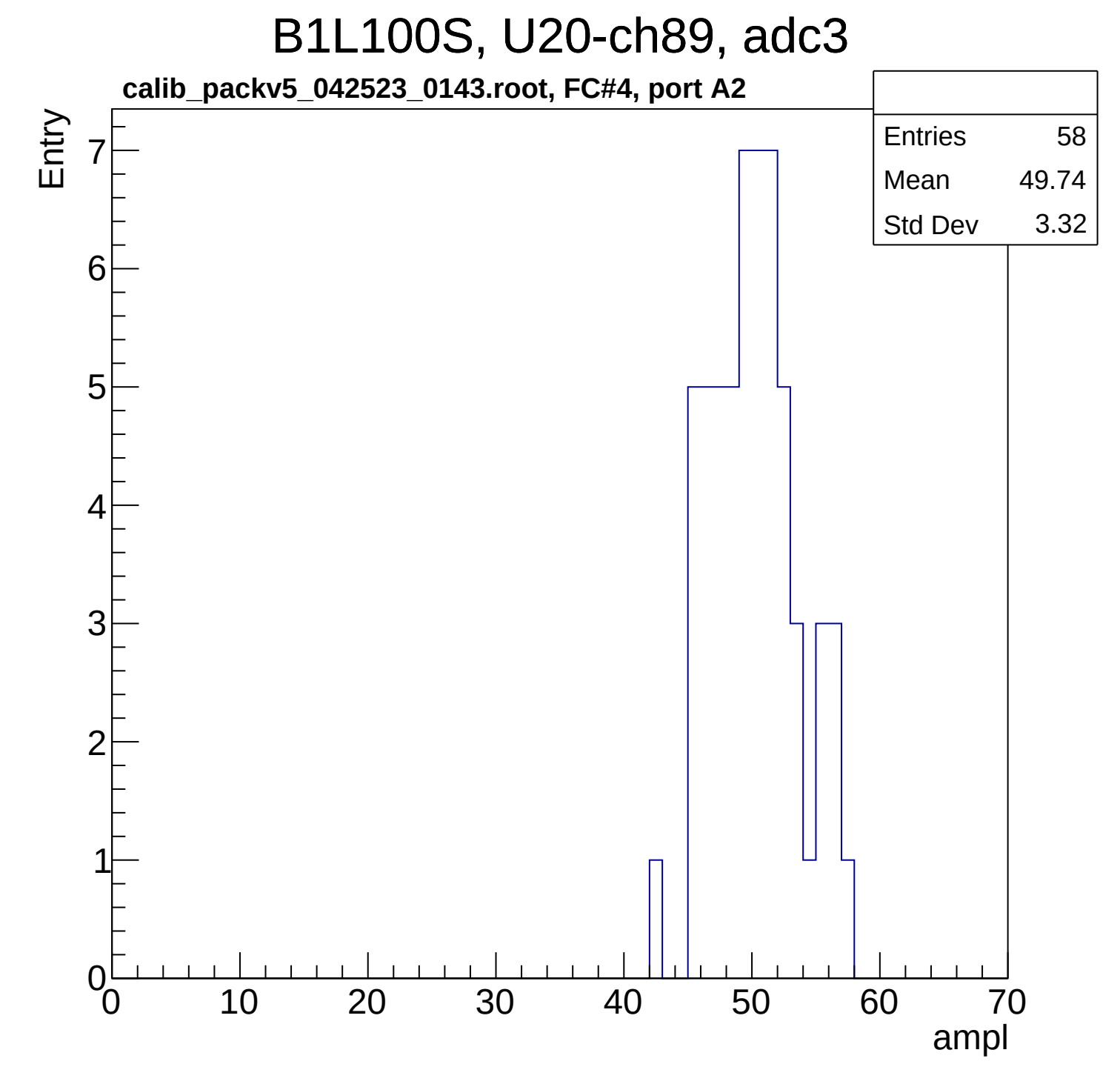
B1L100S, U20-ch89, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.74
Std Dev 3.32
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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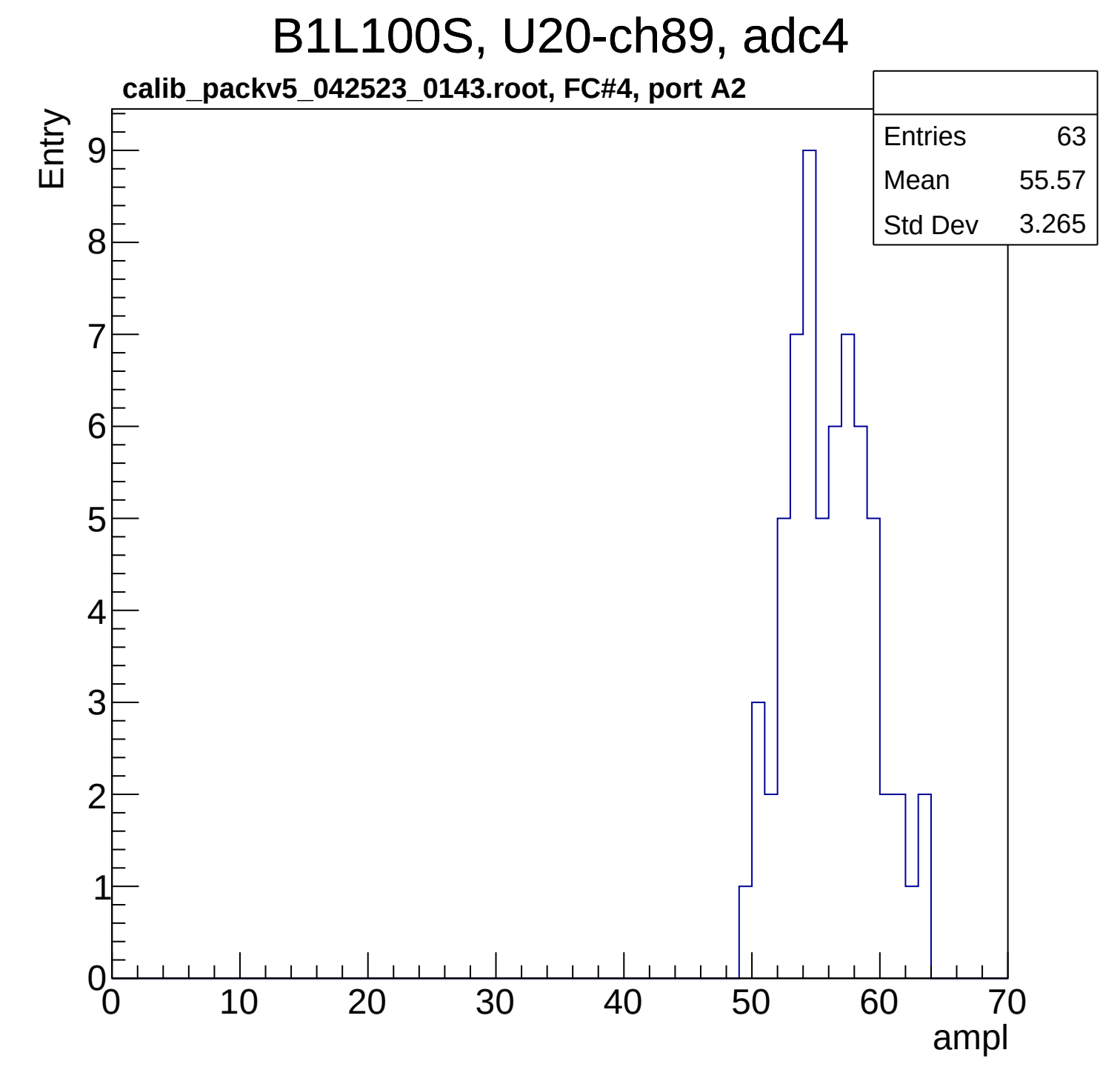
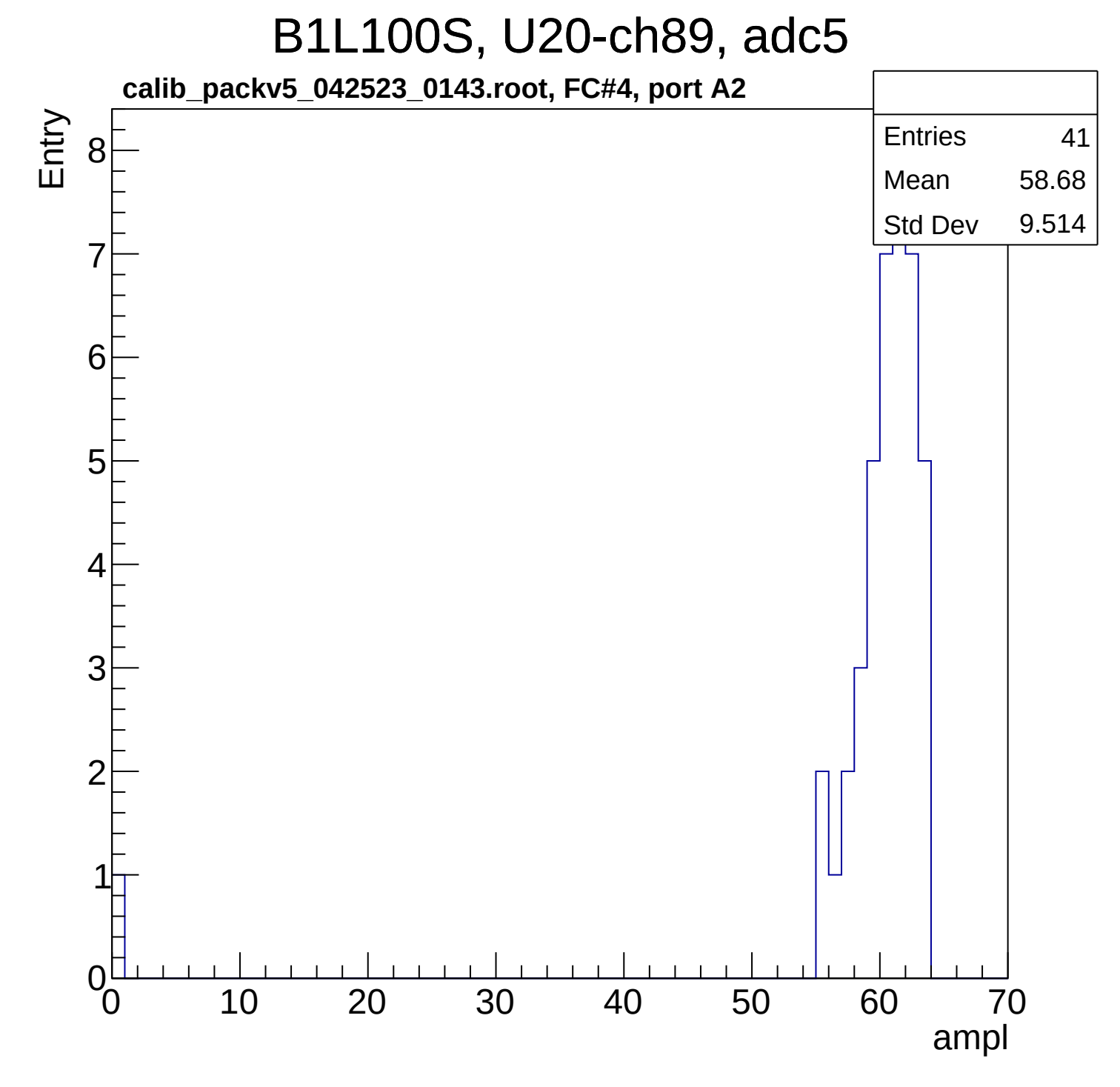

B1L100S, U20-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.57
Std Dev 3.265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.68
Std Dev 9.514
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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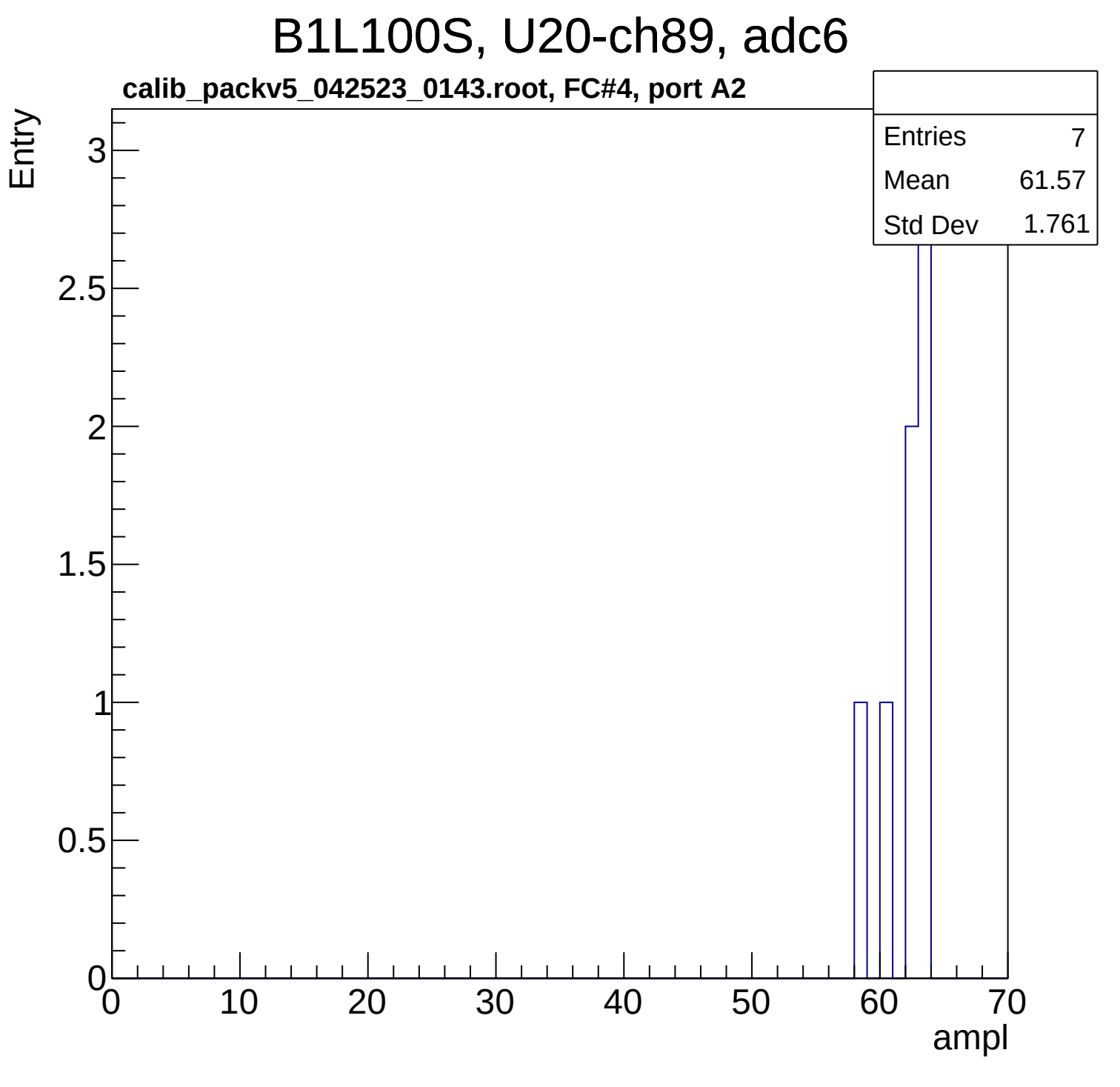
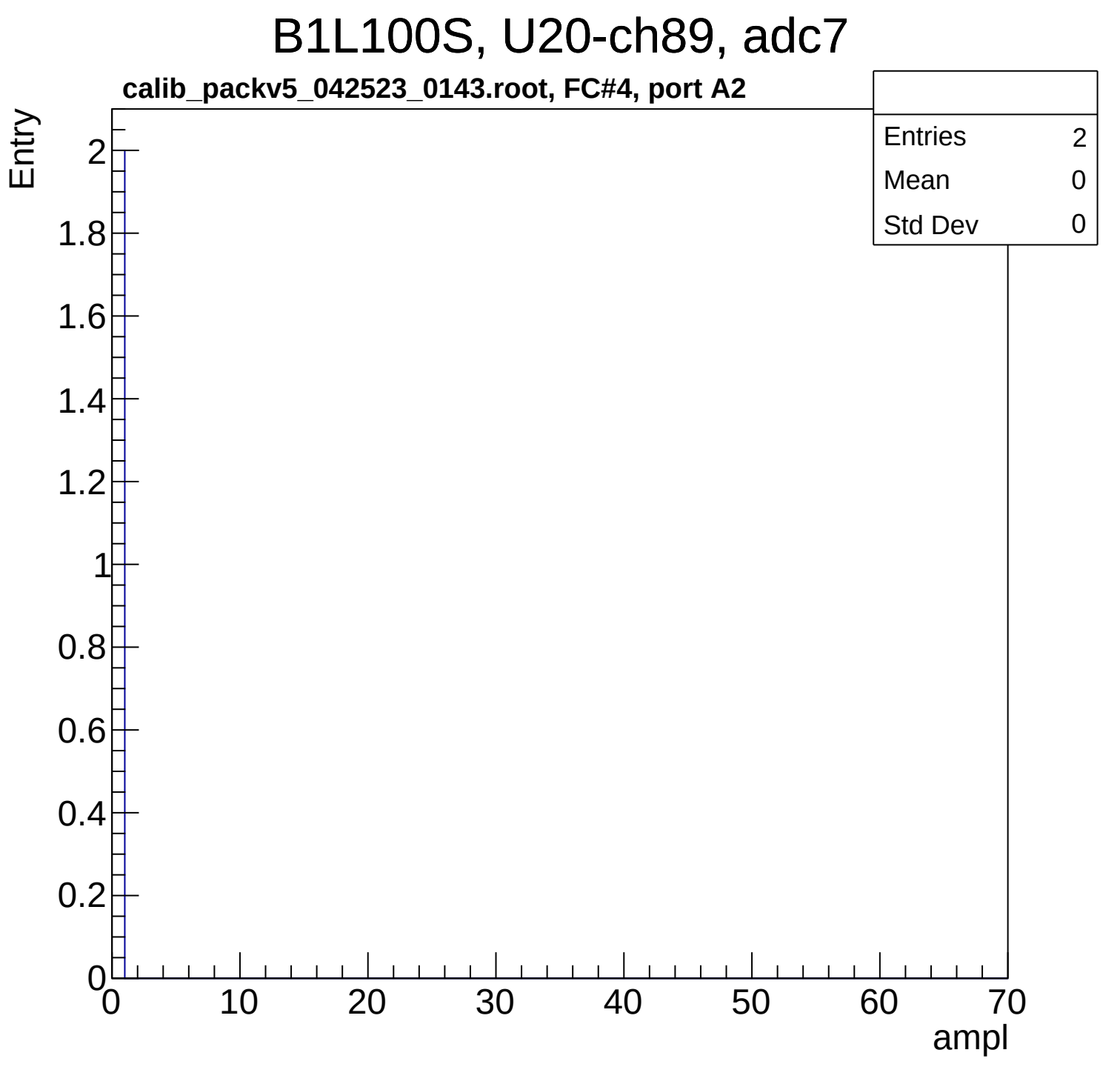
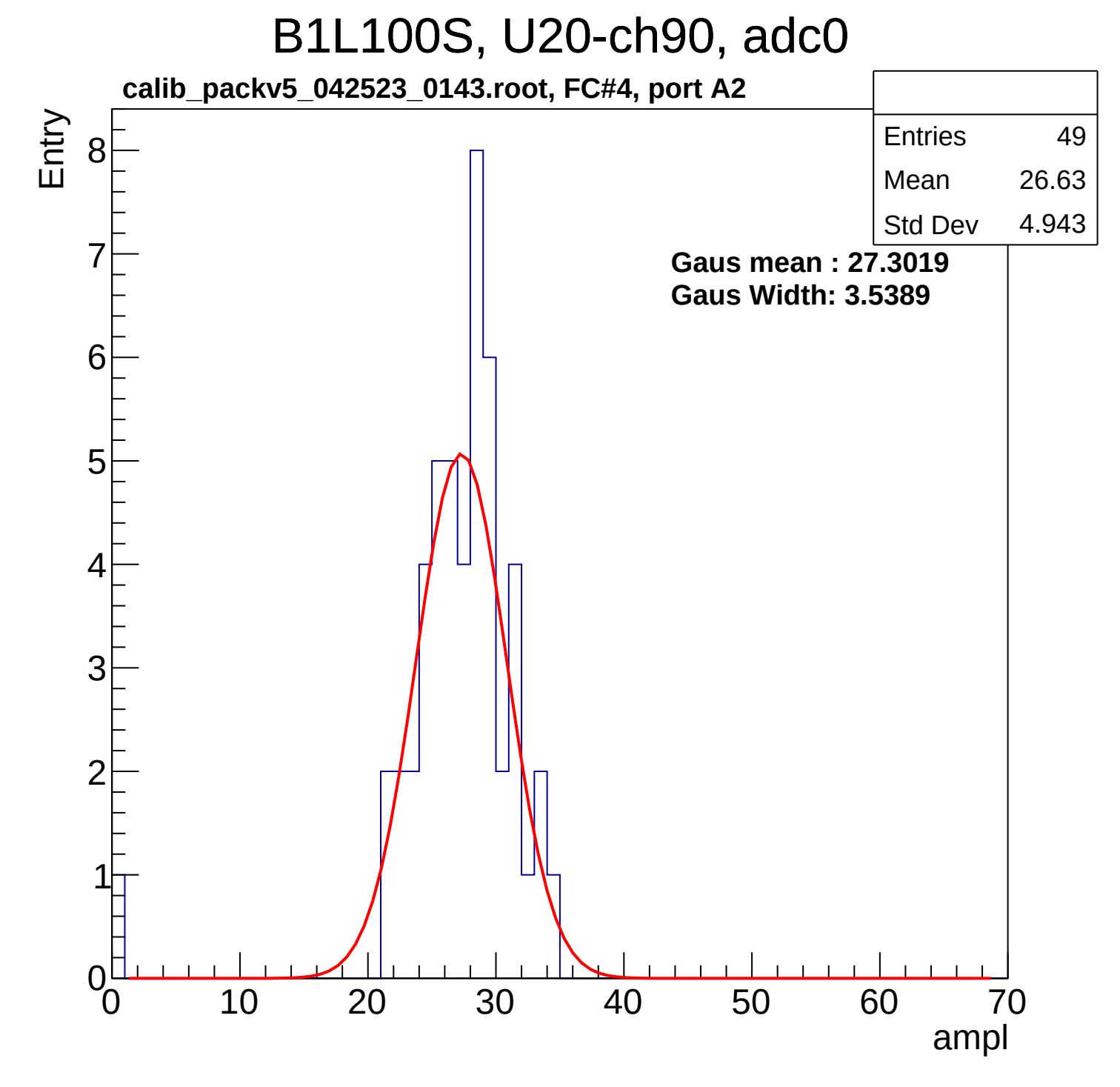
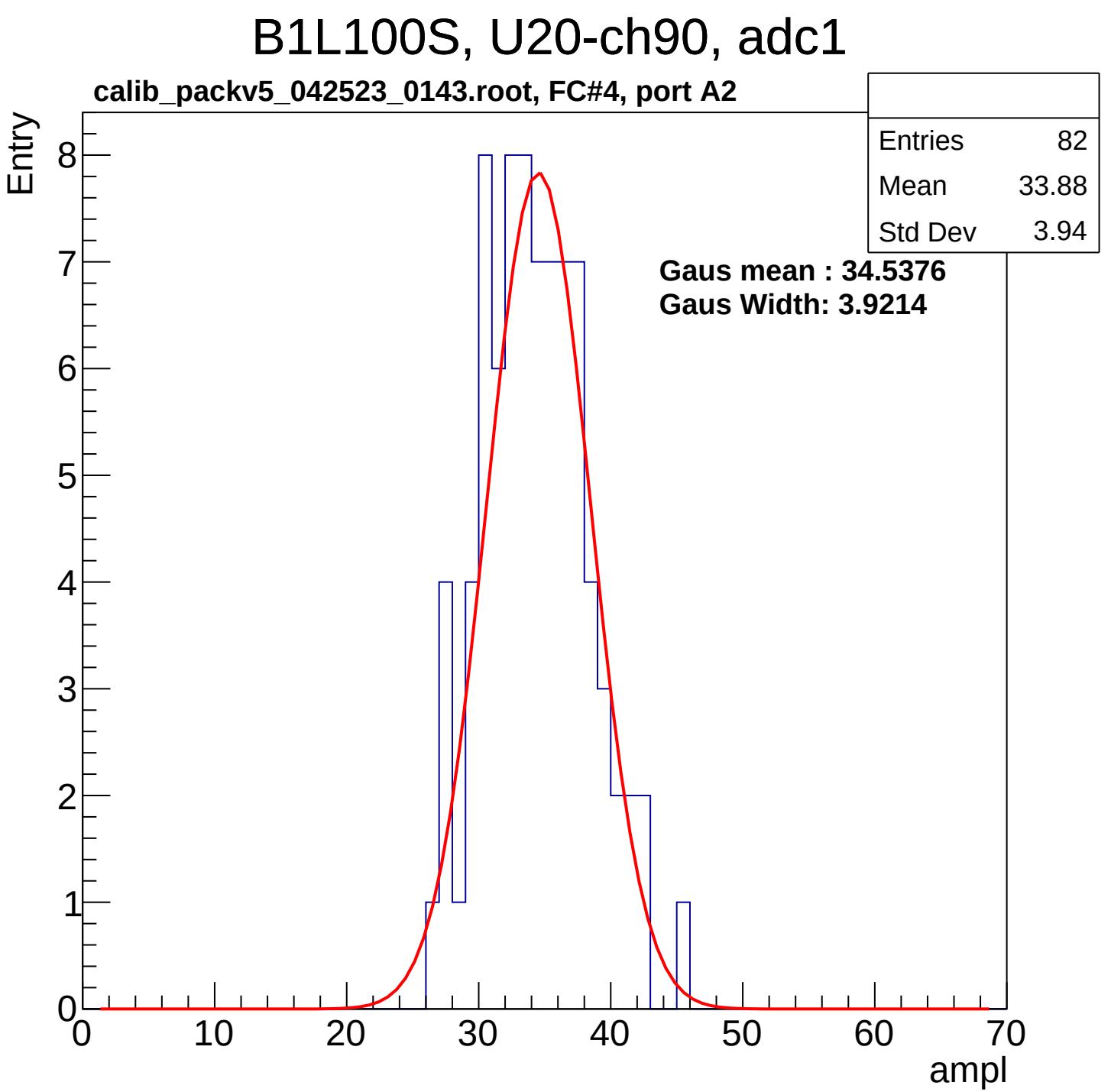

B1L100S, U20-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.57
Std Dev 1.761
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch89, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 26.63
Std Dev 4.943
Gaus mean : 27.3019
Gaus Width: 3.5389
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 33.88
Std Dev 3.94
Gaus mean : 34.5376
Gaus Width: 3.9214
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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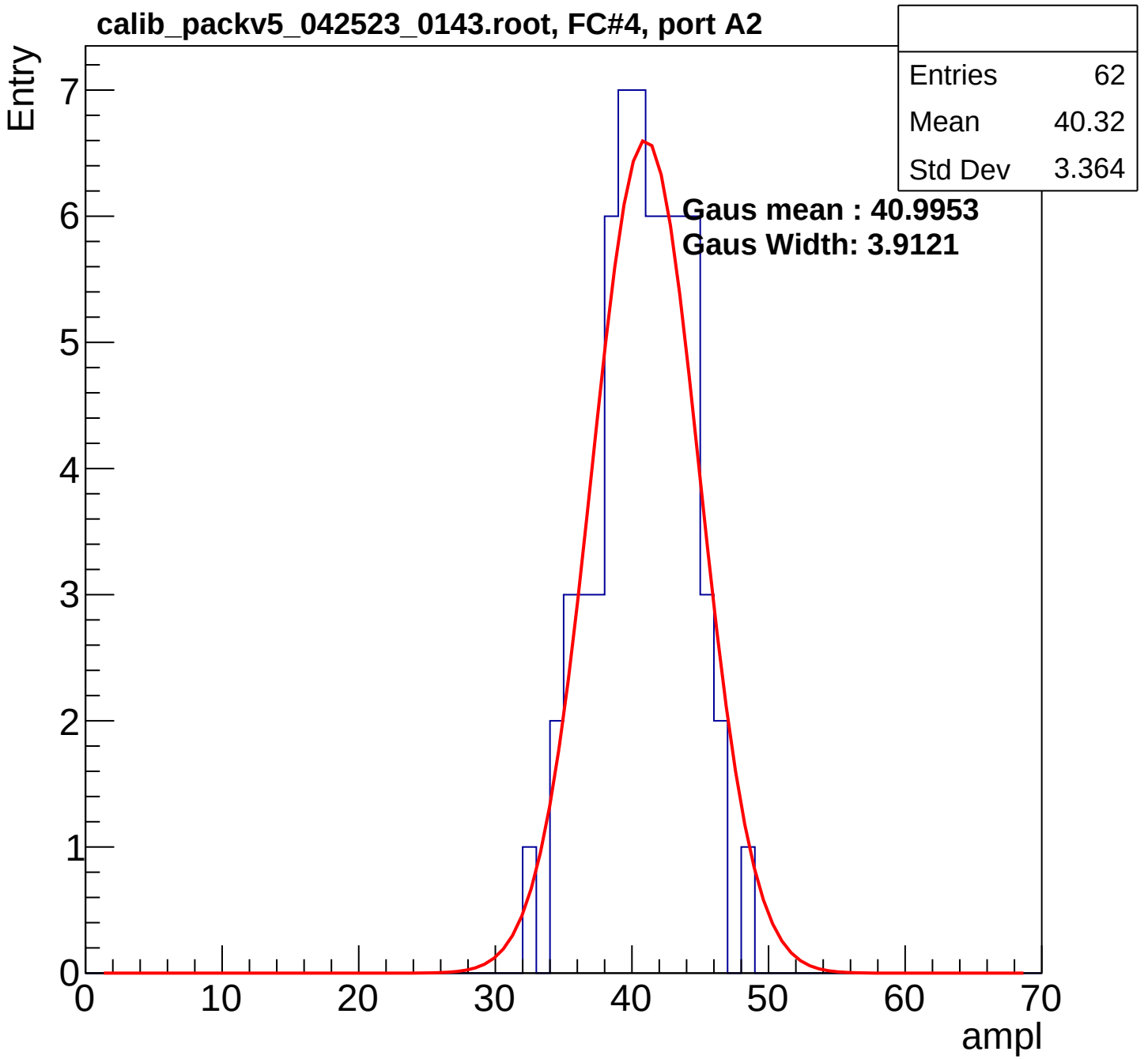
B1L100S, U20-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 40.32
Std Dev 3.364
Gaus mean : 40.9953
Gaus Width: 3.9121
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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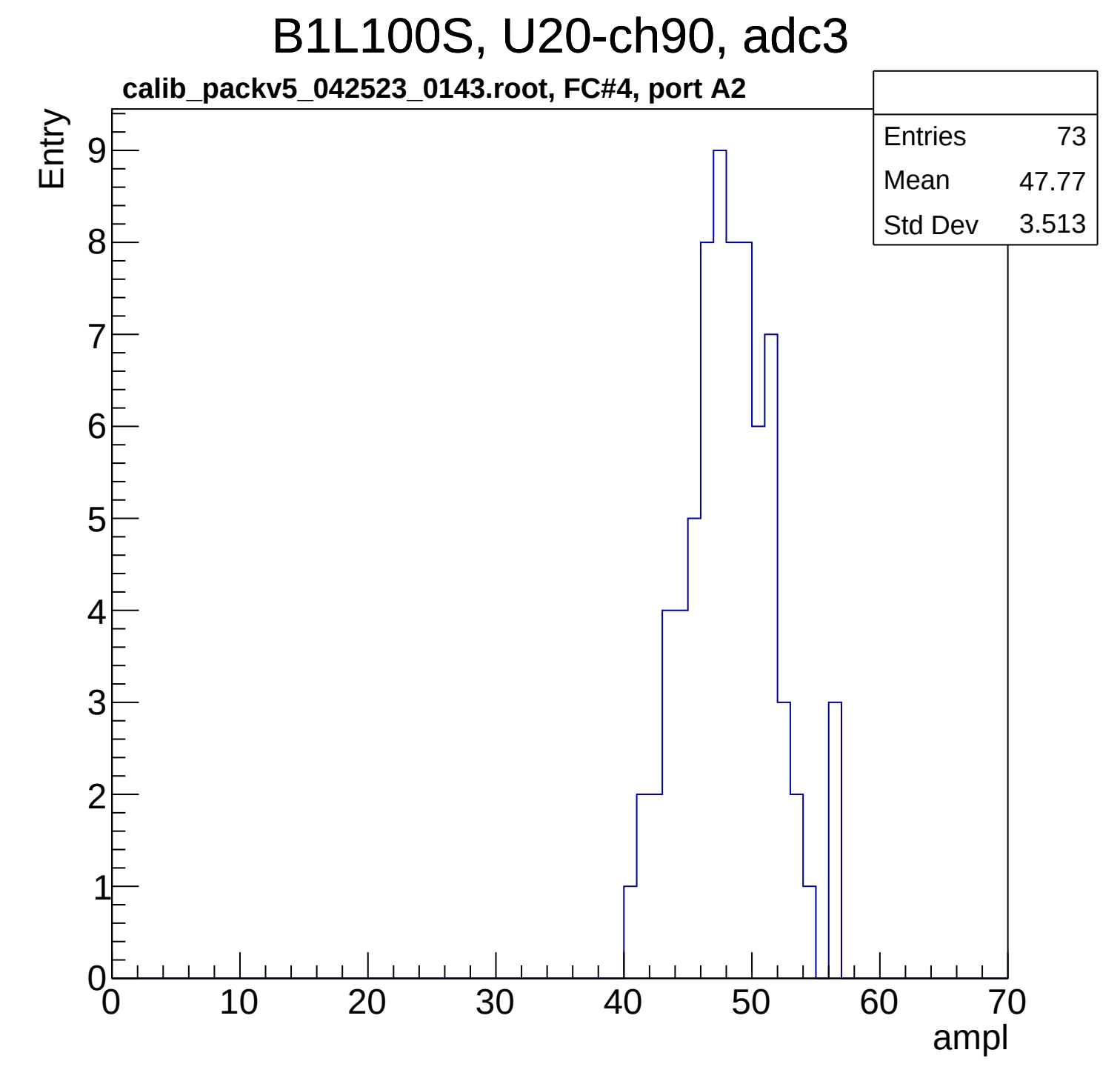
B1L100S, U20-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 47.77
Std Dev 3.513
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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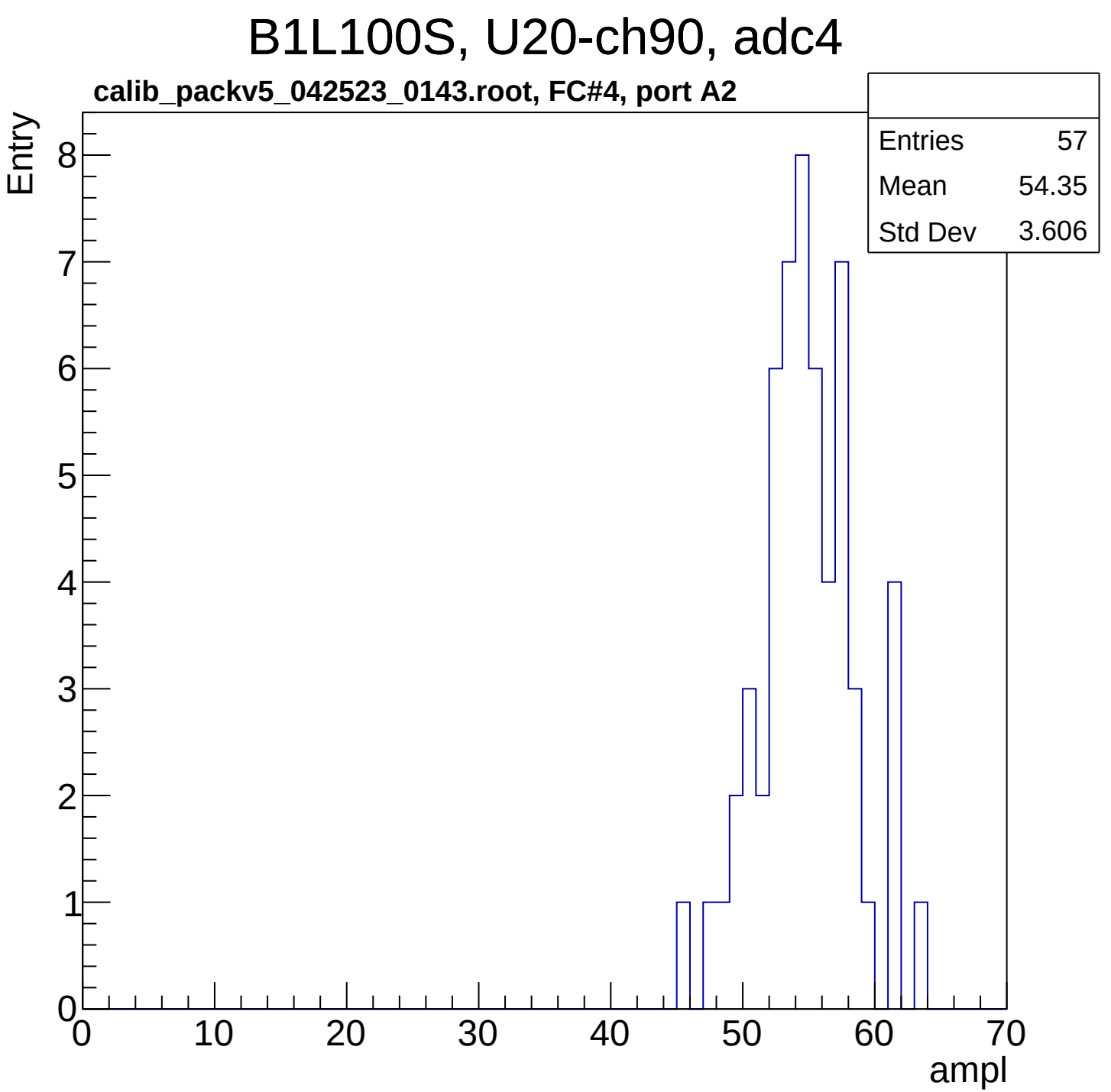
B1L100S, U20-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 54.35
Std Dev 3.606
Entry
ampl
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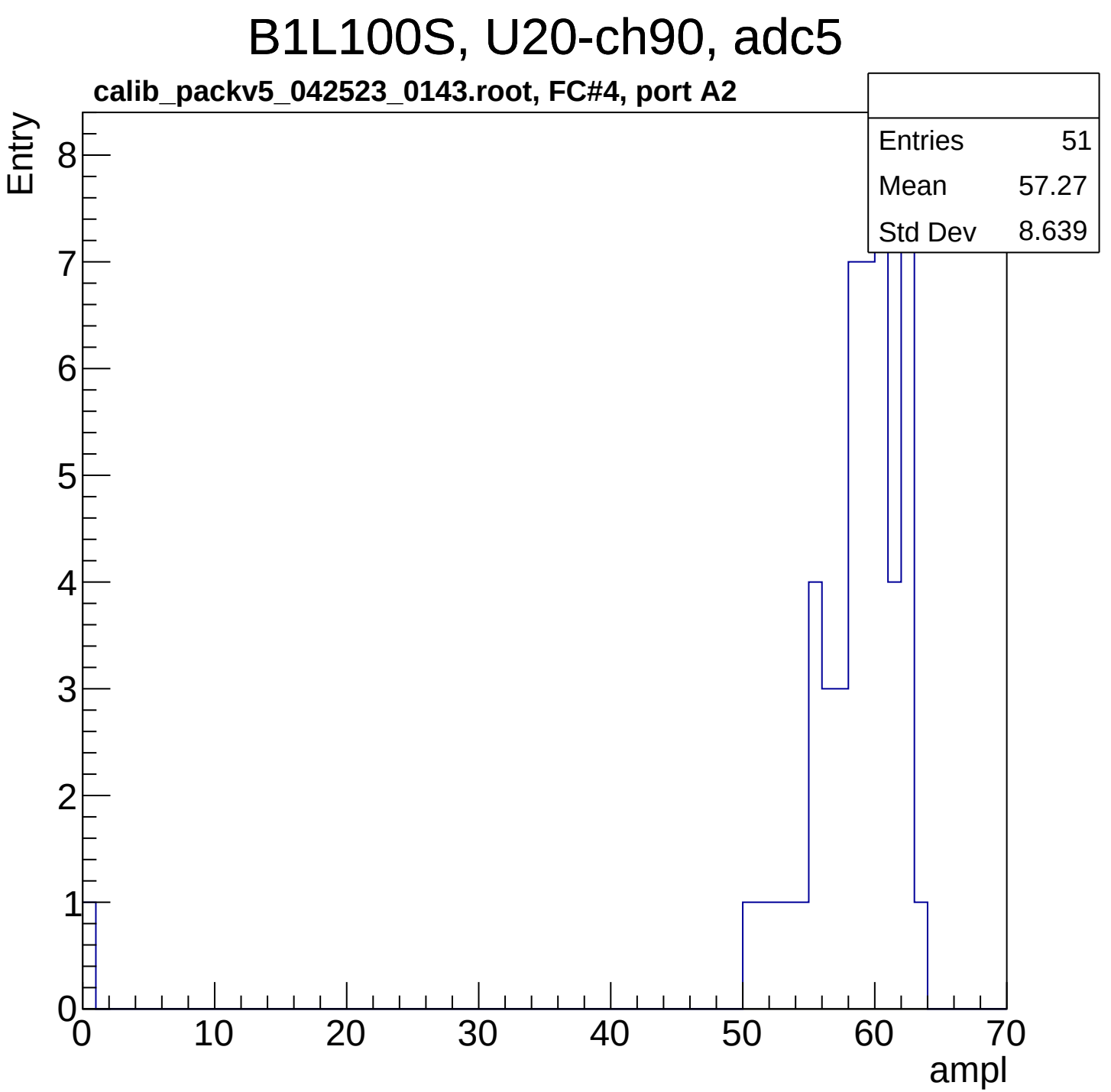
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.27
Std Dev 8.639
Entry
ampl
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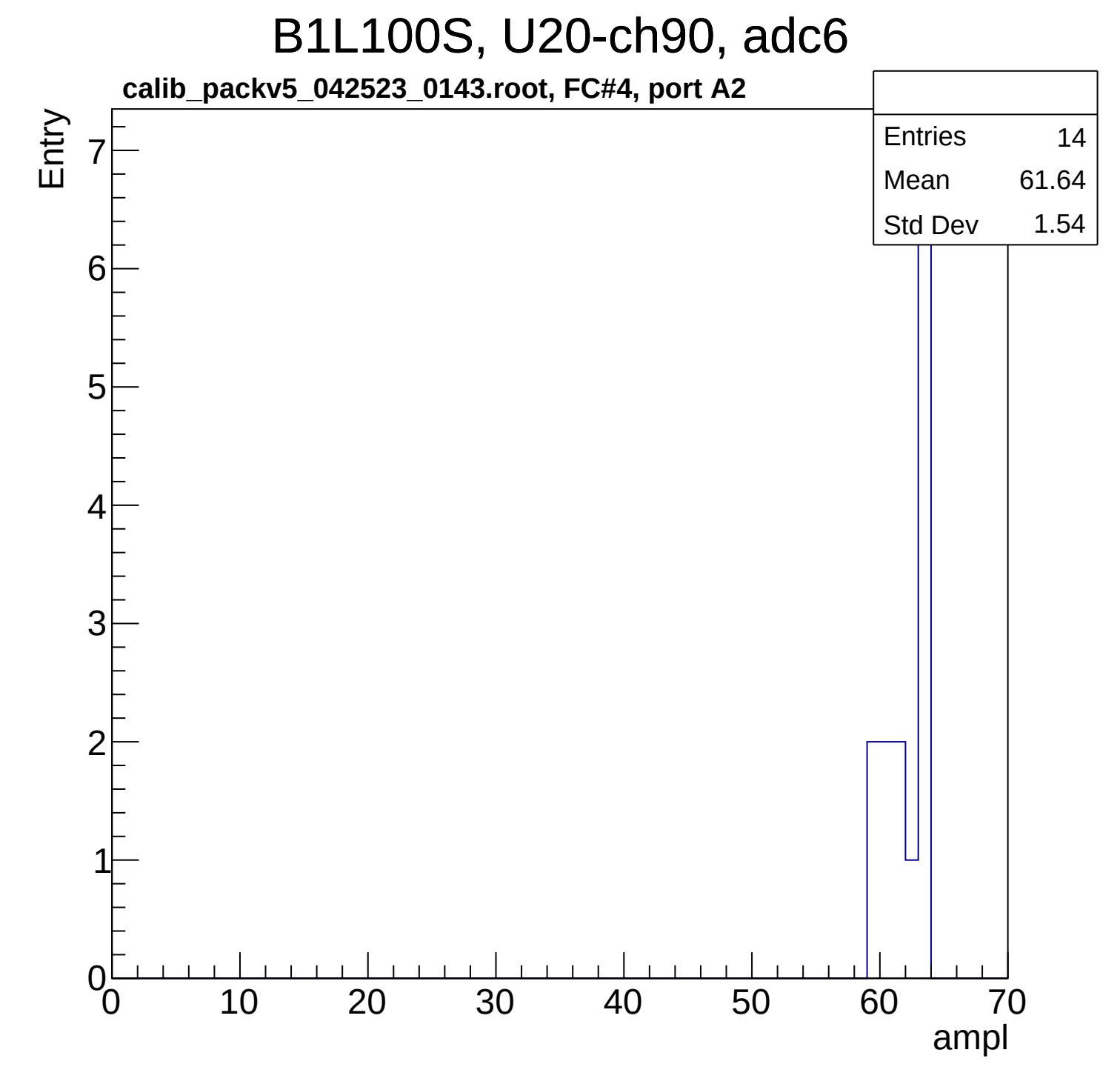
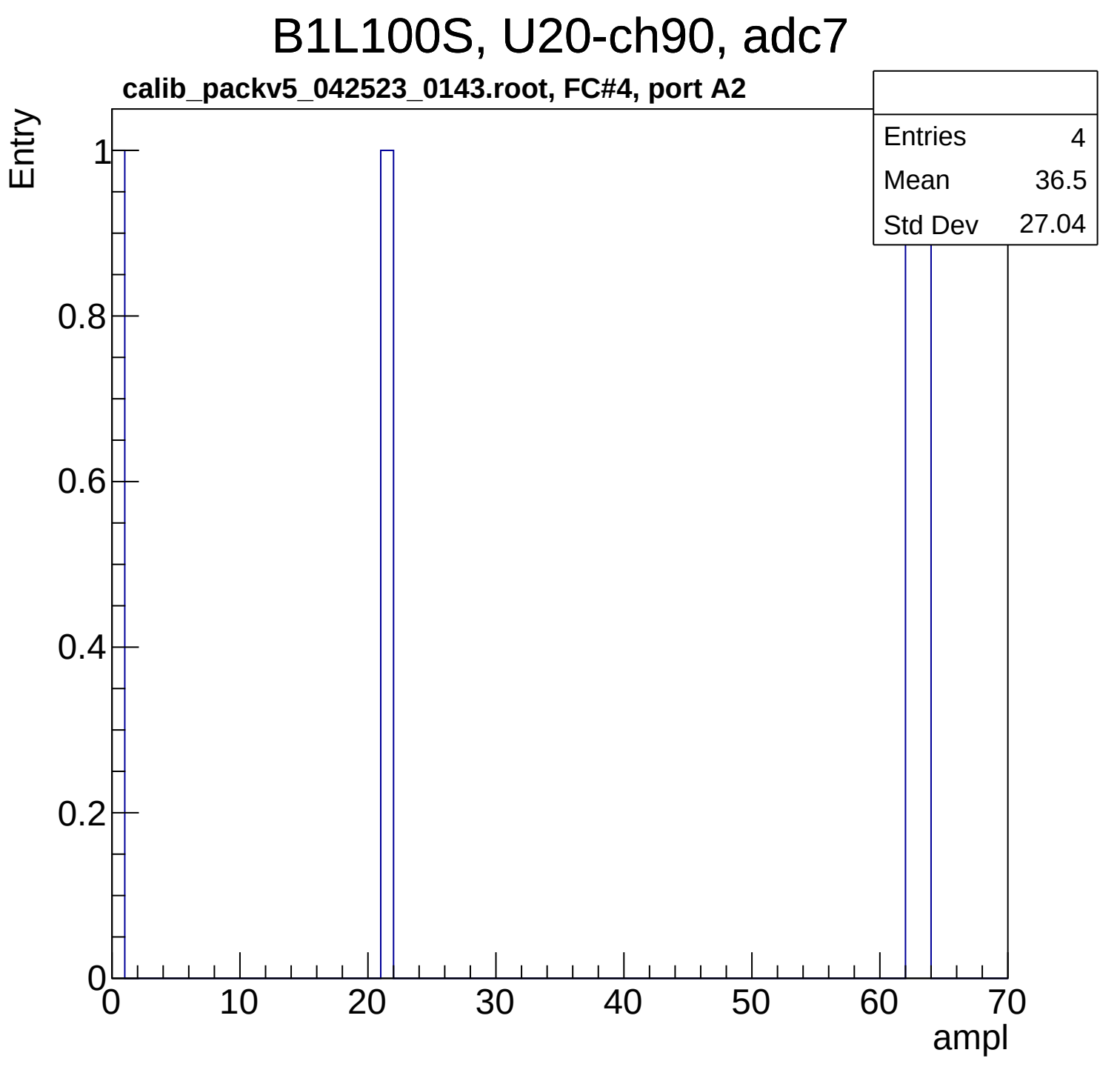
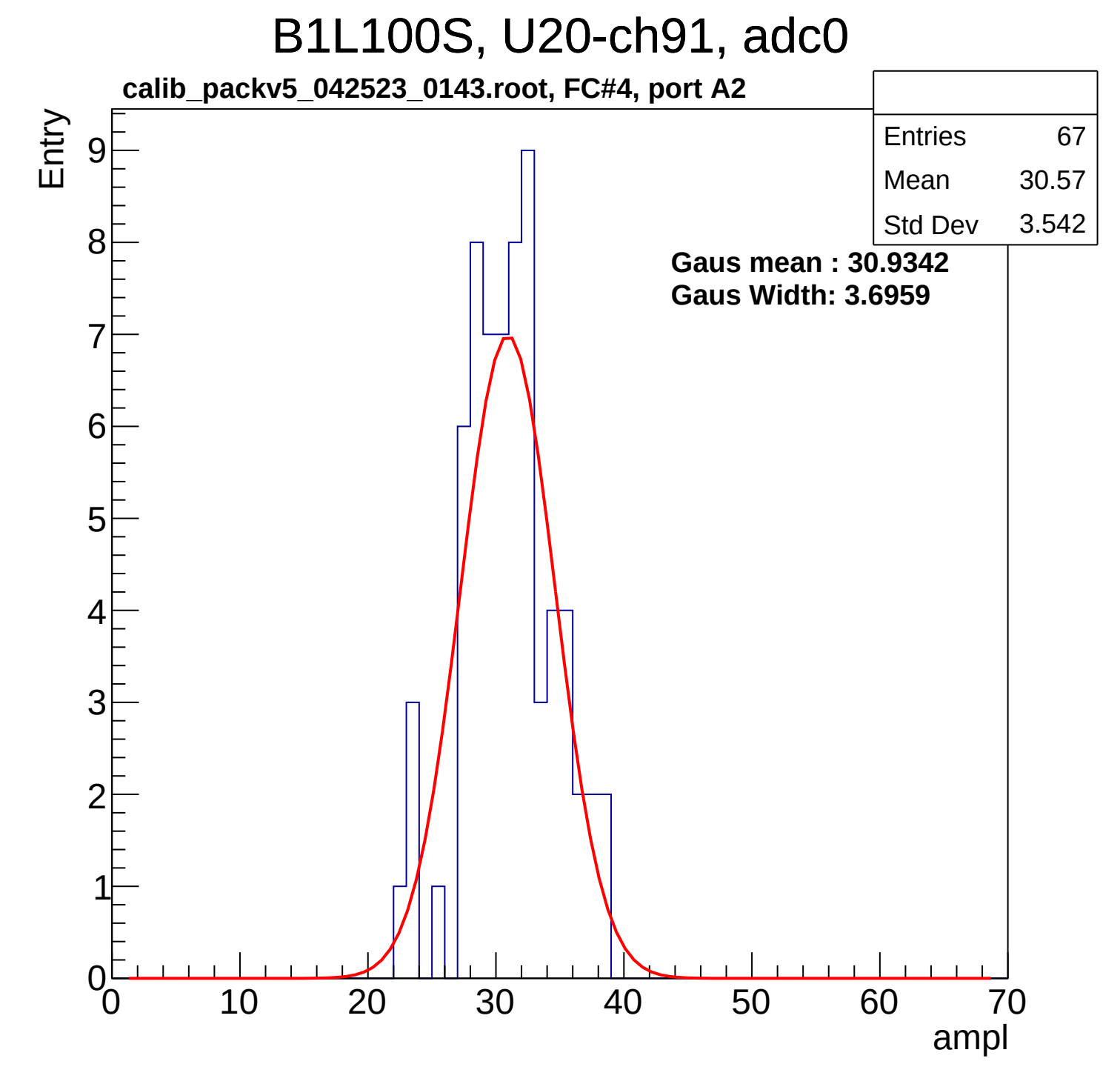
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch90, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 61.64
Std Dev 1.54
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 36.5
Std Dev 27.04
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.57
Std Dev 3.542
Gaus mean : 30.9342
Gaus Width: 3.6959
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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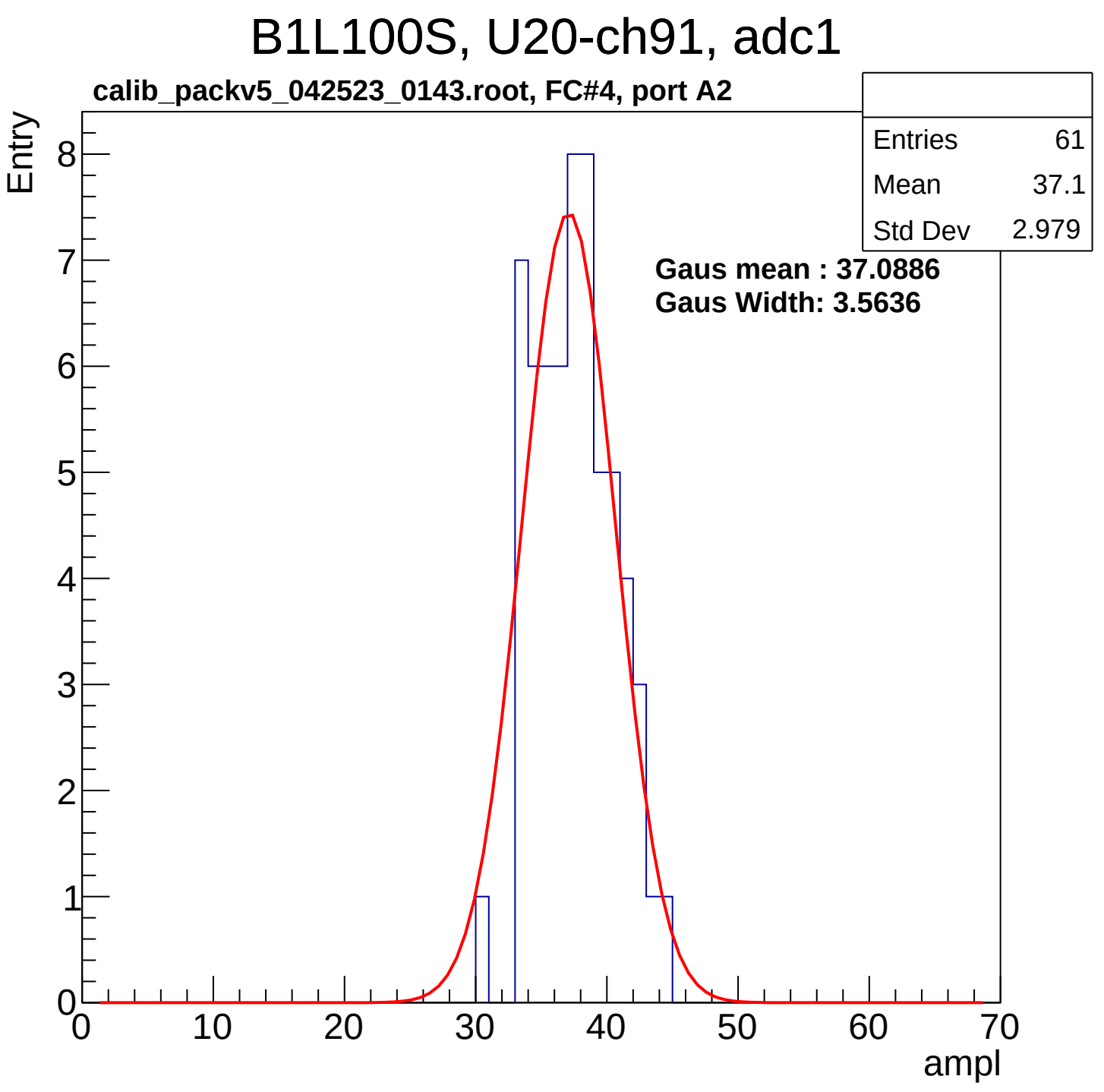
20
30
40
50
60
70


B1L100S, U20-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.1
Std Dev 2.979
Gaus mean : 37.0886
Gaus Width: 3.5636
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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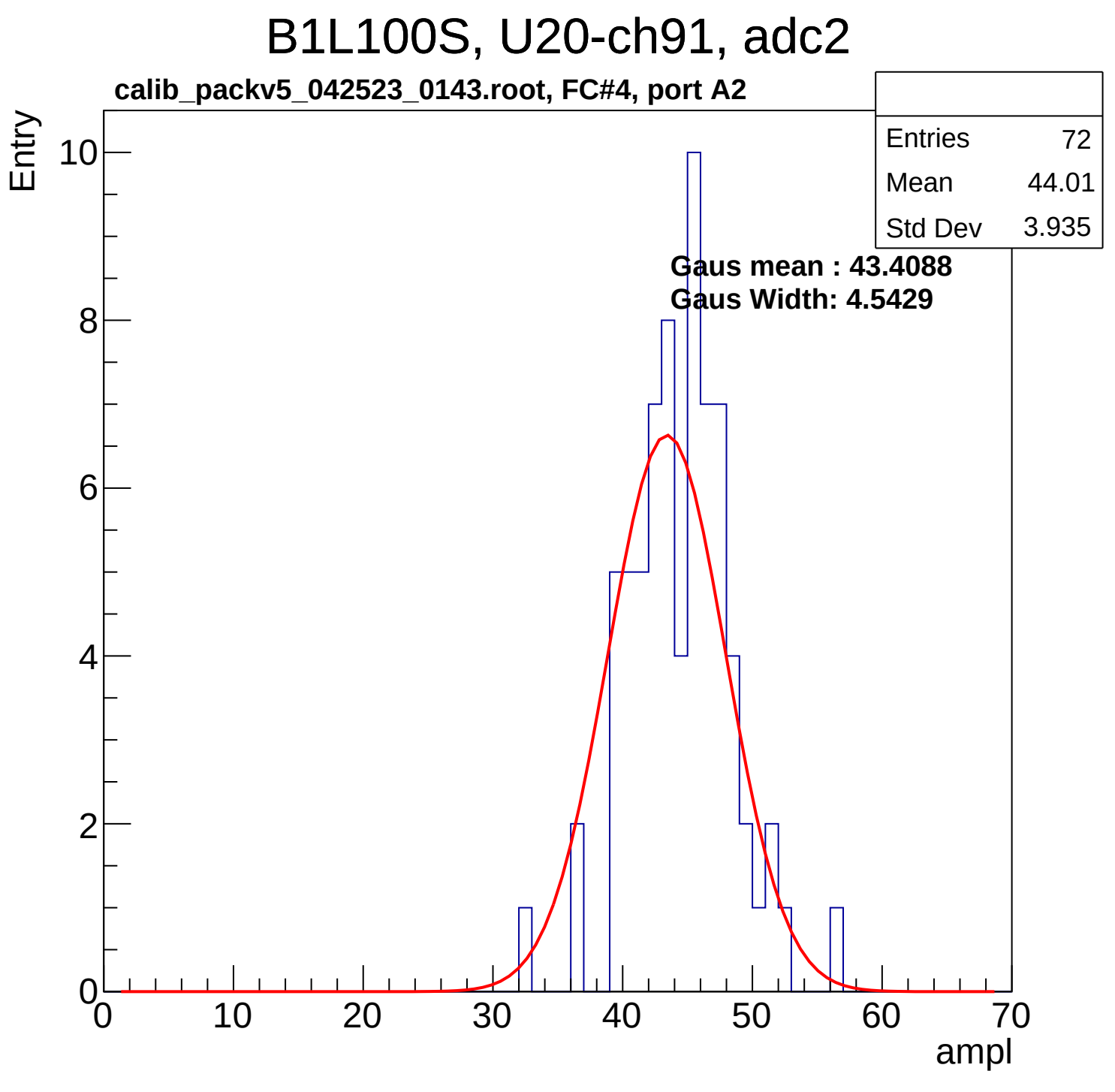
B1L100S, U20-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 44.01
Std Dev 3.935
Gaus mean : 43.4088
Gaus Width: 4.5429
Entry
ampl
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2
4
6
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10
0
10
20
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40
50
60
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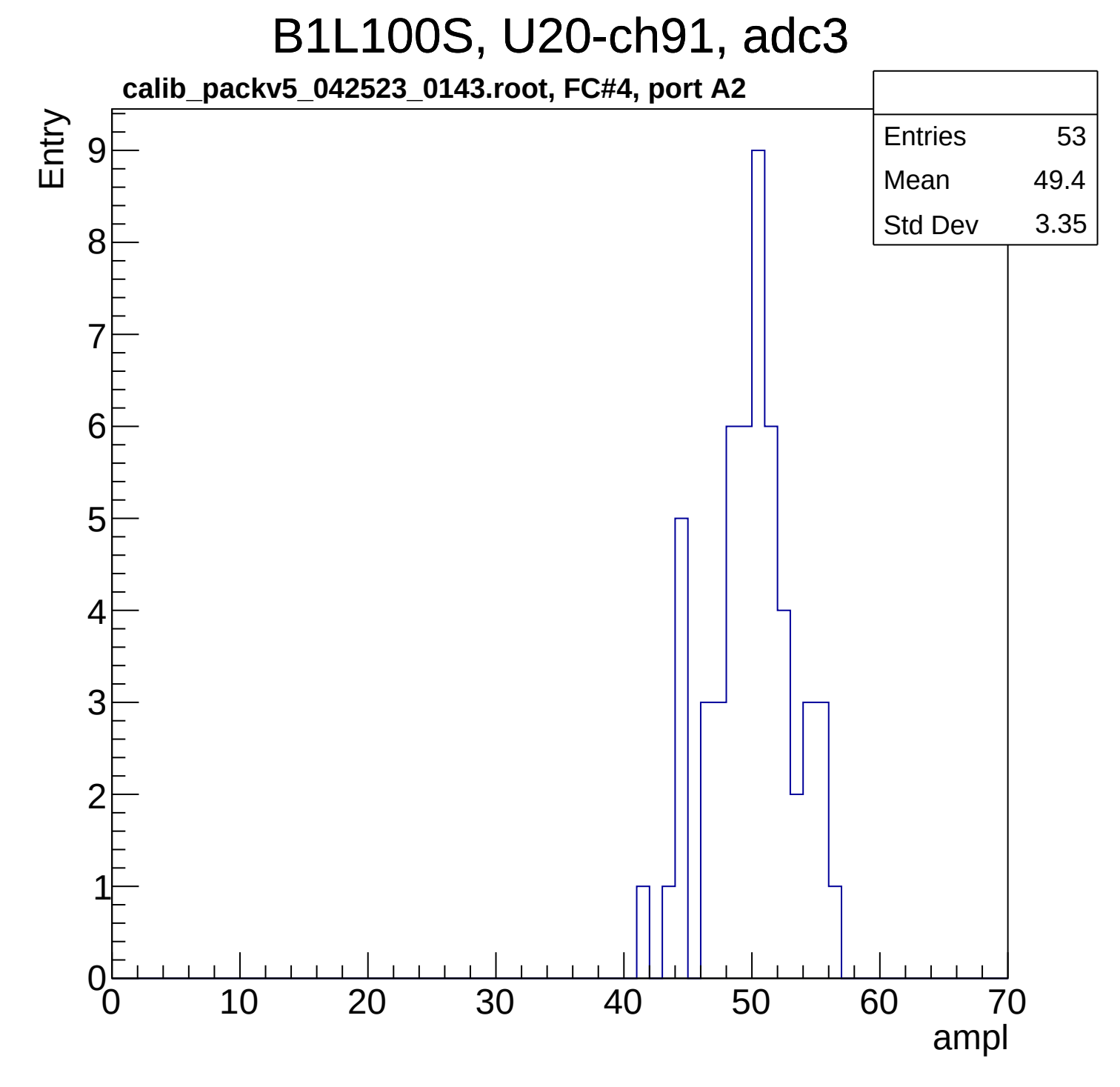
B1L100S, U20-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 49.4
Std Dev 3.35
Entry
ampl
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3
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6
7
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0
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60
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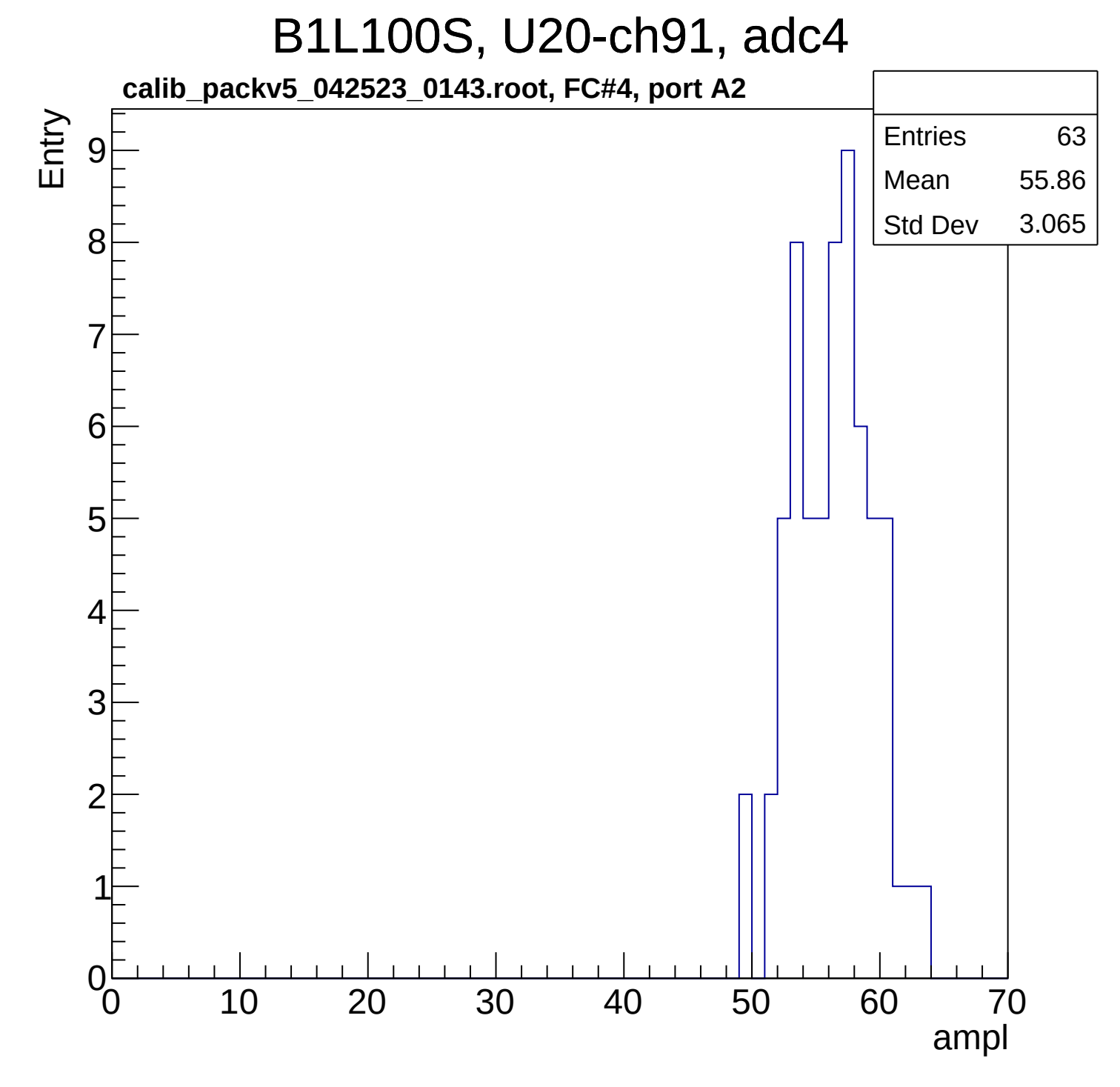
B1L100S, U20-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.86
Std Dev 3.065
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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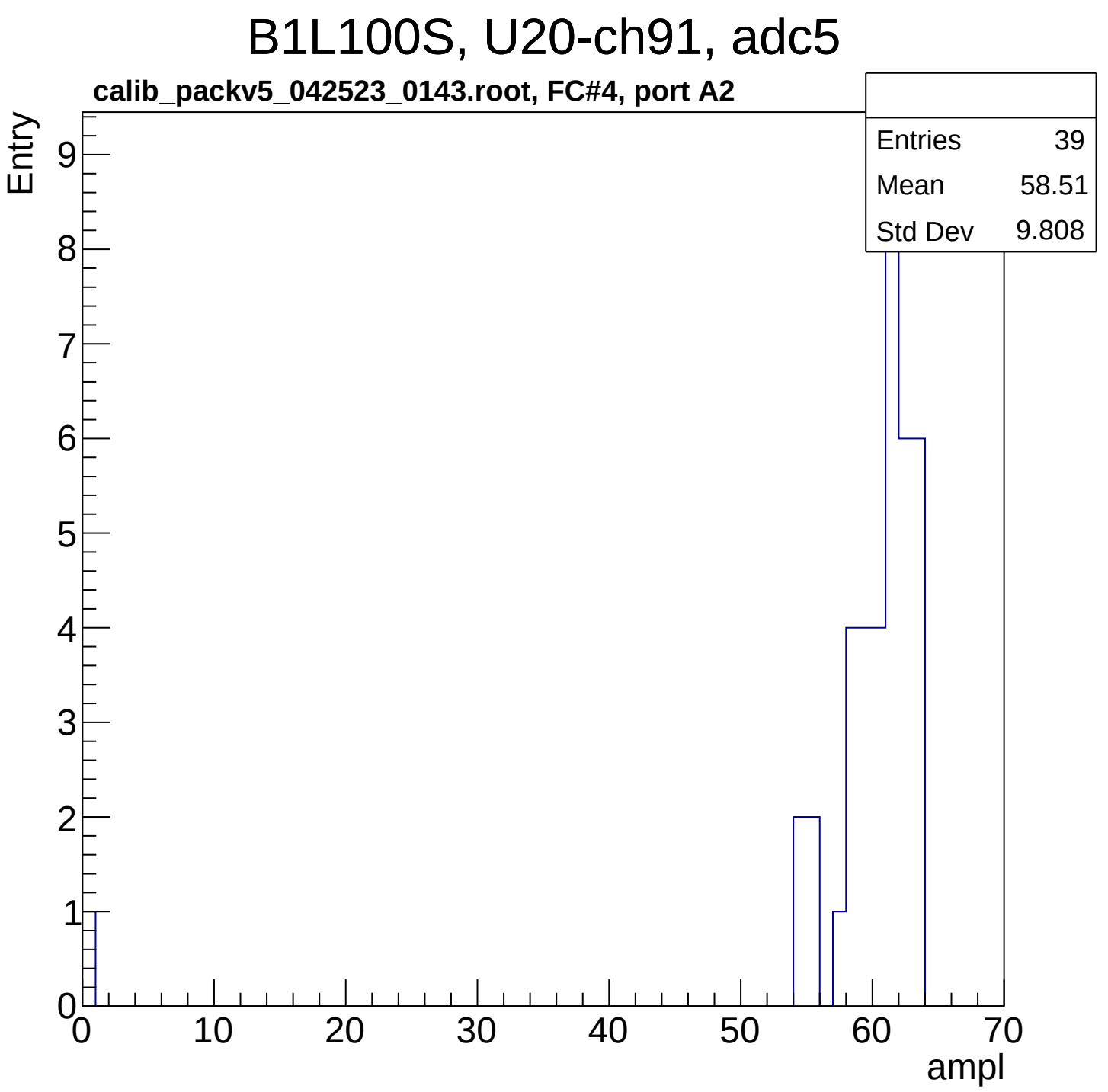
B1L100S, U20-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.51
Std Dev 9.808
Entry
ampl
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2
3
4
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6
7
8
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0
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60
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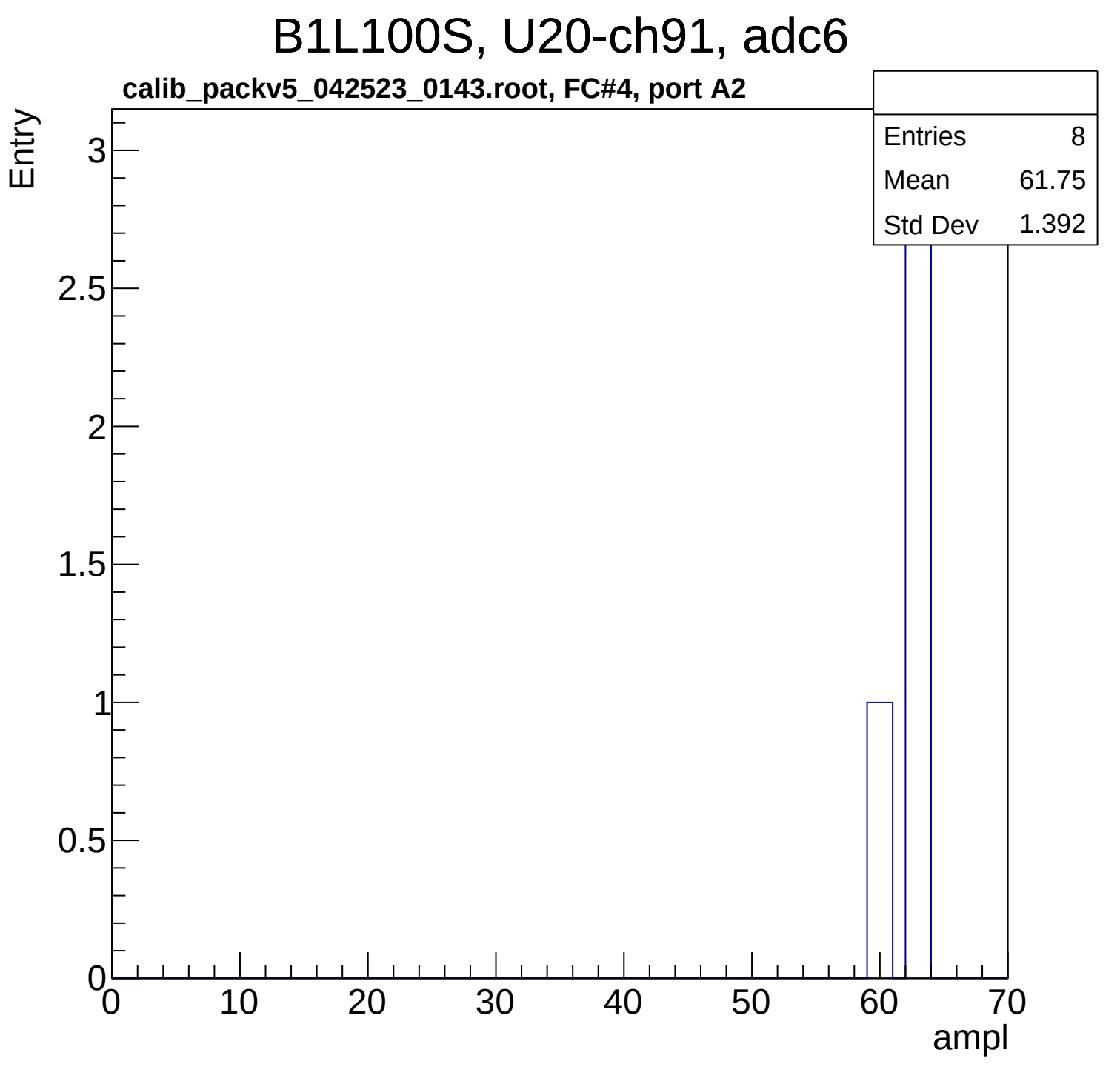
B1L100S, U20-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.75
Std Dev 1.392
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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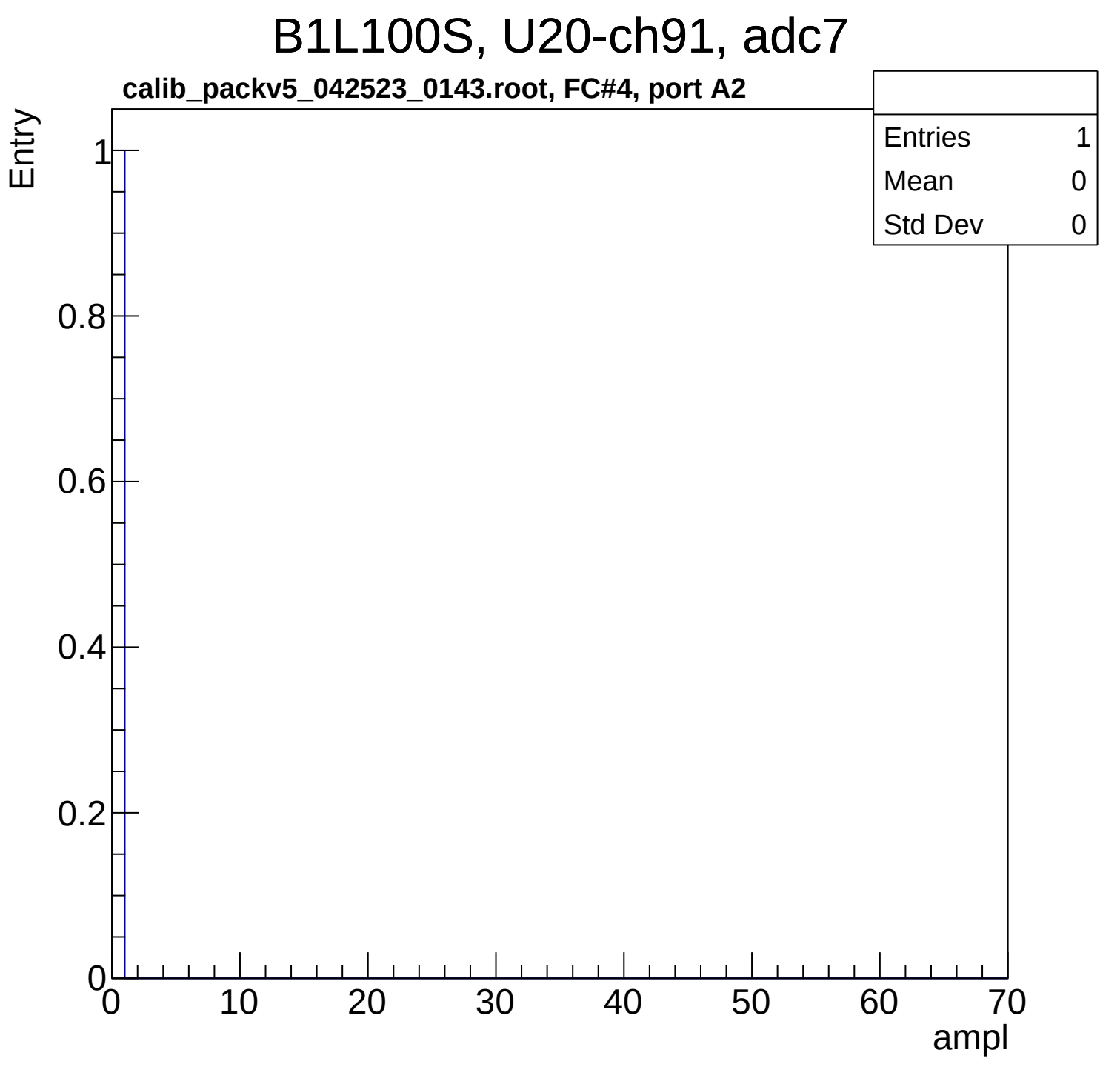
40
50
60
70


B1L100S, U20-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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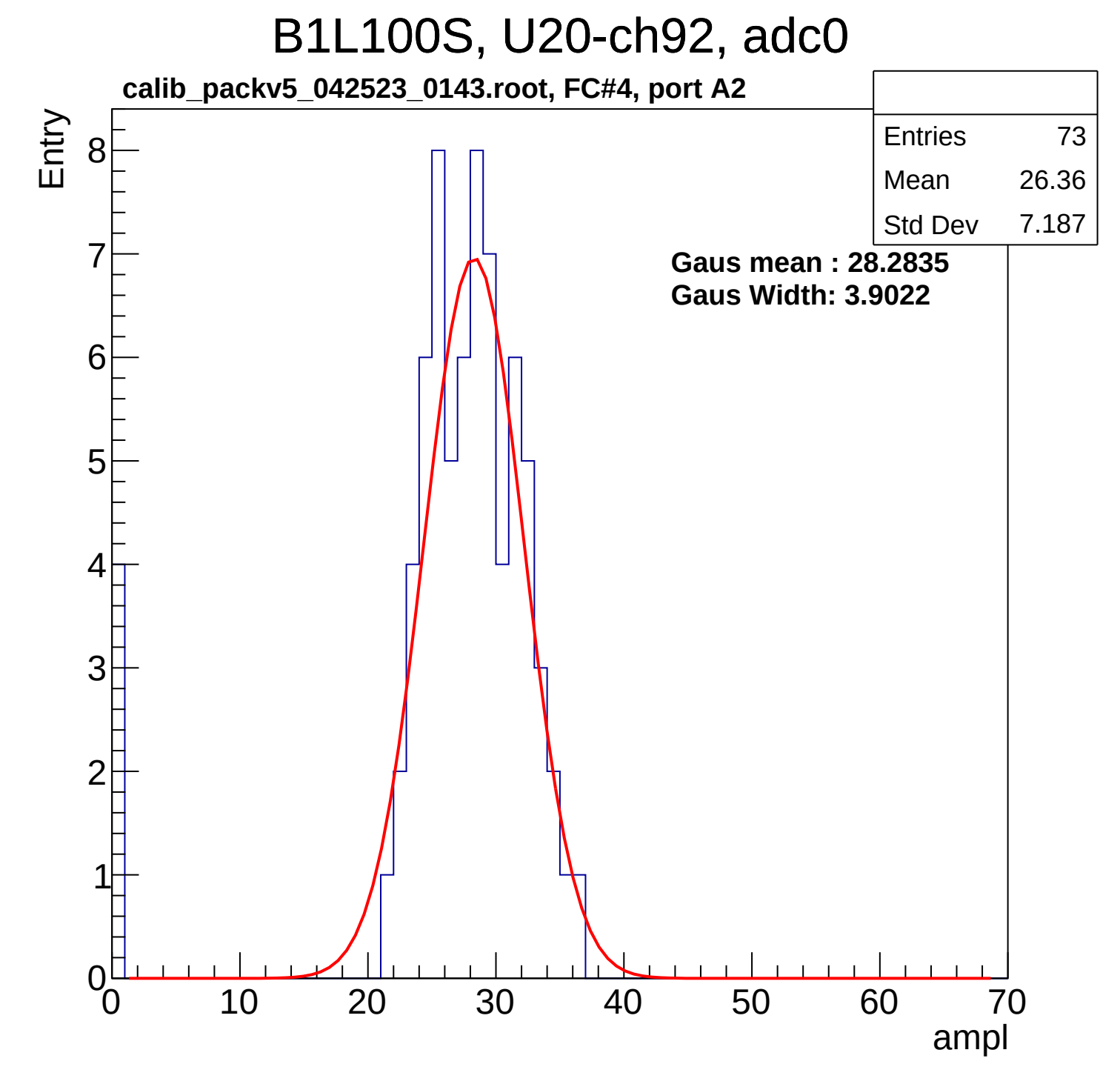
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 26.36
Std Dev 7.187
Gaus mean : 28.2835
Gaus Width: 3.9022
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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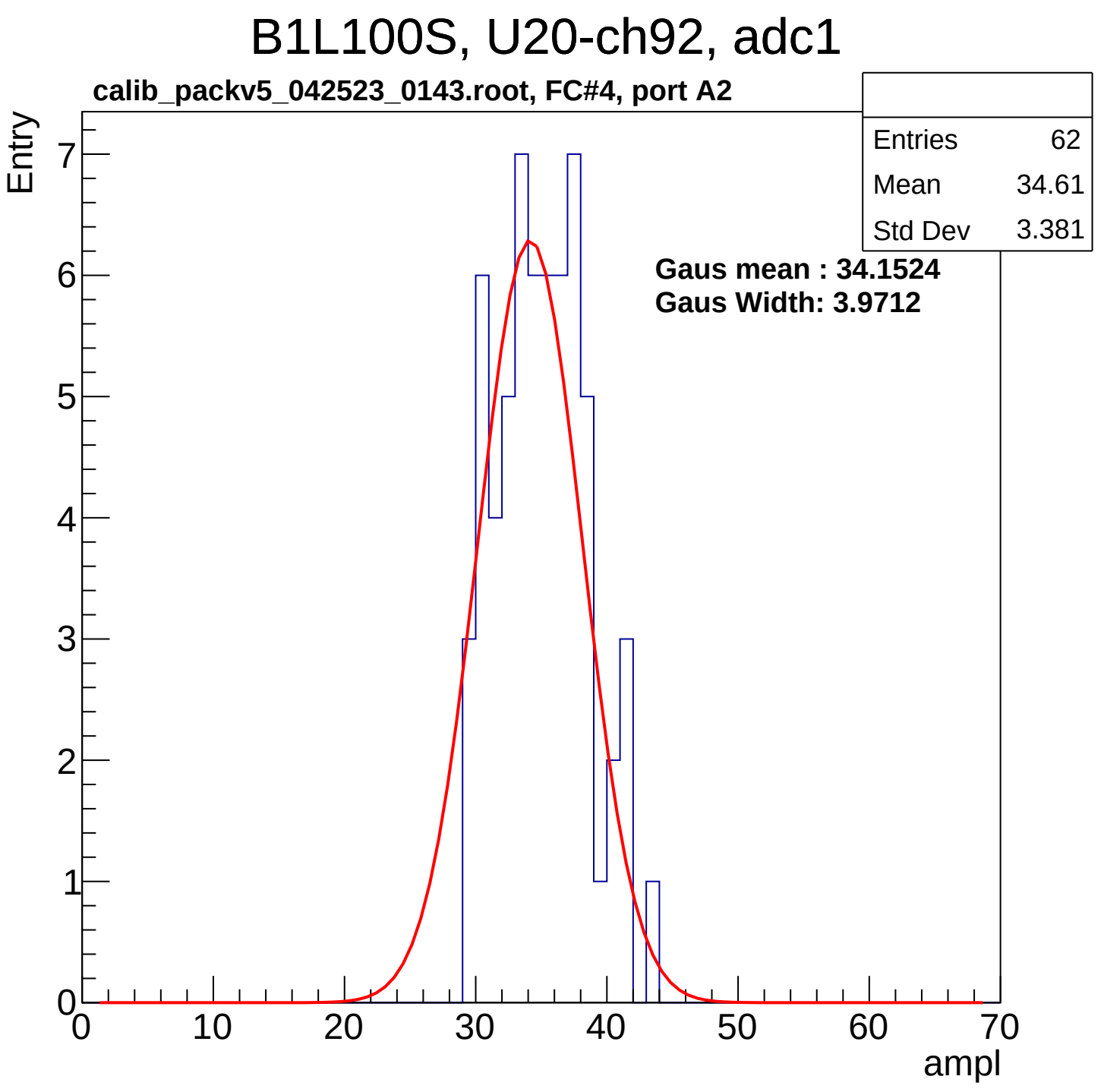
B1L100S, U20-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 34.61
Std Dev 3.381
Gaus mean : 34.1524
Gaus Width: 3.9712
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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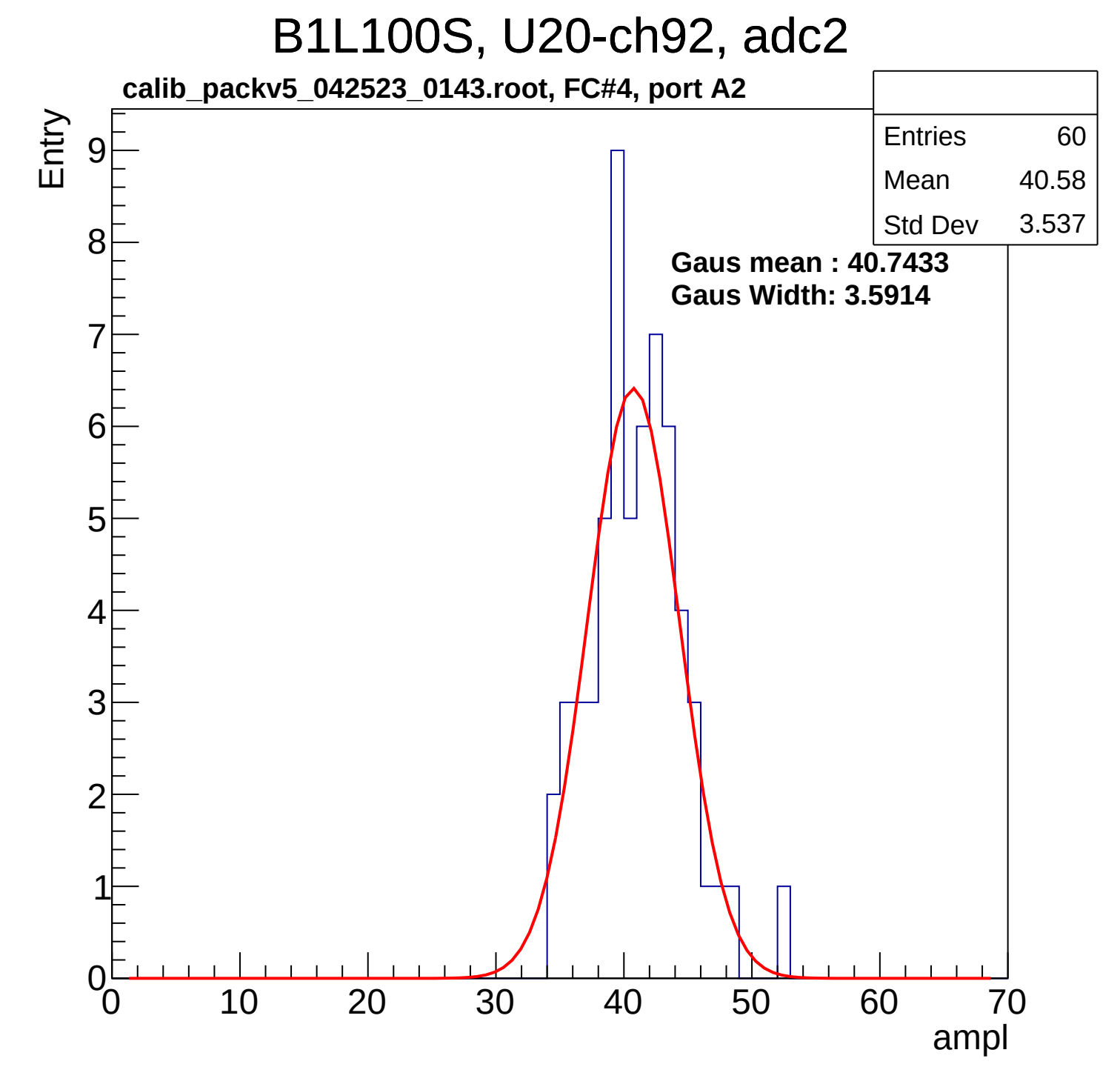
B1L100S, U20-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 40.58
Std Dev 3.537
Gaus mean : 40.7433
Gaus Width: 3.5914
Entry
ampl
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2
3
4
5
6
7
8
9
0
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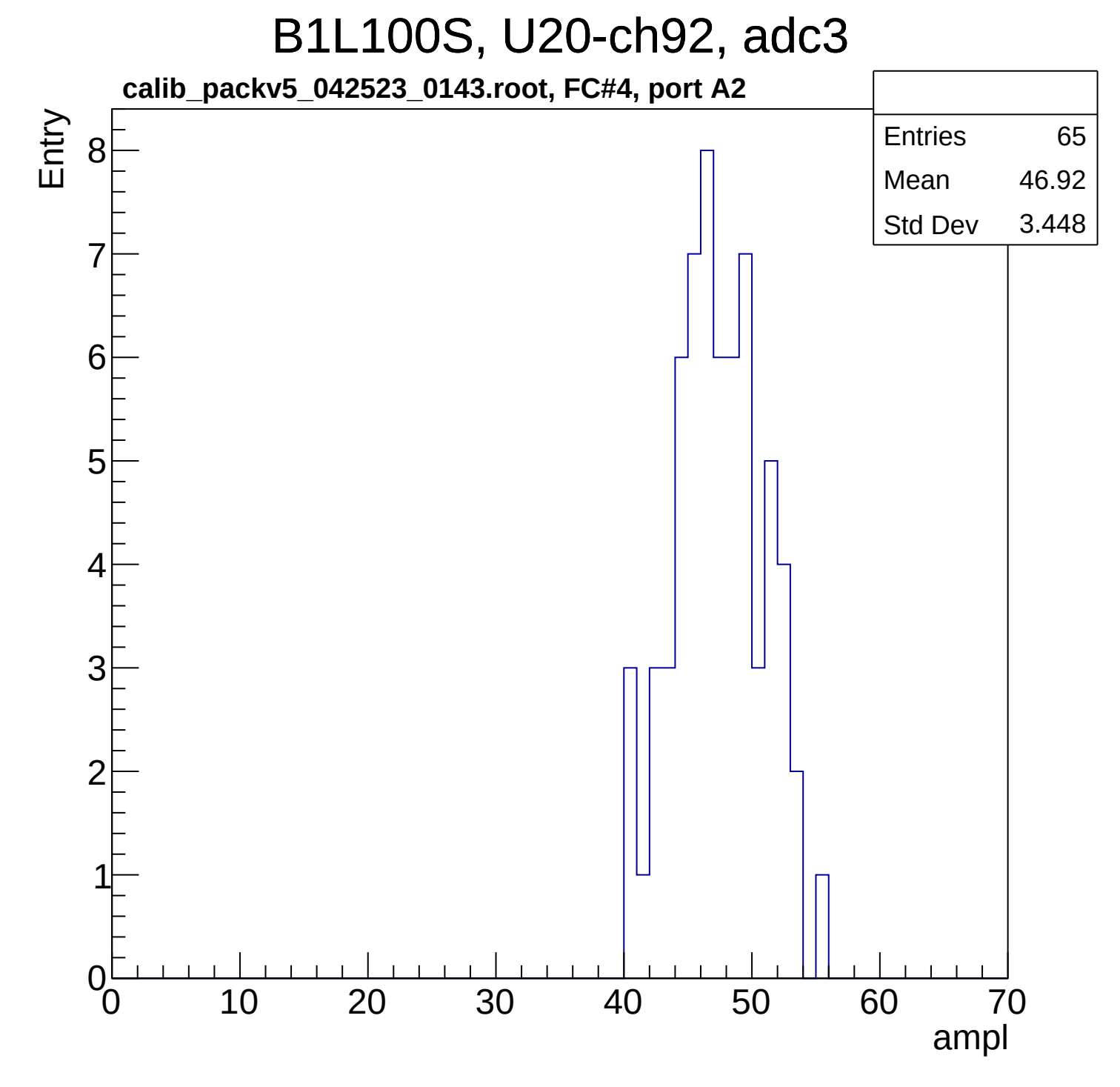
B1L100S, U20-ch92, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 46.92
Std Dev 3.448
Entry
ampl
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5
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8
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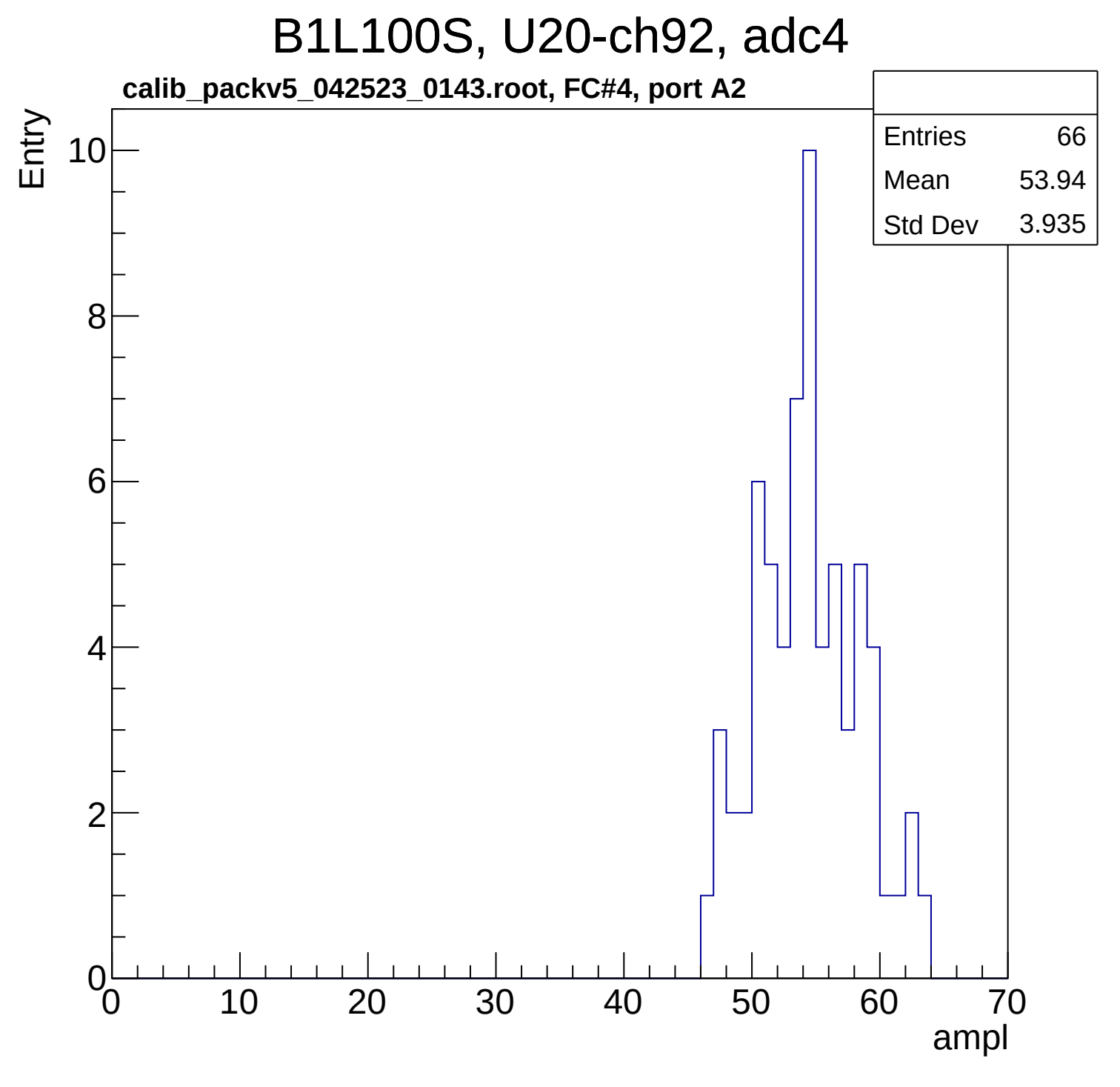
40
50
60
70


B1L100S, U20-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 53.94
Std Dev 3.935
Entry
ampl
0
2
4
6
8
10
0
10
20
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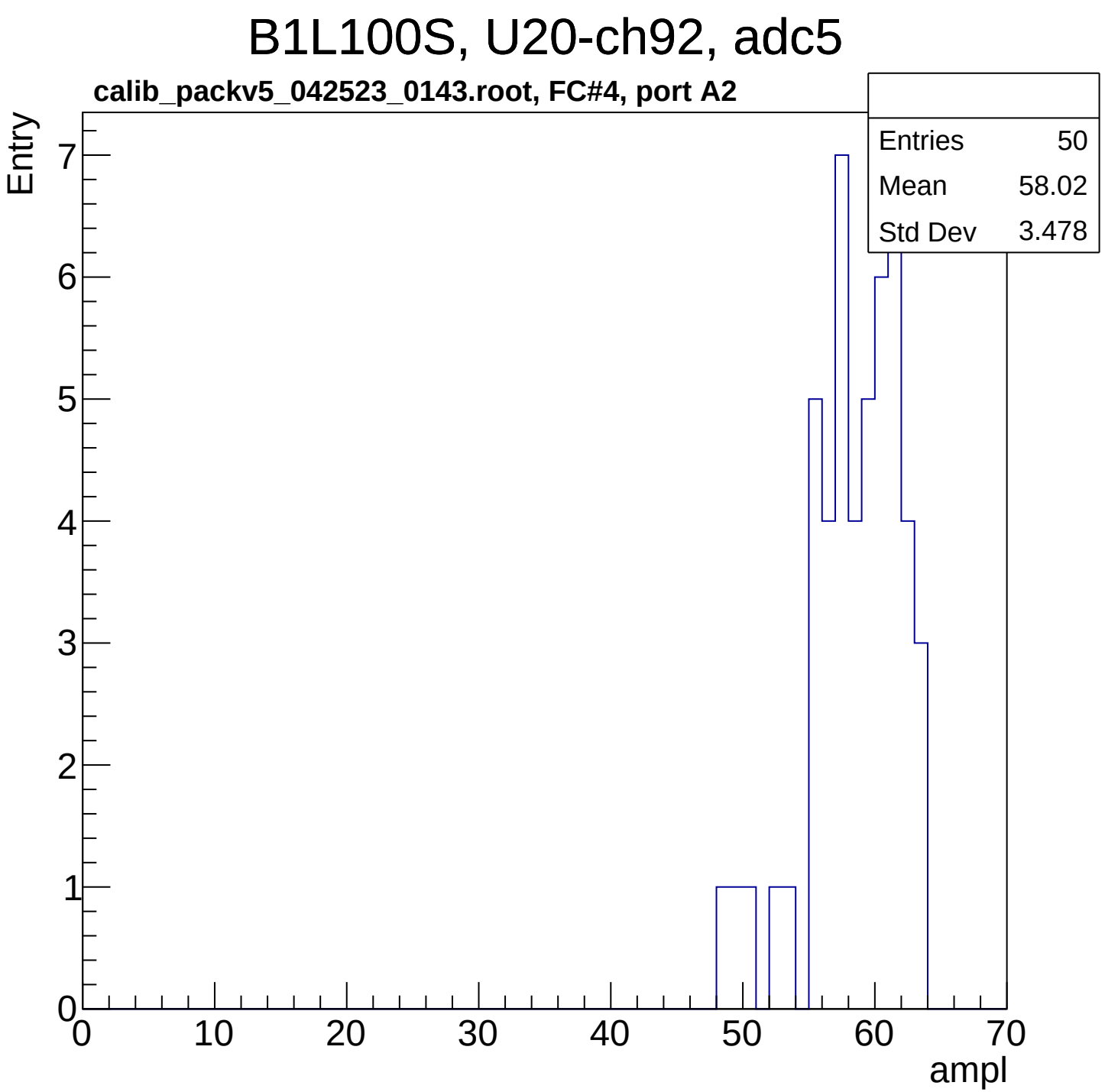
40
50
60
70


B1L100S, U20-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.02
Std Dev 3.478
Entry
ampl
0
1
2
3
4
5
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0
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60
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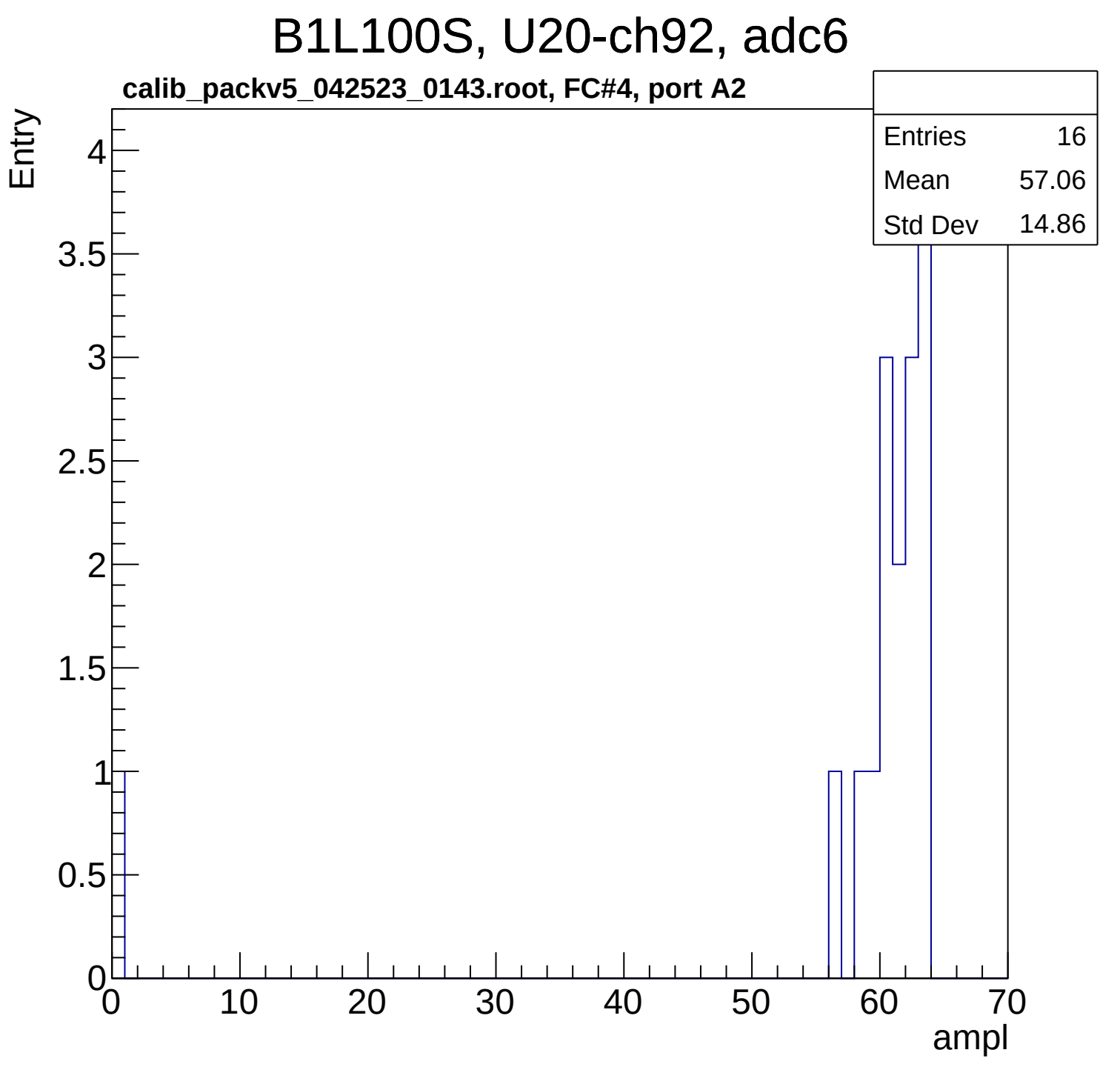
B1L100S, U20-ch92, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 16
Mean 57.06
Std Dev 14.86
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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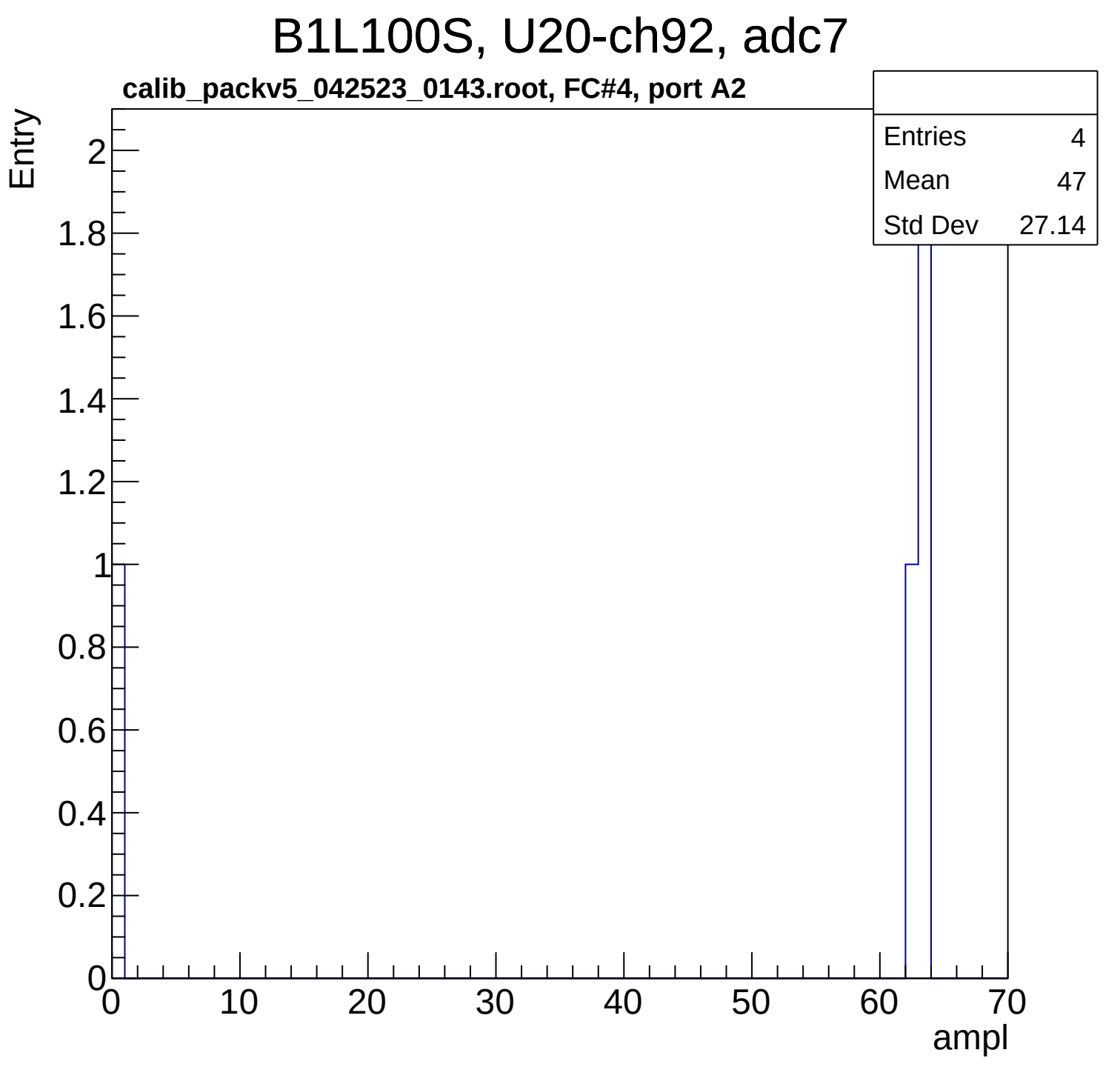
B1L100S, U20-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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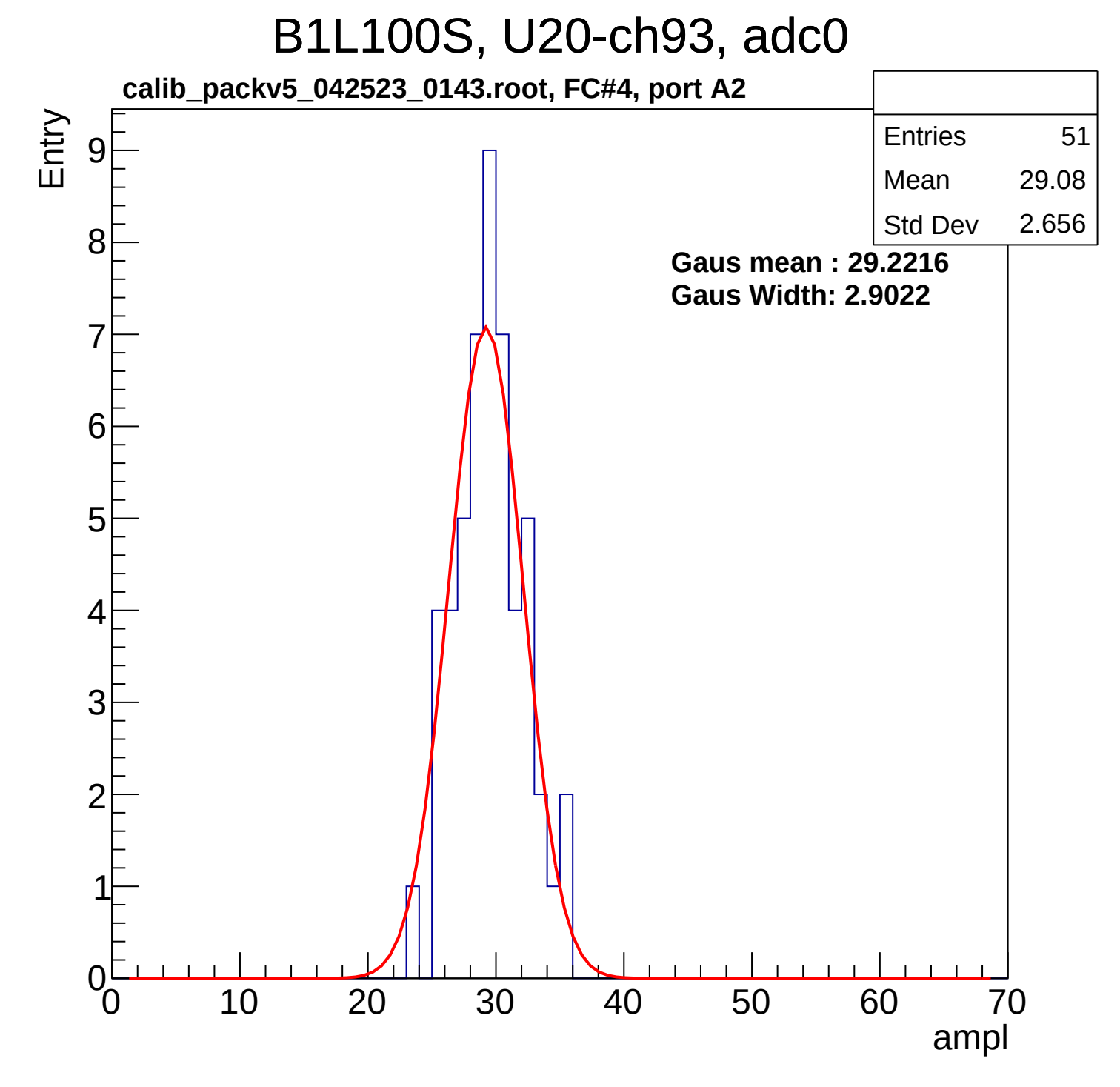
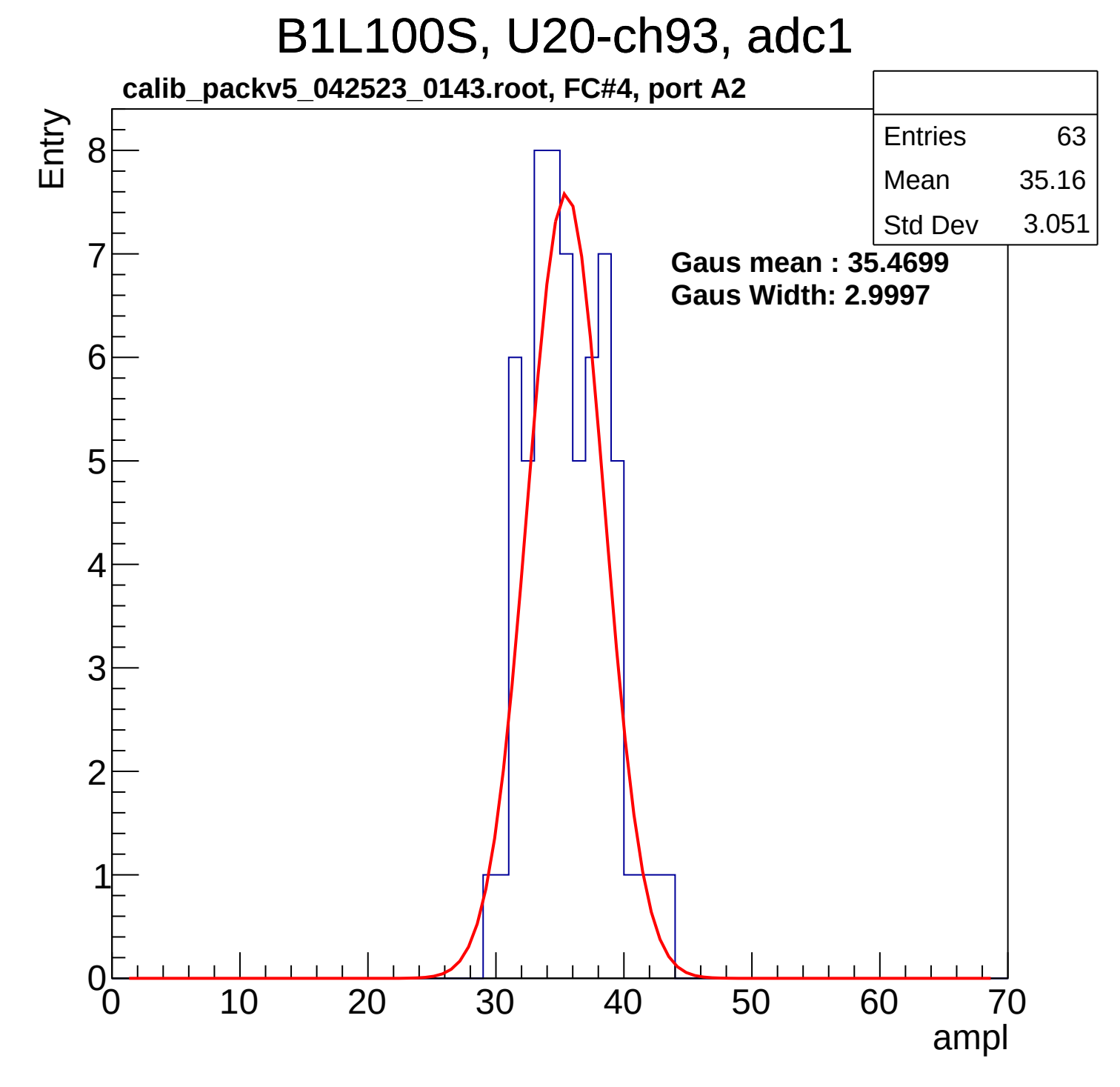

B1L100S, U20-ch93, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 29.08
Std Dev 2.656
Gaus mean : 29.2216
Gaus Width: 2.9022
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.16
Std Dev 3.051
Gaus mean : 35.4699
Gaus Width: 2.9997
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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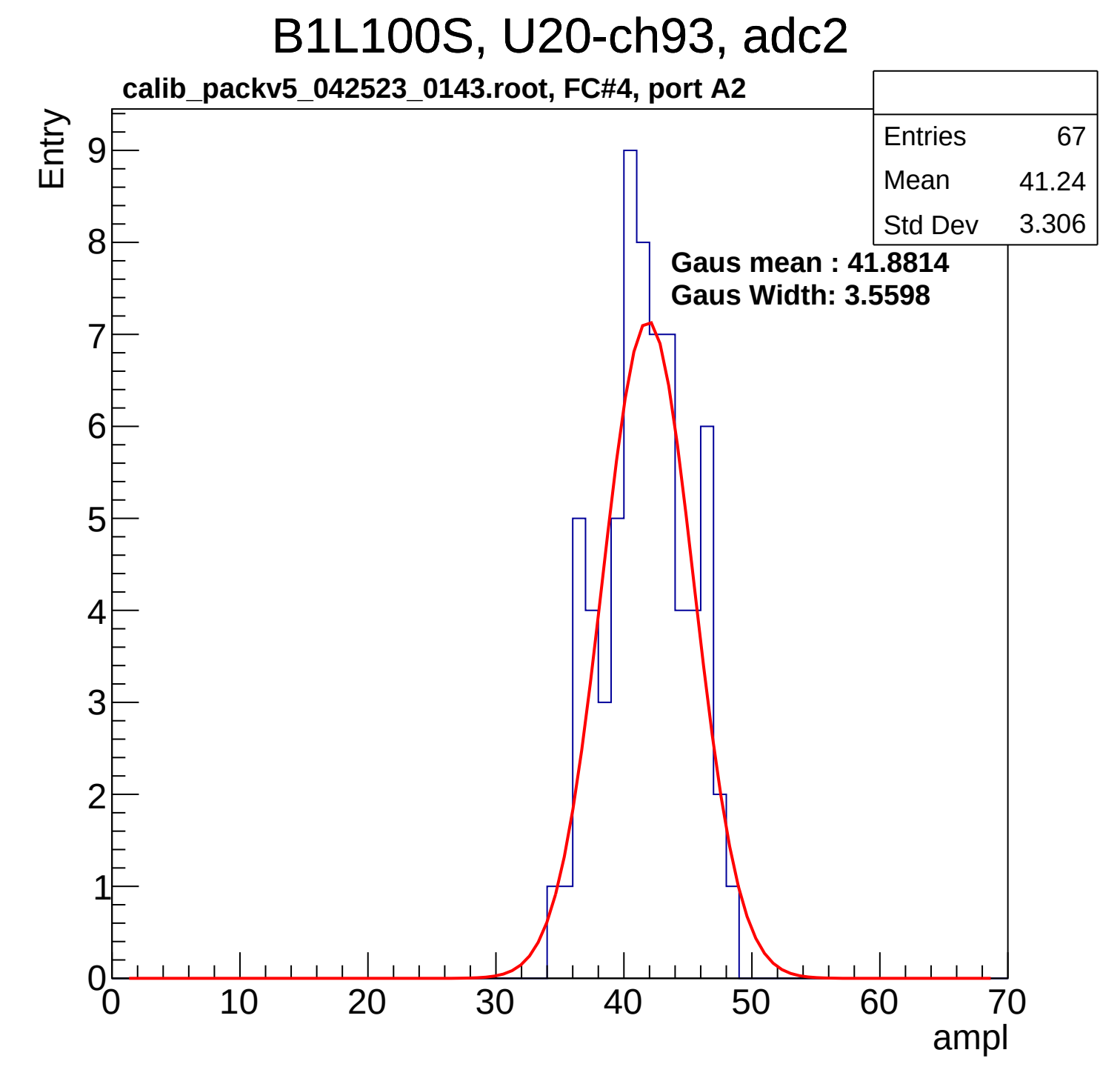
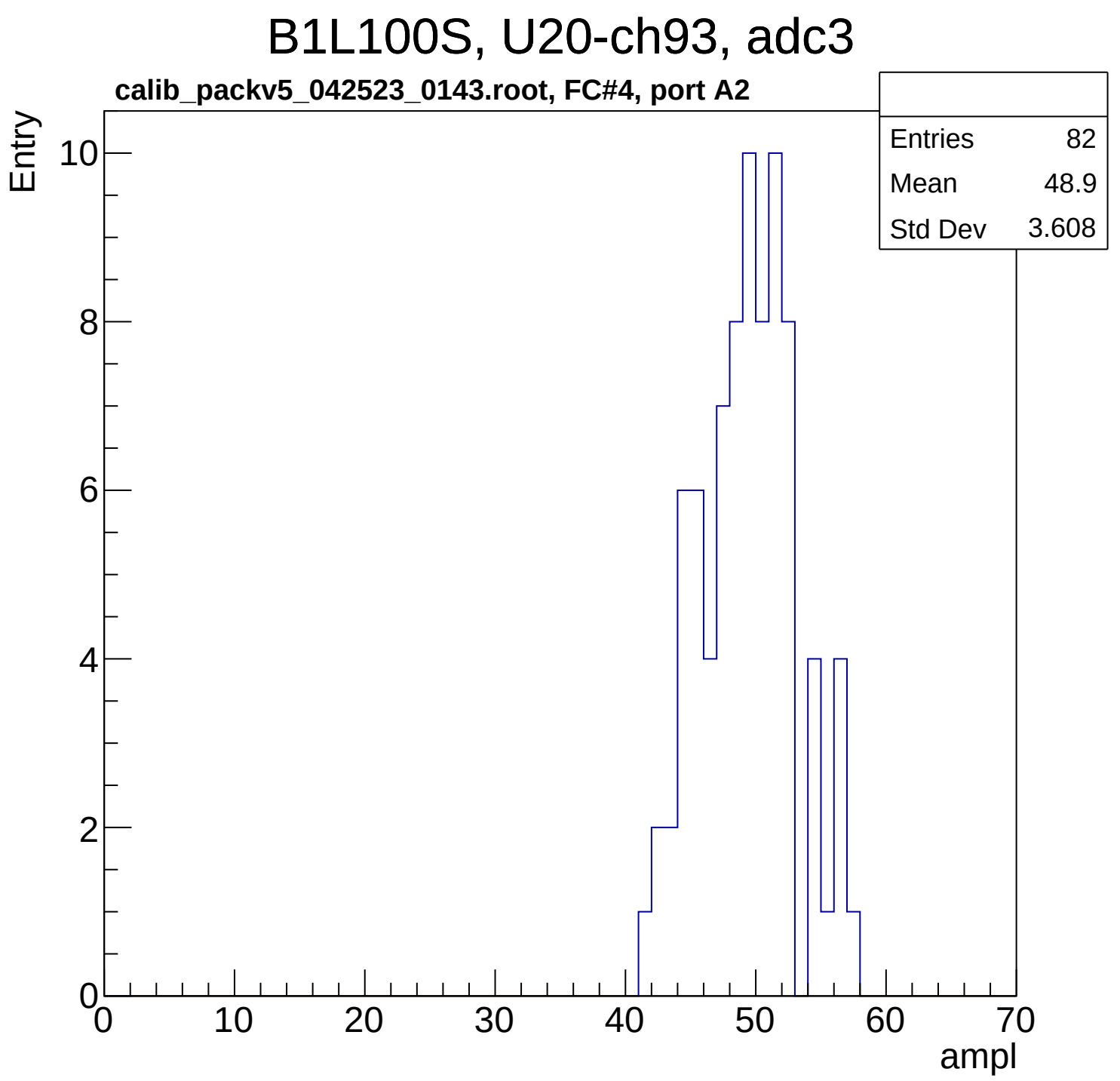
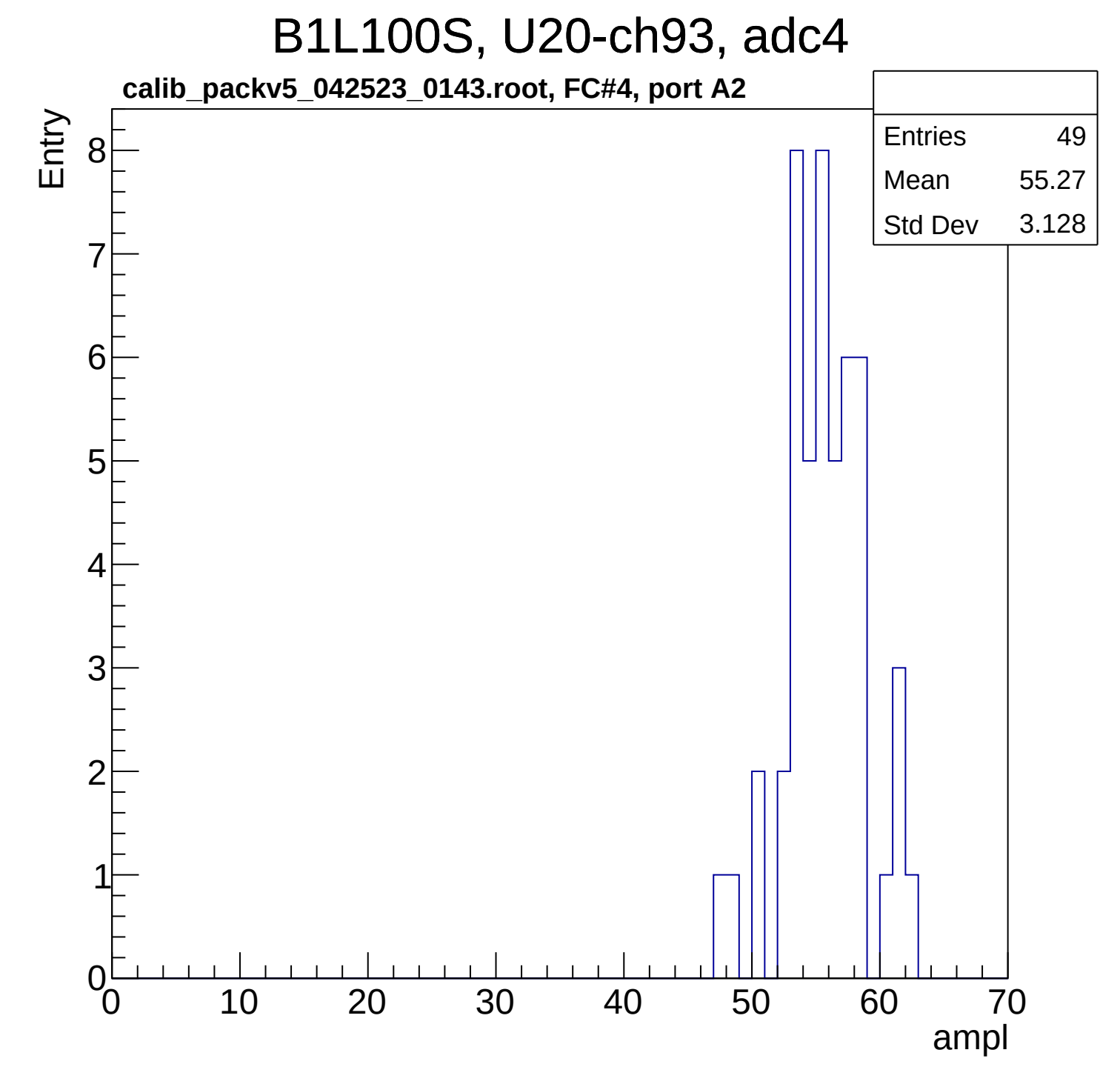
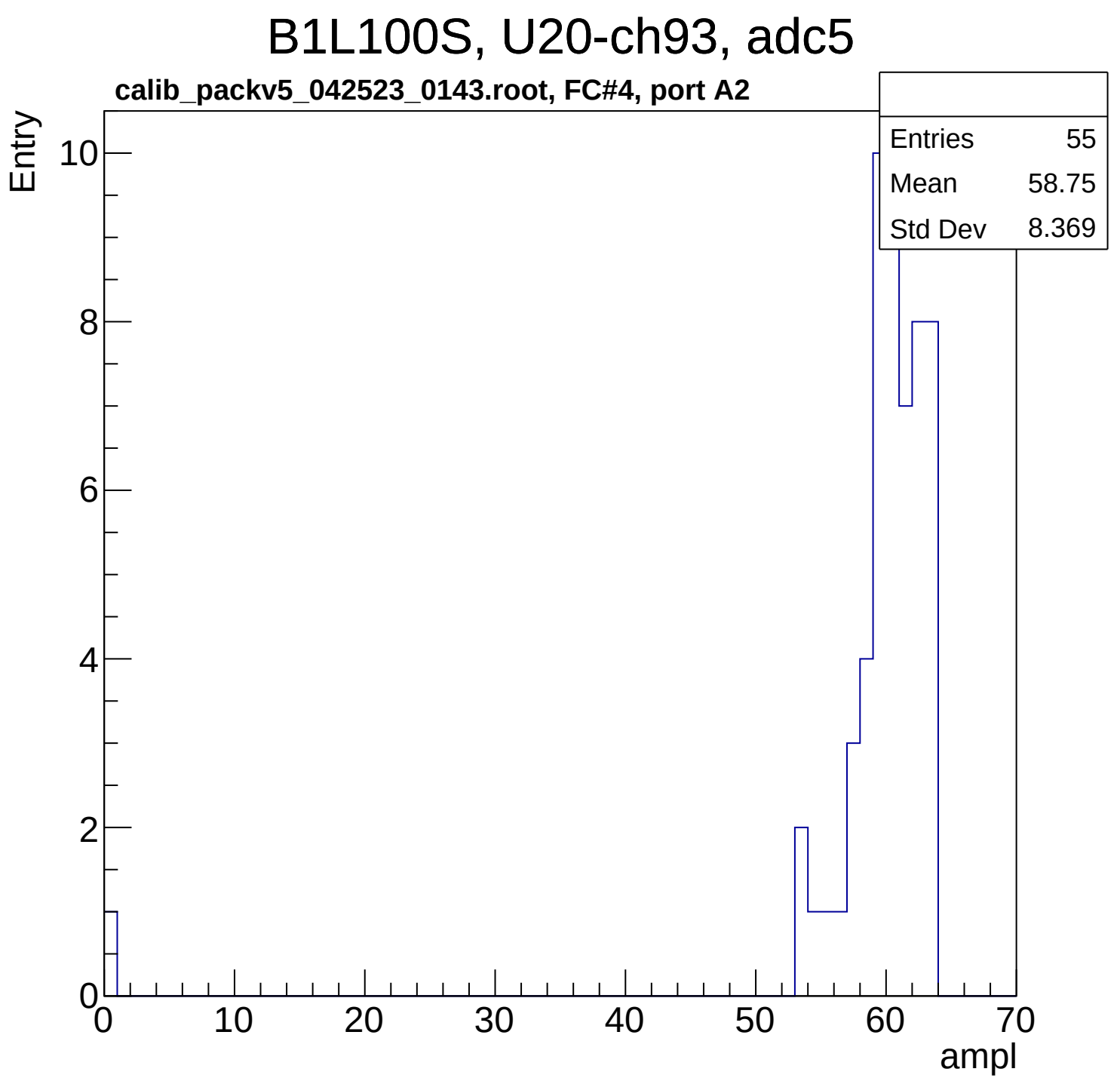
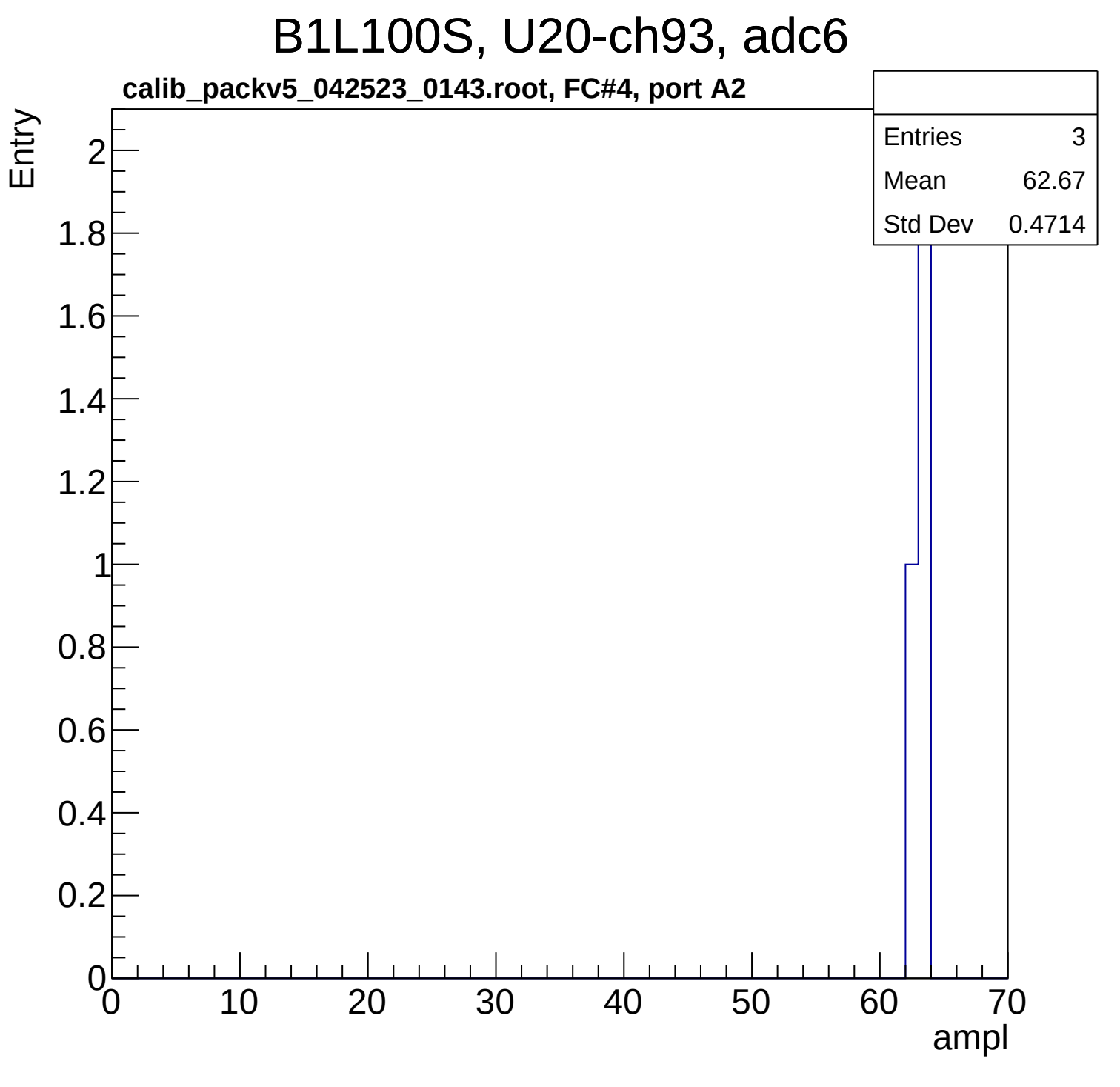
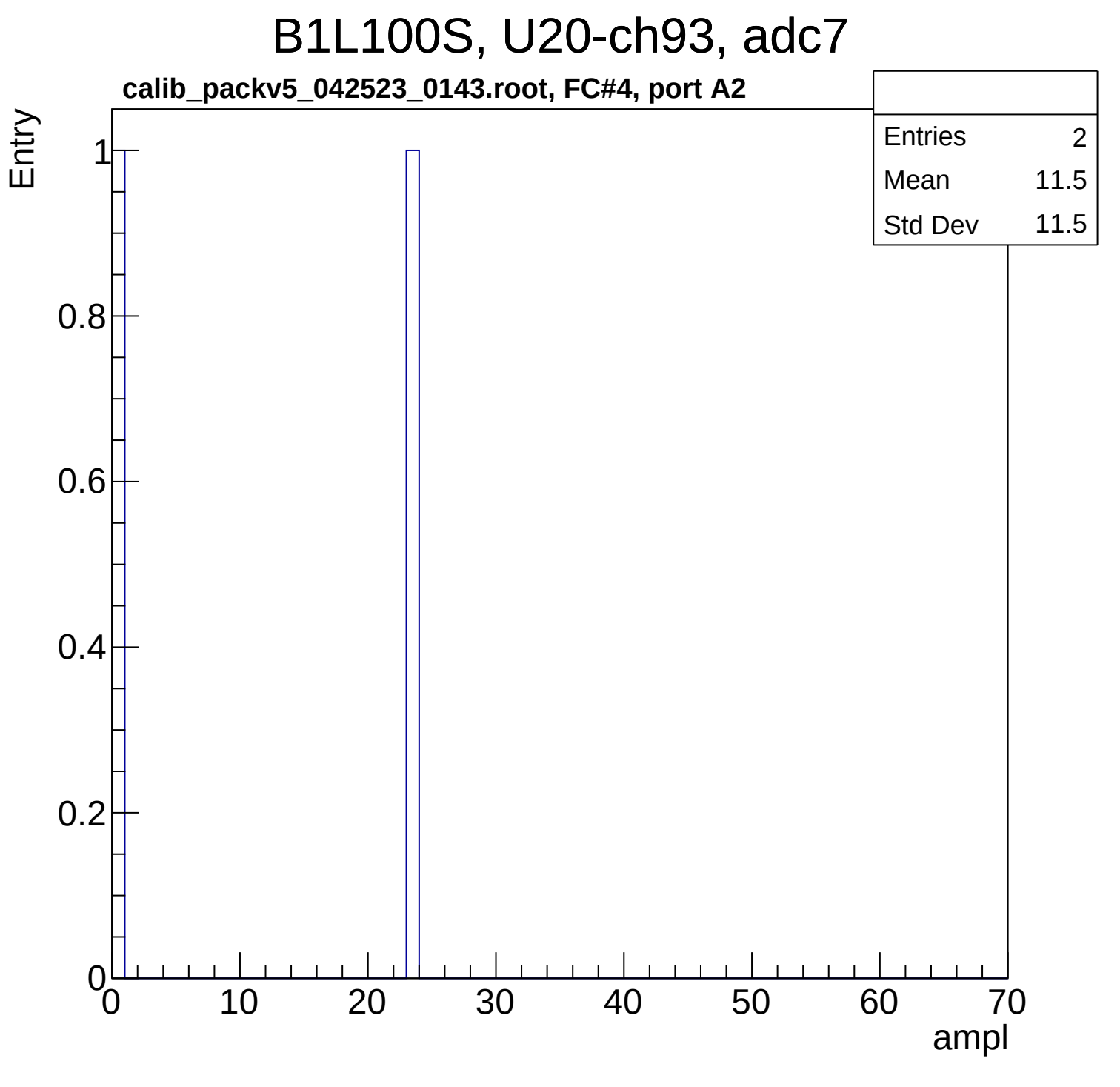

B1L100S, U20-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 41.24
Std Dev 3.306
Gaus mean : 41.8814
Gaus Width: 3.5598
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

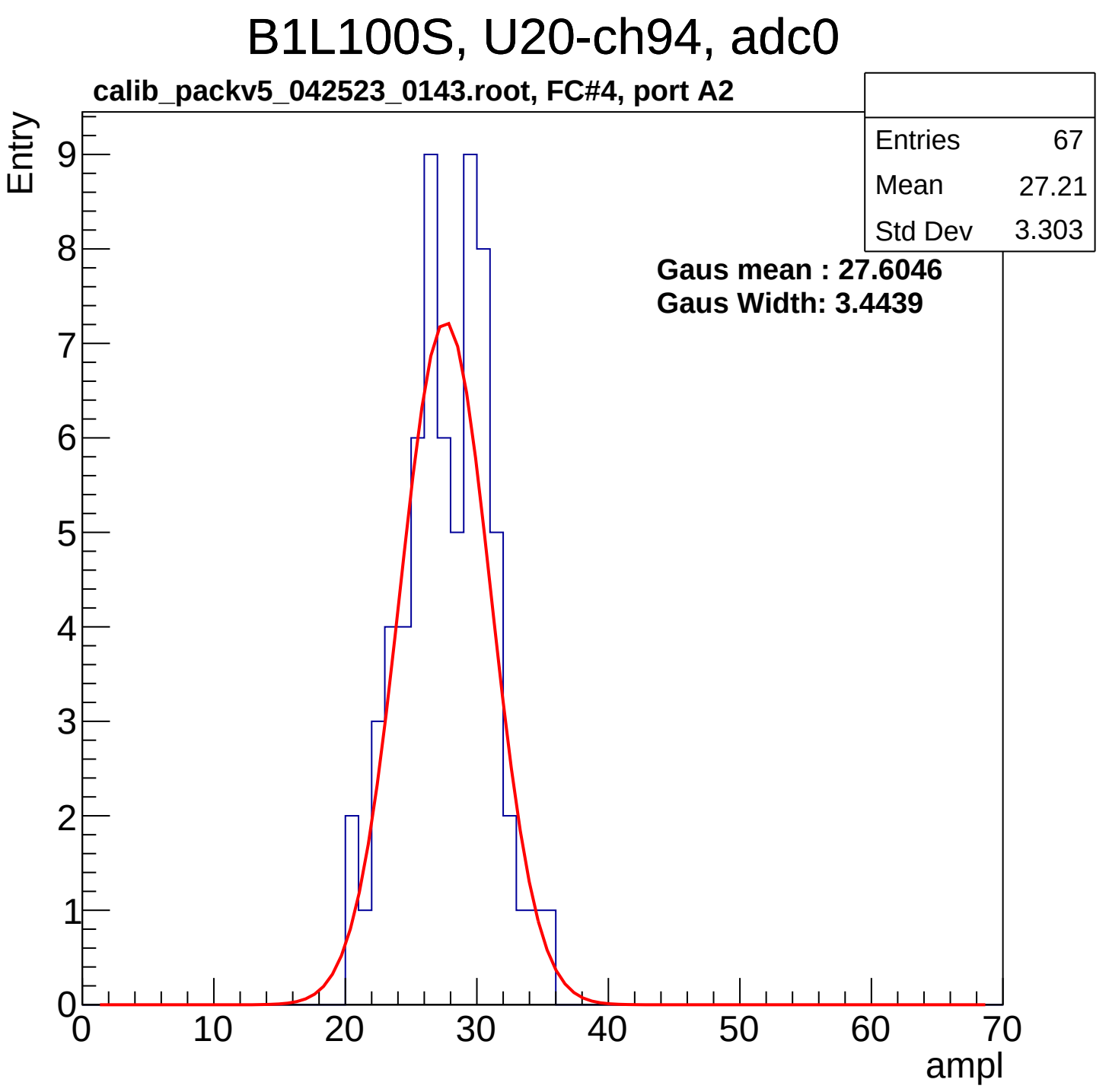

B1L100S, U20-ch93, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 48.9
Std Dev 3.608
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

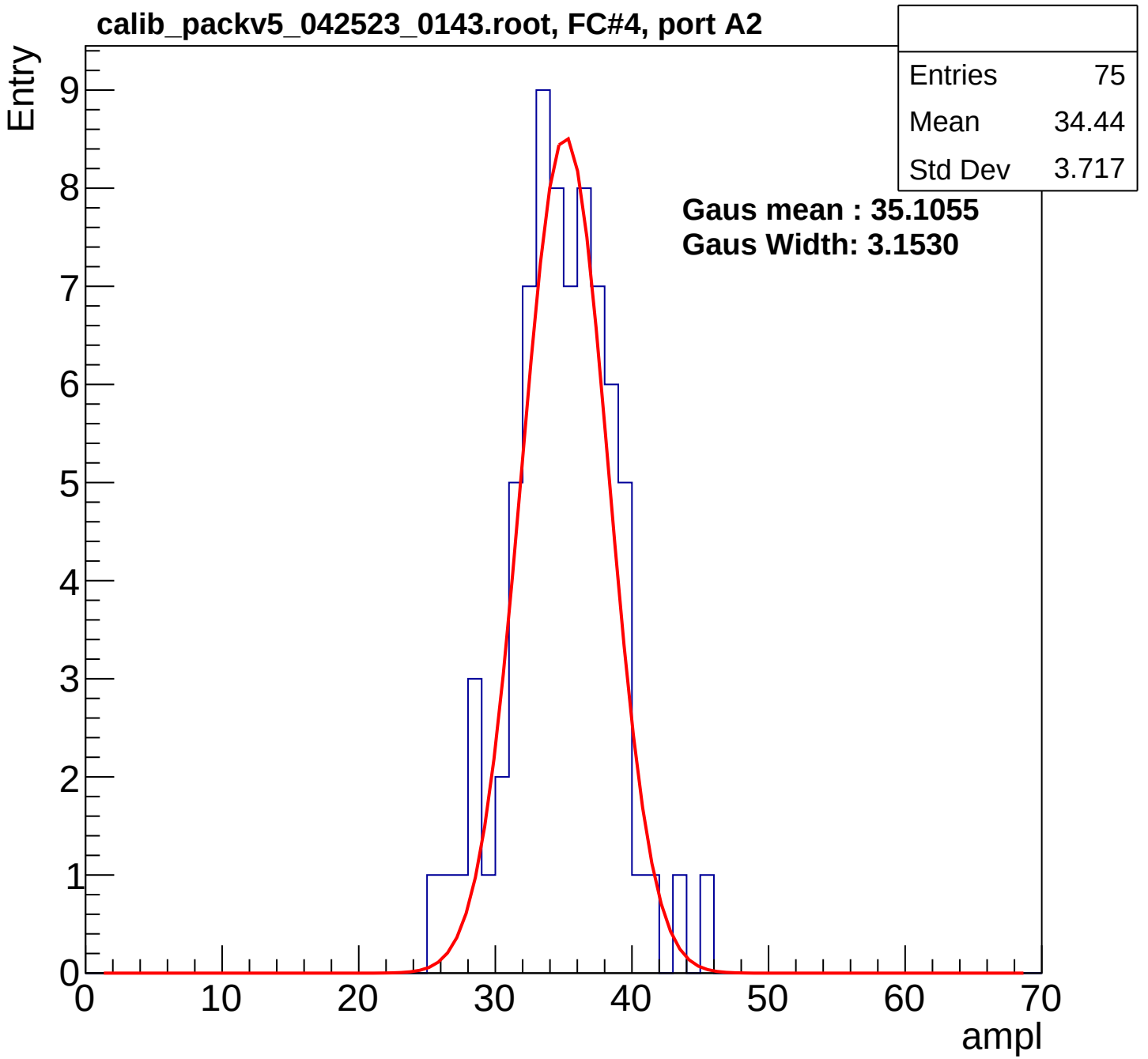

B1L100S, U20-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.27
Std Dev 3.128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

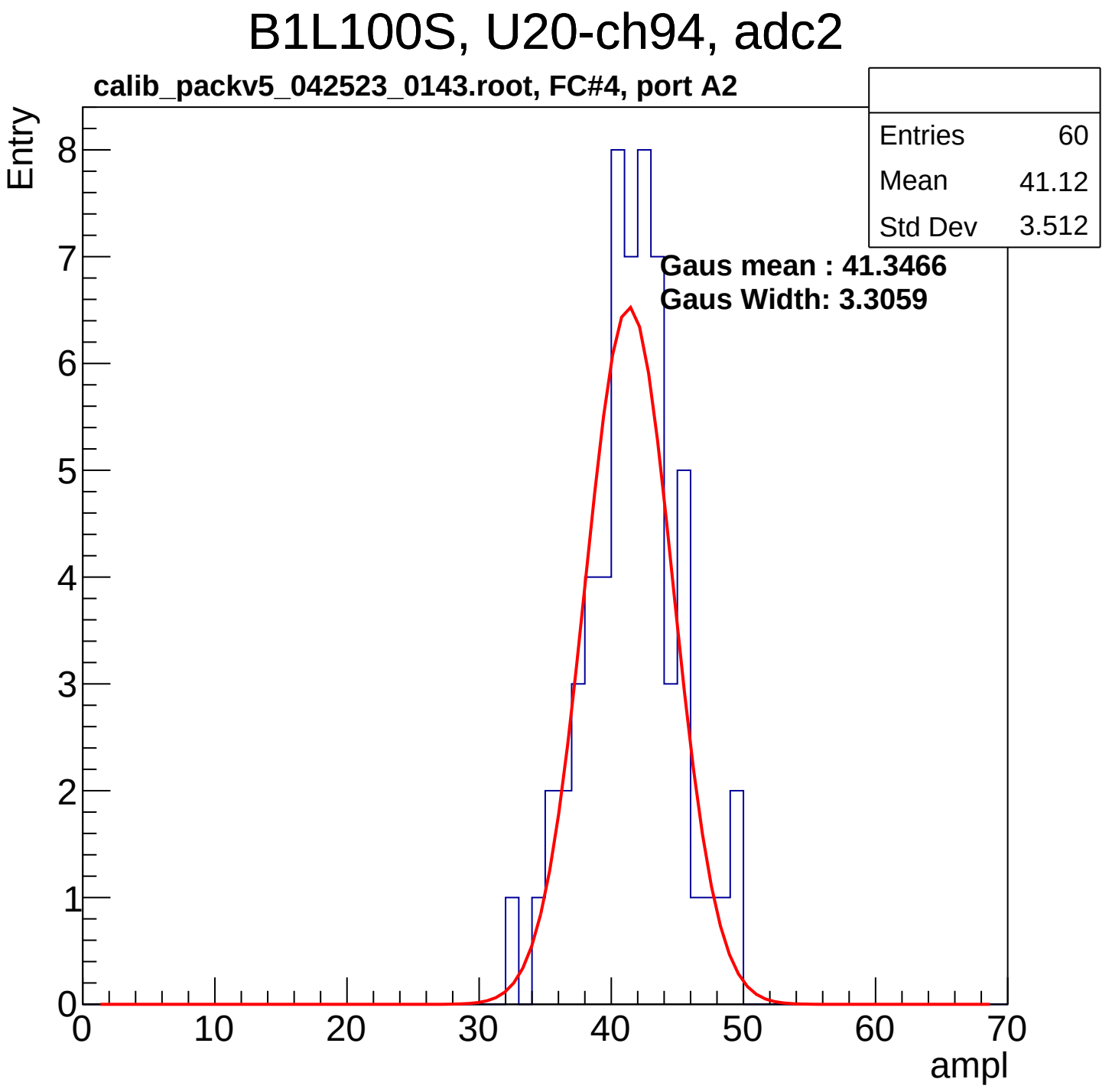

B1L100S, U20-ch93, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 58.75
Std Dev 8.369
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

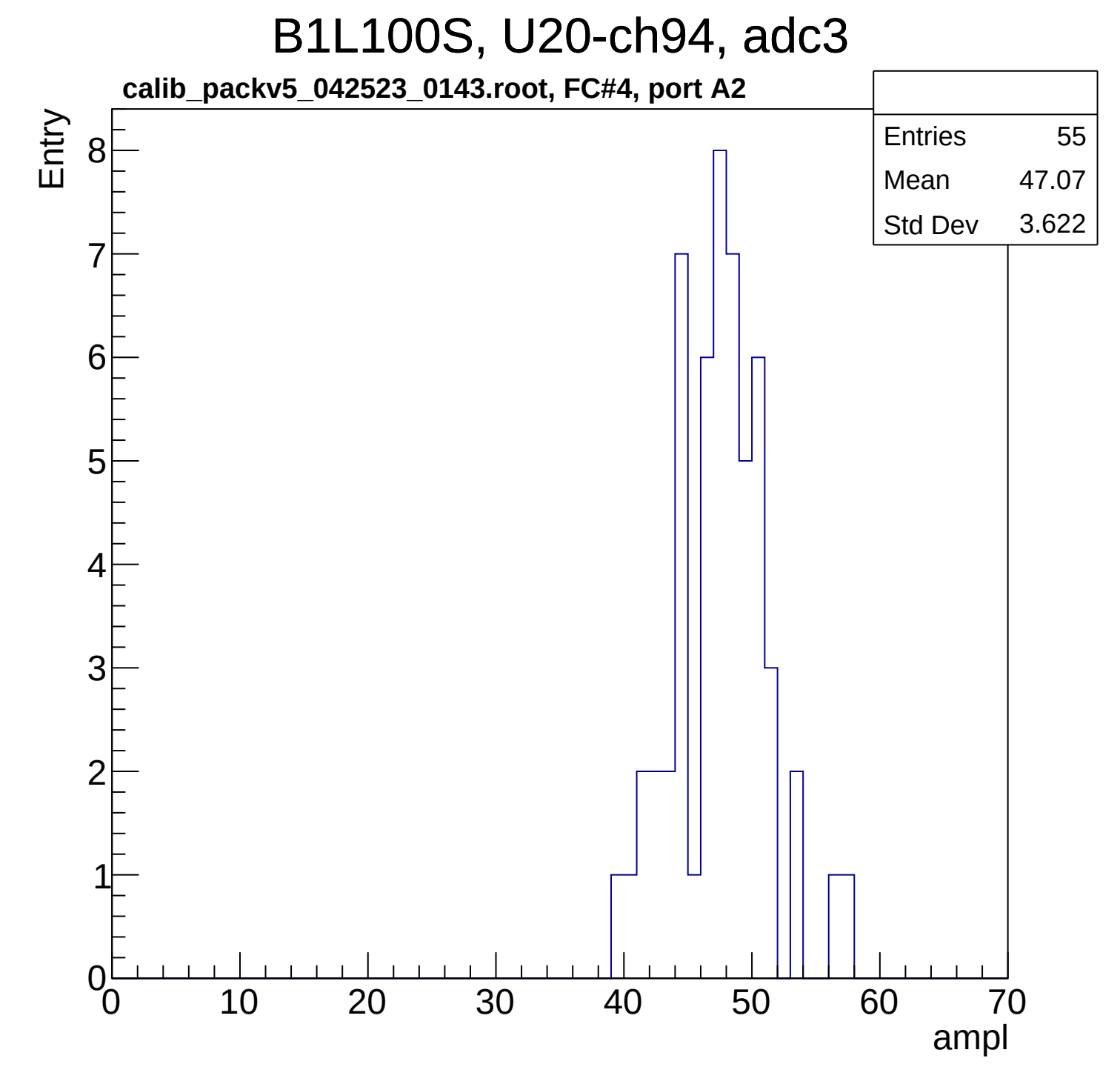

B1L100S, U20-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

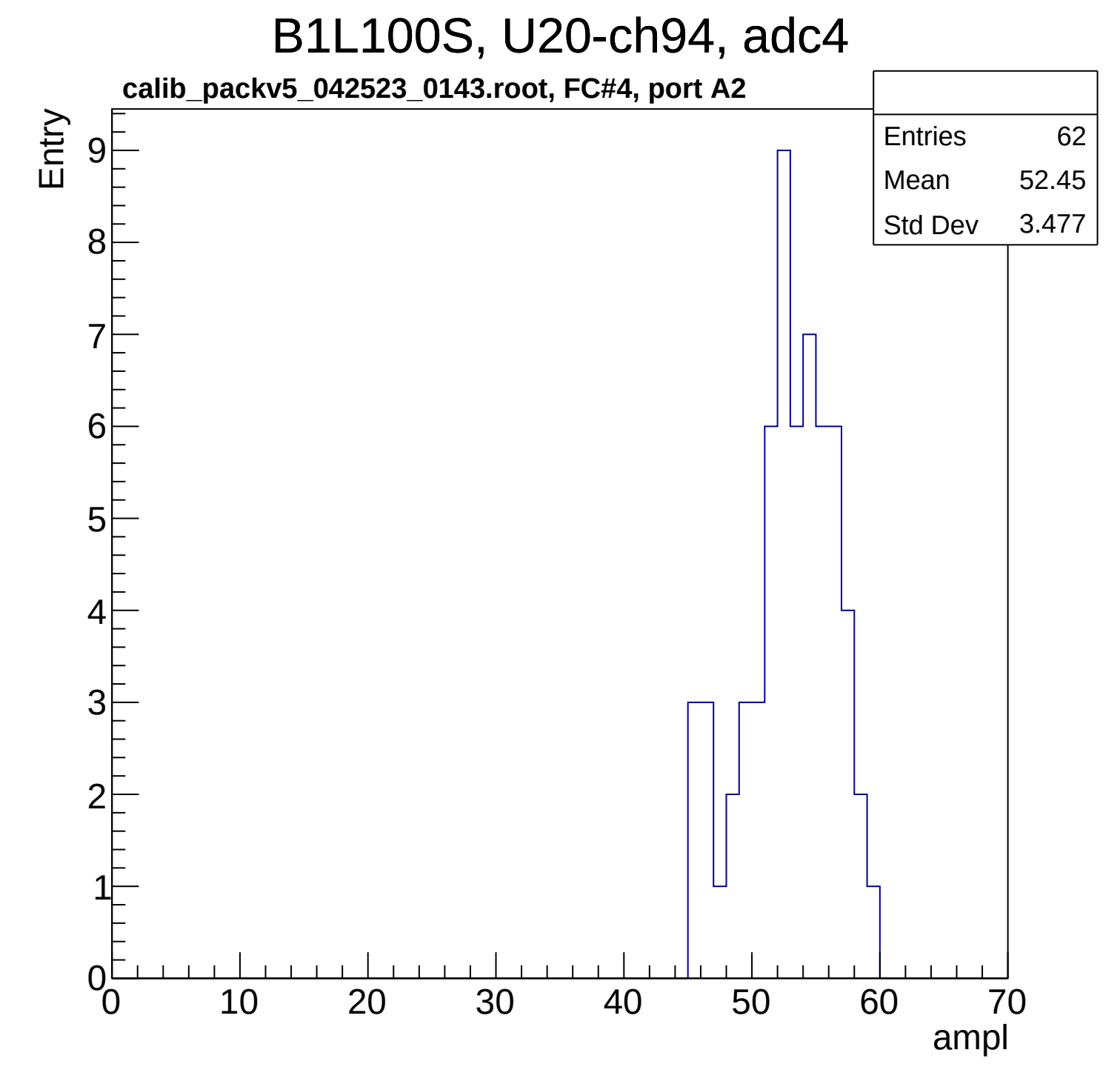

B1L100S, U20-ch93, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

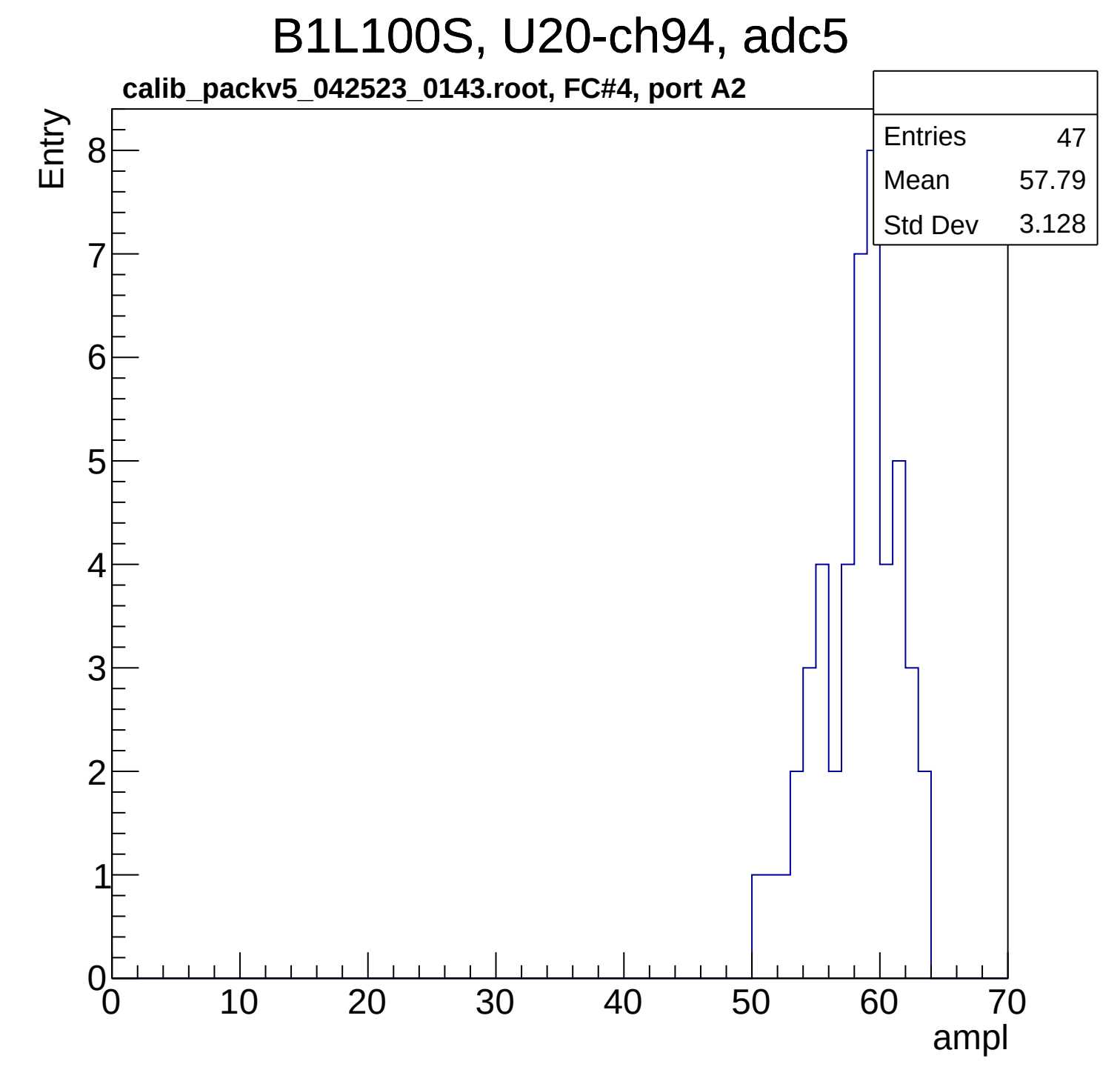

B1L100S, U20-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.21
Std Dev 3.303
Gaus mean : 27.6046
Gaus Width: 3.4439
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

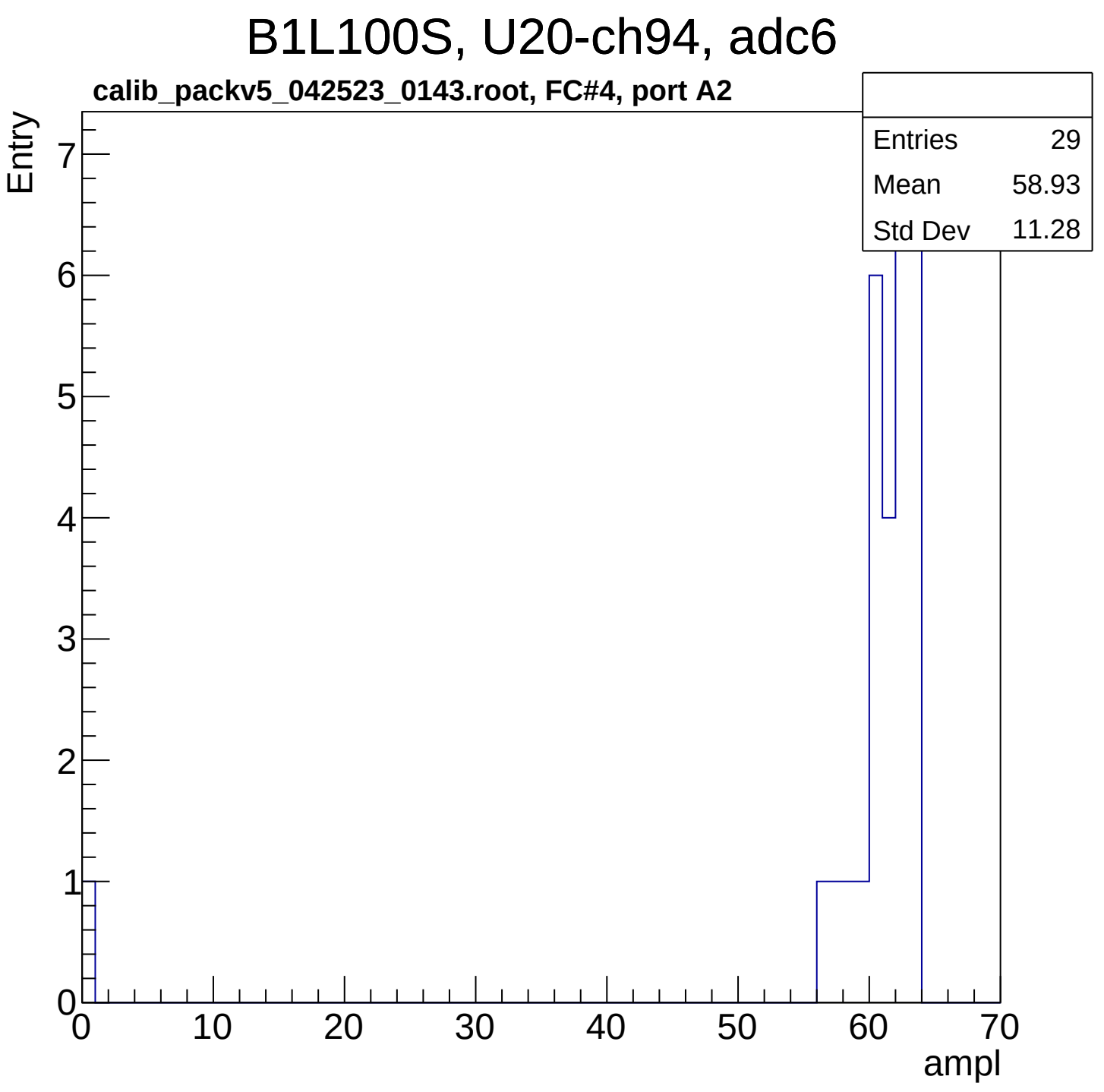

B1L100S, U20-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 34.44
Std Dev 3.717
Gaus mean : 35.1055
Gaus Width: 3.1530
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

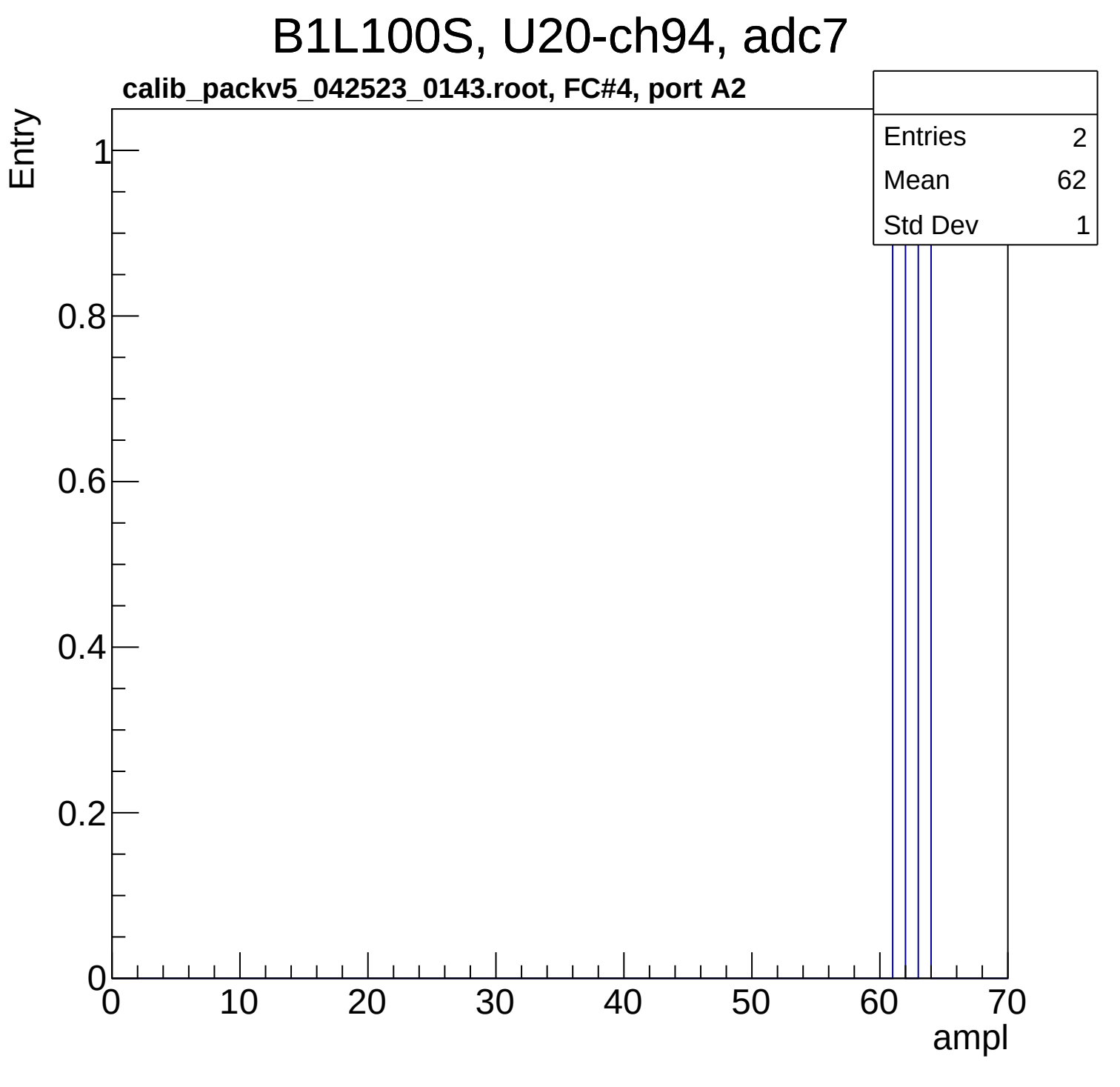

B1L100S, U20-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.12
Std Dev 3.512
Gaus mean : 41.3466
Gaus Width: 3.3059
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

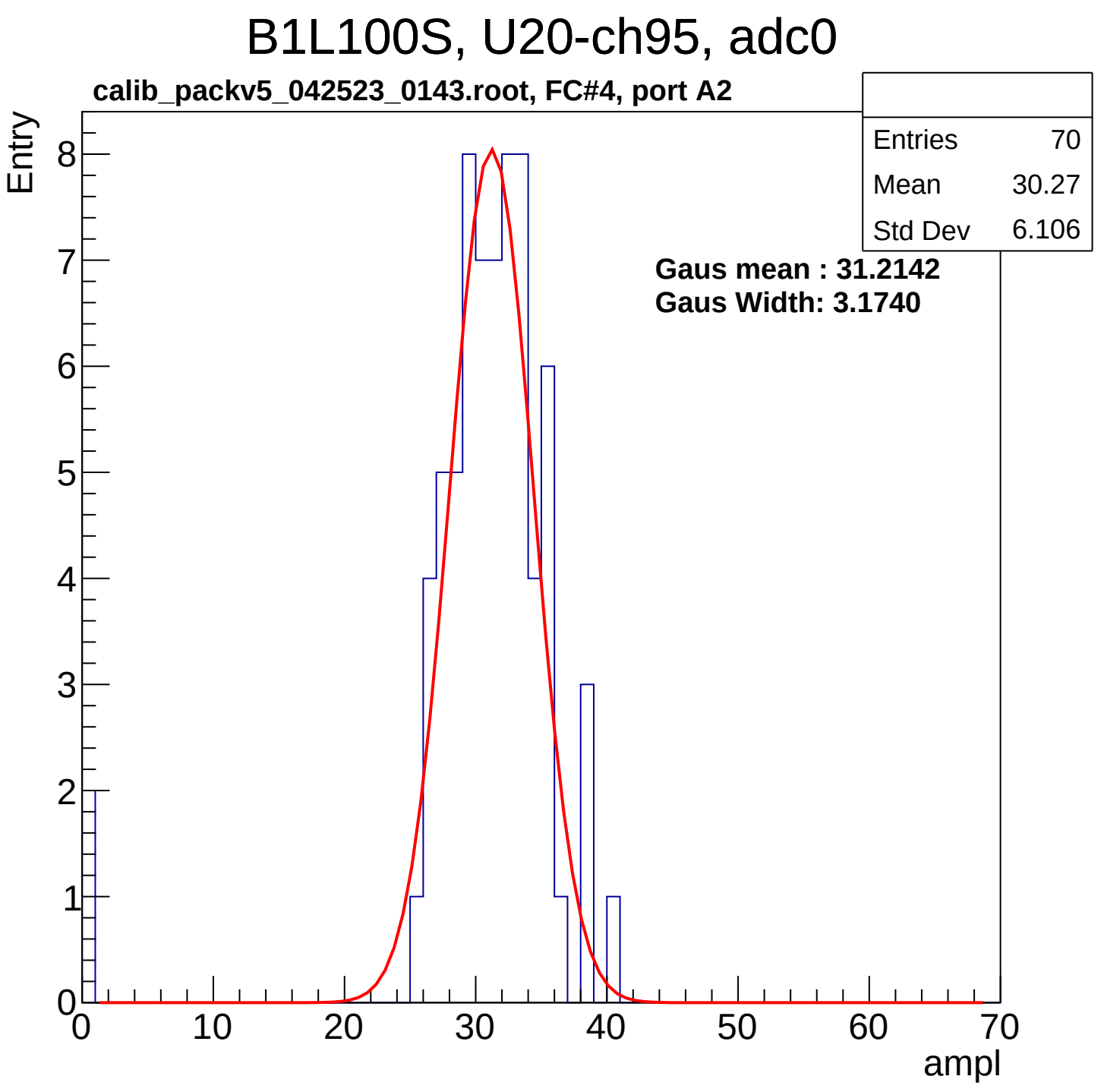

B1L100S, U20-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 47.07
Std Dev 3.622
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

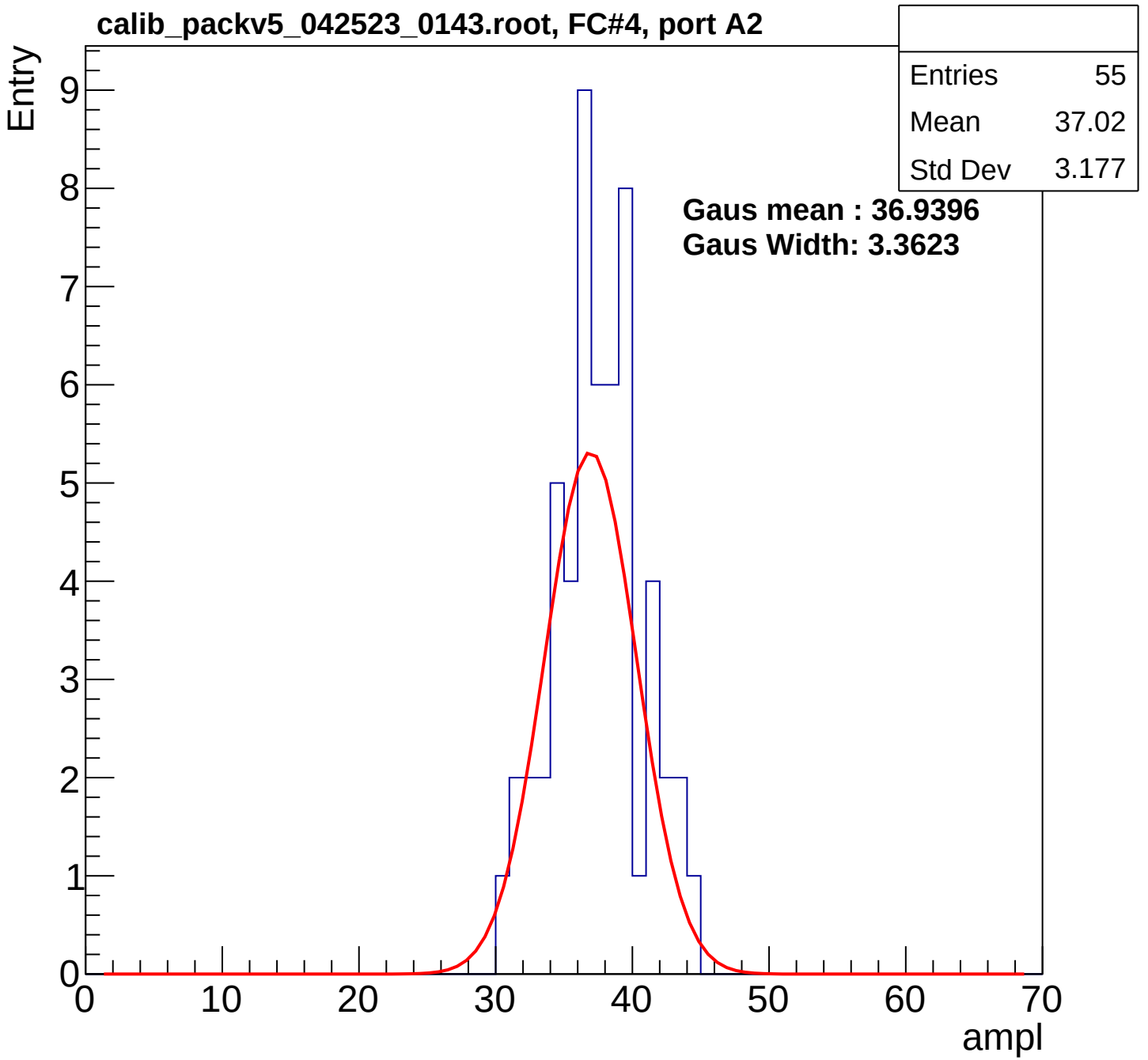

B1L100S, U20-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 52.45
Std Dev 3.477
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch94, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 57.79
Std Dev 3.128
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch94, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.93
Std Dev 11.28
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch94, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 30.27
Std Dev 6.106
Gaus mean : 31.2142
Gaus Width: 3.1740
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 37.02
Std Dev 3.177
Gaus mean : 36.9396
Gaus Width: 3.3623
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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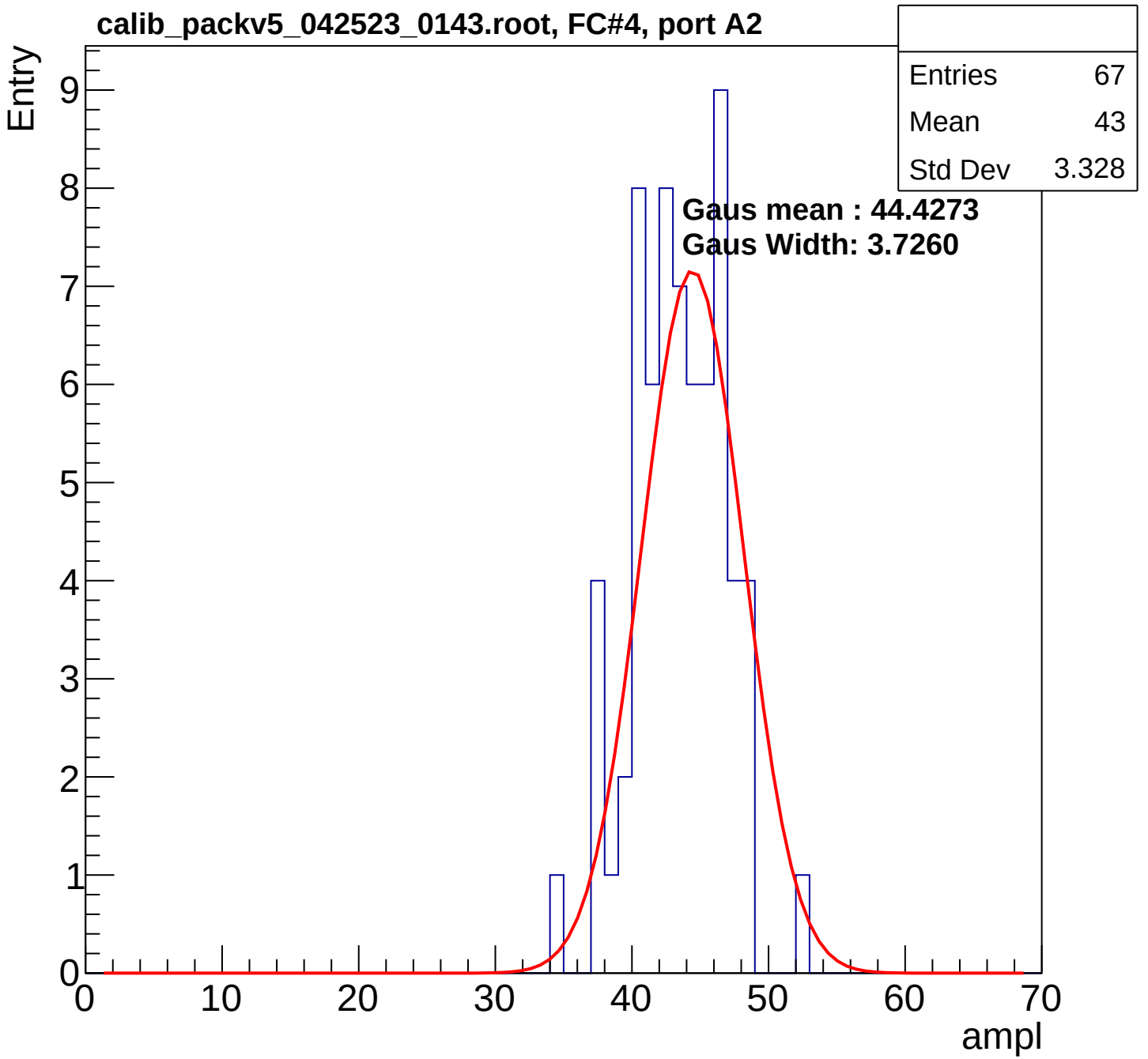
B1L100S, U20-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 43
Std Dev 3.328
Gaus mean : 44.4273
Gaus Width: 3.7260
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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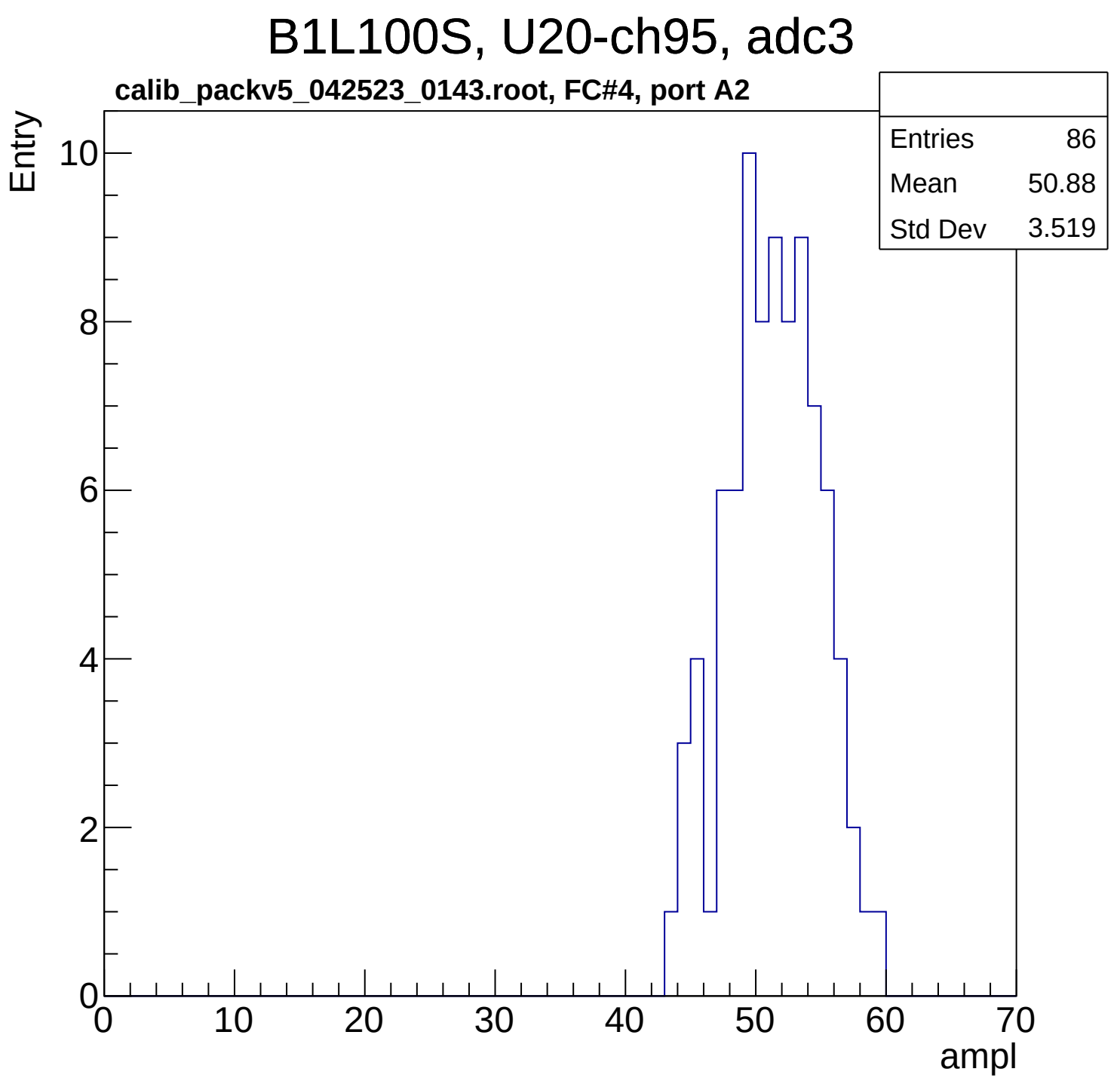
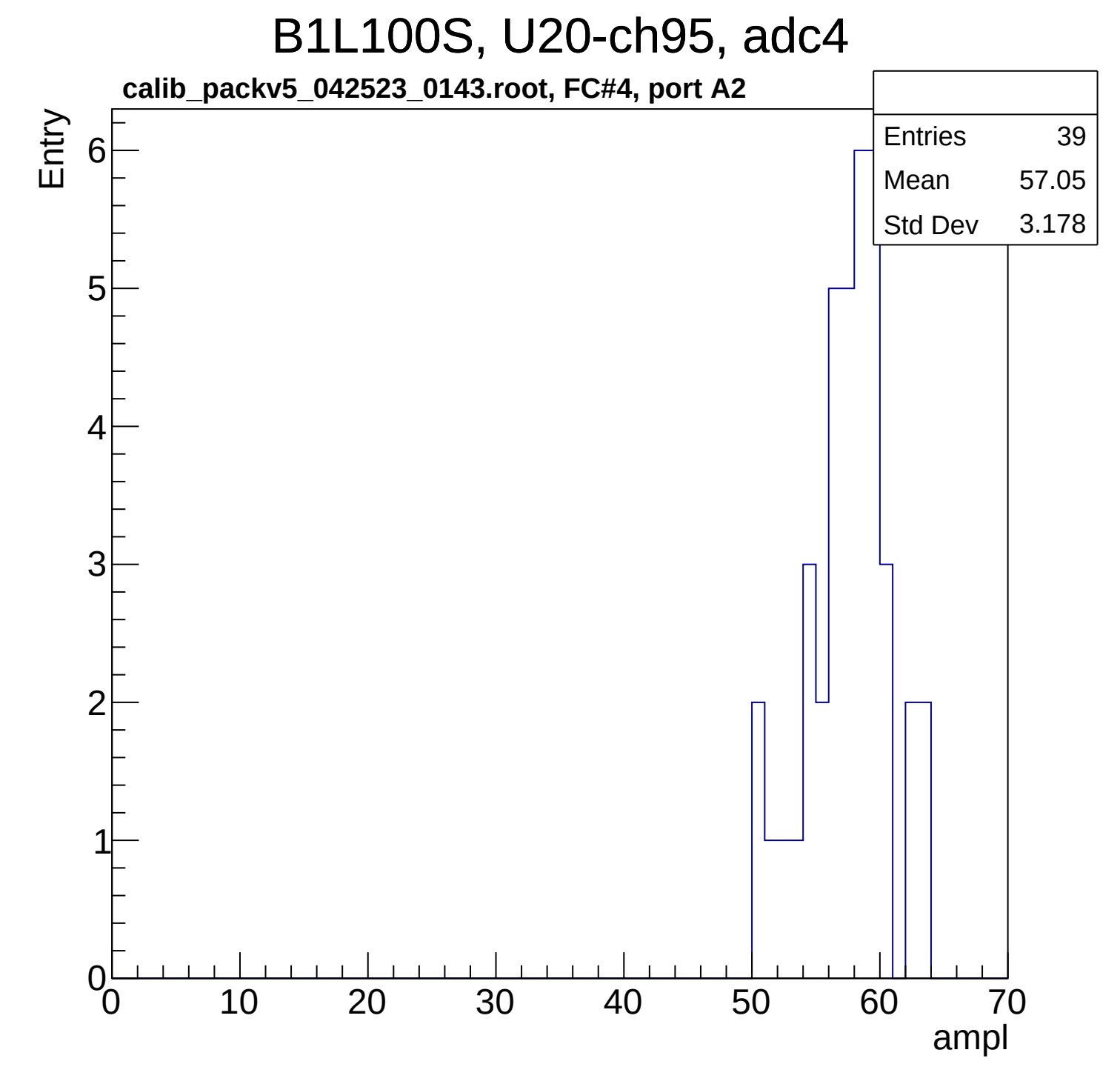

B1L100S, U20-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 50.88
Std Dev 3.519
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch95, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 57.05
Std Dev 3.178
Entry
ampl
0
1
2
3
4
5
6
0
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20
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40
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60
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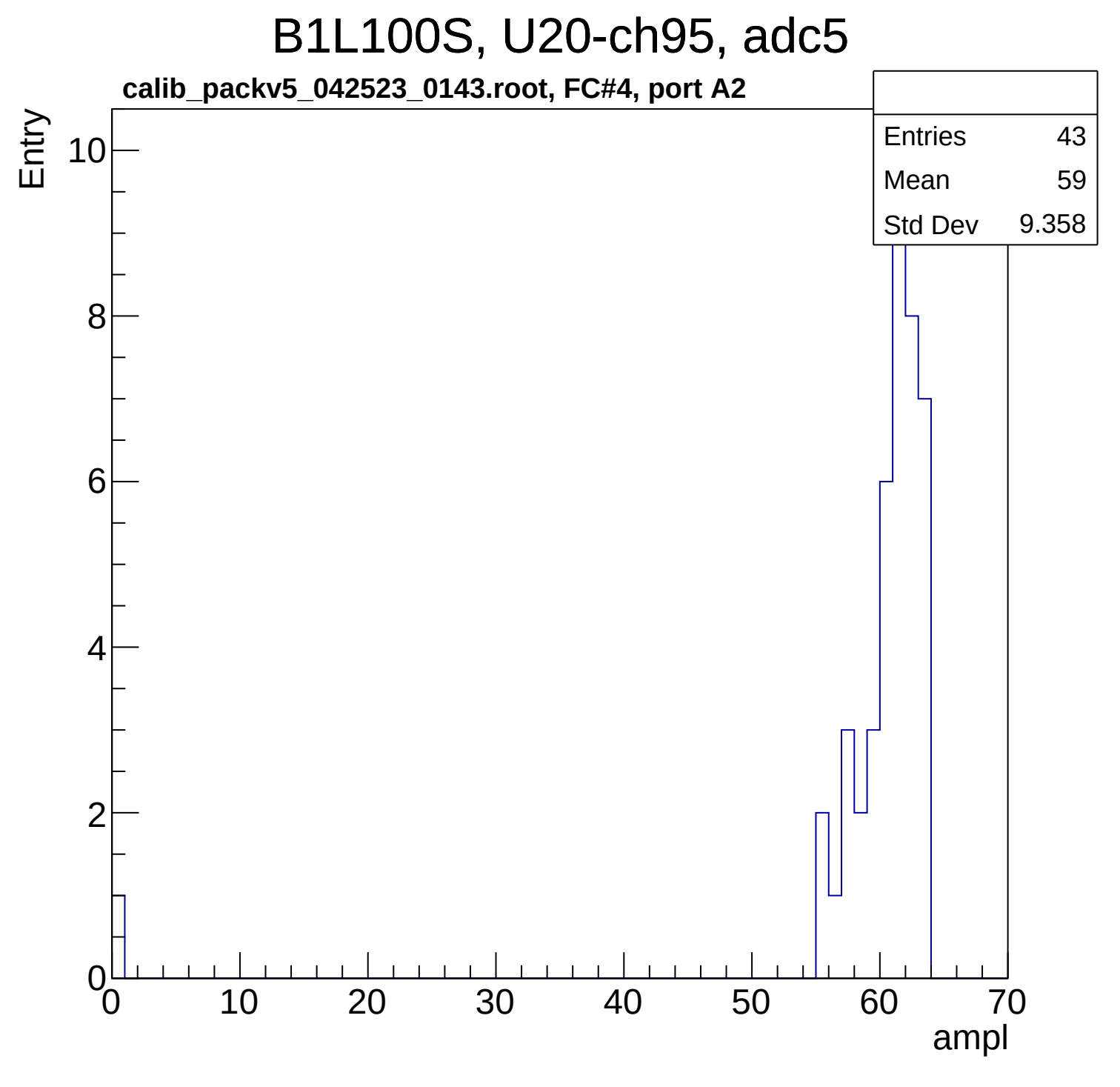
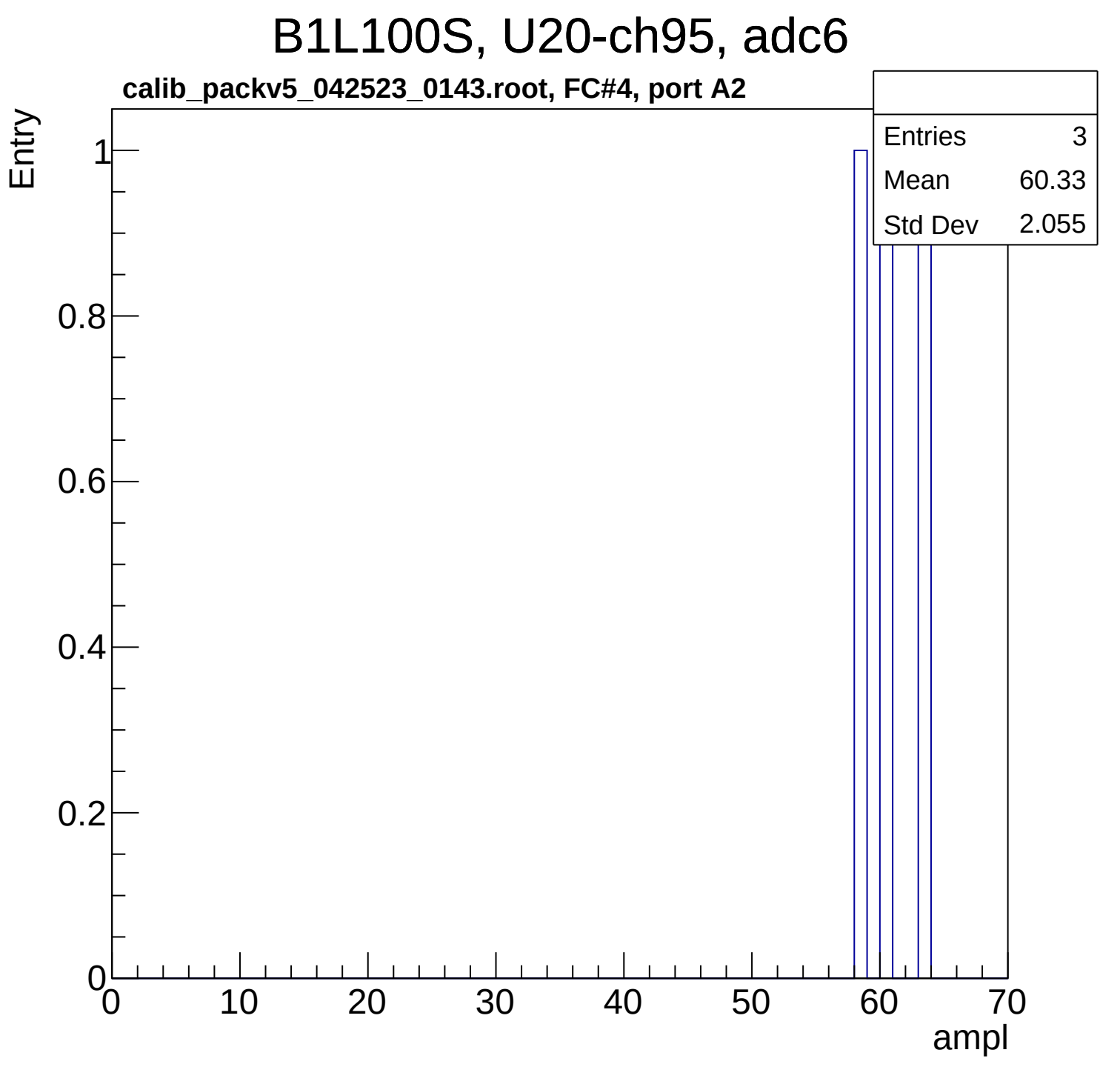
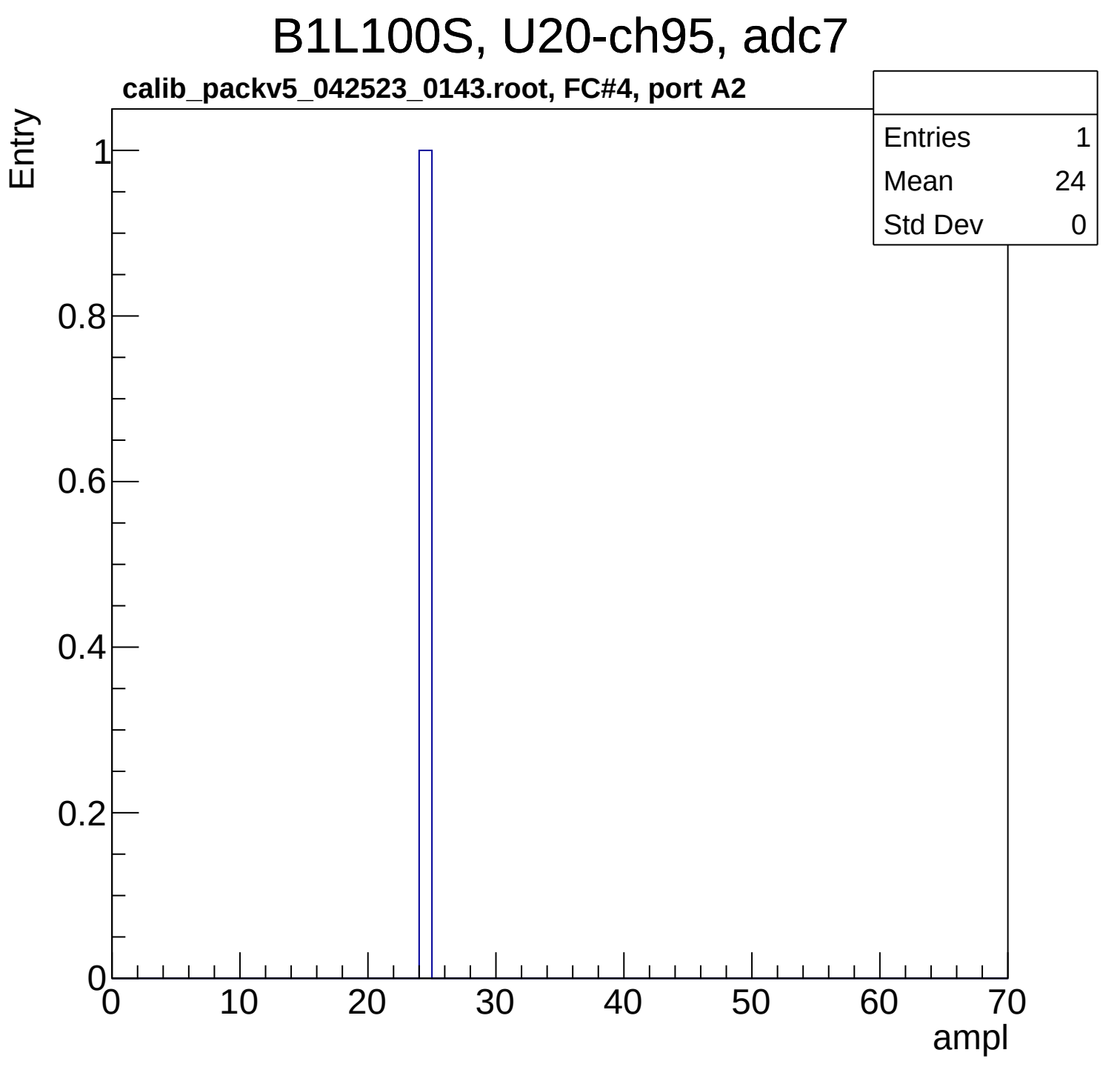

B1L100S, U20-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59
Std Dev 9.358
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.33
Std Dev 2.055
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U20-ch95, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
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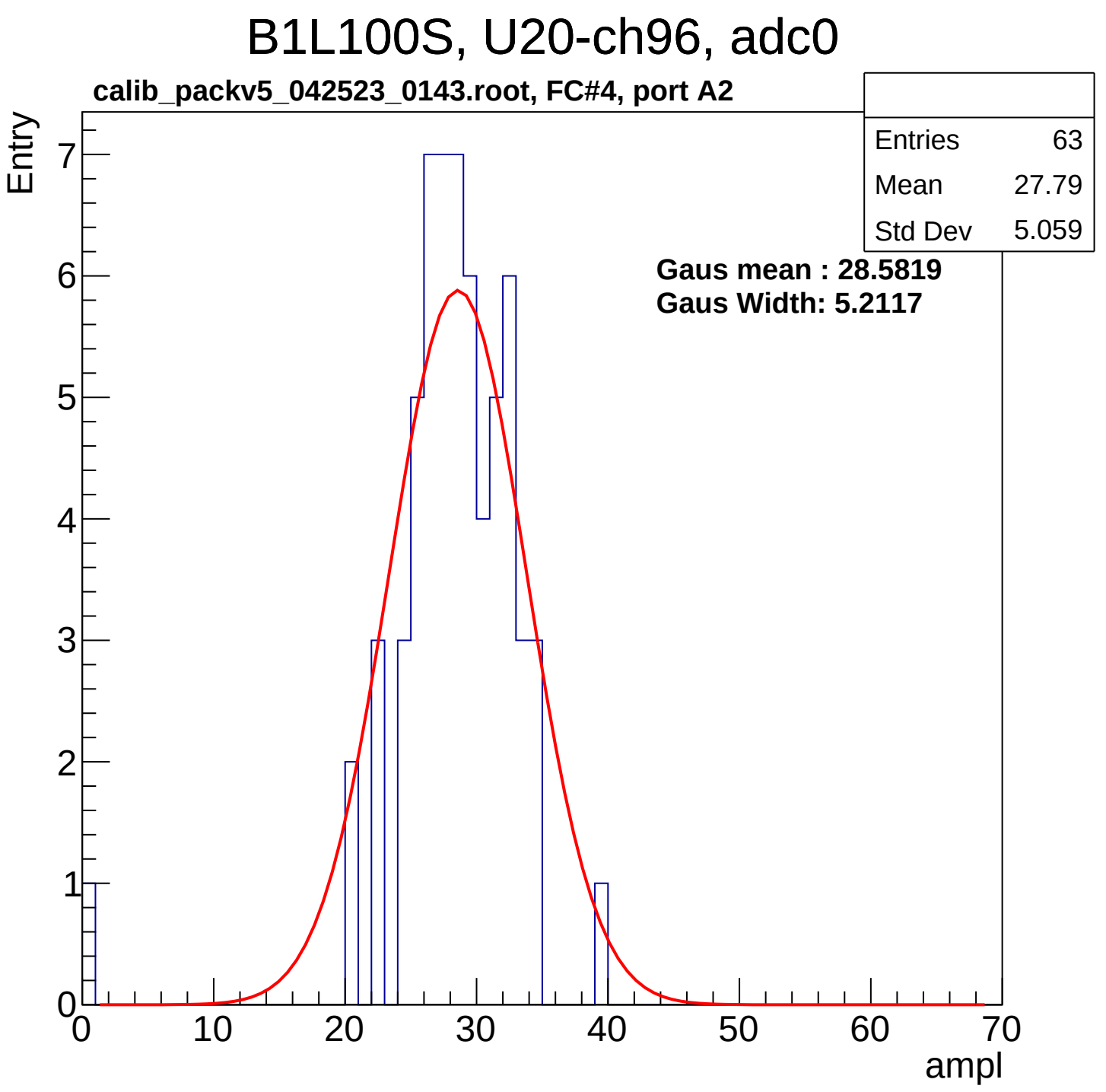
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 27.79
Std Dev 5.059
Gaus mean : 28.5819
Gaus Width: 5.2117
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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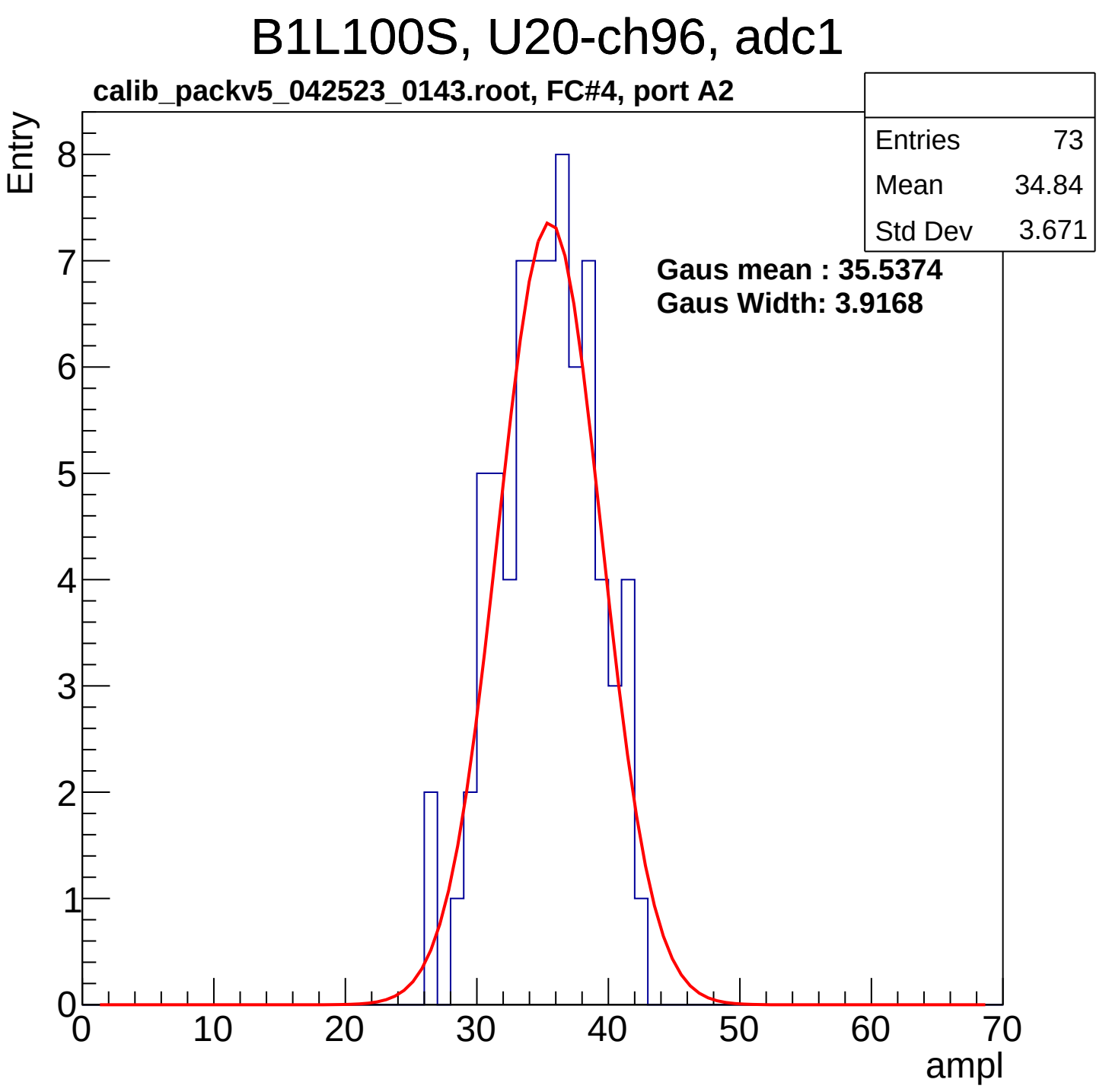
B1L100S, U20-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 34.84
Std Dev 3.671
Gaus mean : 35.5374
Gaus Width: 3.9168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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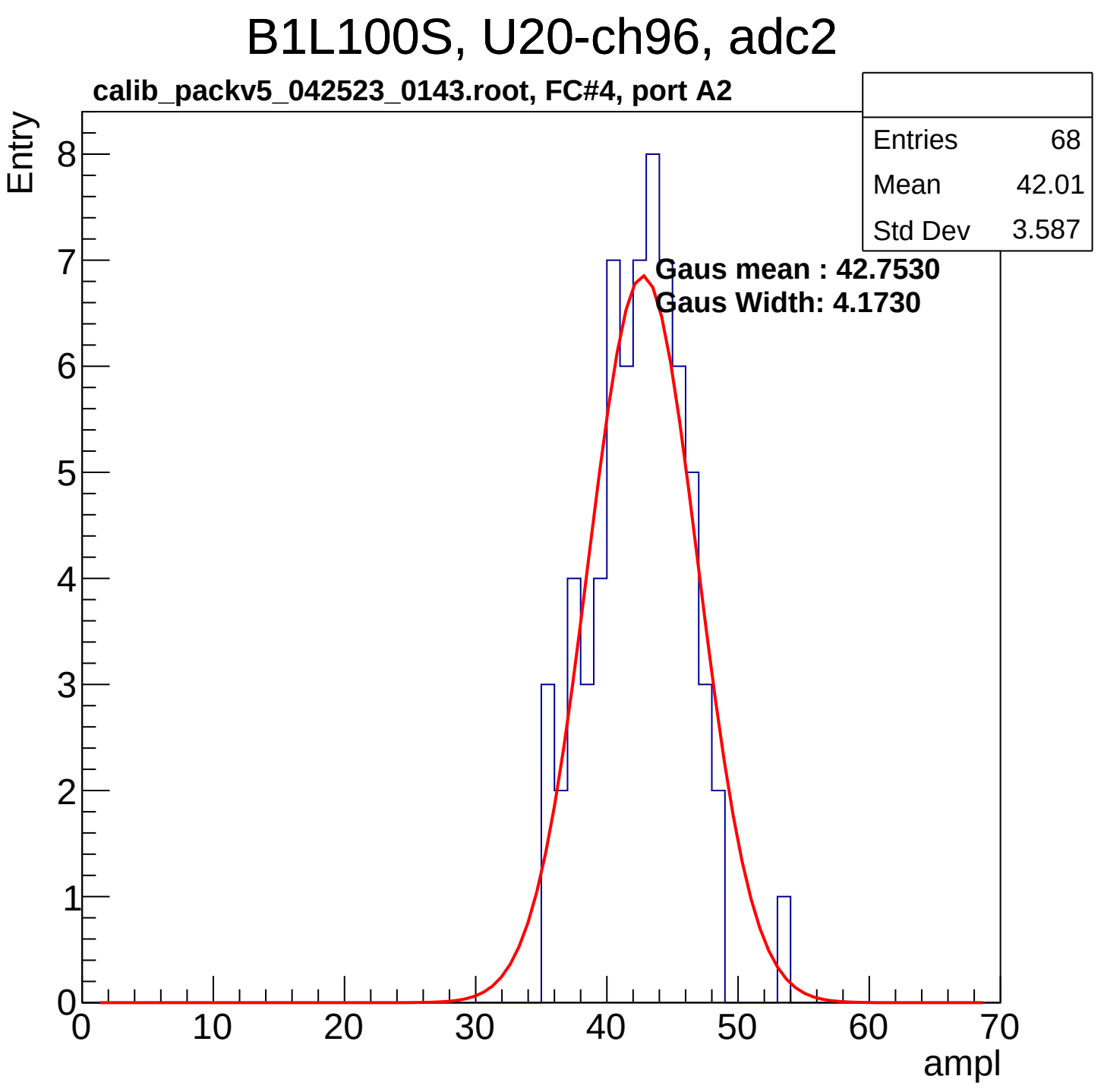
B1L100S, U20-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 42.01
Std Dev 3.587
Gaus mean : 42.7530
Gaus Width: 4.1730
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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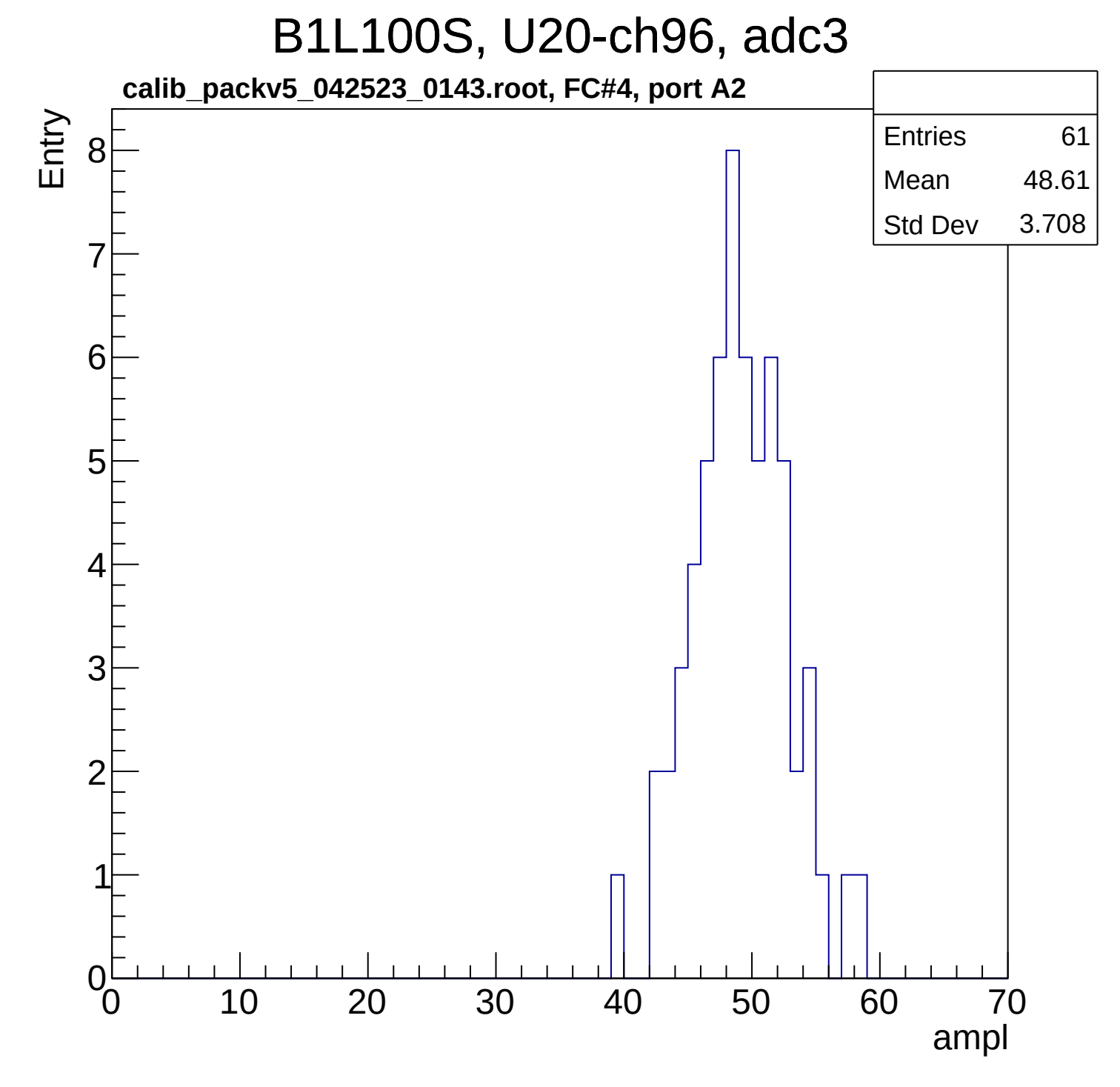
40
50
60
70


B1L100S, U20-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 48.61
Std Dev 3.708
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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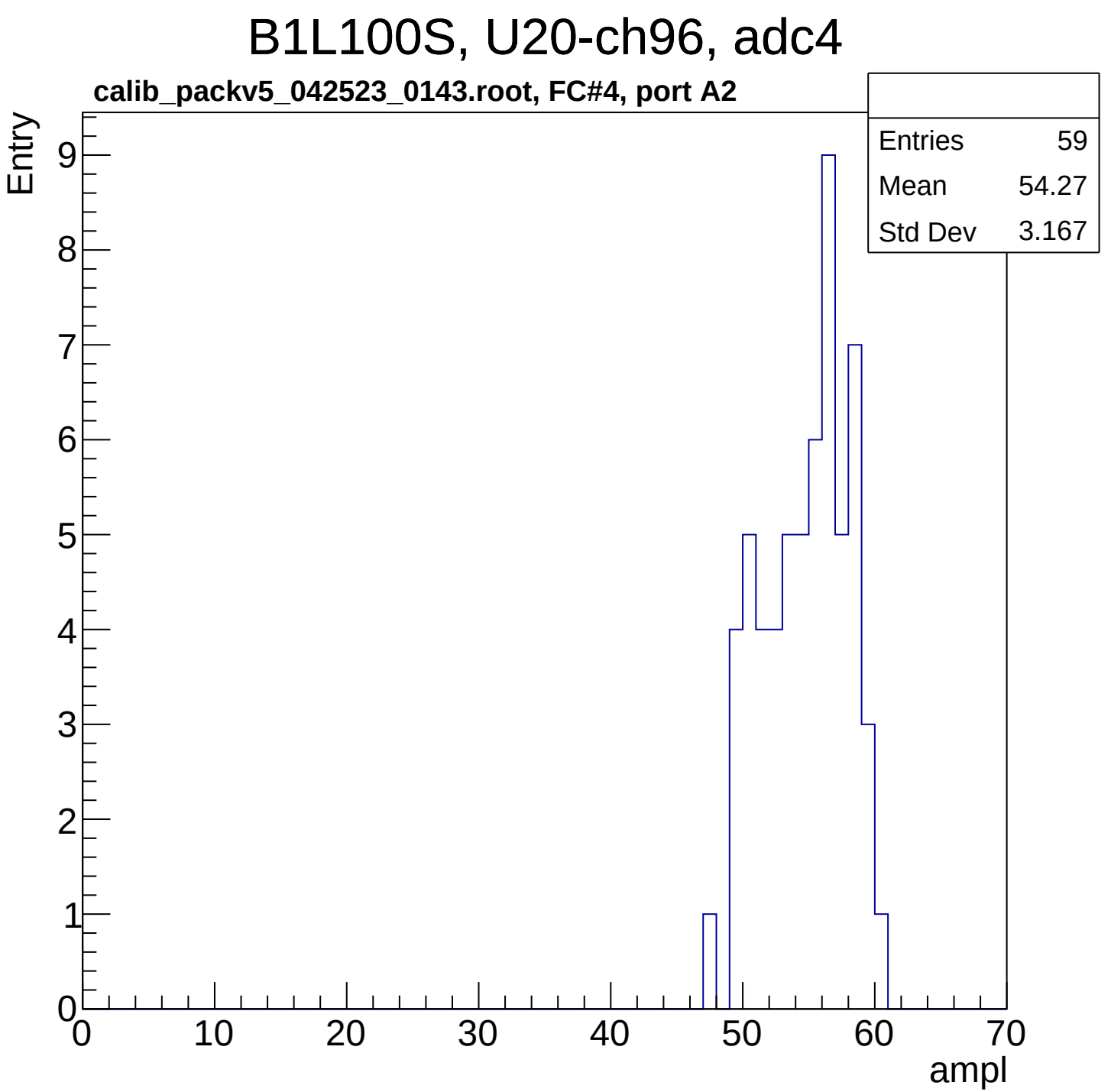
40
50
60
70


B1L100S, U20-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.27
Std Dev 3.167
Entry
ampl
0
1
2
3
4
5
6
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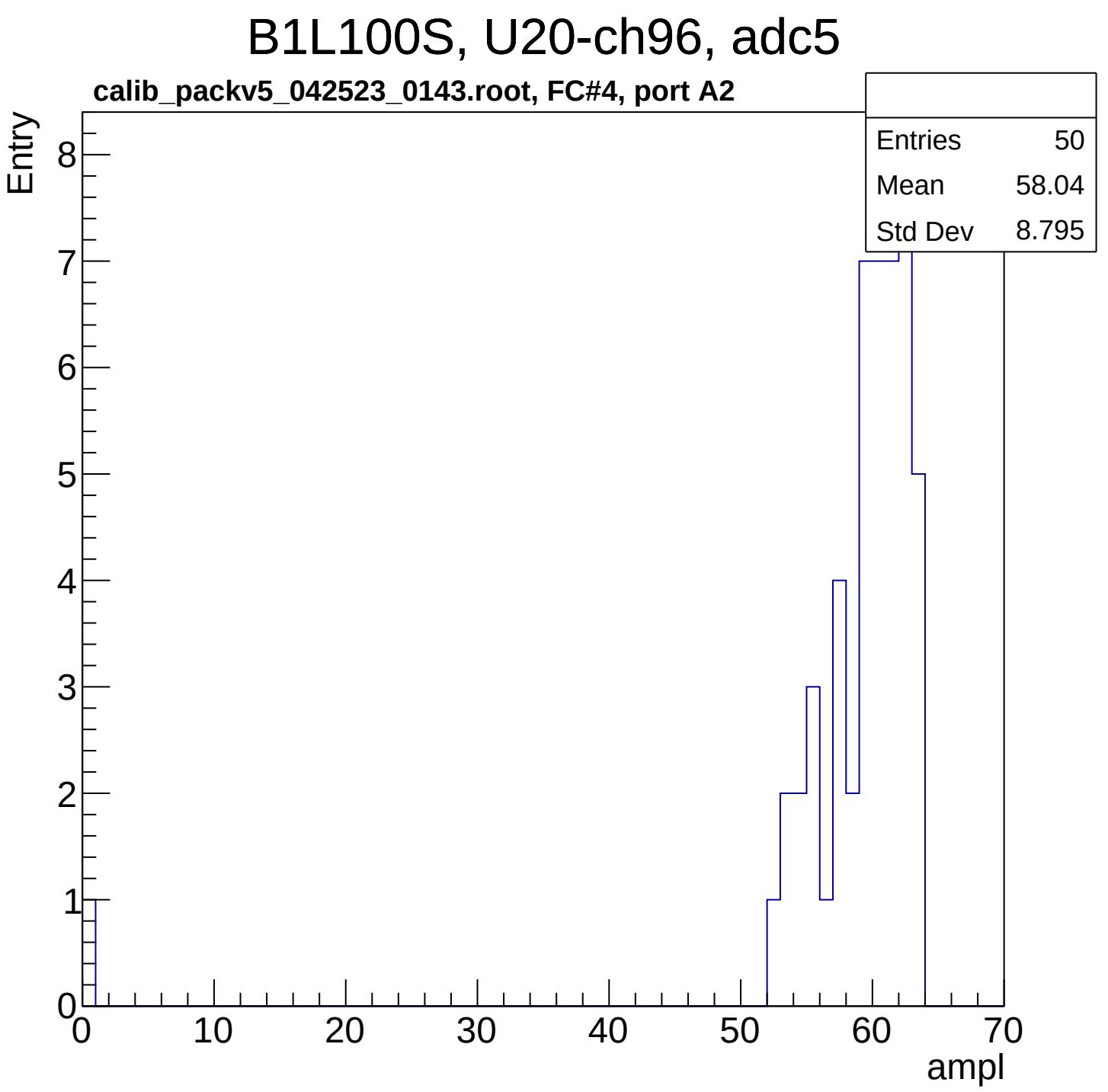
B1L100S, U20-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.04
Std Dev 8.795
Entry
ampl
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1
2
3
4
5
6
7
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0
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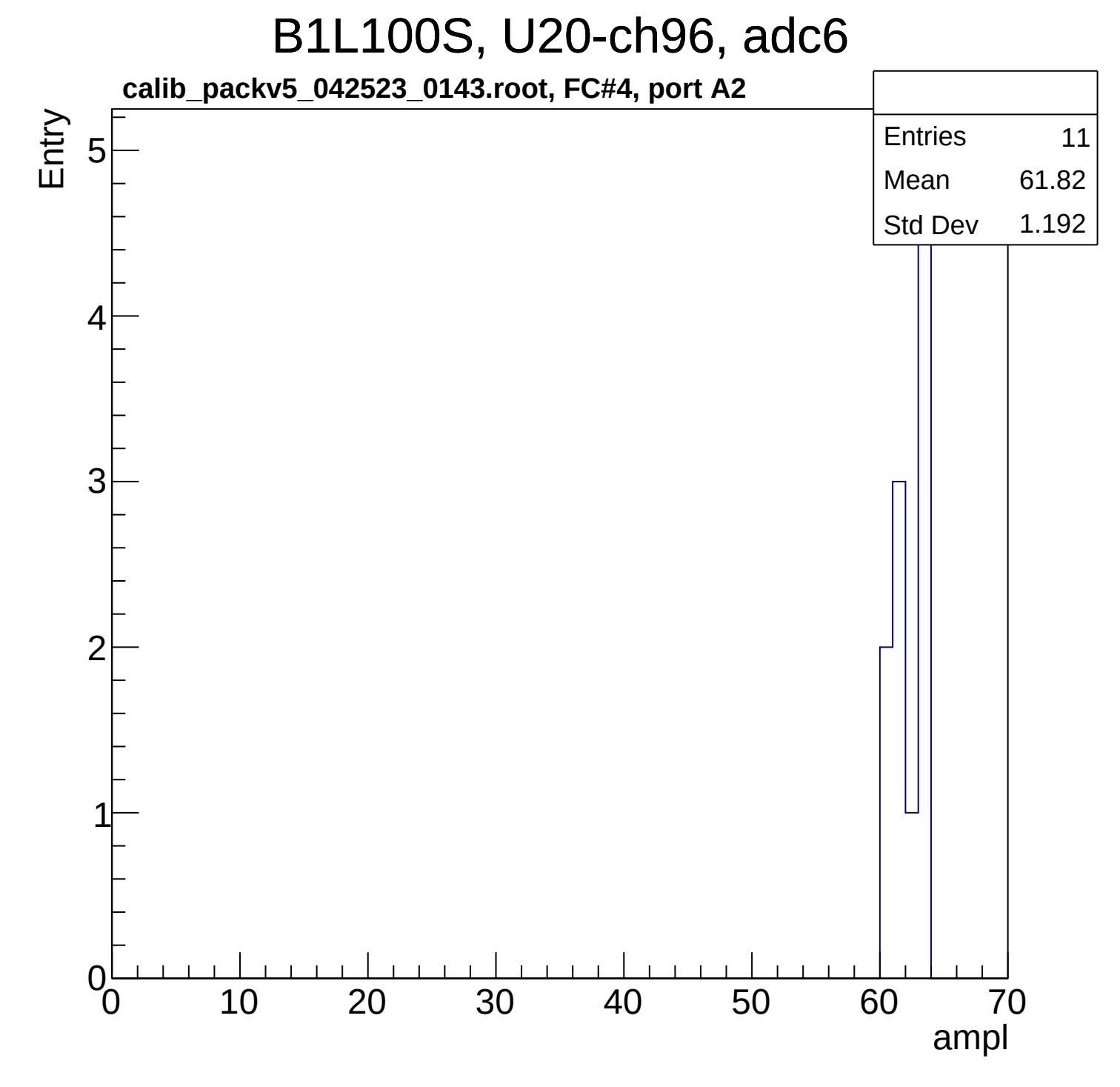
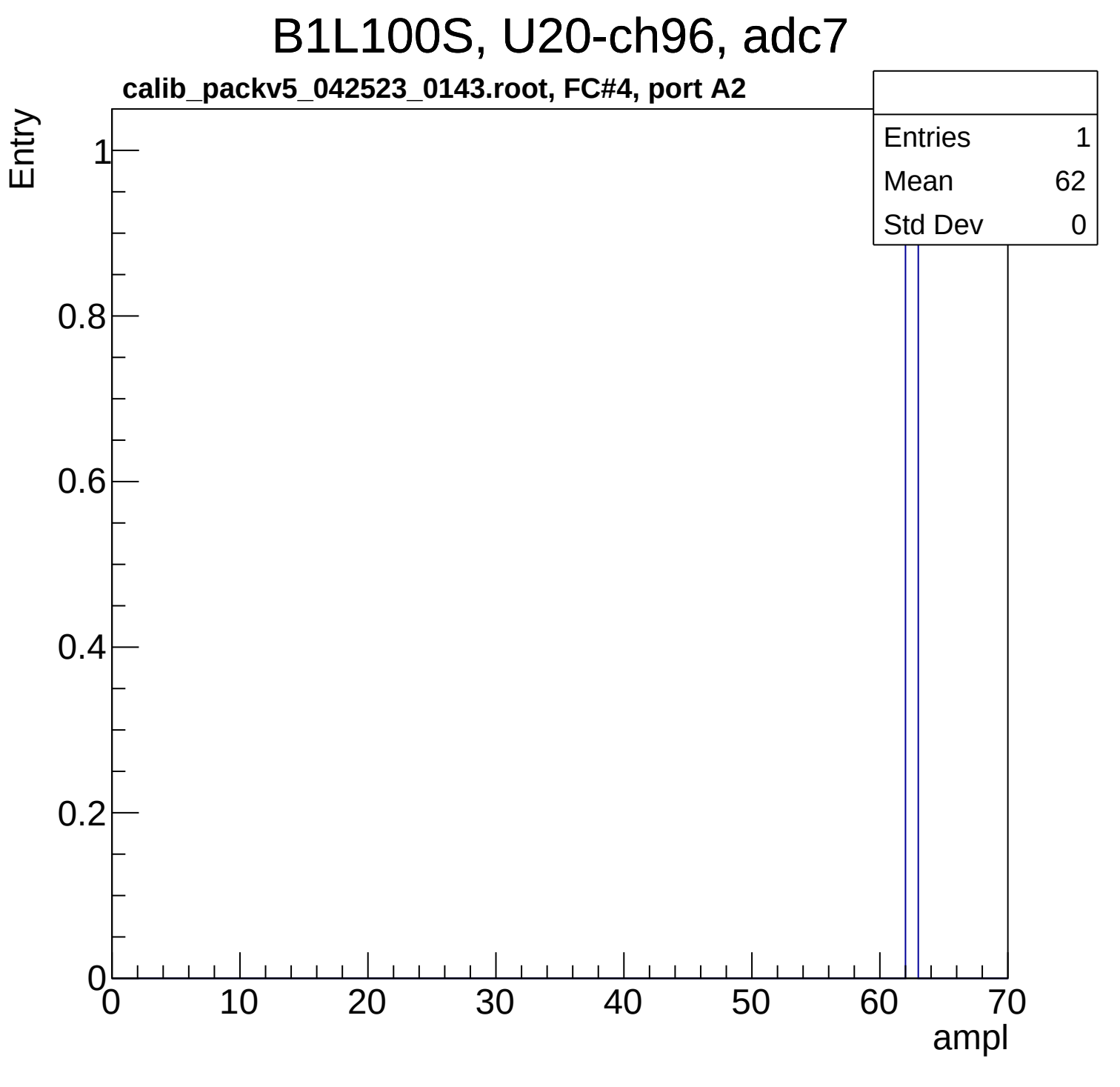
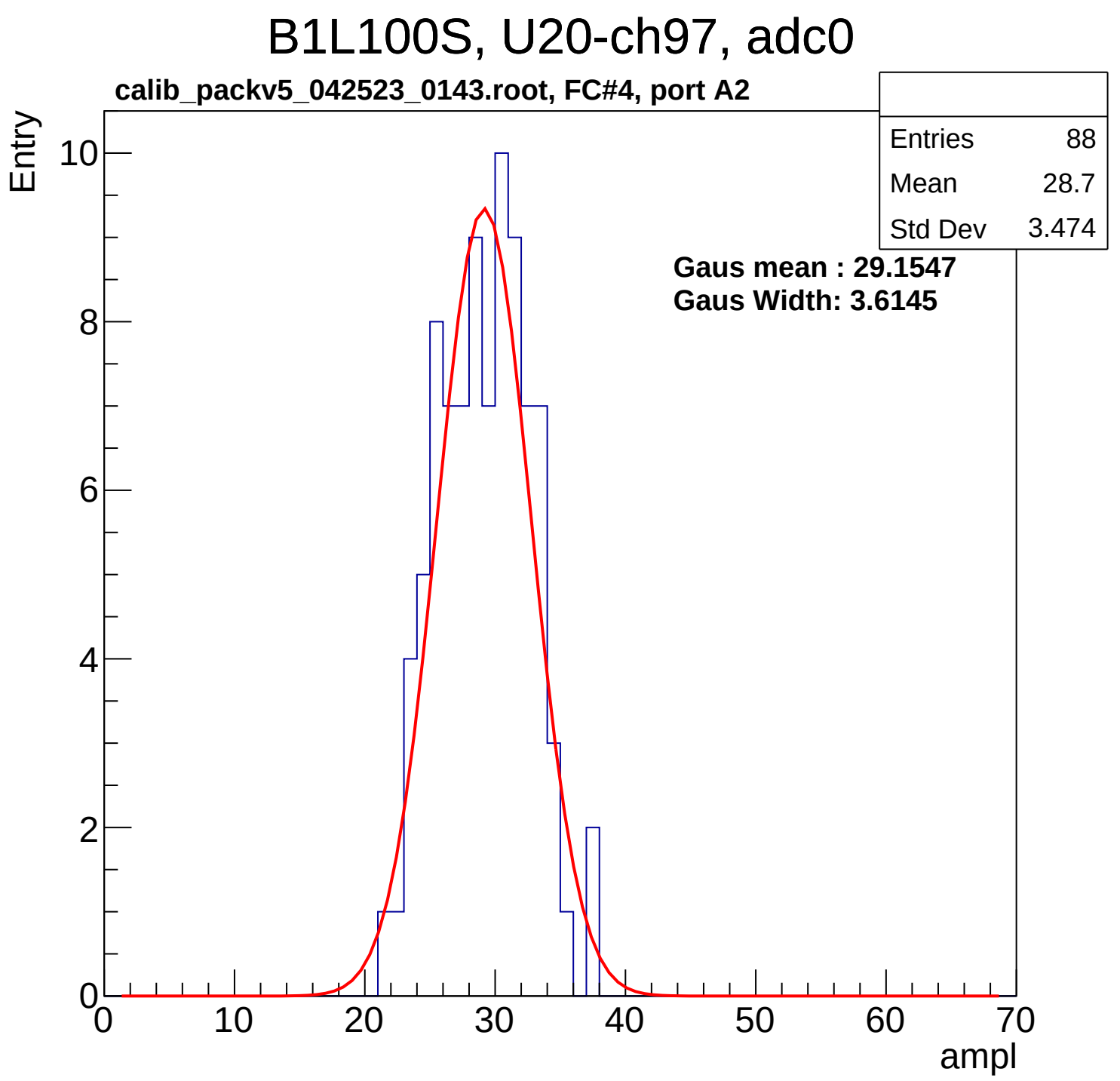
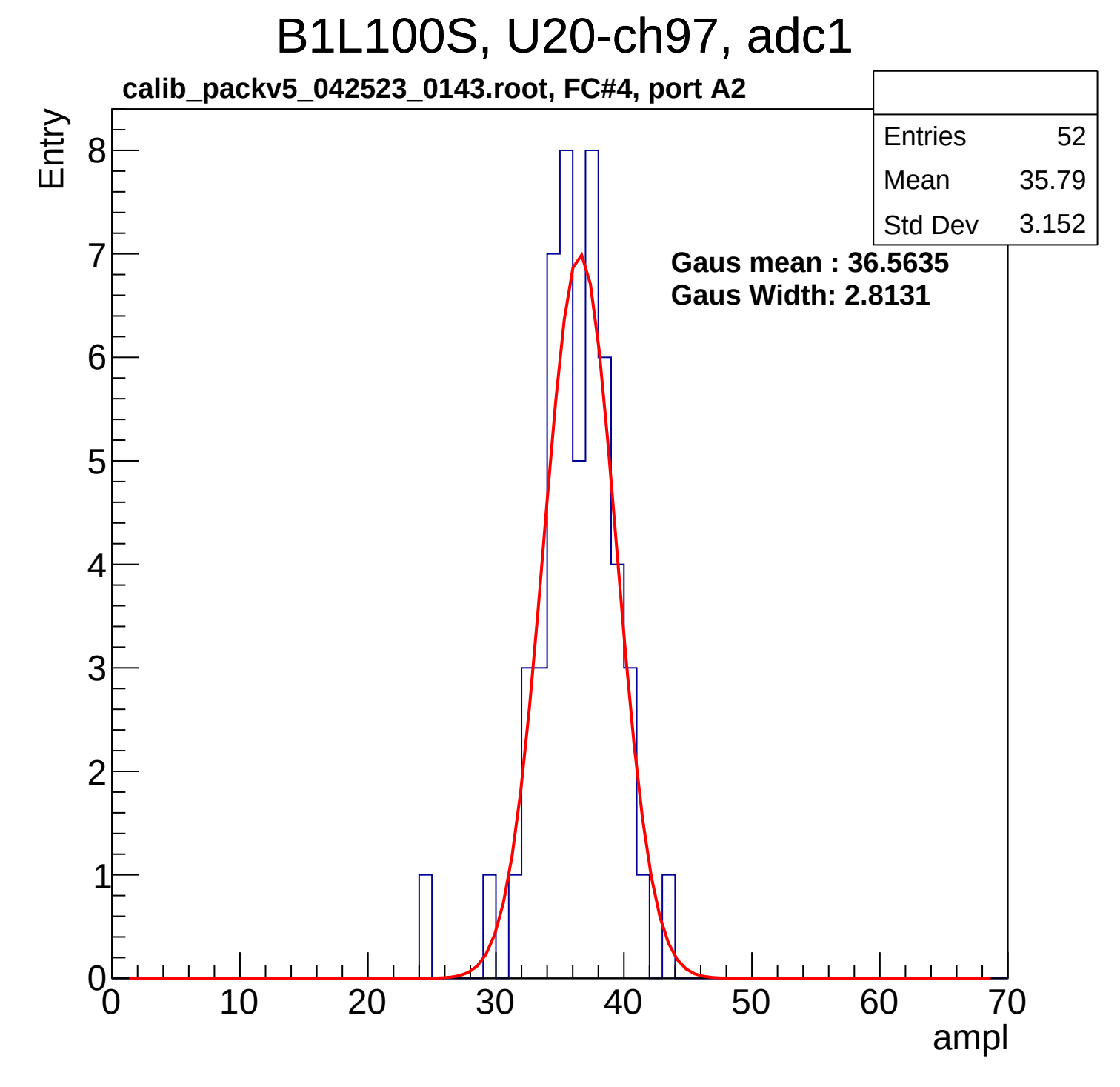
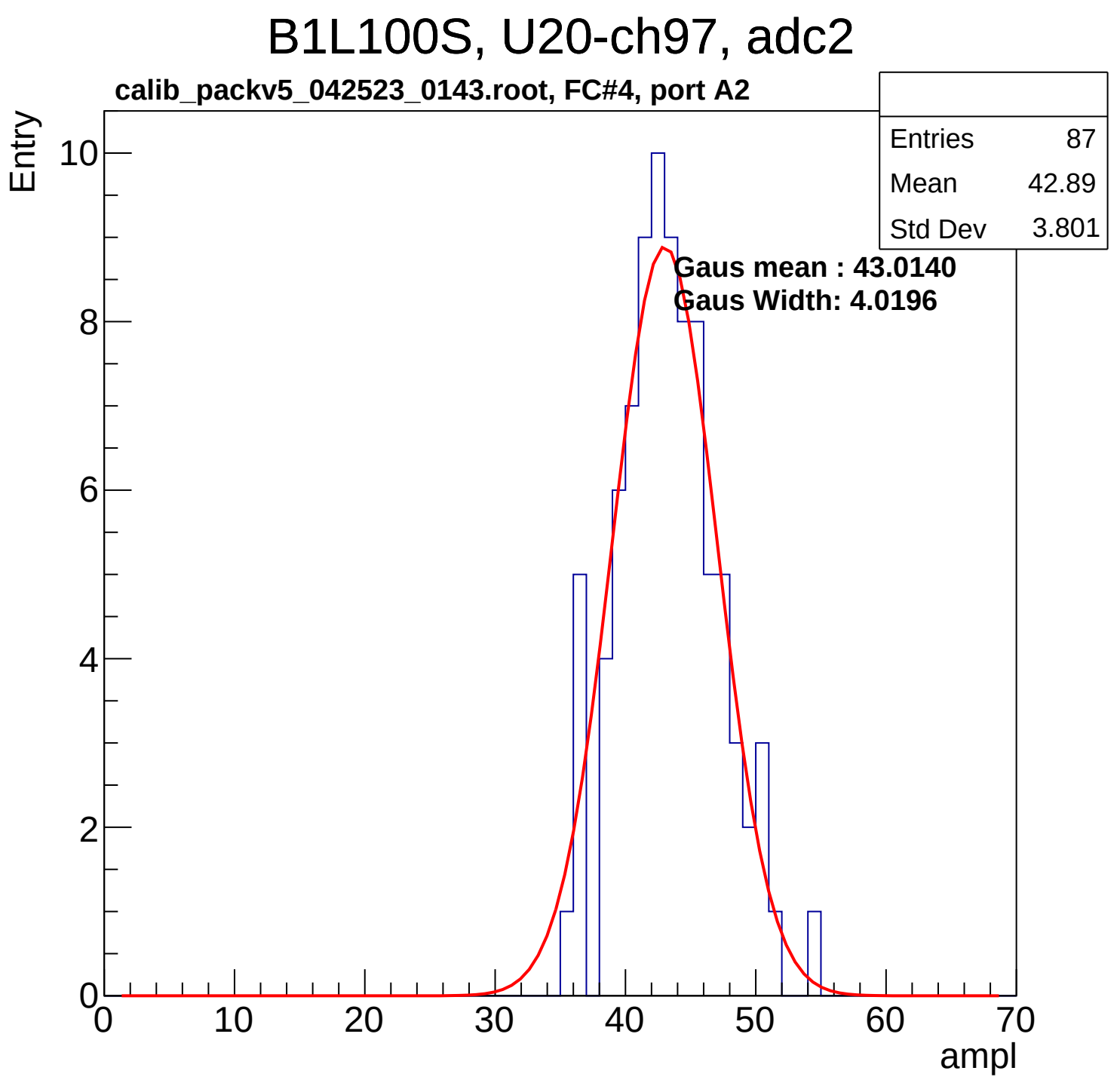
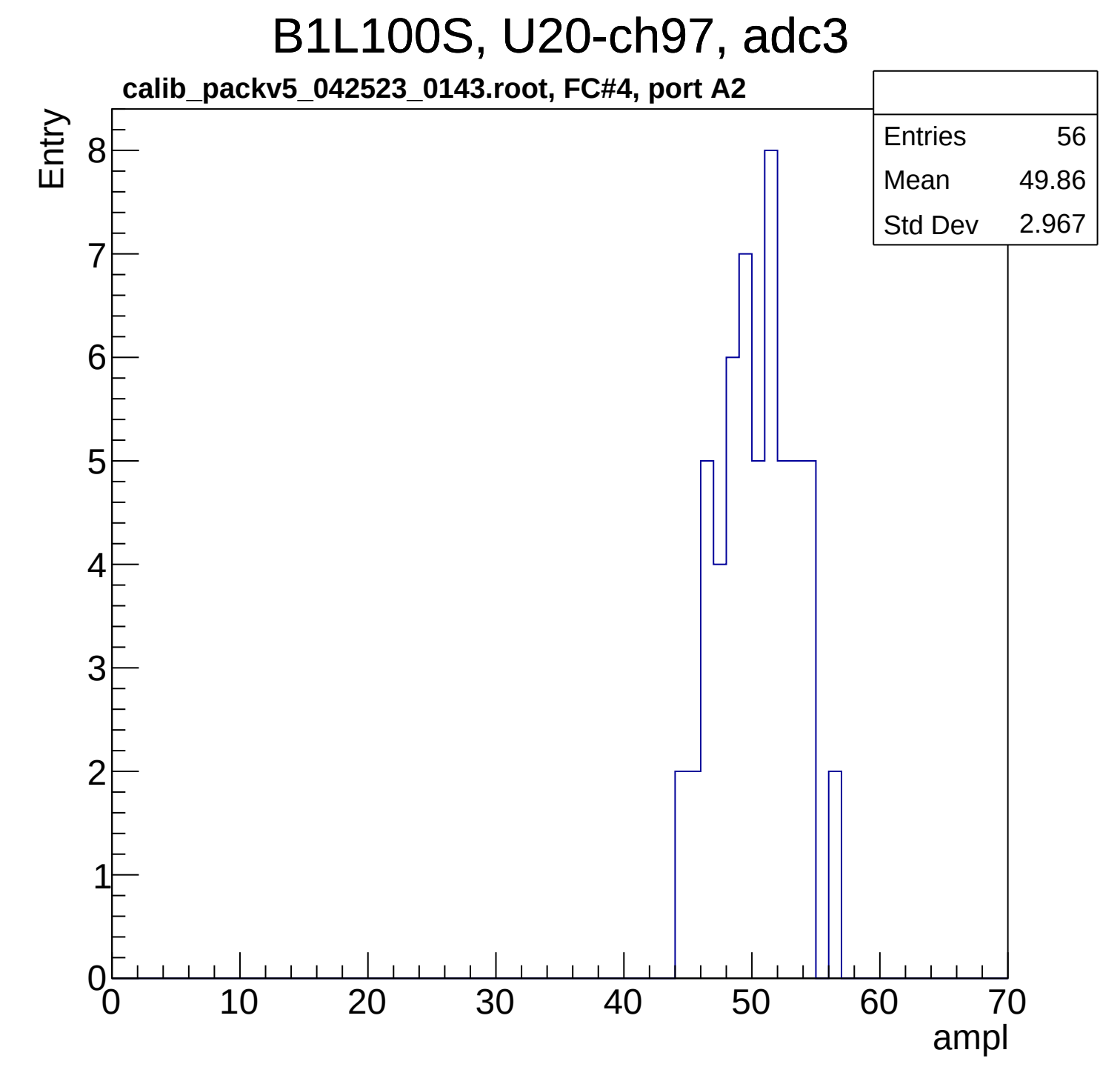

B1L100S, U20-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.82
Std Dev 1.192
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

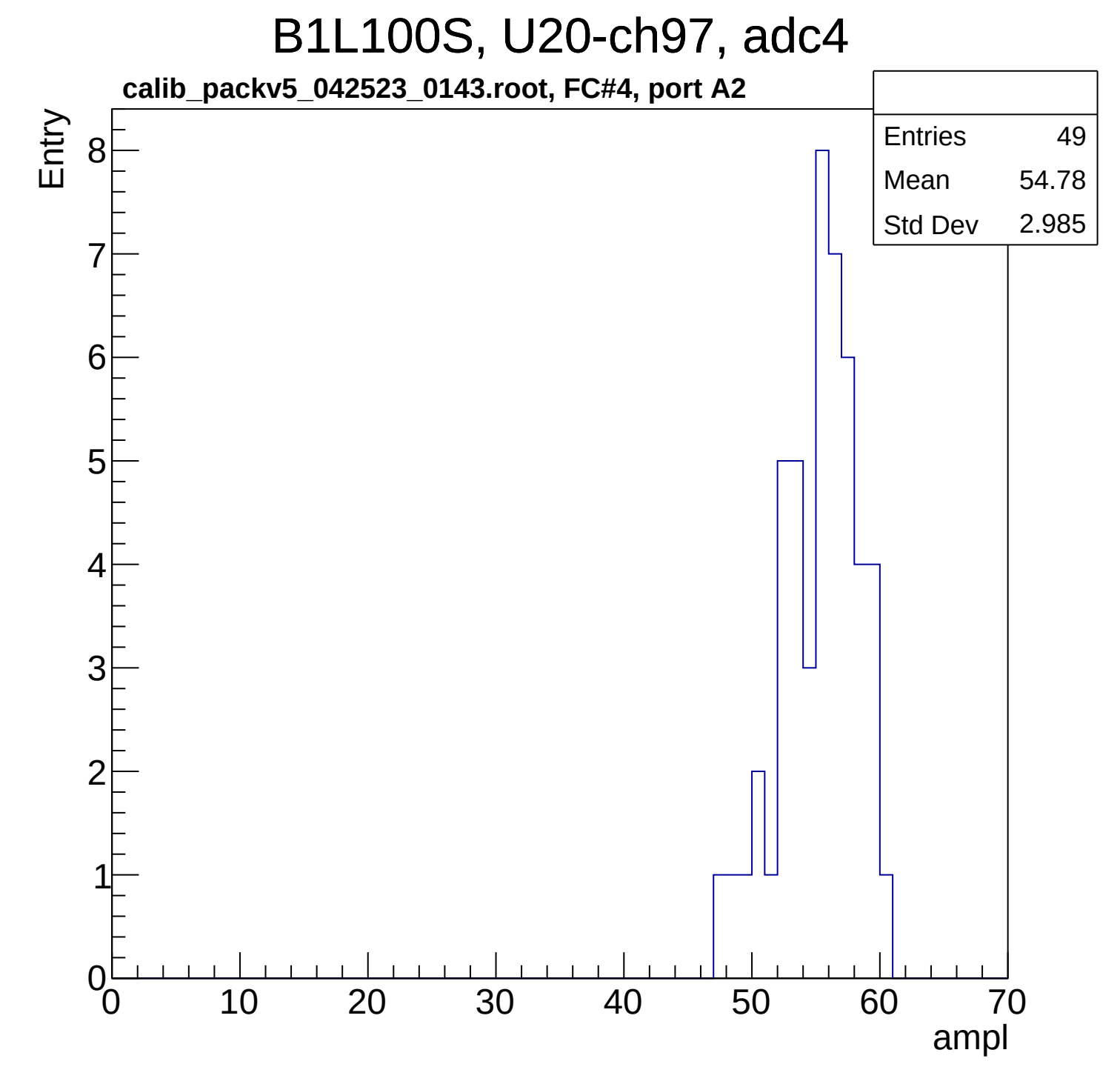

B1L100S, U20-ch96, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

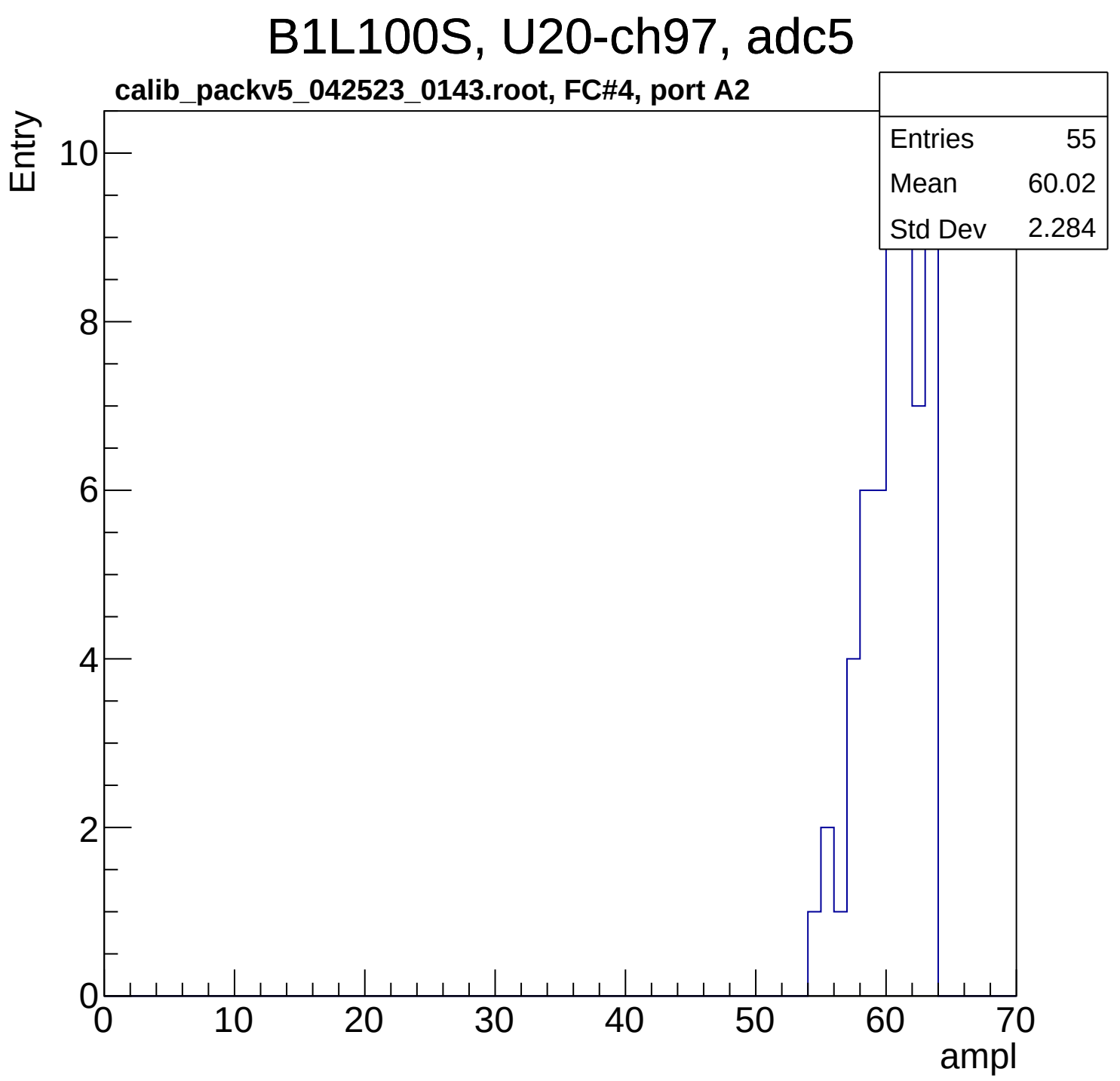

B1L100S, U20-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 28.7
Std Dev 3.474
Gaus mean : 29.1547
Gaus Width: 3.6145
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

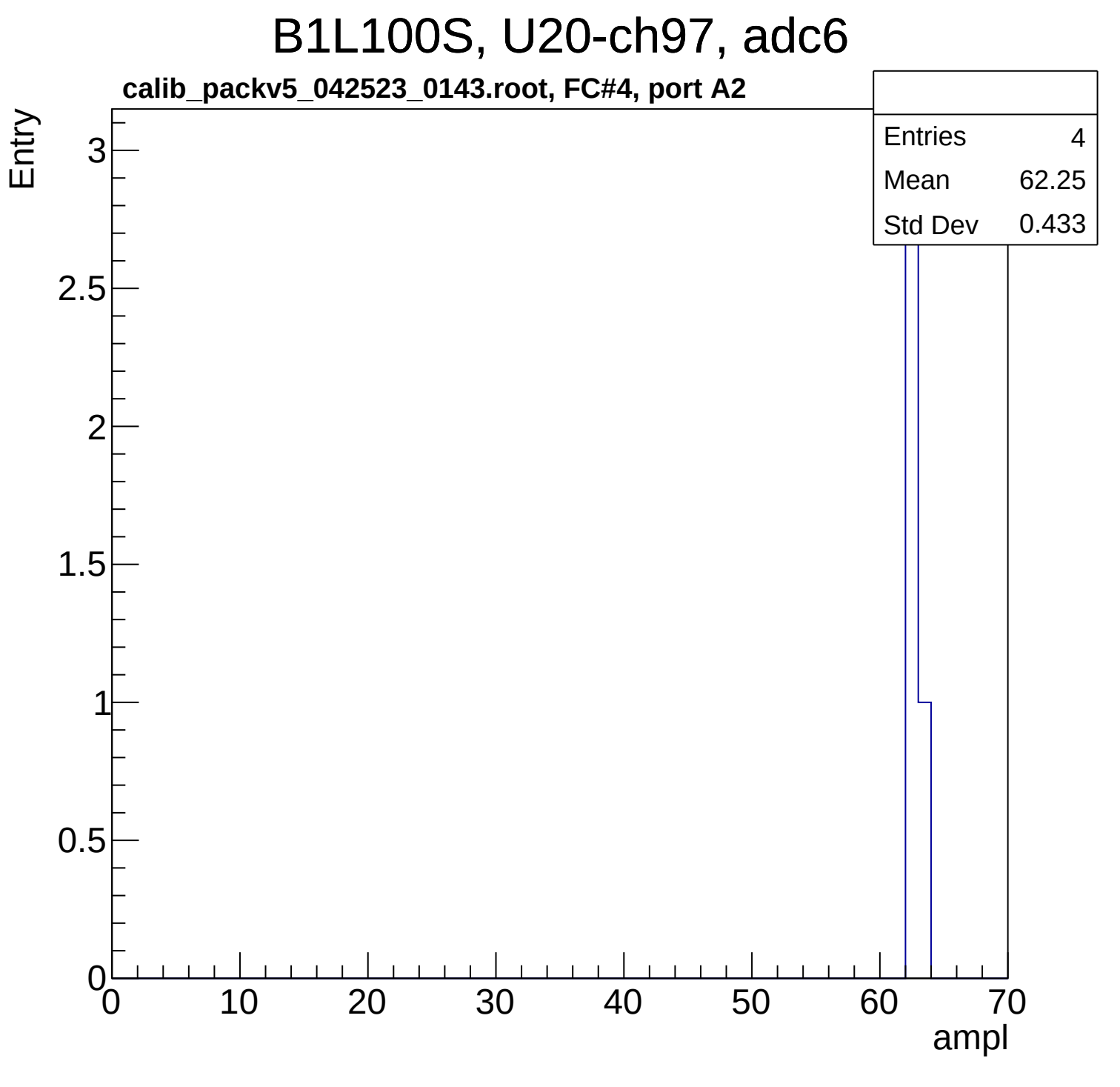

B1L100S, U20-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 35.79
Std Dev 3.152
Gaus mean : 36.5635
Gaus Width: 2.8131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

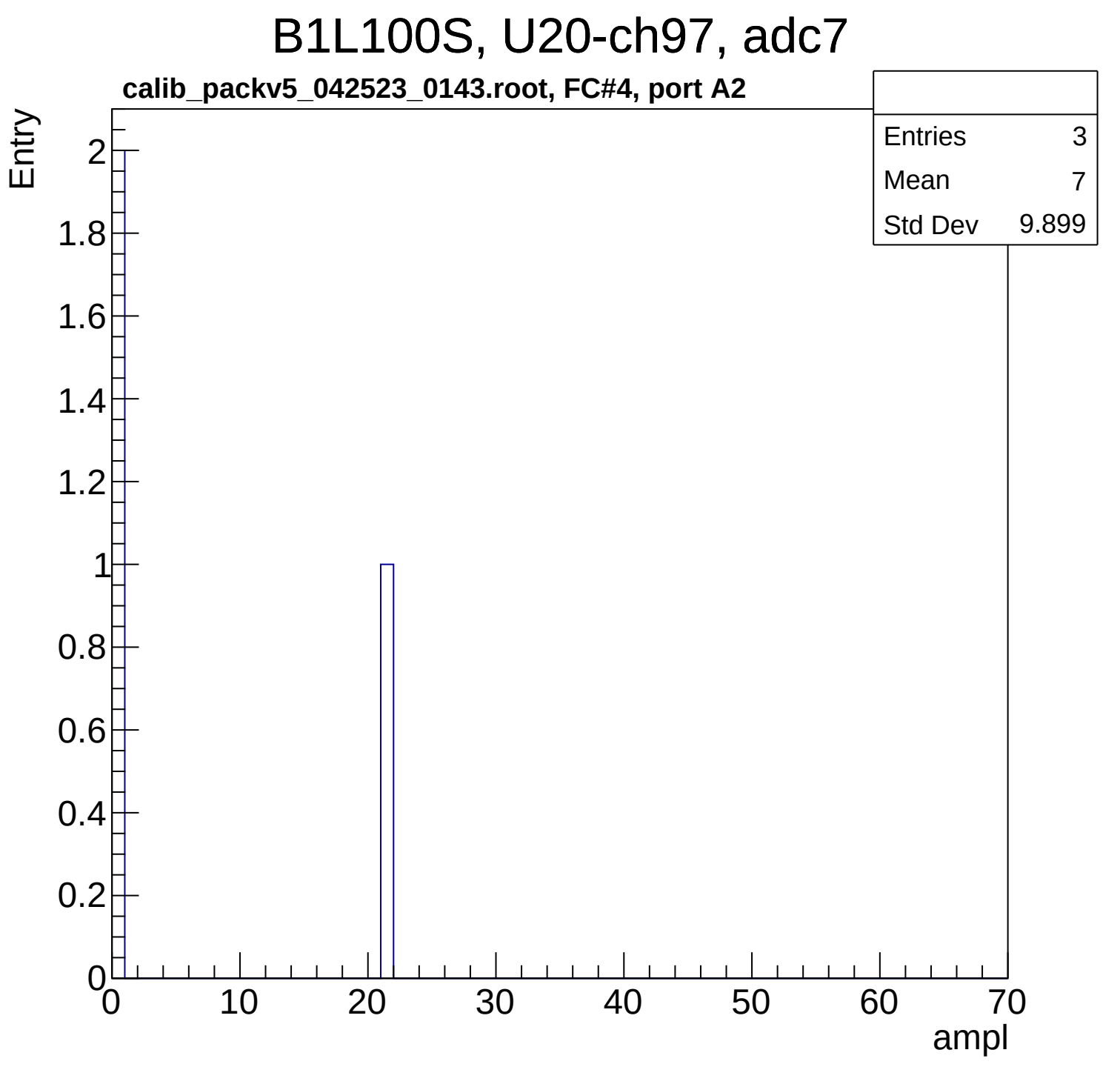

B1L100S, U20-ch97, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 42.89
Std Dev 3.801
Gaus mean : 43.0140
Gaus Width: 4.0196
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 49.86
Std Dev 2.967
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch97, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 54.78
Std Dev 2.985
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 60.02
Std Dev 2.284
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.433
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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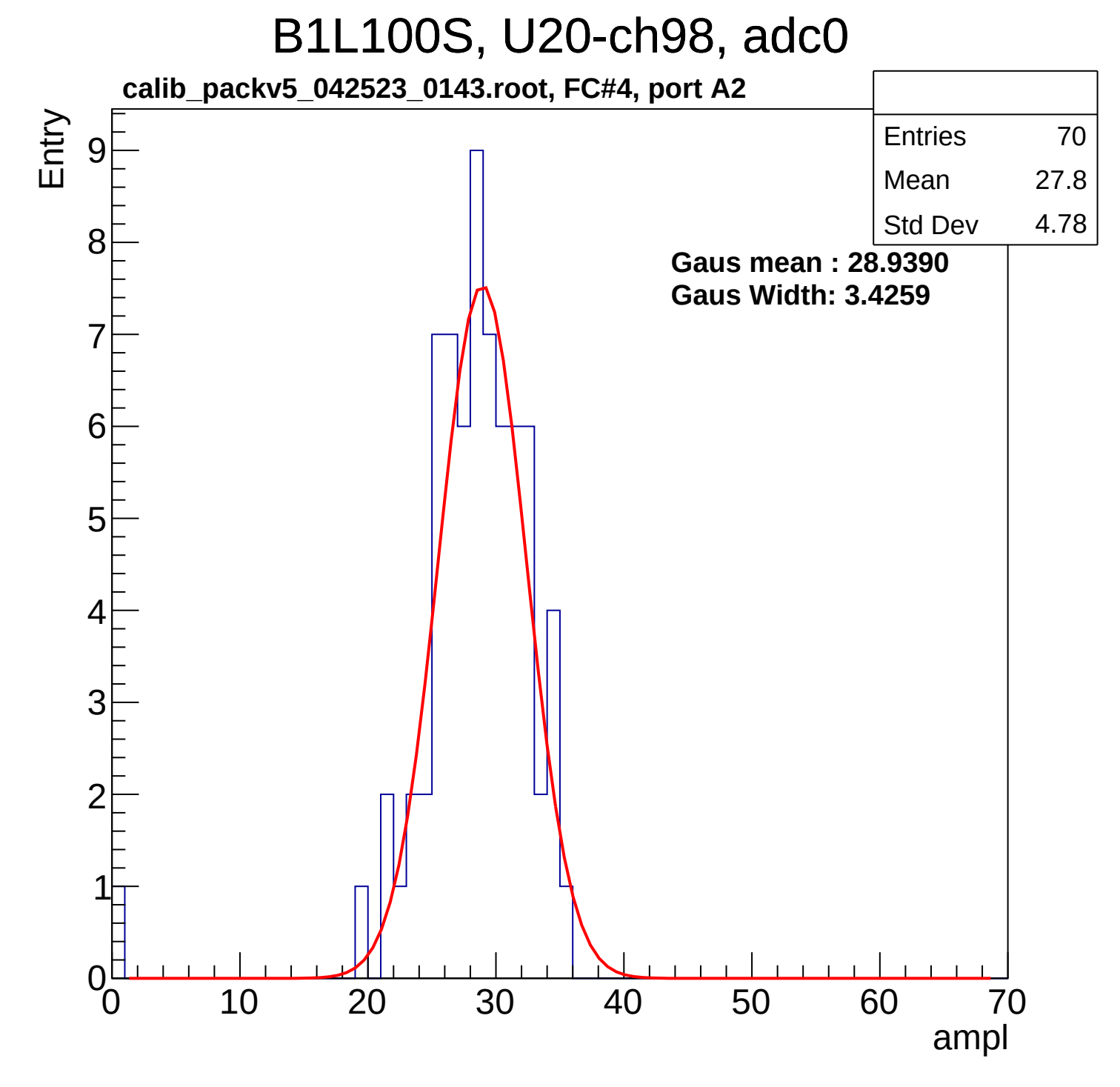
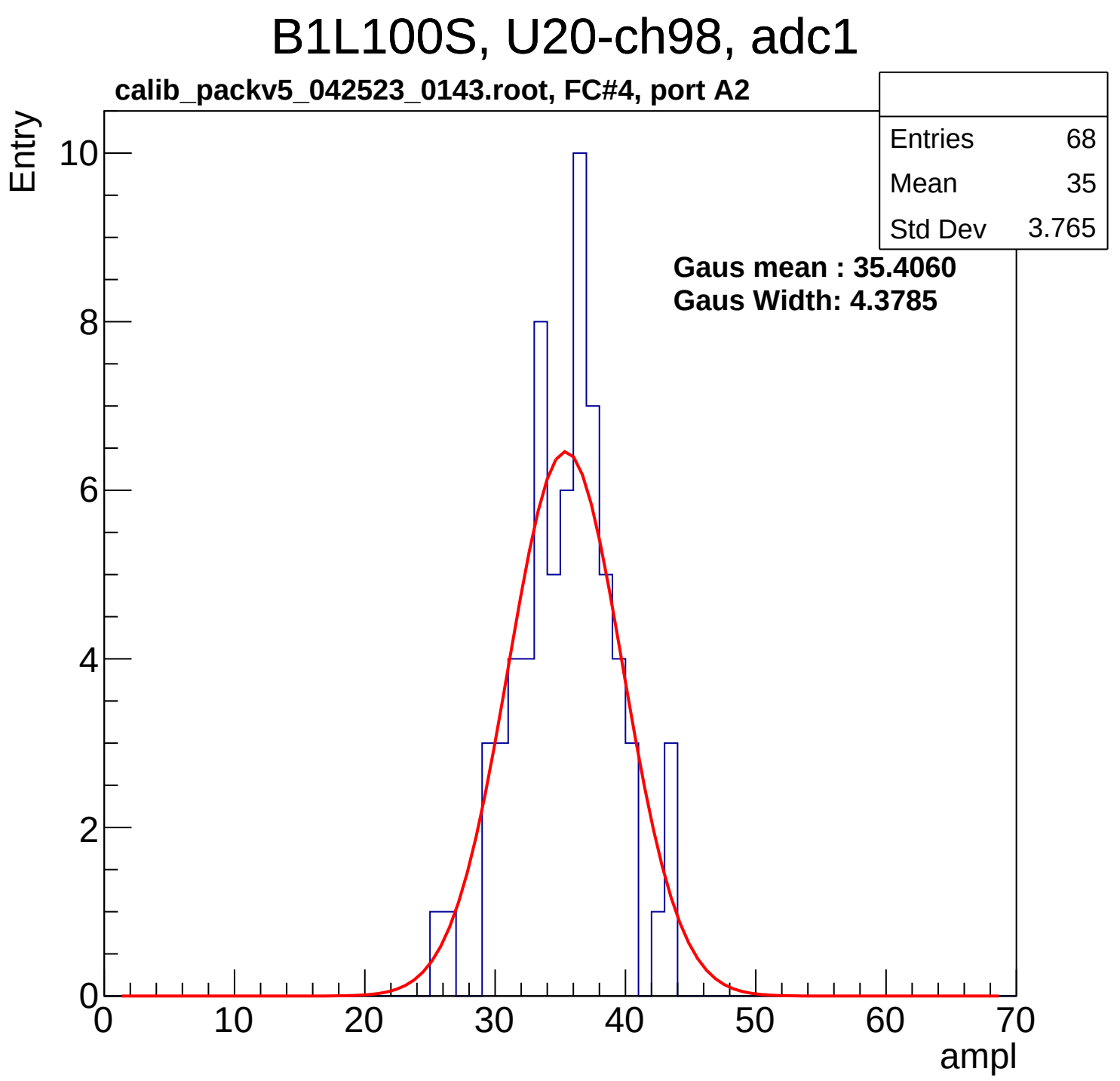
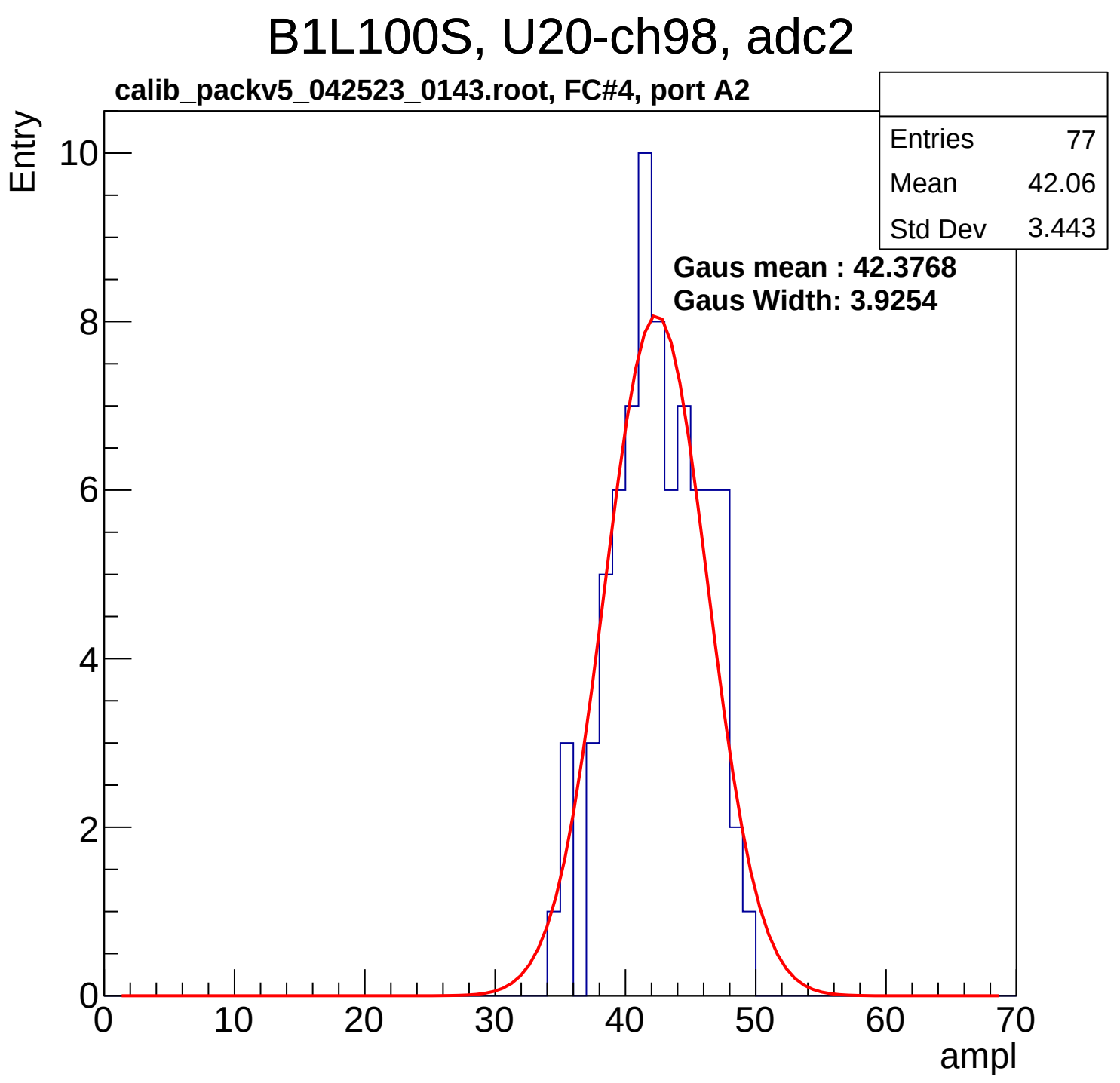
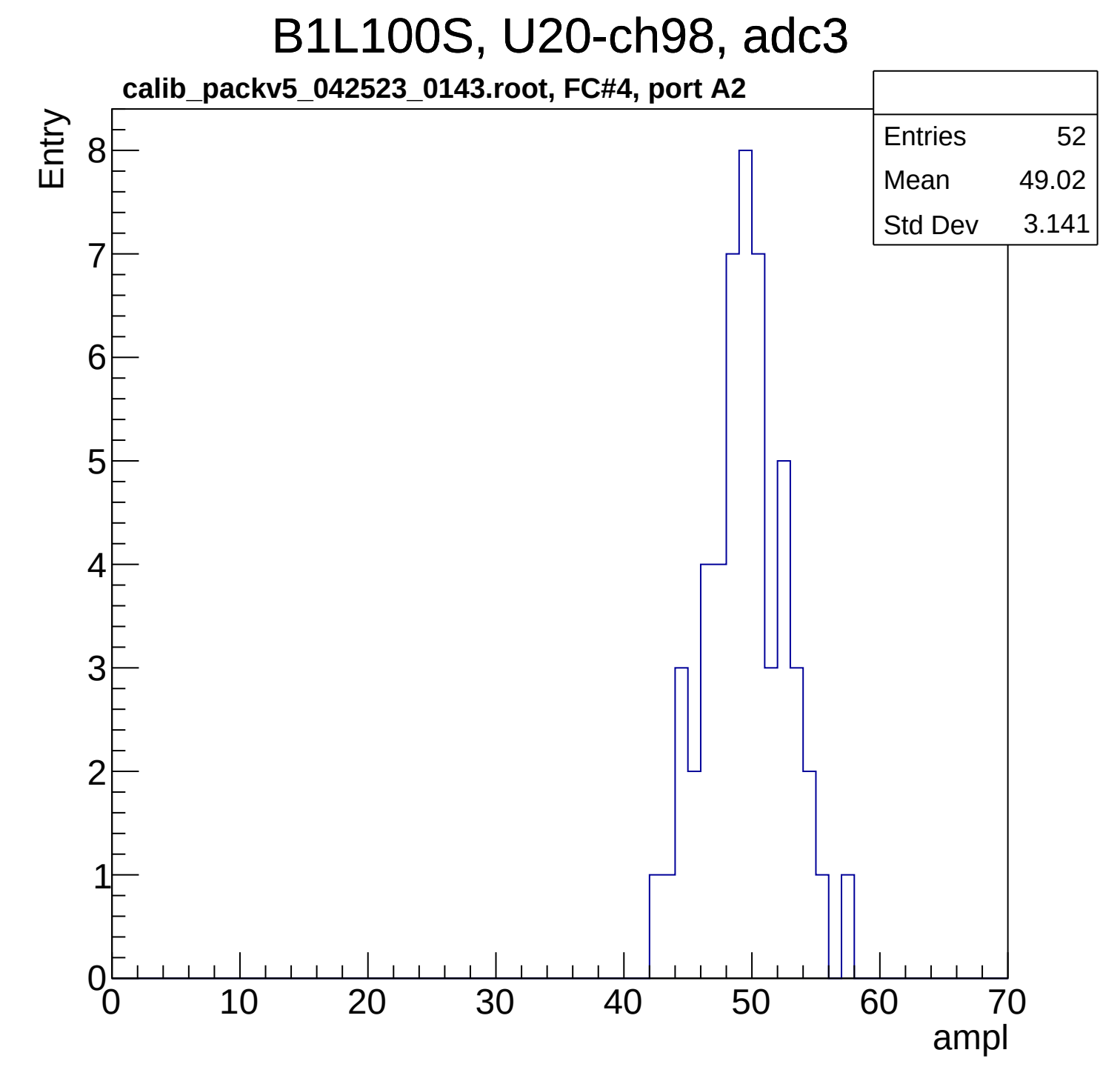
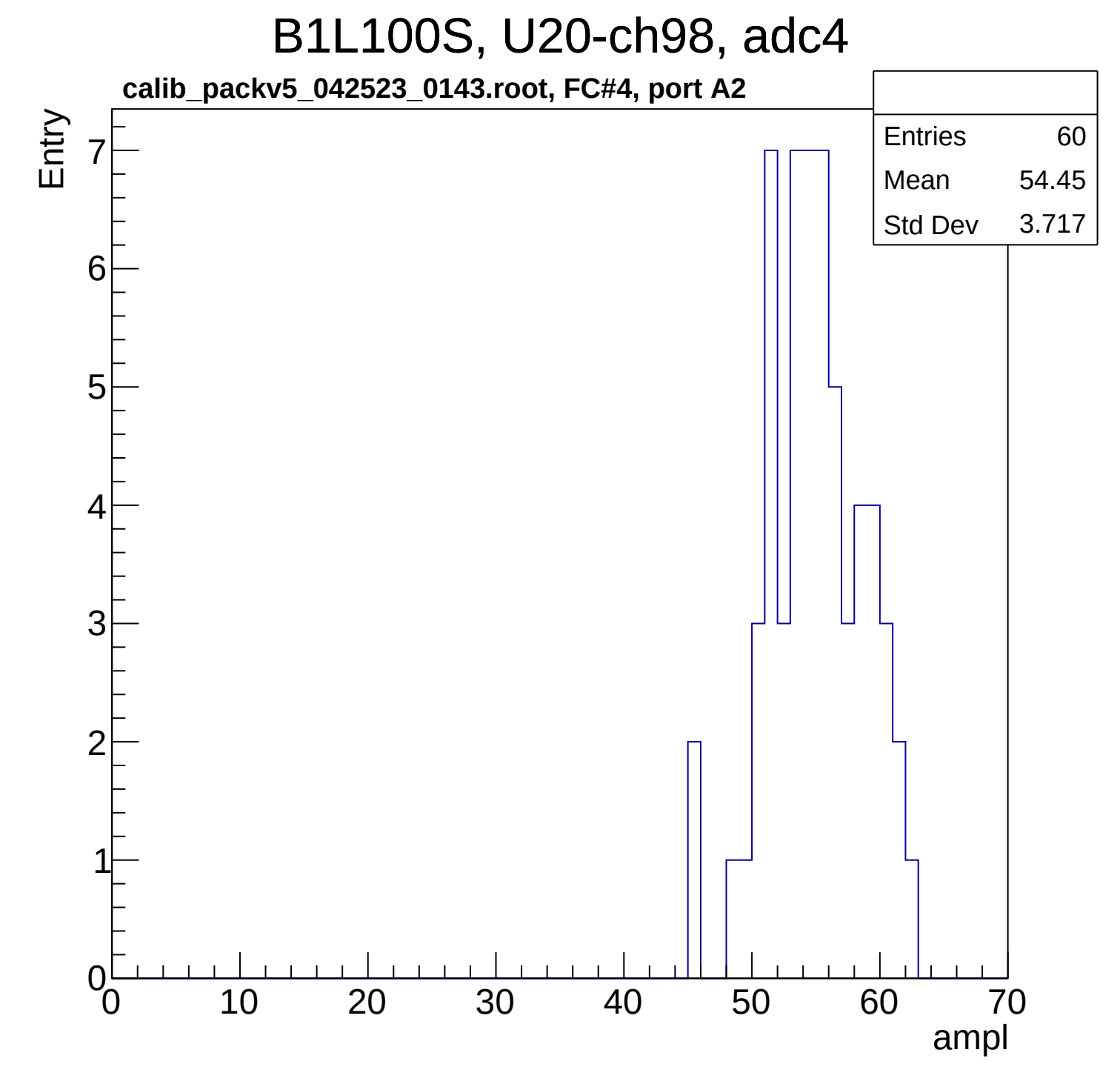
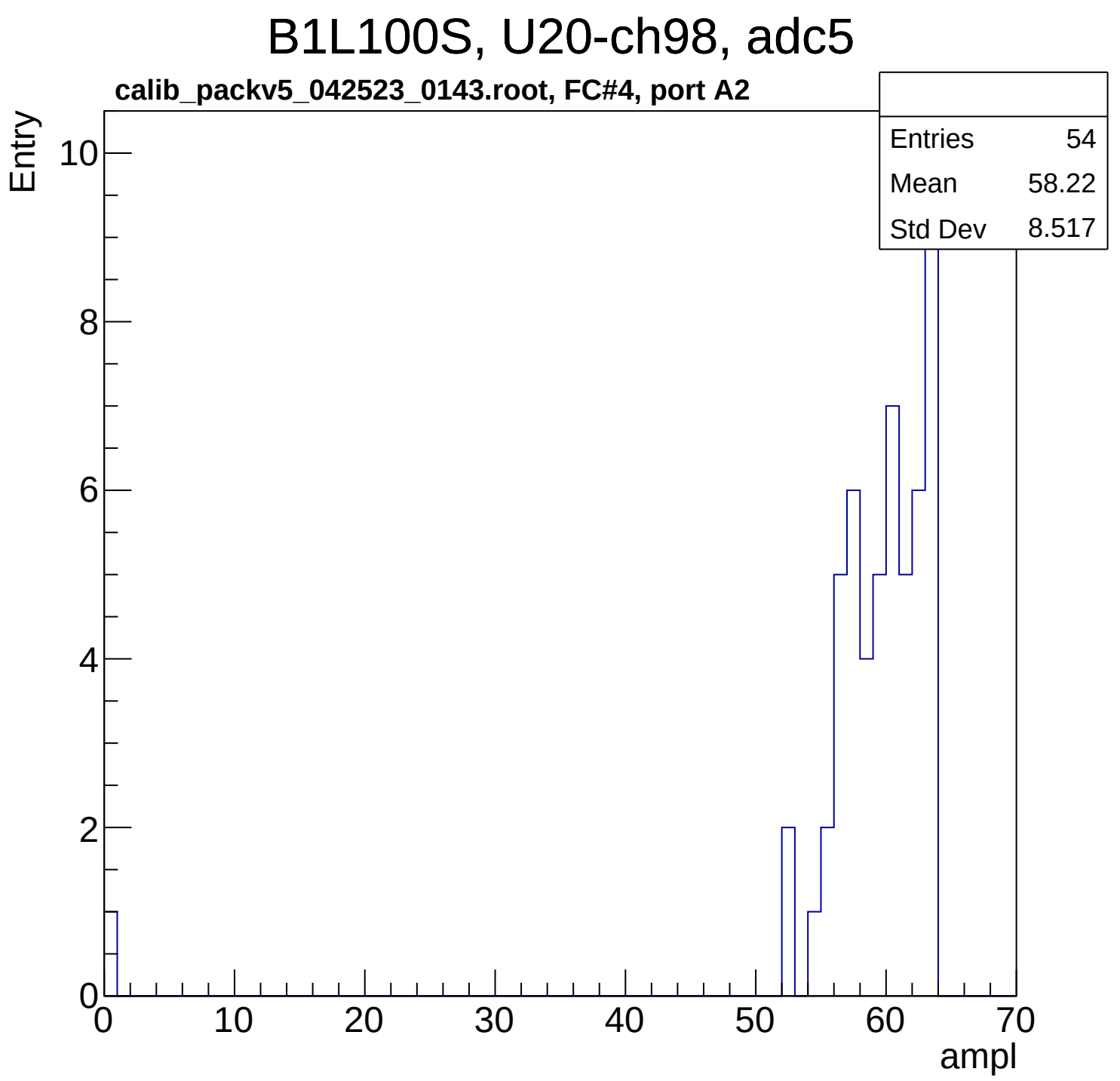

B1L100S, U20-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 27.8
Std Dev 4.78
Gaus mean : 28.9390
Gaus Width: 3.4259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

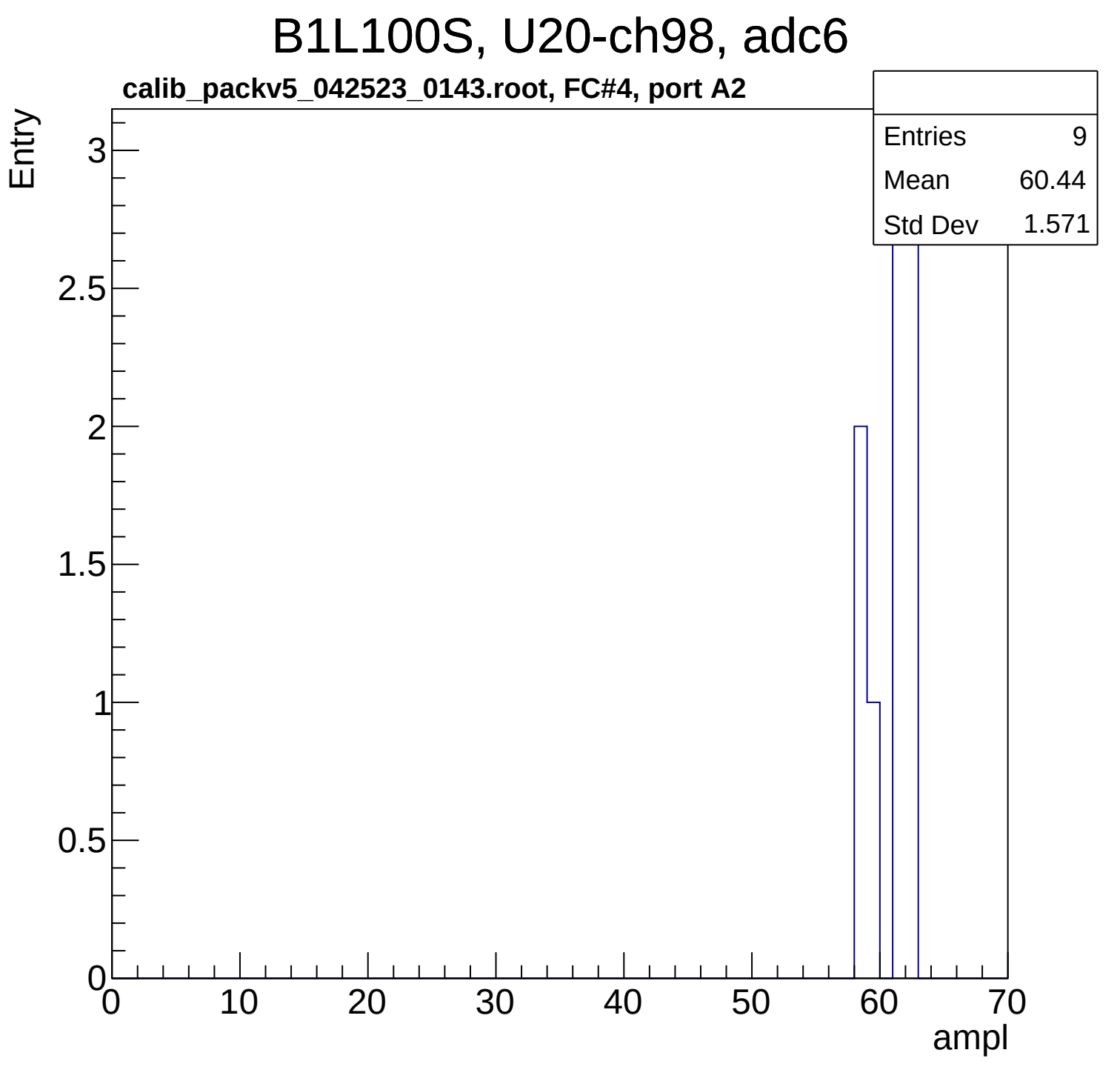

B1L100S, U20-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 35
Std Dev 3.765
Gaus mean : 35.4060
Gaus Width: 4.3785
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 42.06
Std Dev 3.443
Gaus mean : 42.3768
Gaus Width: 3.9254
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch98, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 49.02
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 54.45
Std Dev 3.717
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 58.22
Std Dev 8.517
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch98, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 60.44
Std Dev 1.571
Entry
ampl
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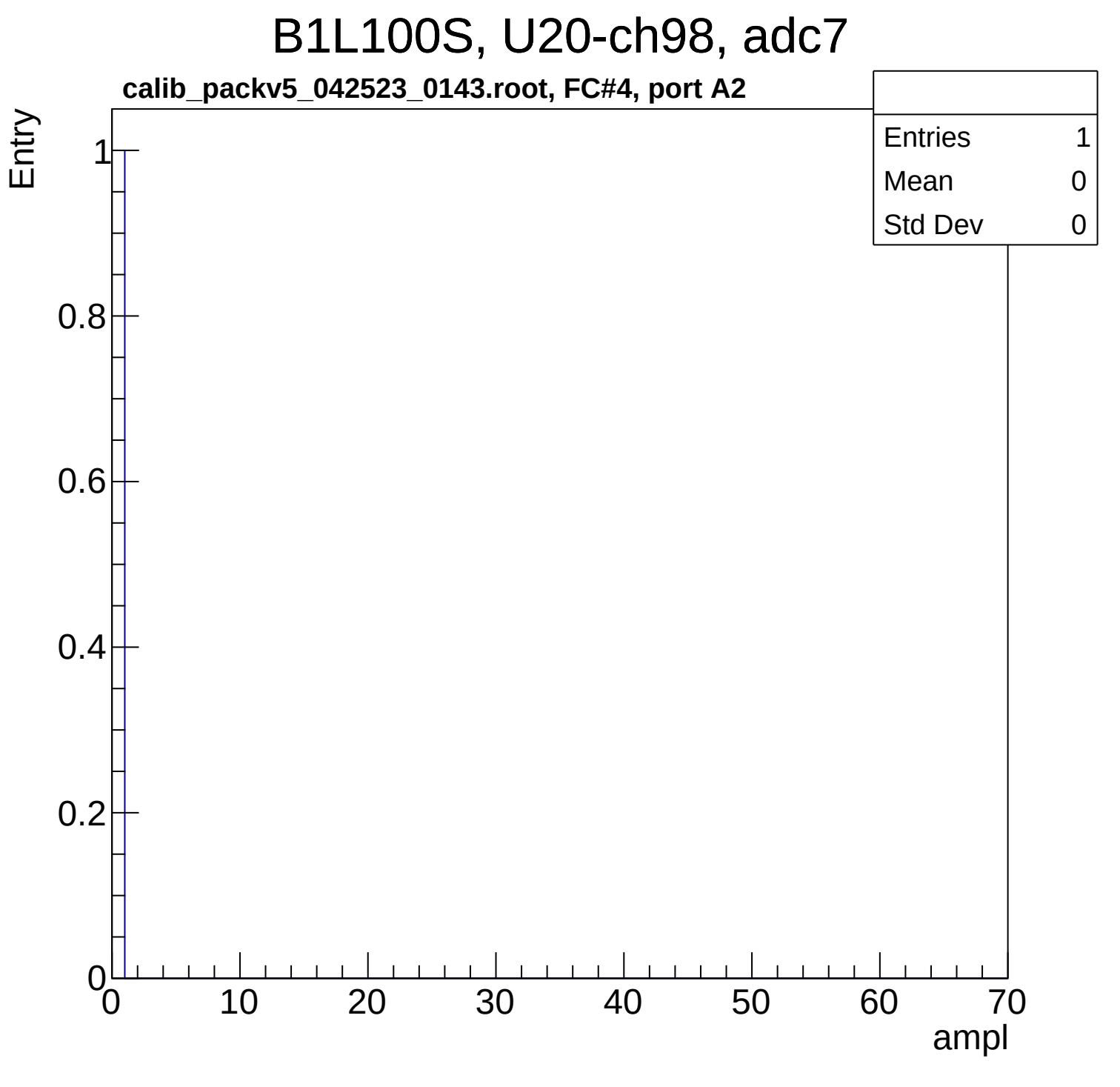
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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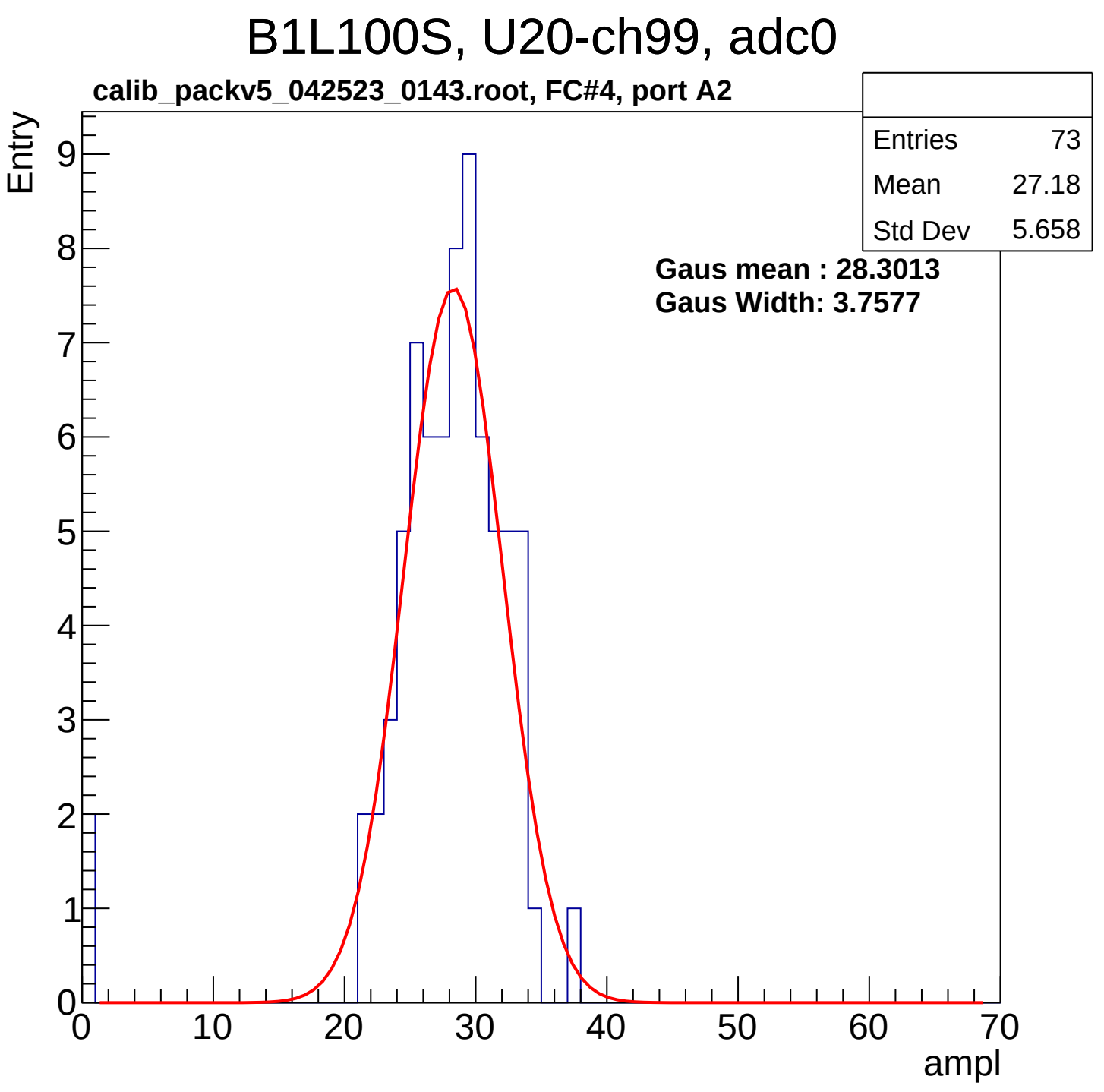
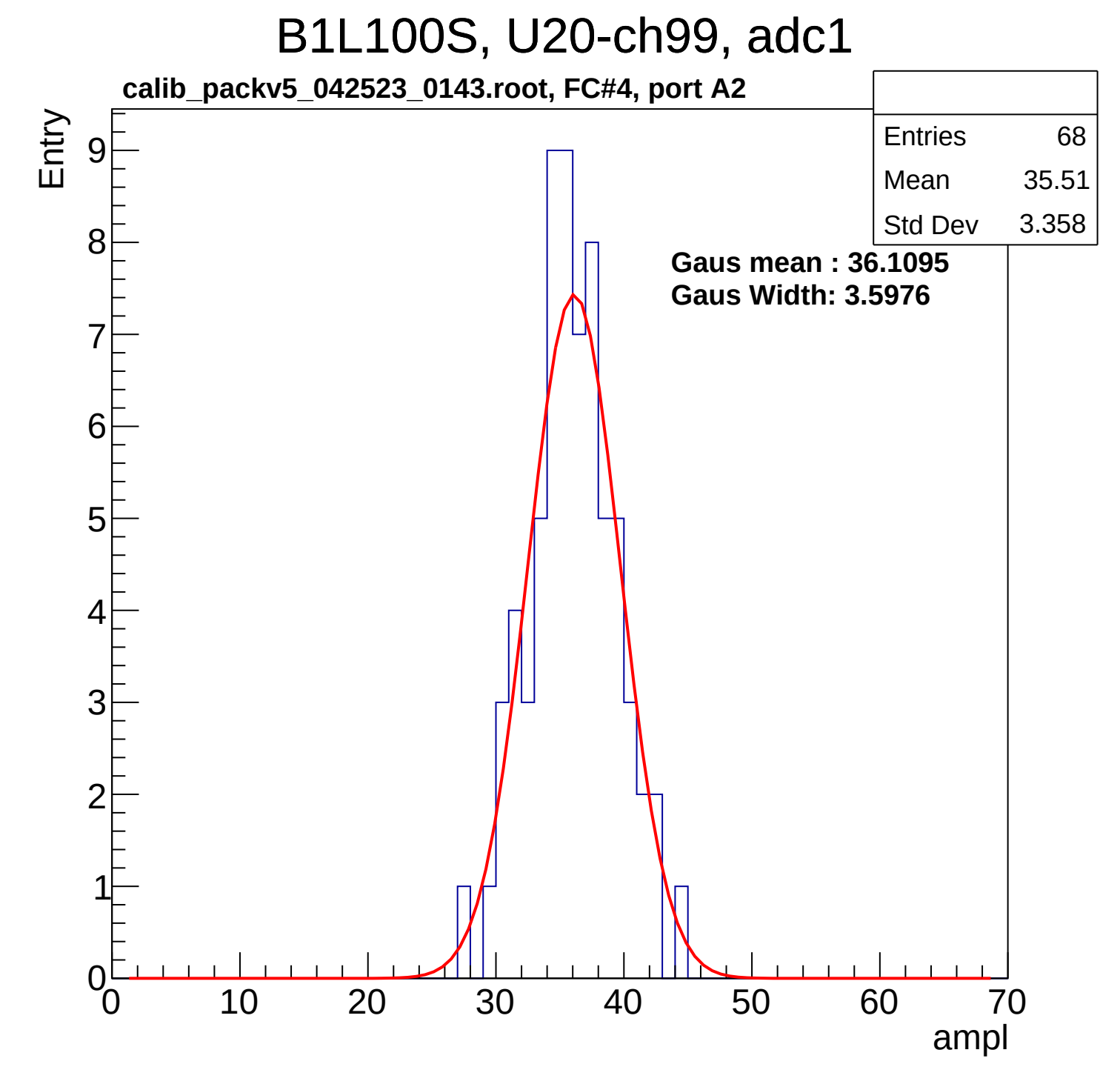
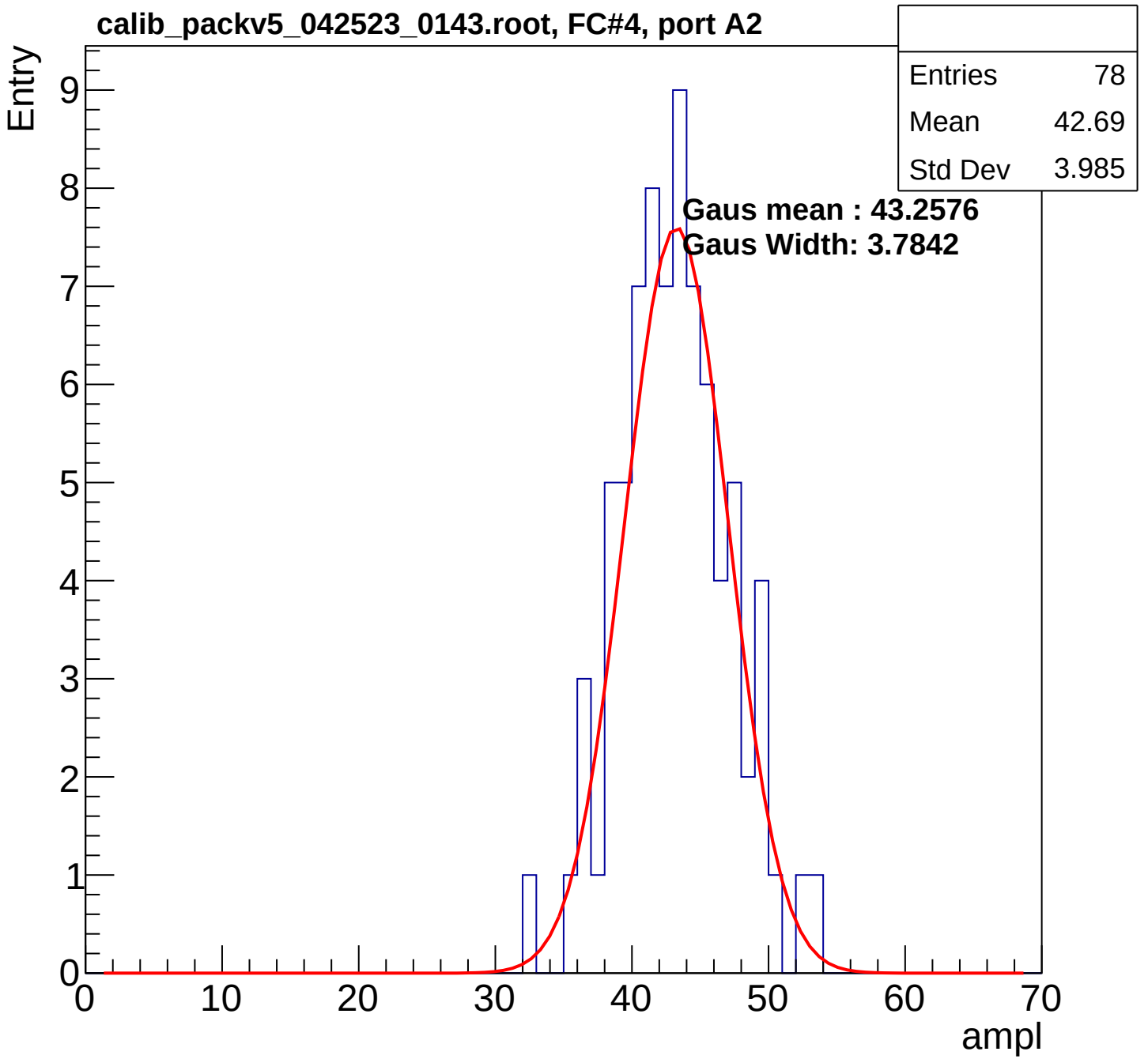
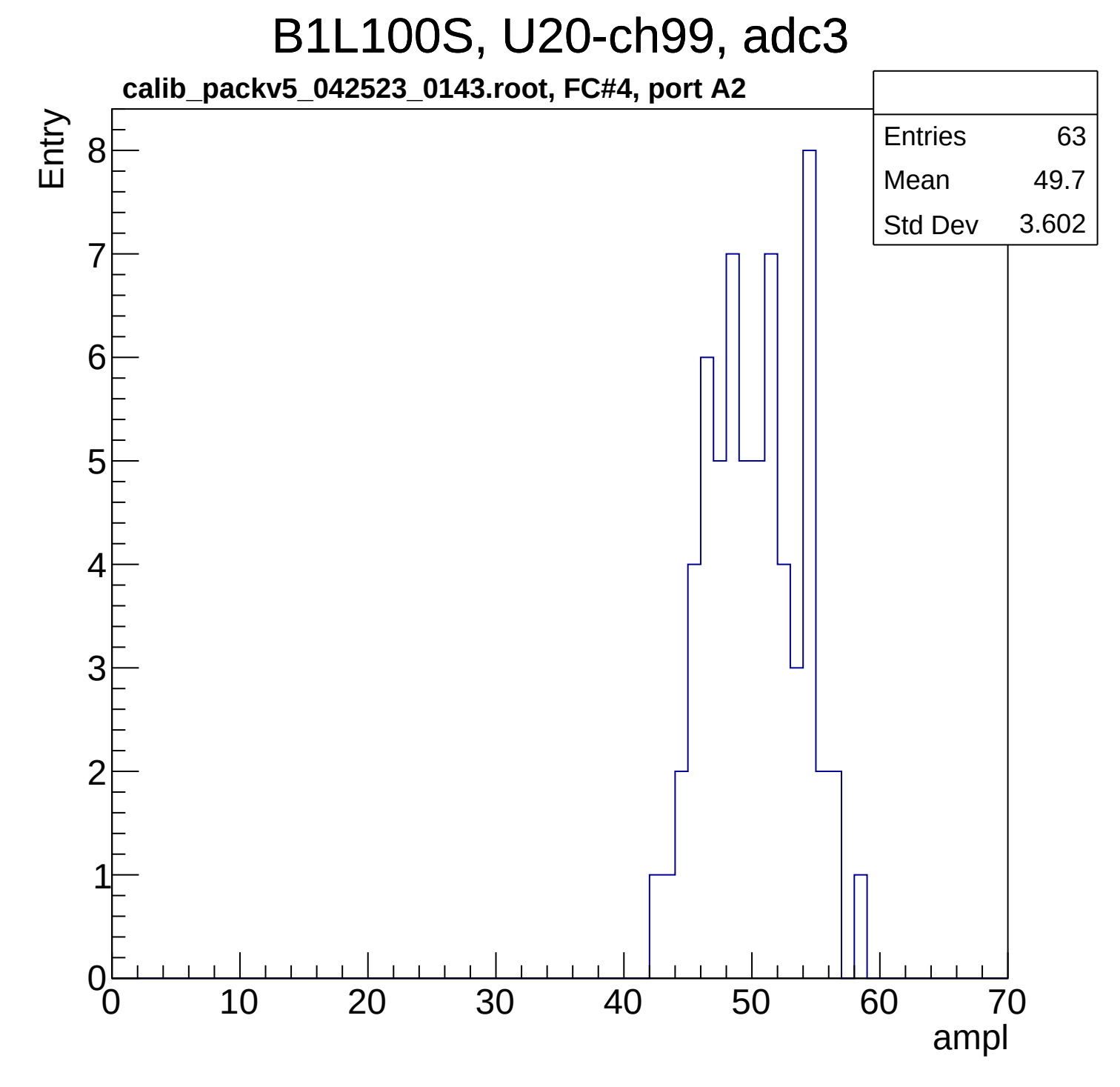
50
60
70
ampl


B1L100S, U20-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 27.18
Std Dev 5.658
Gaus mean : 28.3013
Gaus Width: 3.7577
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 35.51
Std Dev 3.358
Gaus mean : 36.1095
Gaus Width: 3.5976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 42.69
Std Dev 3.985
Gaus mean : 43.2576
Gaus Width: 3.7842
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch99, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.7
Std Dev 3.602
Entry
ampl
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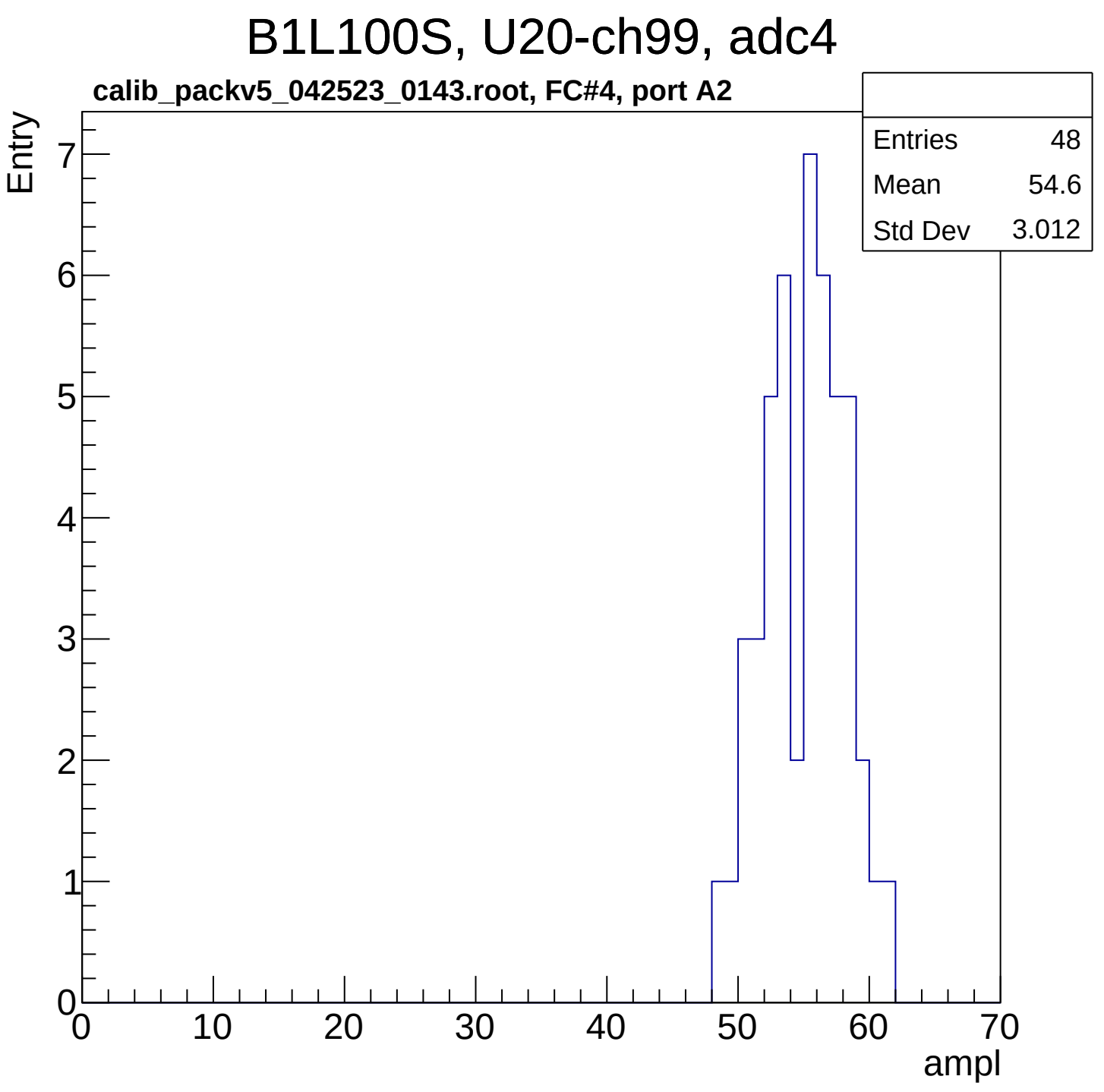
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 54.6
Std Dev 3.012
Entry
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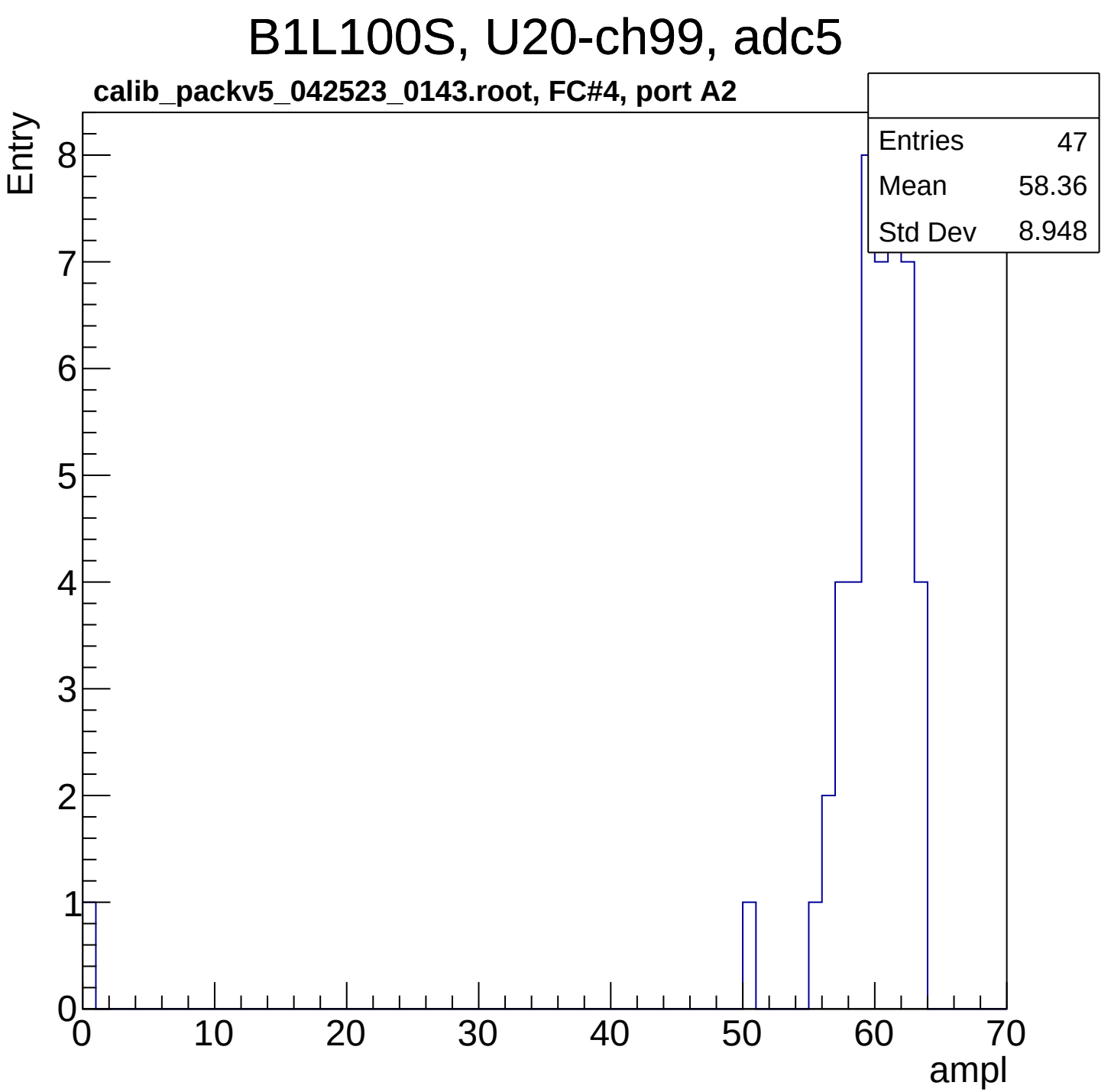
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.36
Std Dev 8.948
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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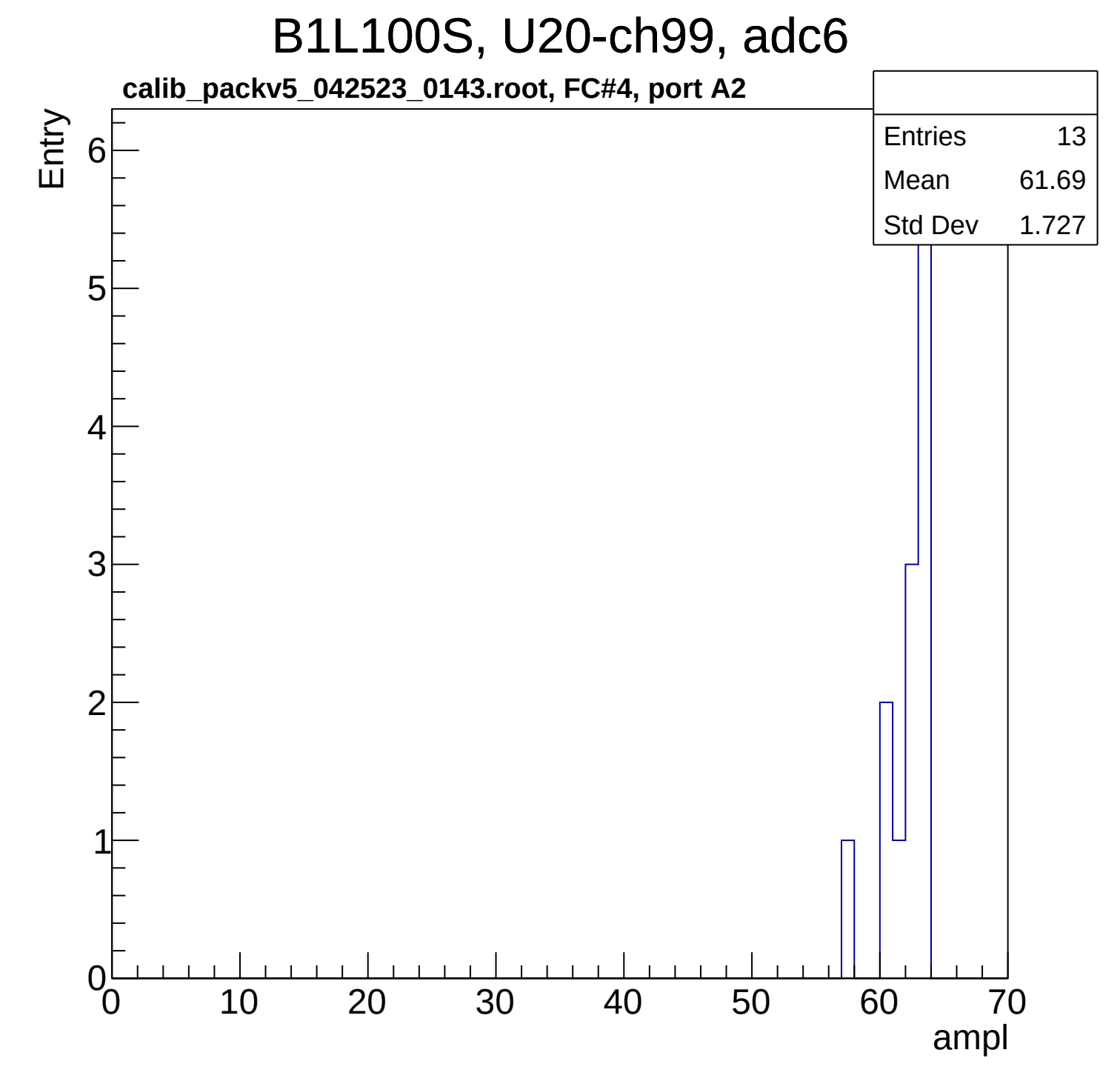
60
70


B1L100S, U20-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 61.69
Std Dev 1.727
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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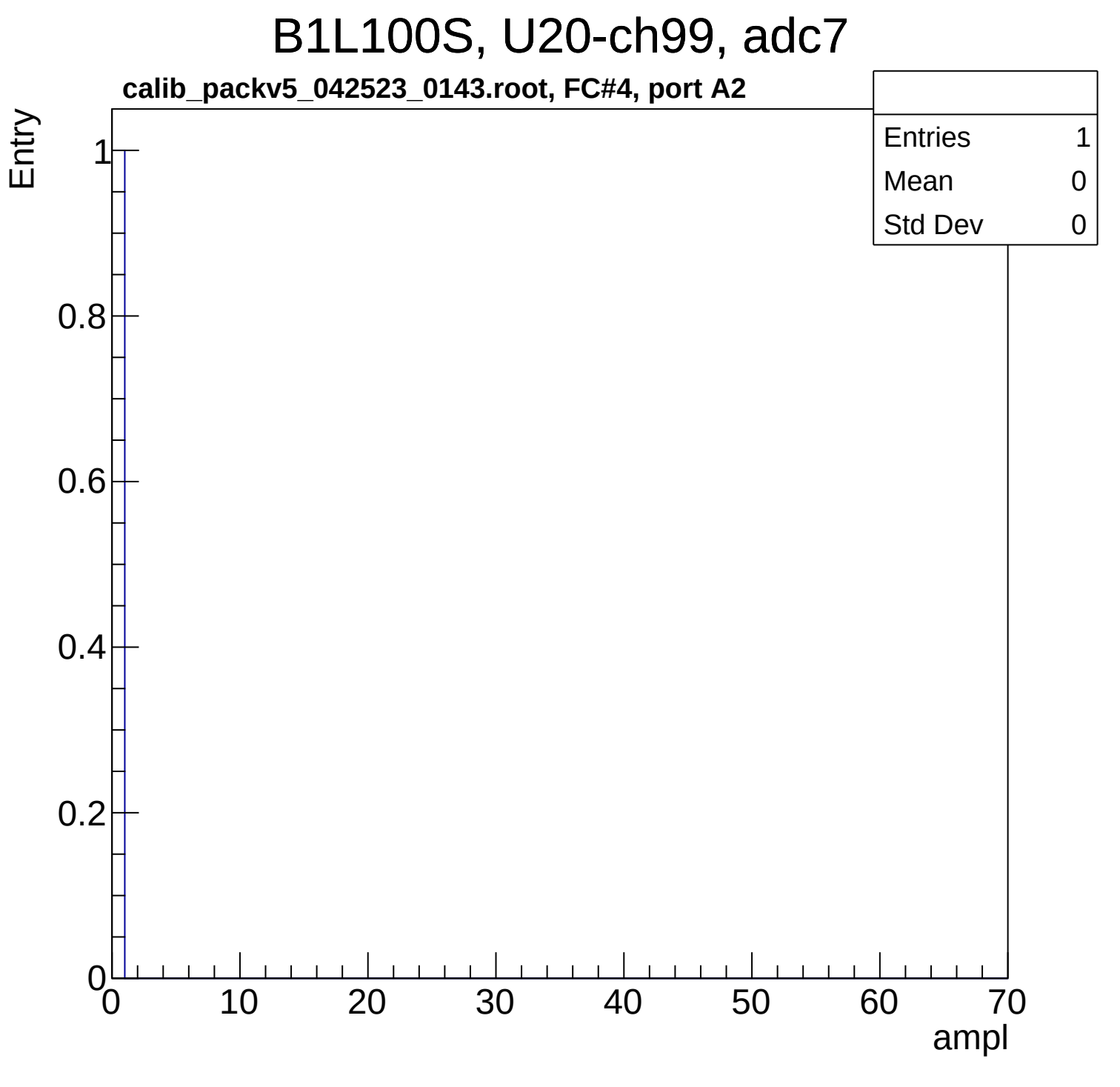
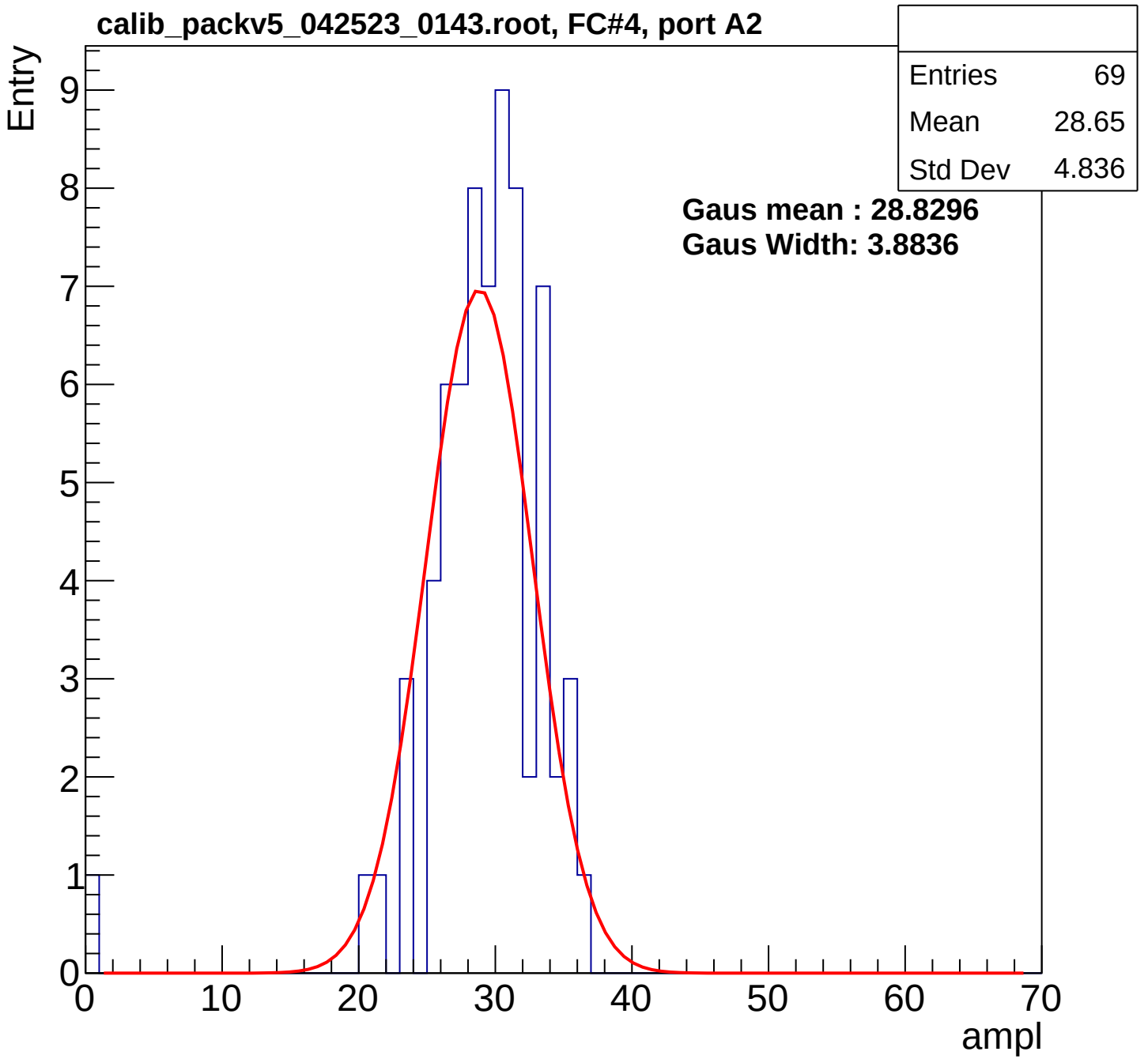
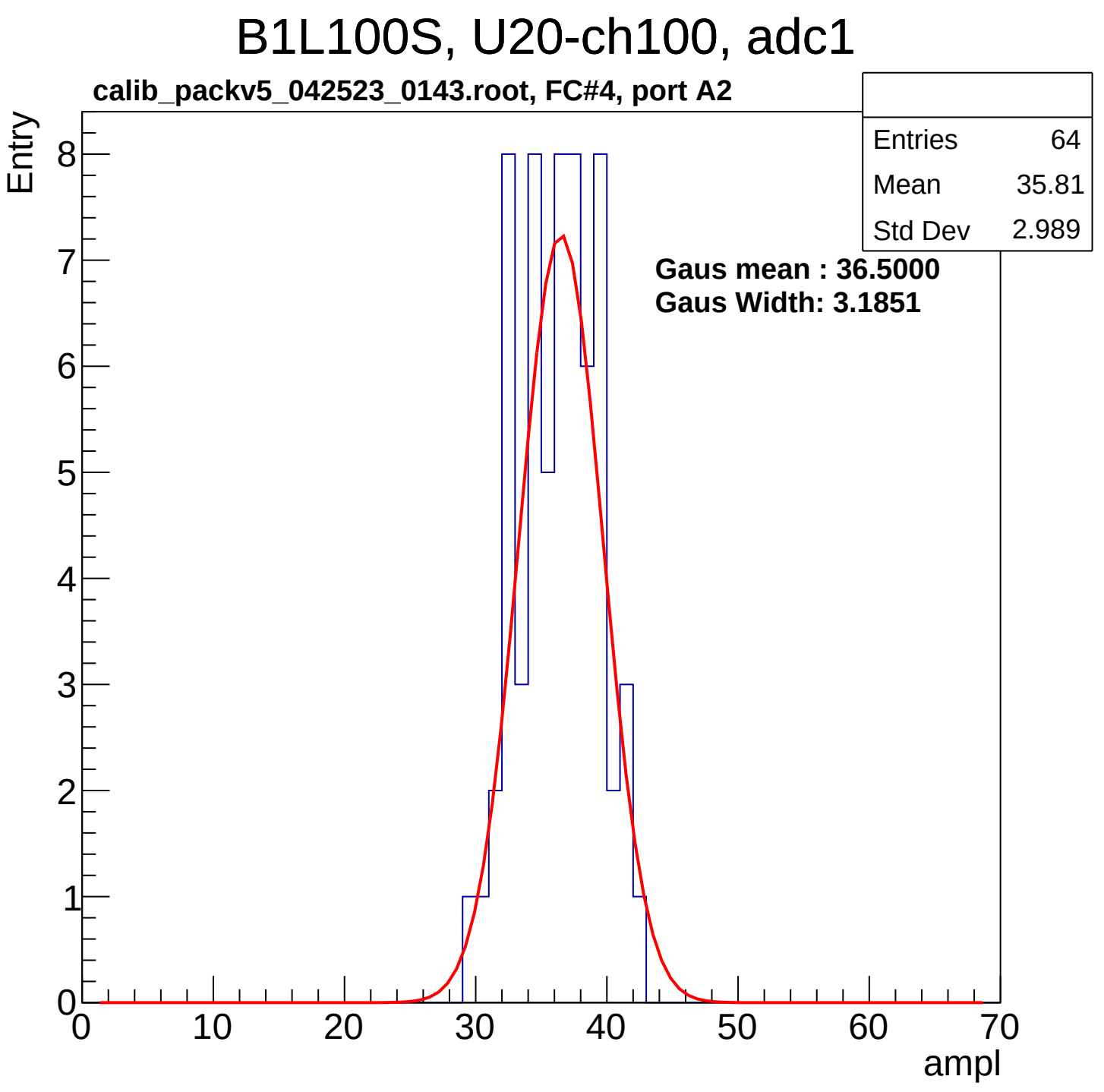

B1L100S, U20-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch100, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.65
Std Dev 4.836
Gaus mean : 28.8296
Gaus Width: 3.8836
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch100, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 35.81
Std Dev 2.989
Gaus mean : 36.5000
Gaus Width: 3.1851
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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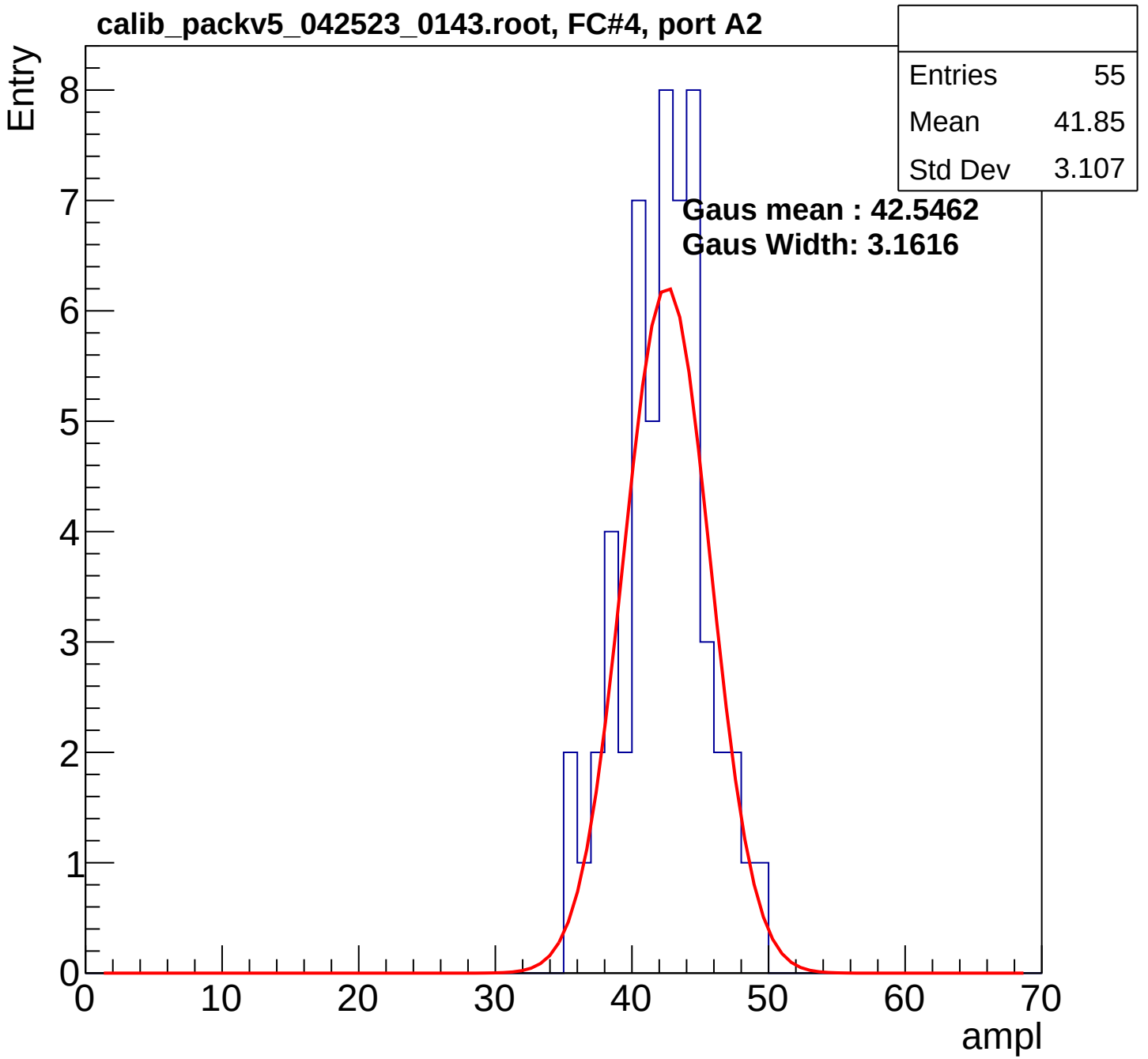
B1L100S, U20-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 41.85
Std Dev 3.107
Gaus mean : 42.5462
Gaus Width: 3.1616
Entry
ampl
0
1
2
3
4
5
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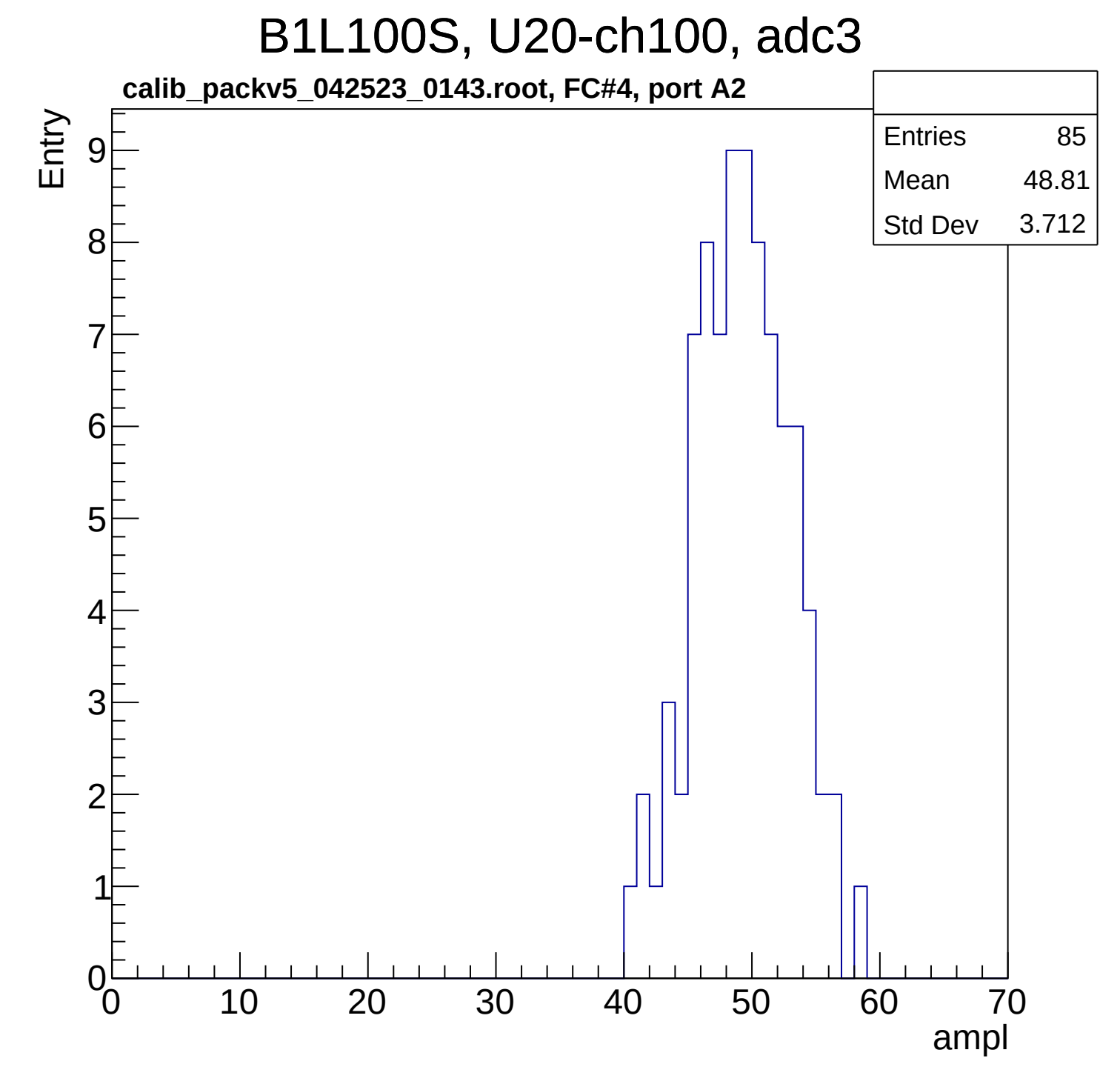
B1L100S, U20-ch100, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 48.81
Std Dev 3.712
Entry
ampl
0
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2
3
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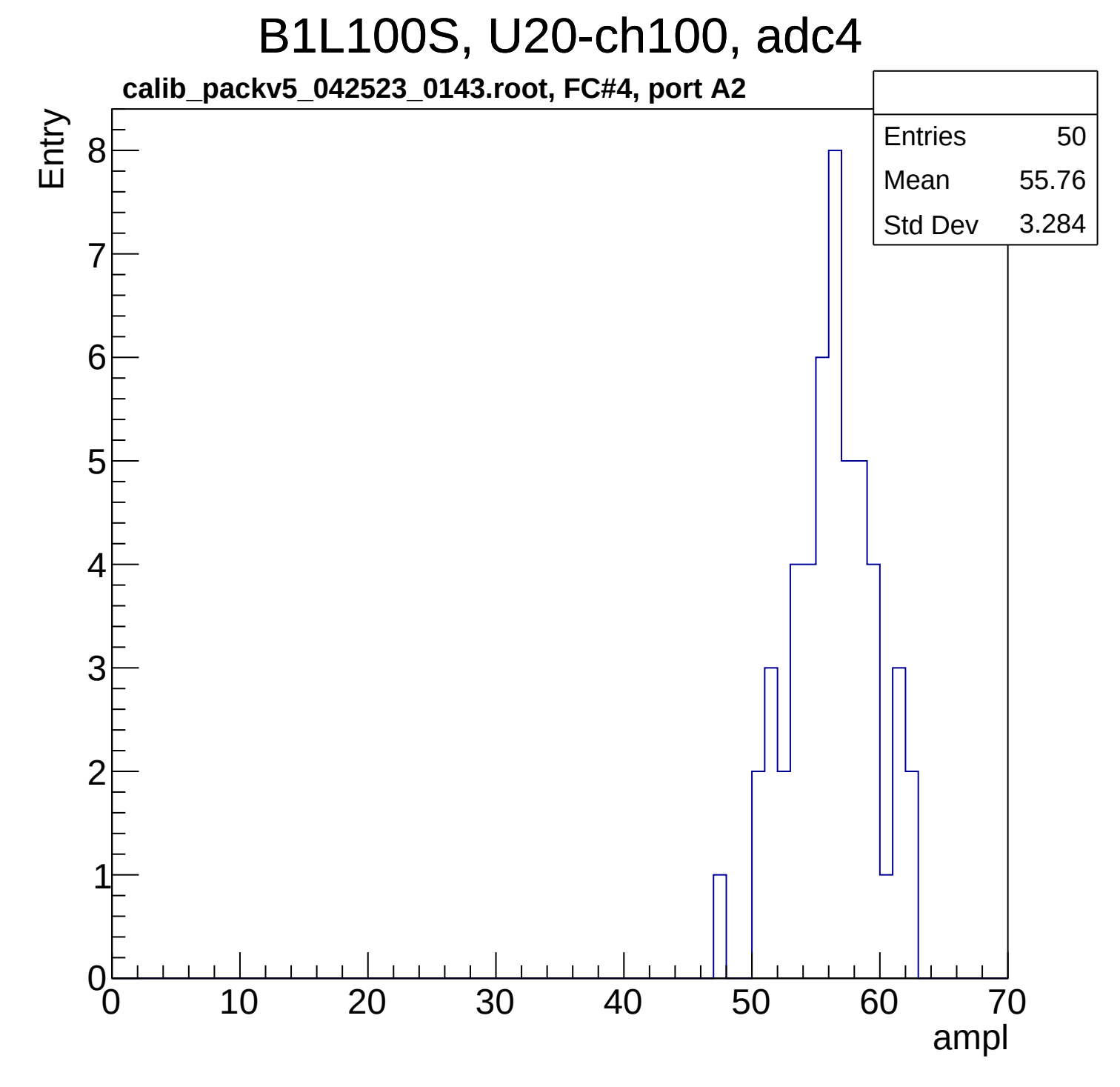
B1L100S, U20-ch100, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 55.76
Std Dev 3.284
Entry
ampl
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1
2
3
4
5
6
7
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60
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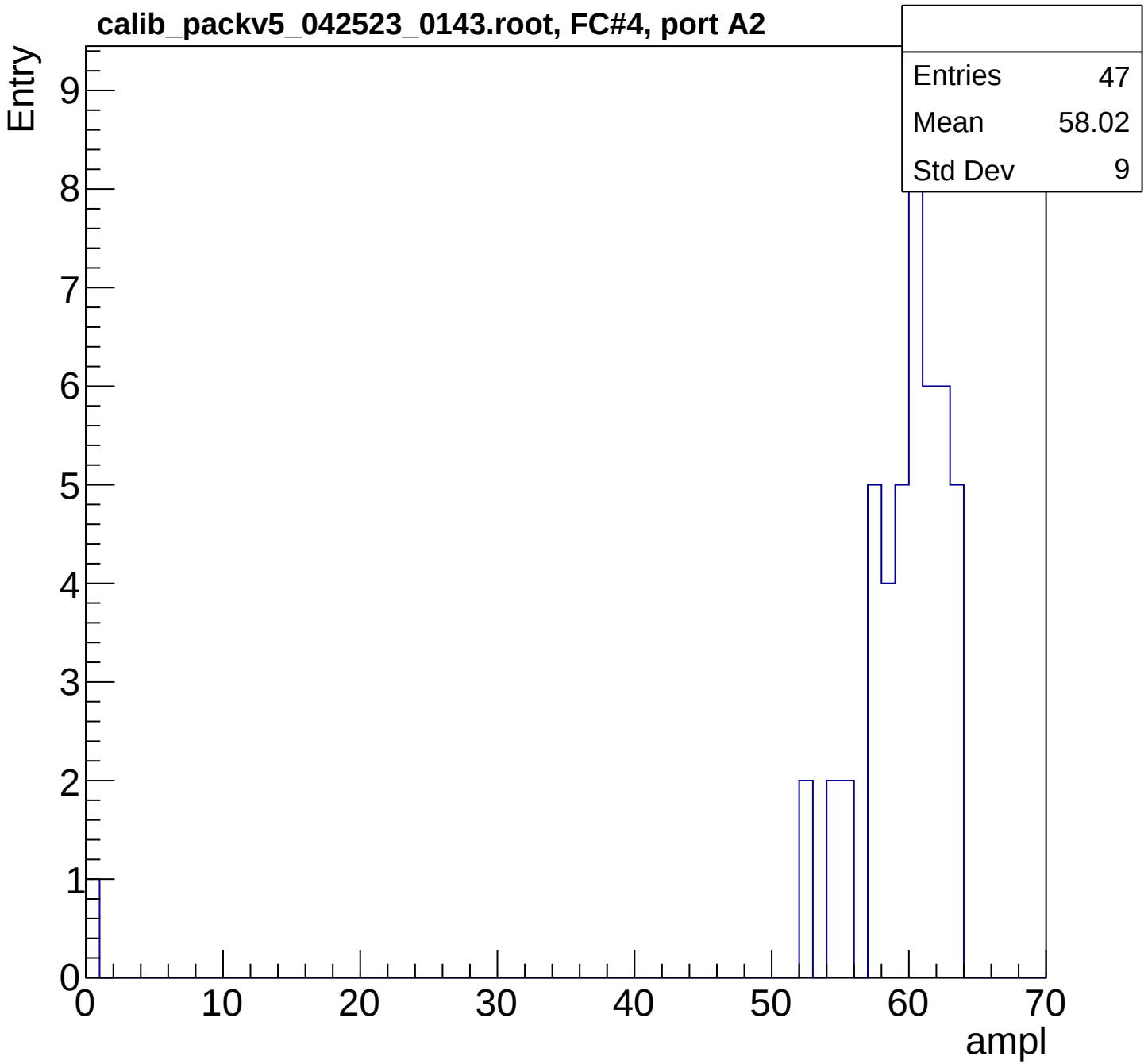
B1L100S, U20-ch100, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.02
Std Dev 9
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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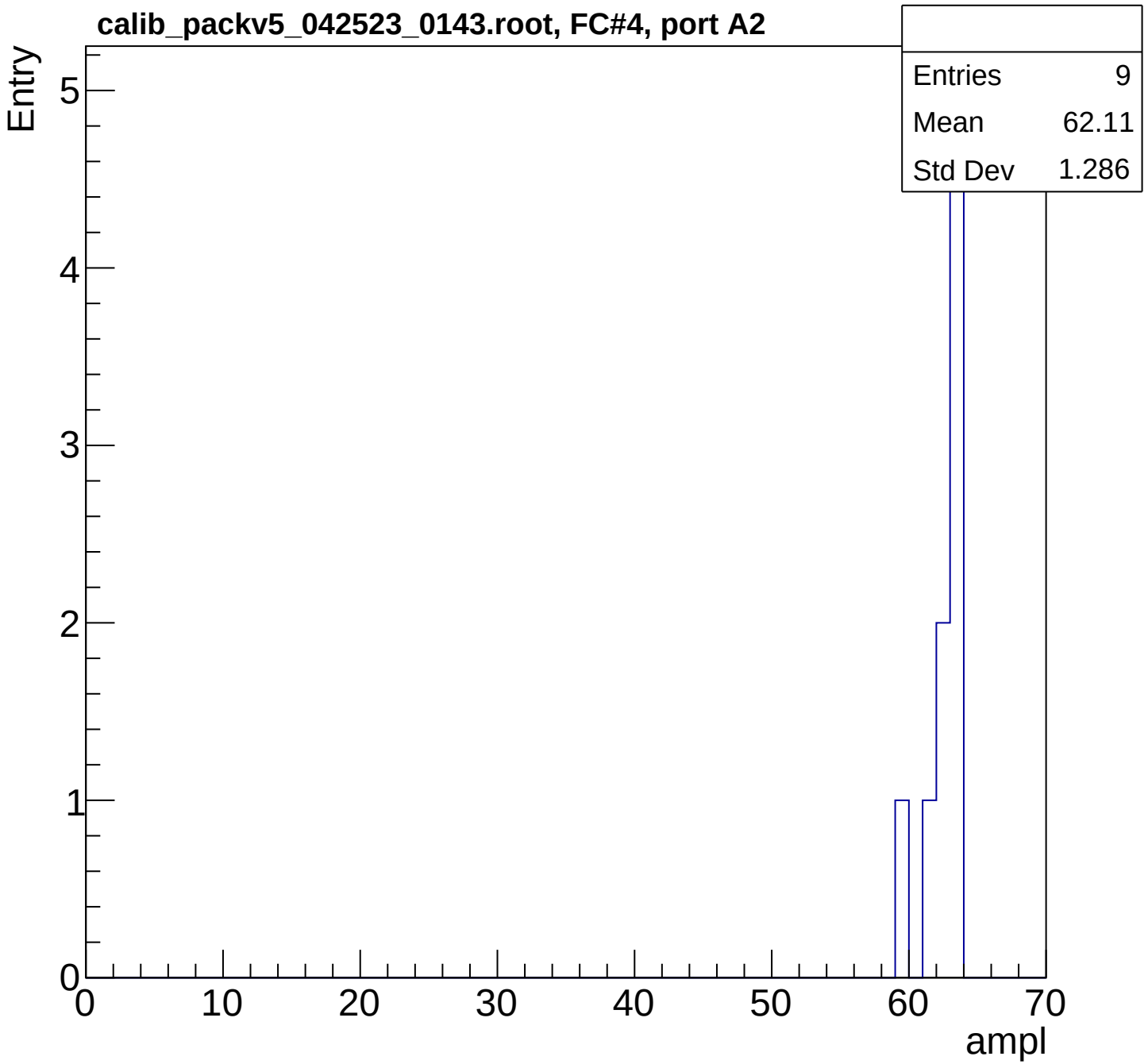
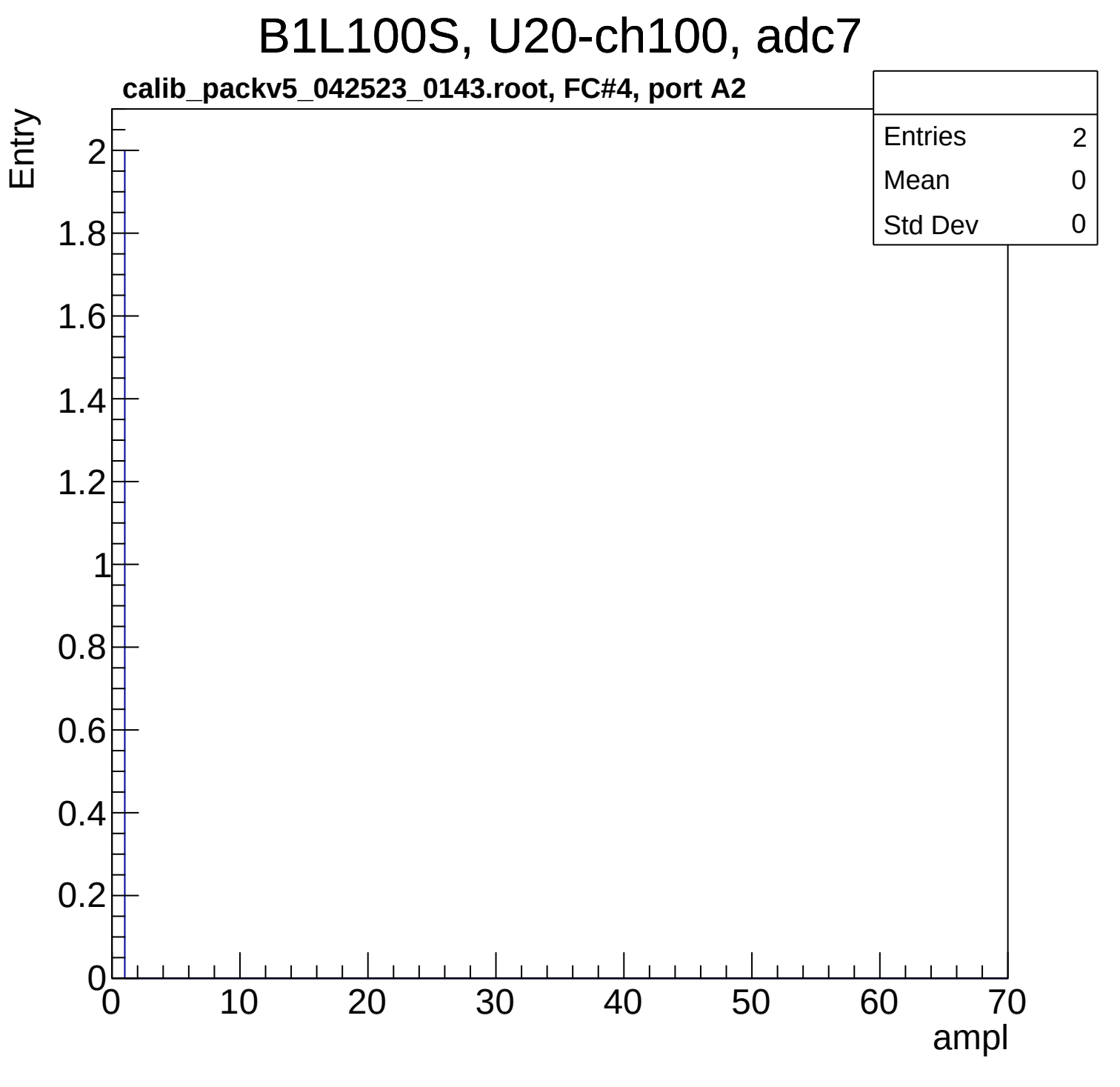
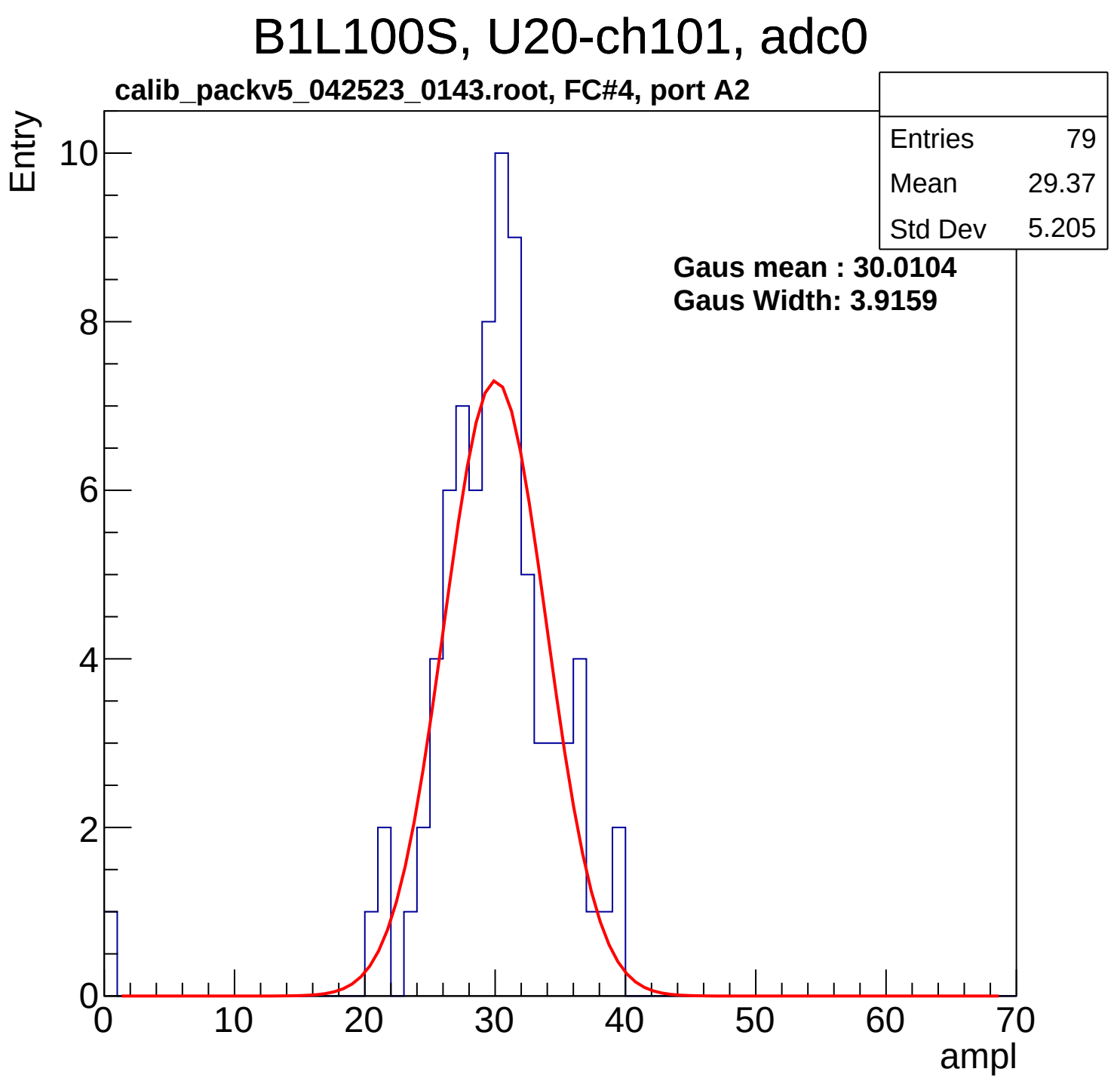

B1L100S, U20-ch100, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62.11
Std Dev 1.286
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.37
Std Dev 5.205
Gaus mean : 30.0104
Gaus Width: 3.9159
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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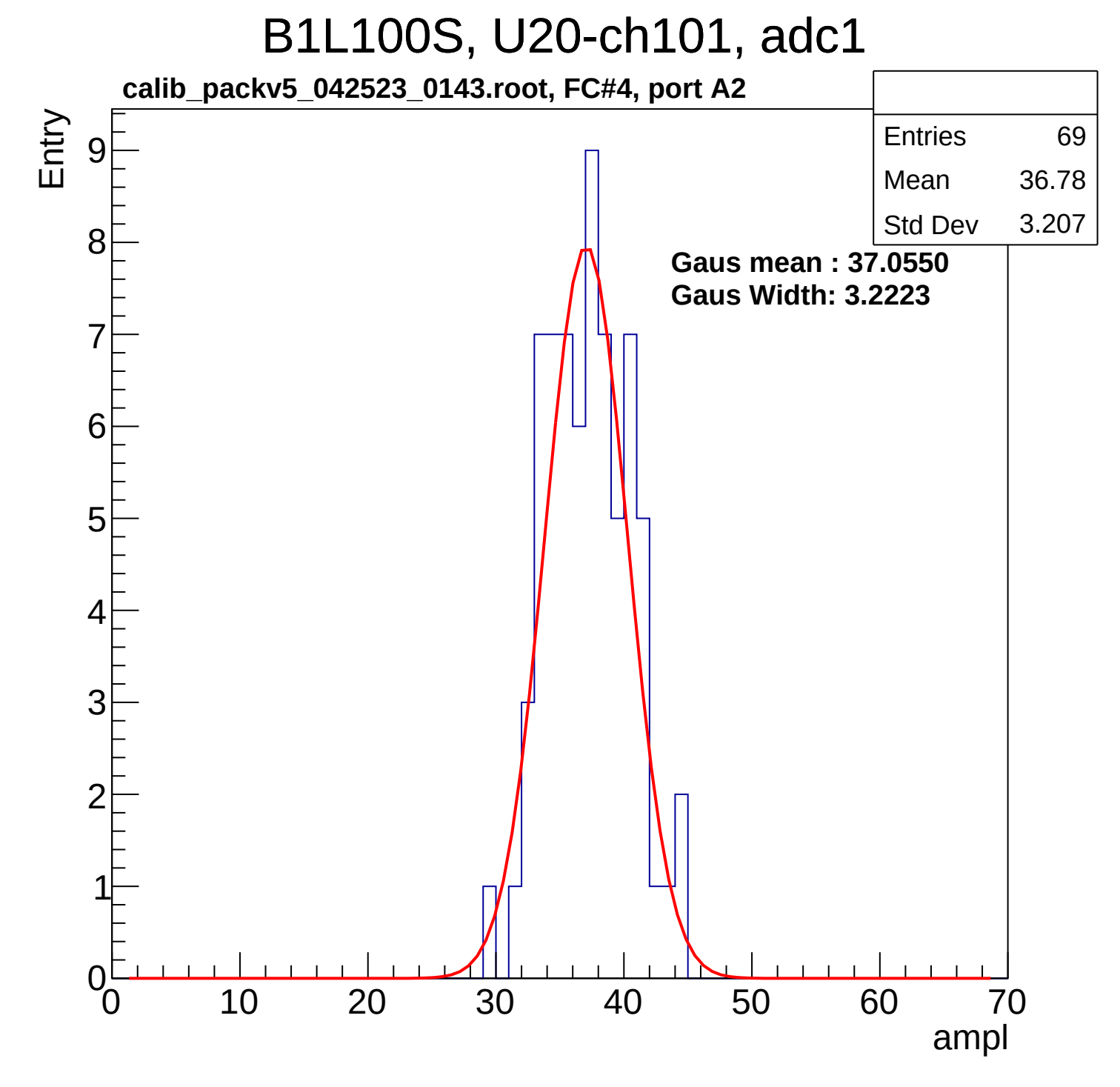
B1L100S, U20-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.78
Std Dev 3.207
Gaus mean : 37.0550
Gaus Width: 3.2223
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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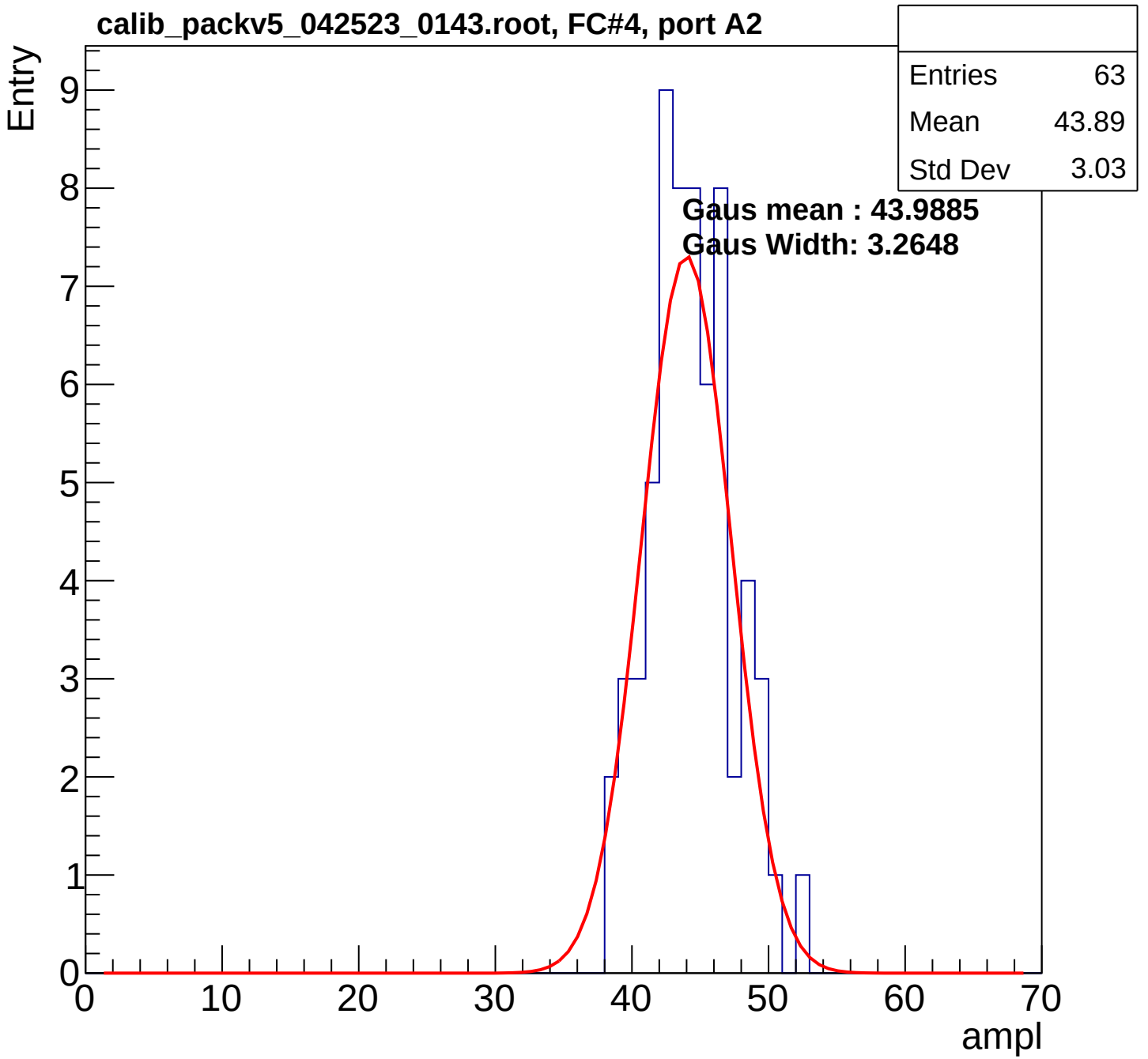
B1L100S, U20-ch101, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.89
Std Dev 3.03
Gaus mean : 43.9885
Gaus Width: 3.2648
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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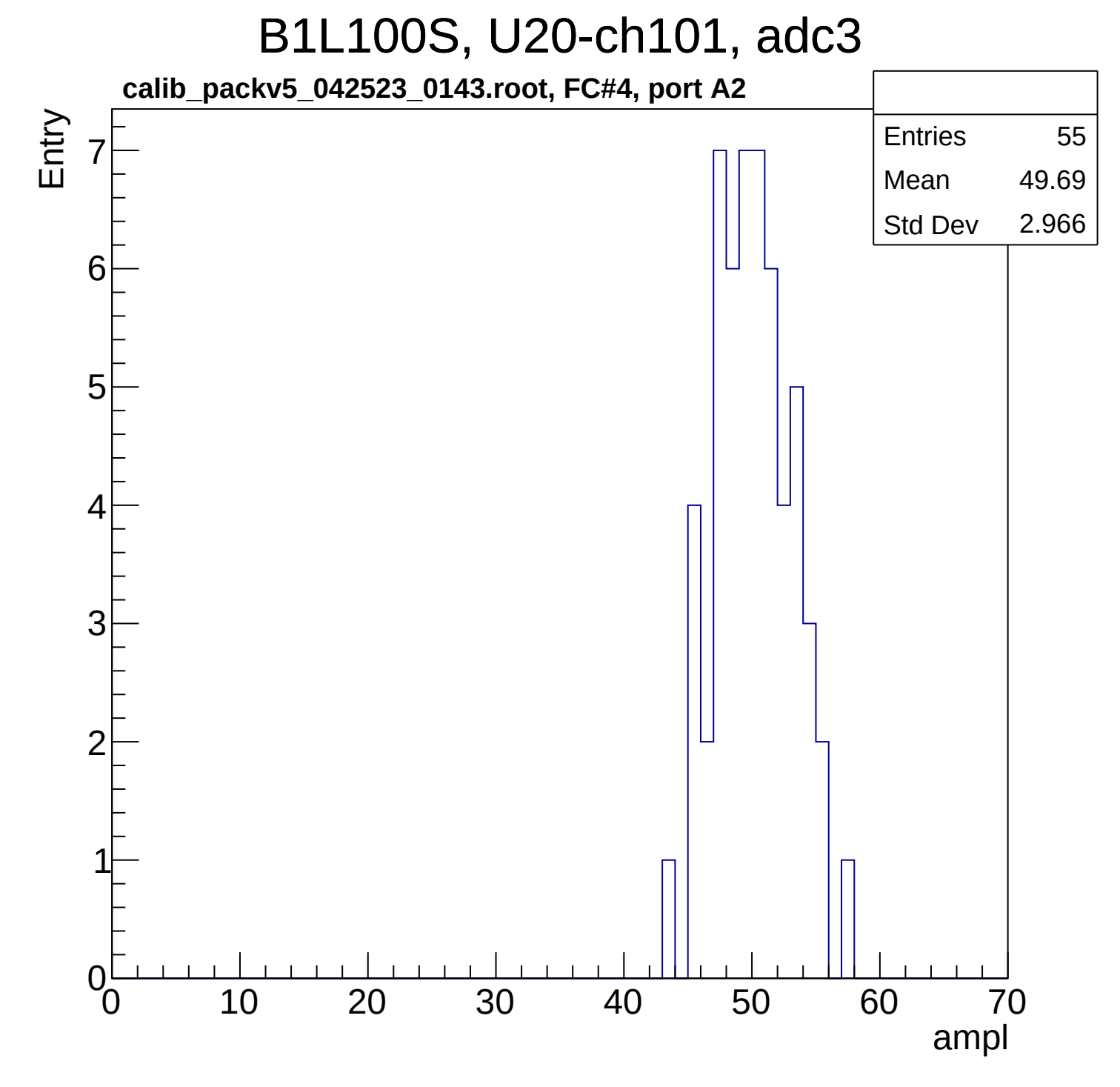
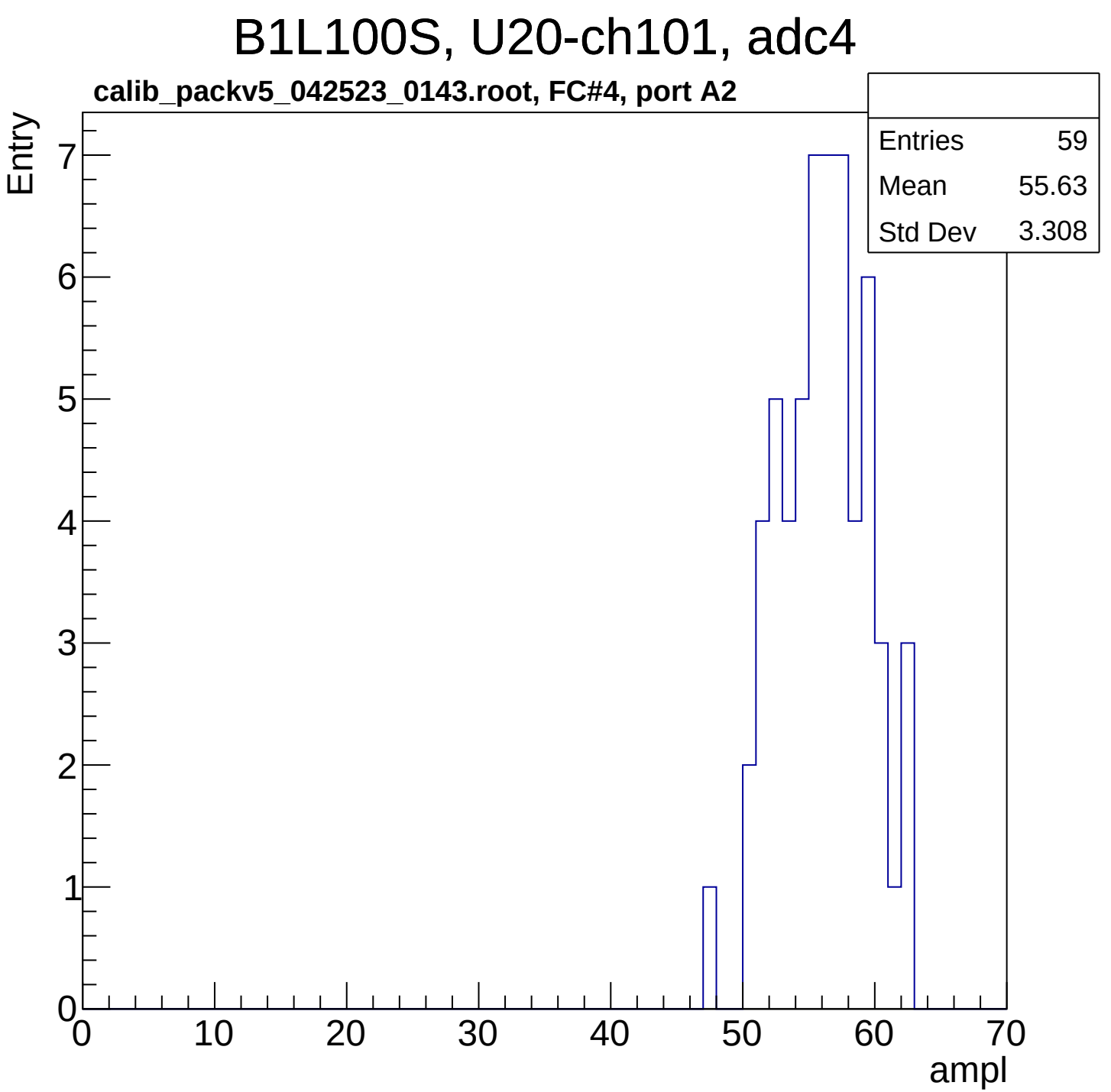

B1L100S, U20-ch101, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 49.69
Std Dev 2.966
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch101, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.63
Std Dev 3.308
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
50
60
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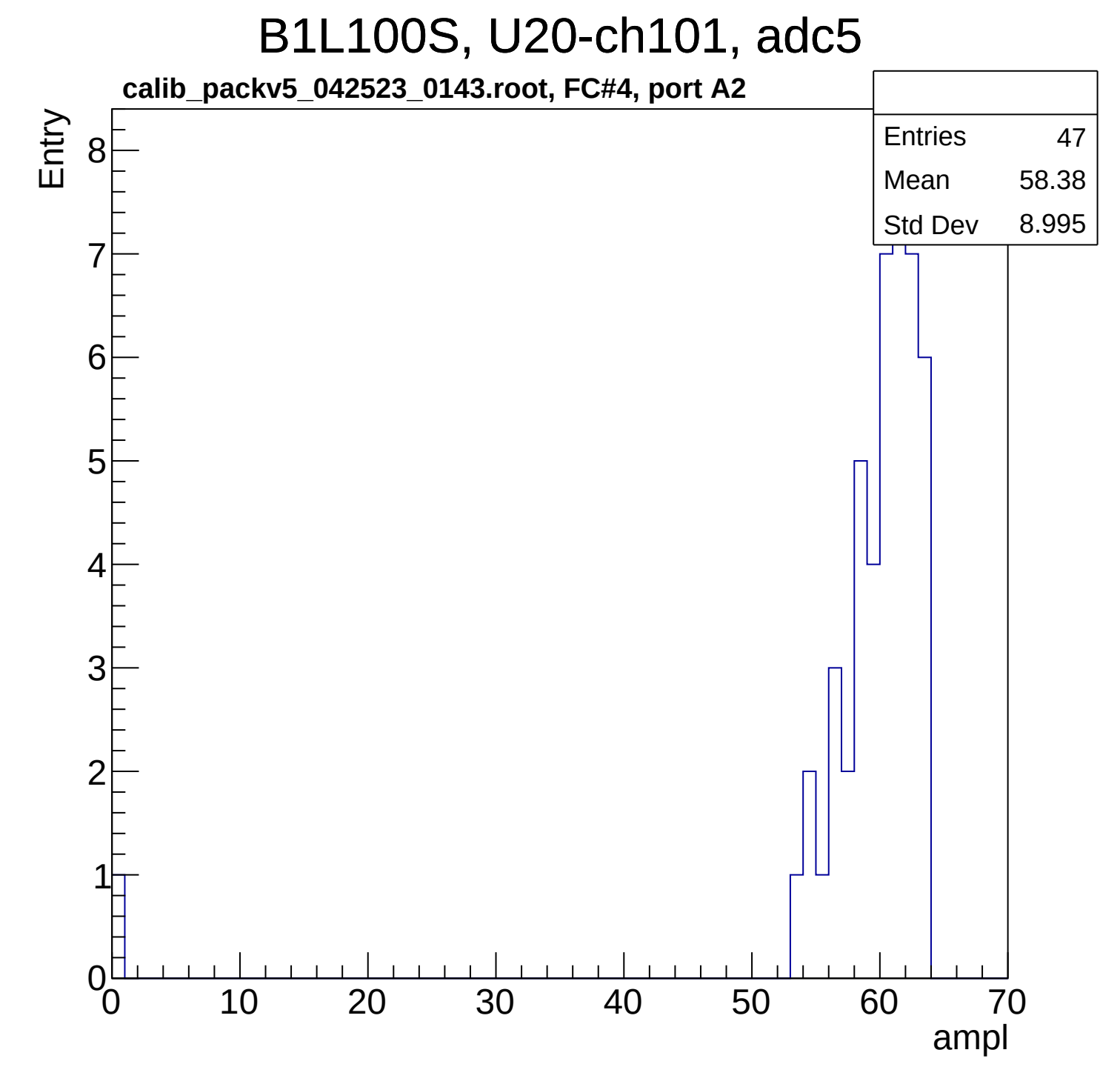
B1L100S, U20-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.38
Std Dev 8.995
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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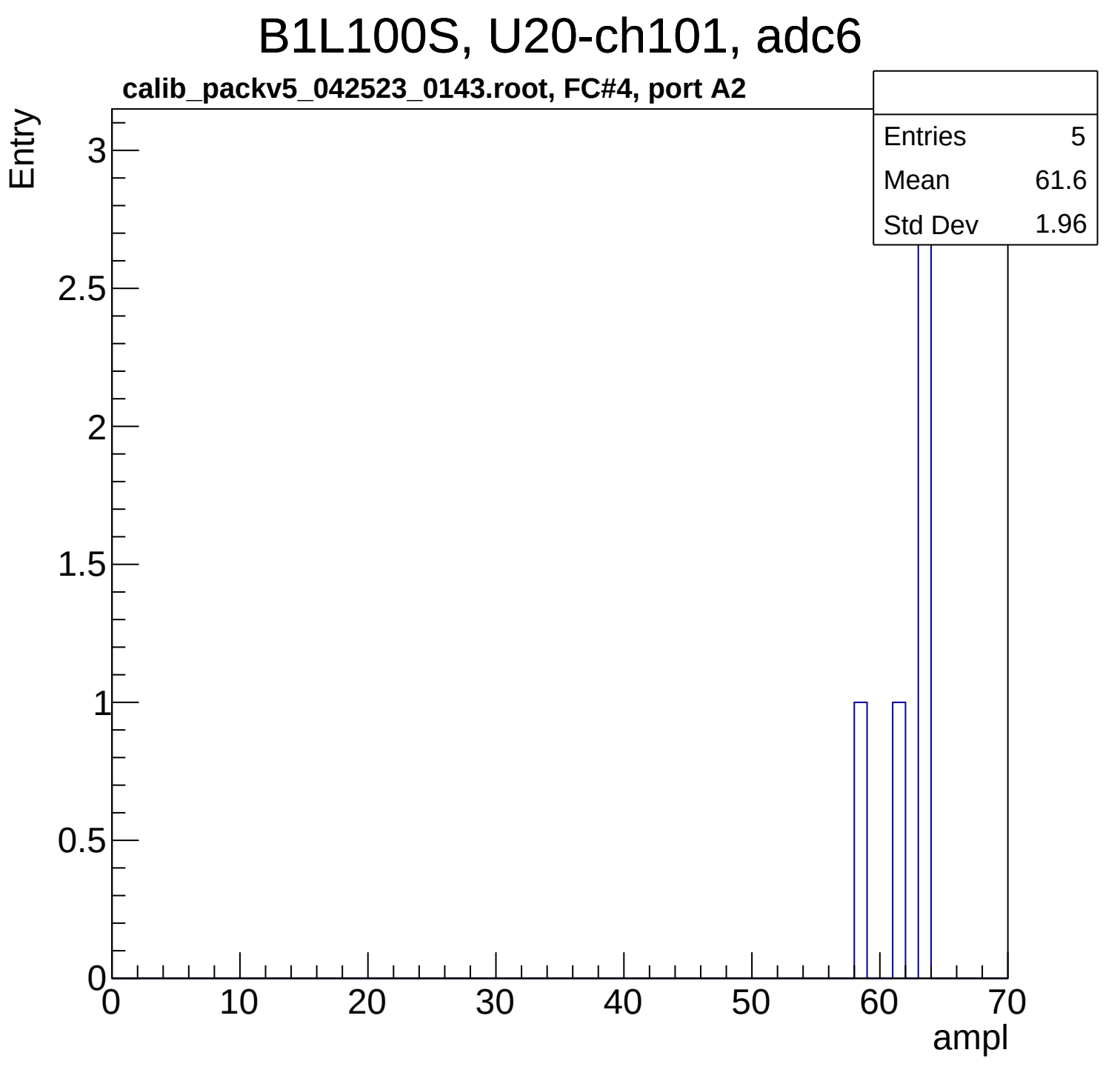
B1L100S, U20-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.6
Std Dev 1.96
Entry
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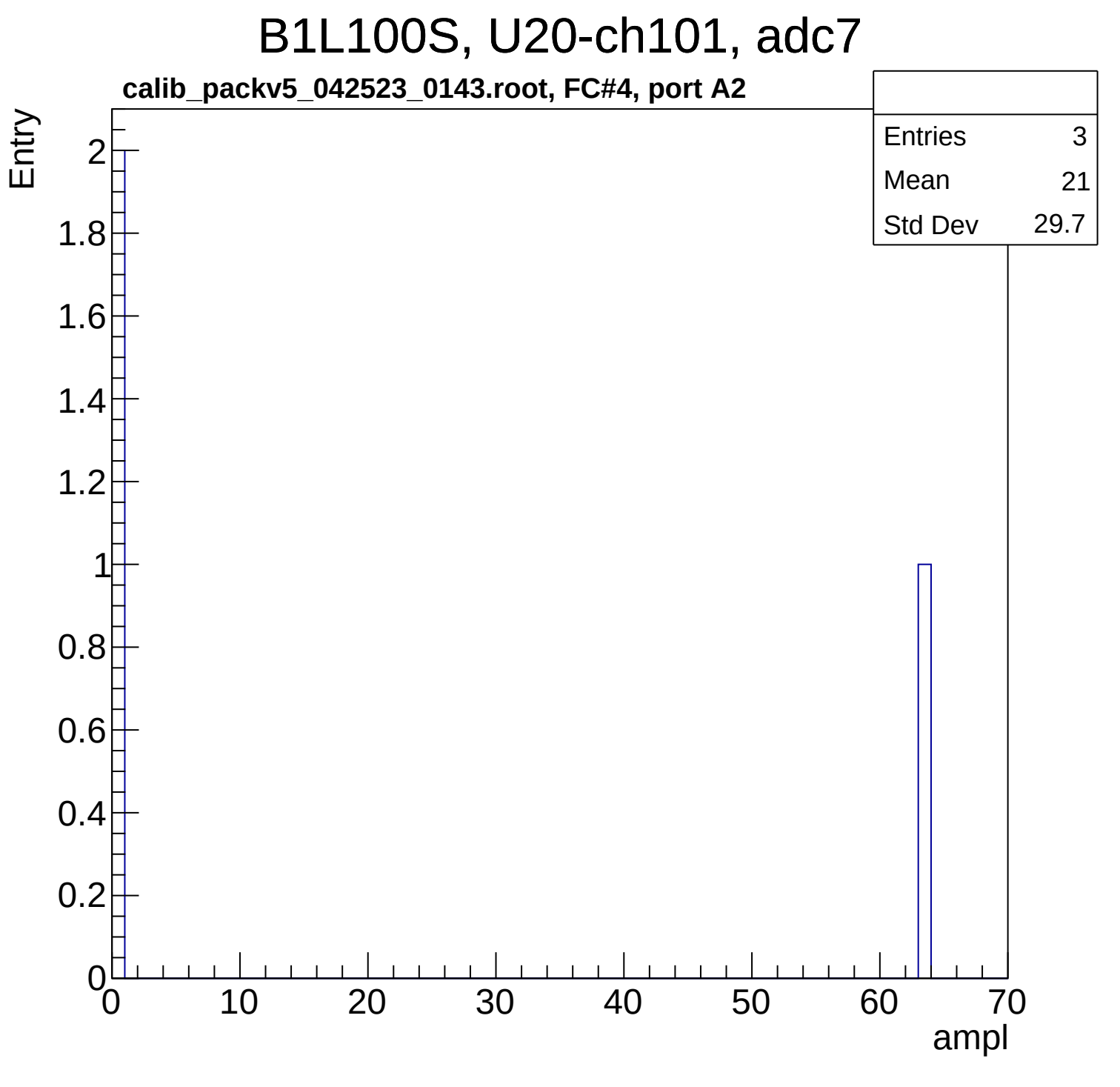
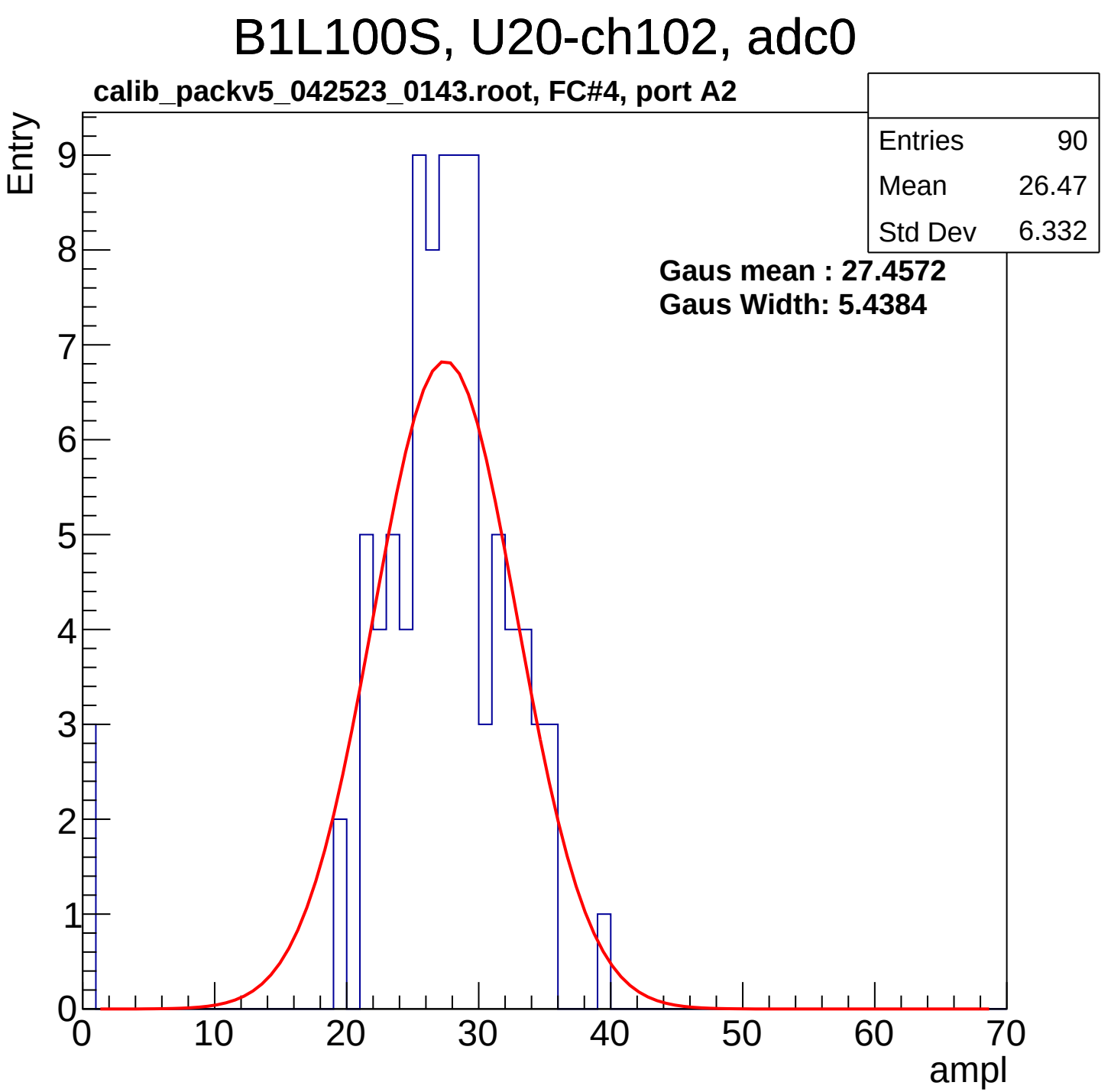
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 26.47
Std Dev 6.332
Gaus mean : 27.4572
Gaus Width: 5.4384
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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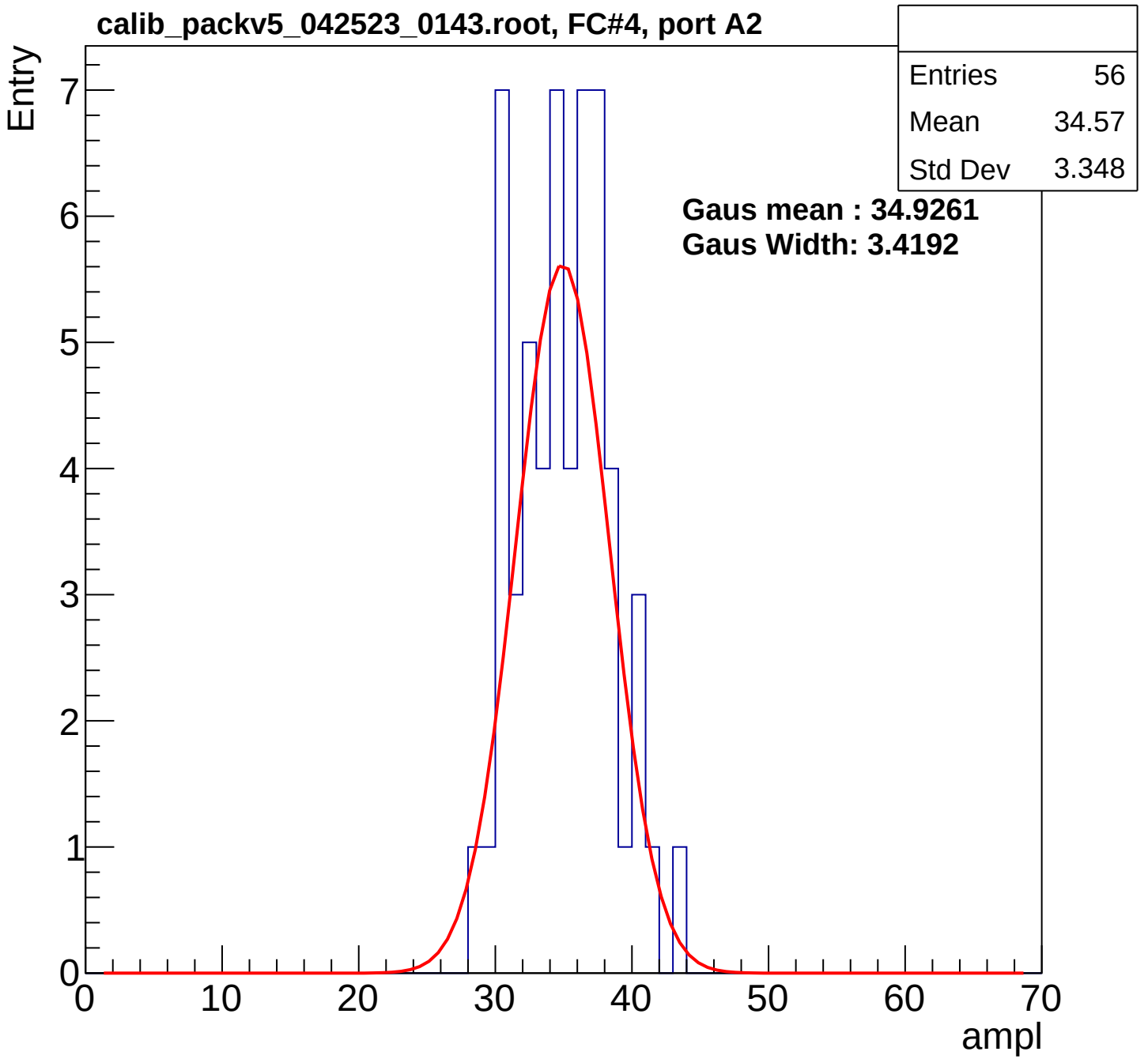
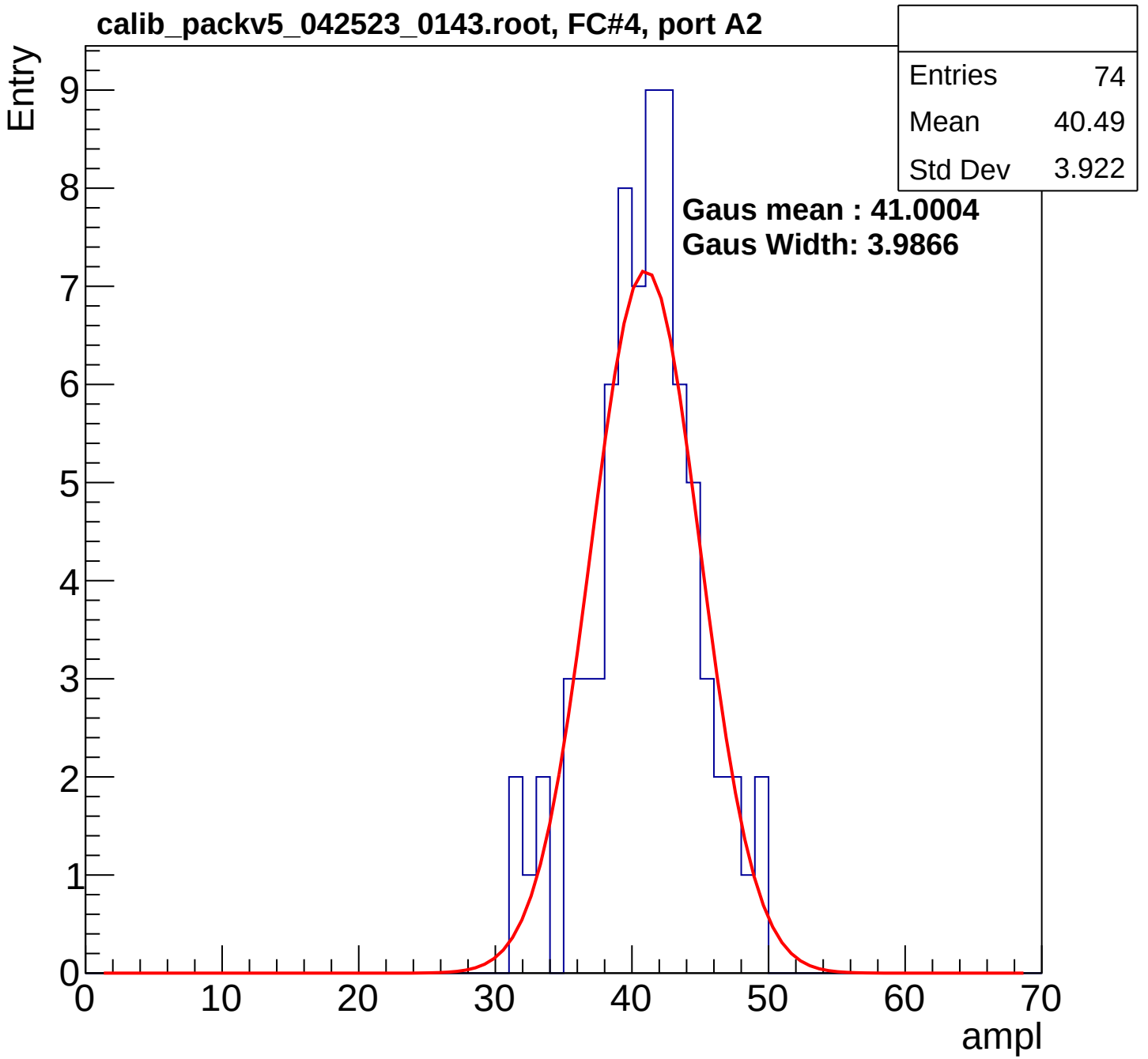

B1L100S, U20-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 34.57
Std Dev 3.348
Gaus mean : 34.9261
Gaus Width: 3.4192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 40.49
Std Dev 3.922
Gaus mean : 41.0004
Gaus Width: 3.9866
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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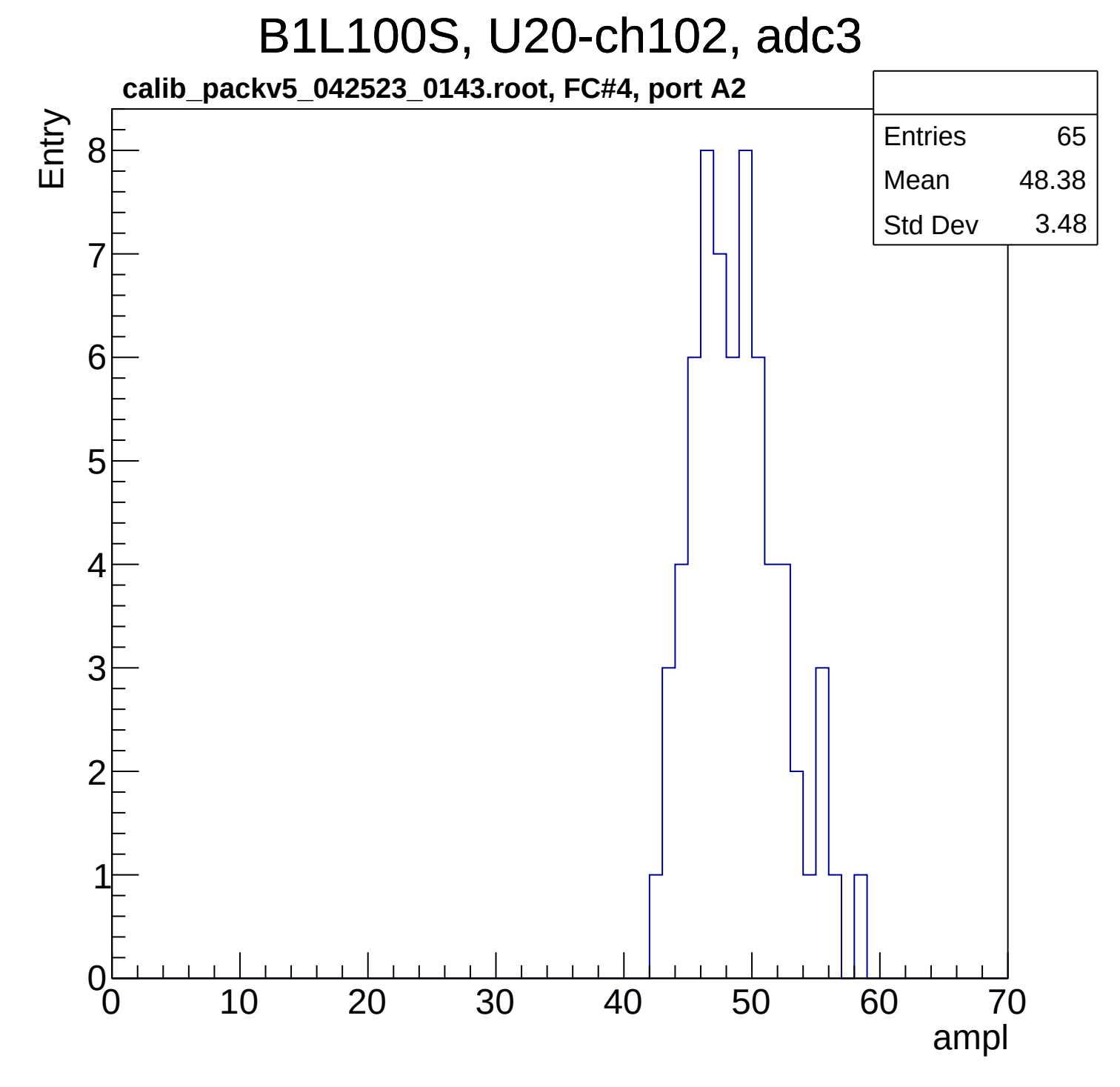
B1L100S, U20-ch102, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 48.38
Std Dev 3.48
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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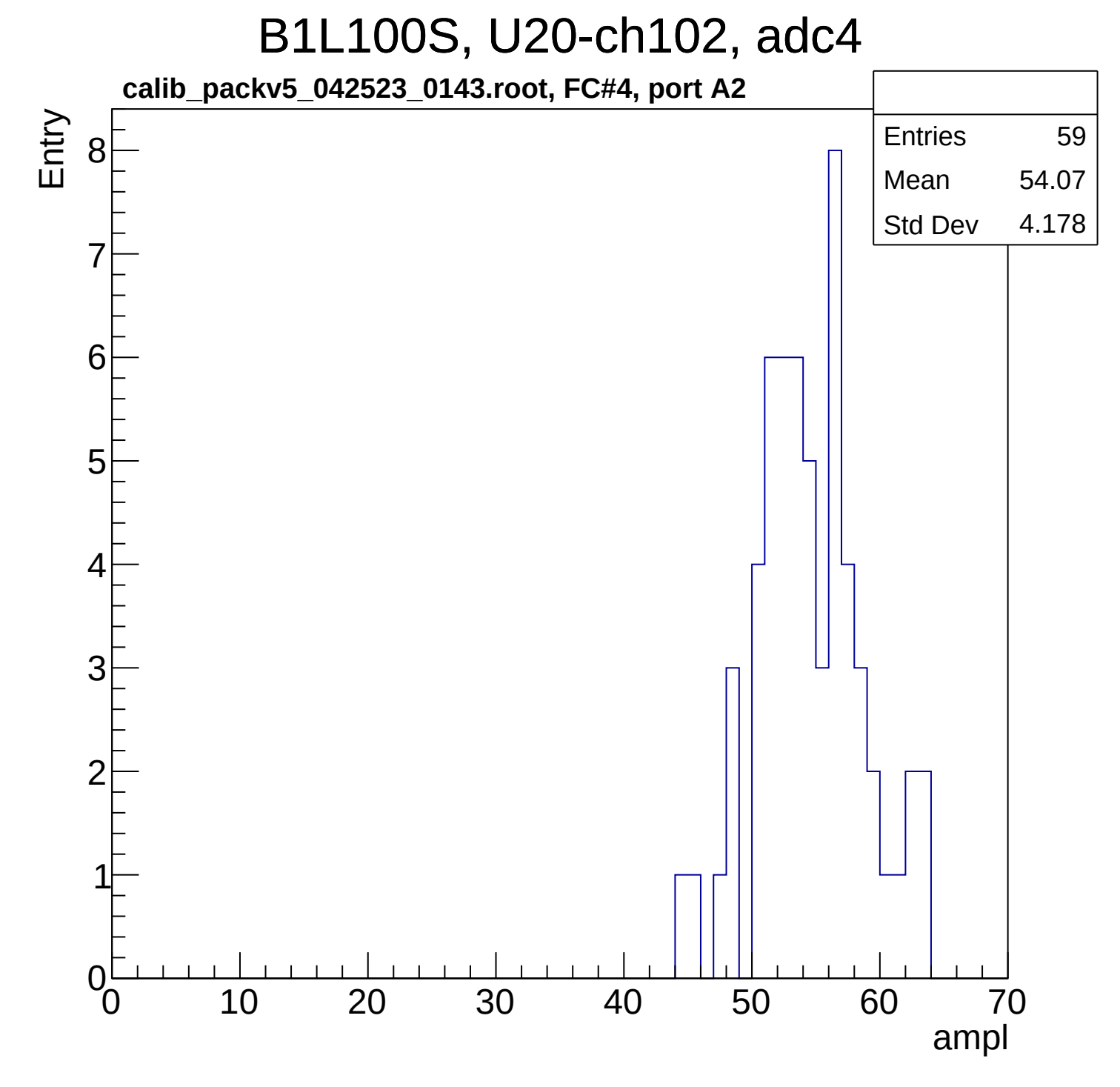
40
50
60
70


B1L100S, U20-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.07
Std Dev 4.178
Entry
ampl
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1
2
3
4
5
6
7
8
0
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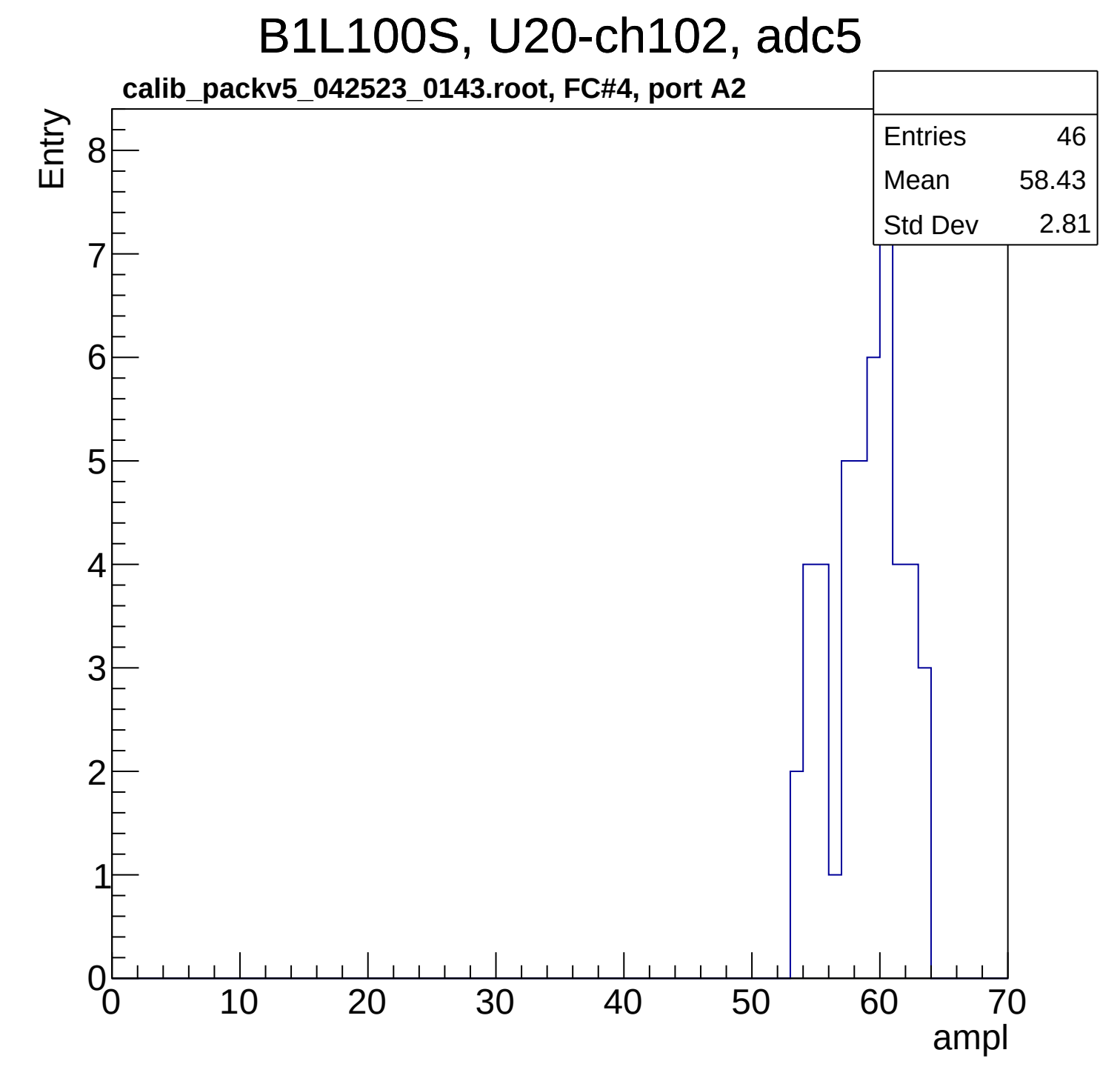
20
30
40
50
60
70


B1L100S, U20-ch102, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.43
Std Dev 2.81
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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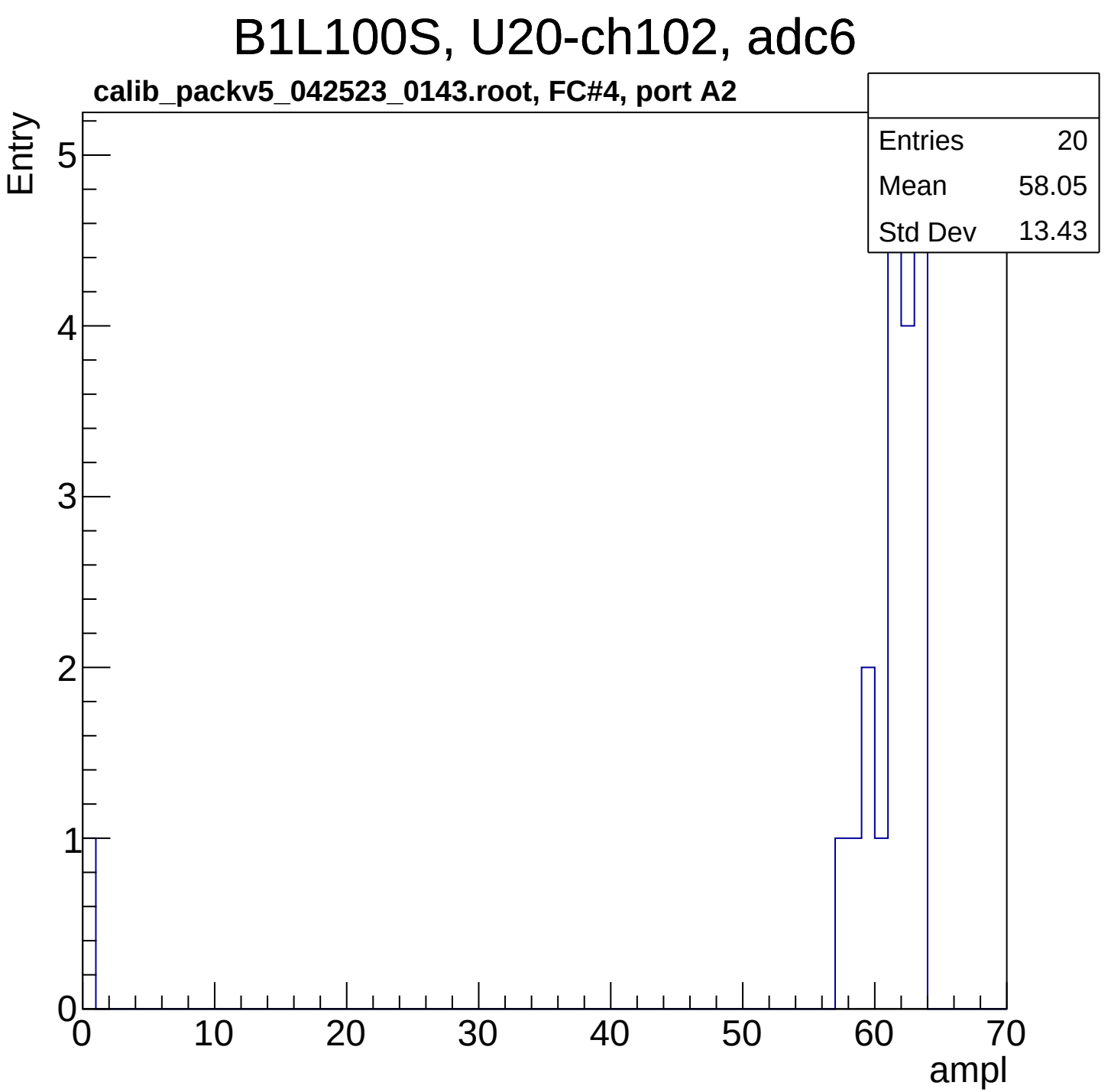
B1L100S, U20-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 20
Mean 58.05
Std Dev 13.43
Entry
ampl
0
1
2
3
4
5
0
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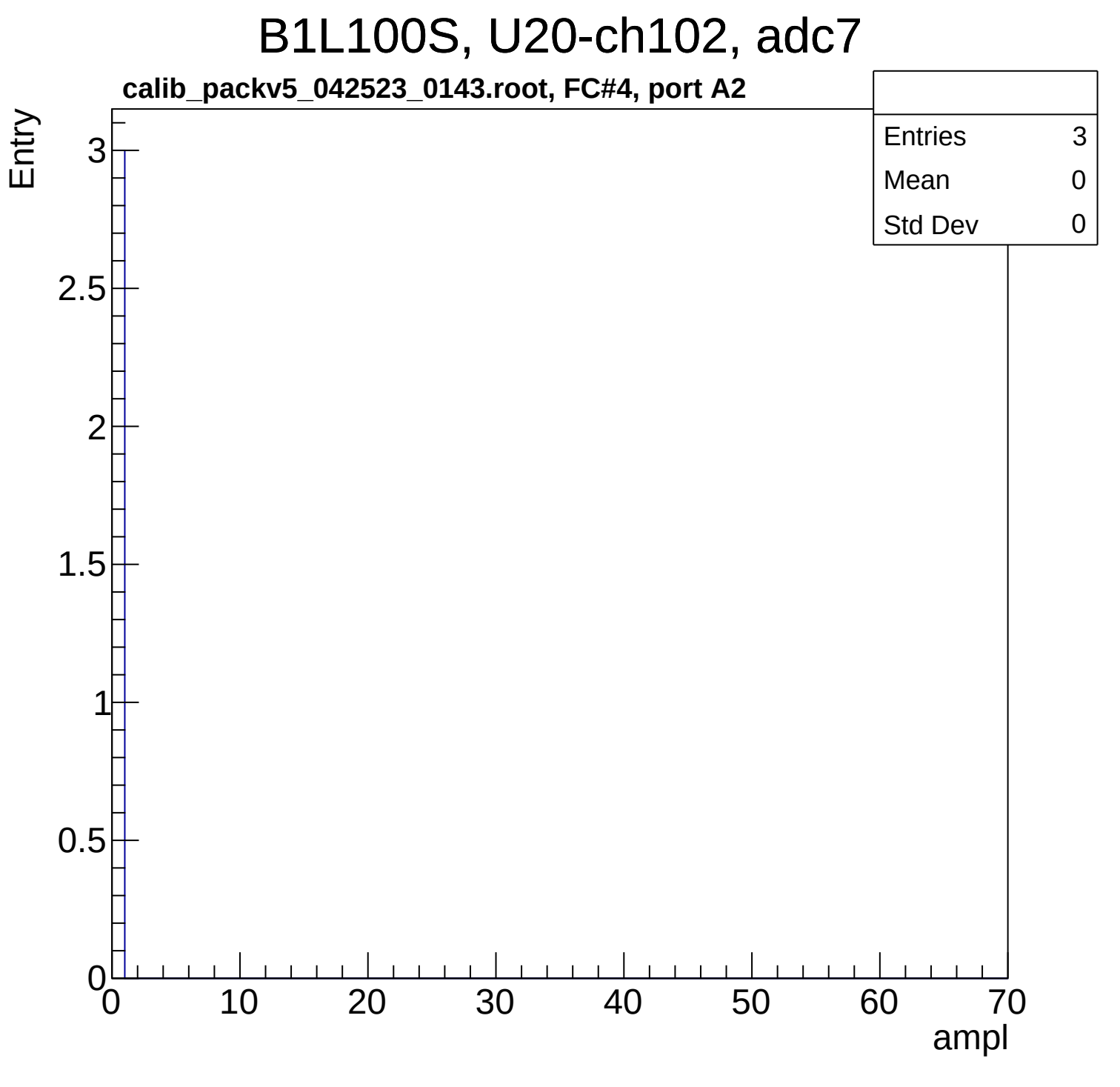
20
30
40
50
60
70


B1L100S, U20-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
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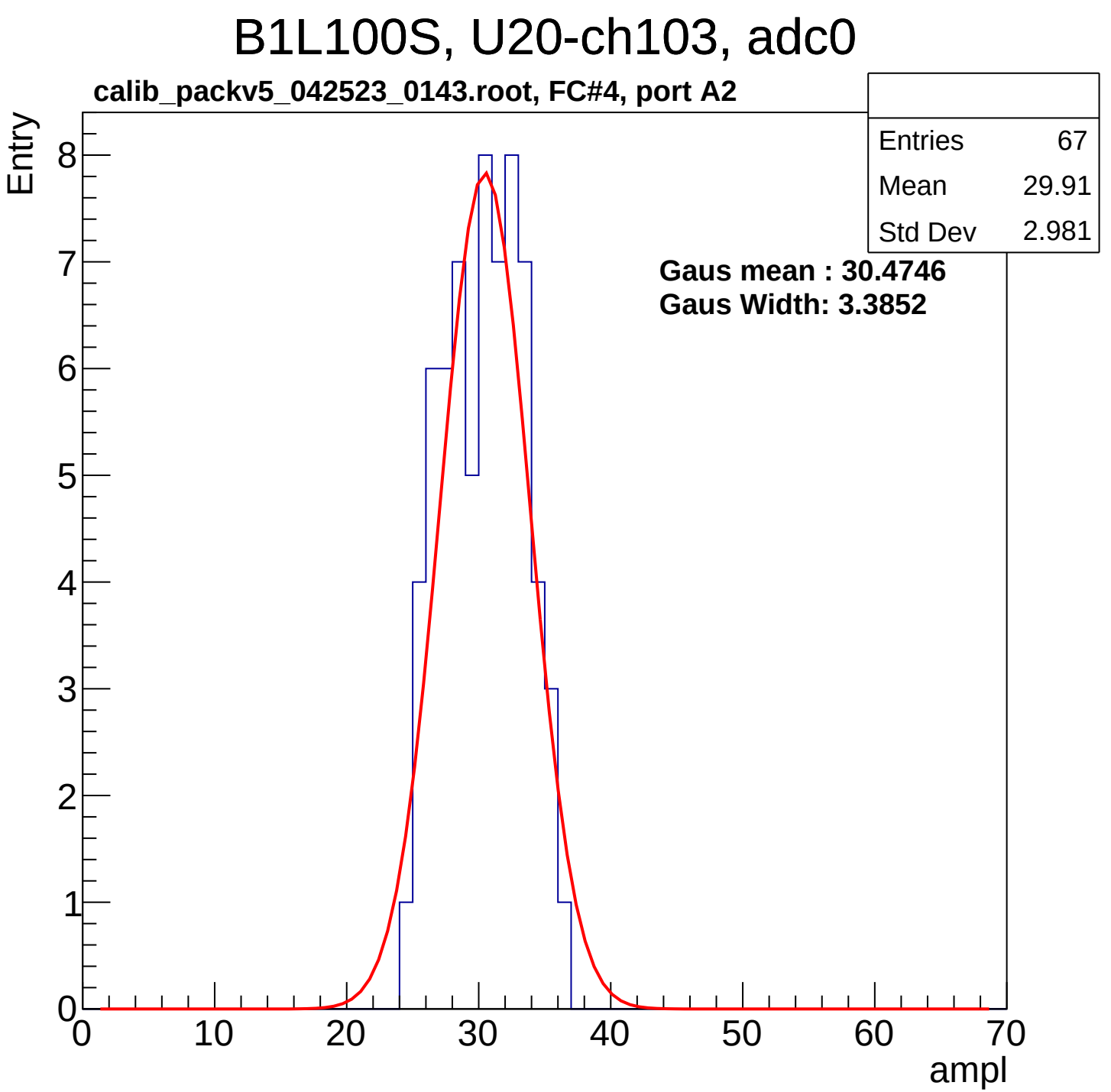
40
50
60
70


B1L100S, U20-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.91
Std Dev 2.981
Gaus mean : 30.4746
Gaus Width: 3.3852
Entry
ampl
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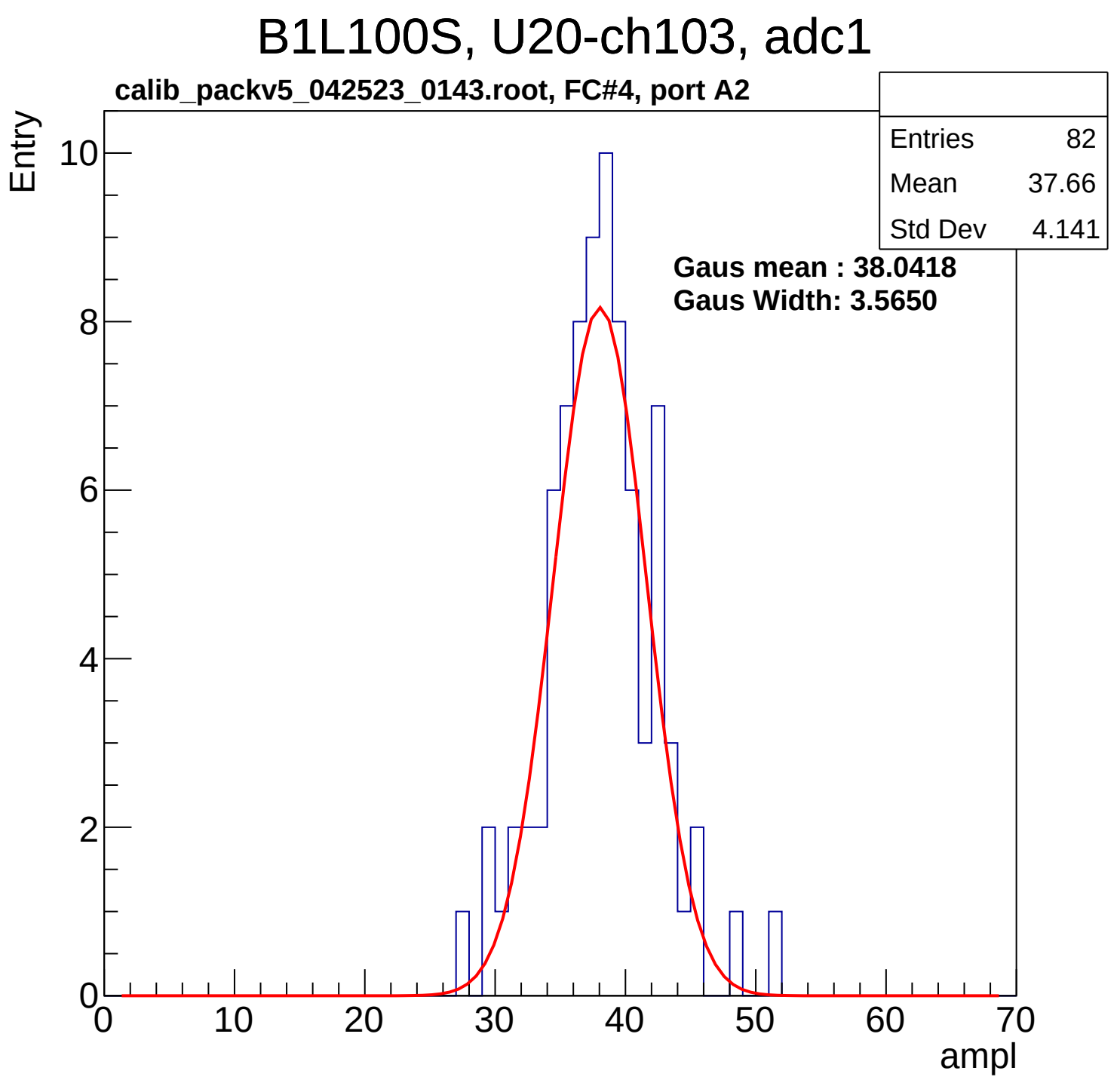
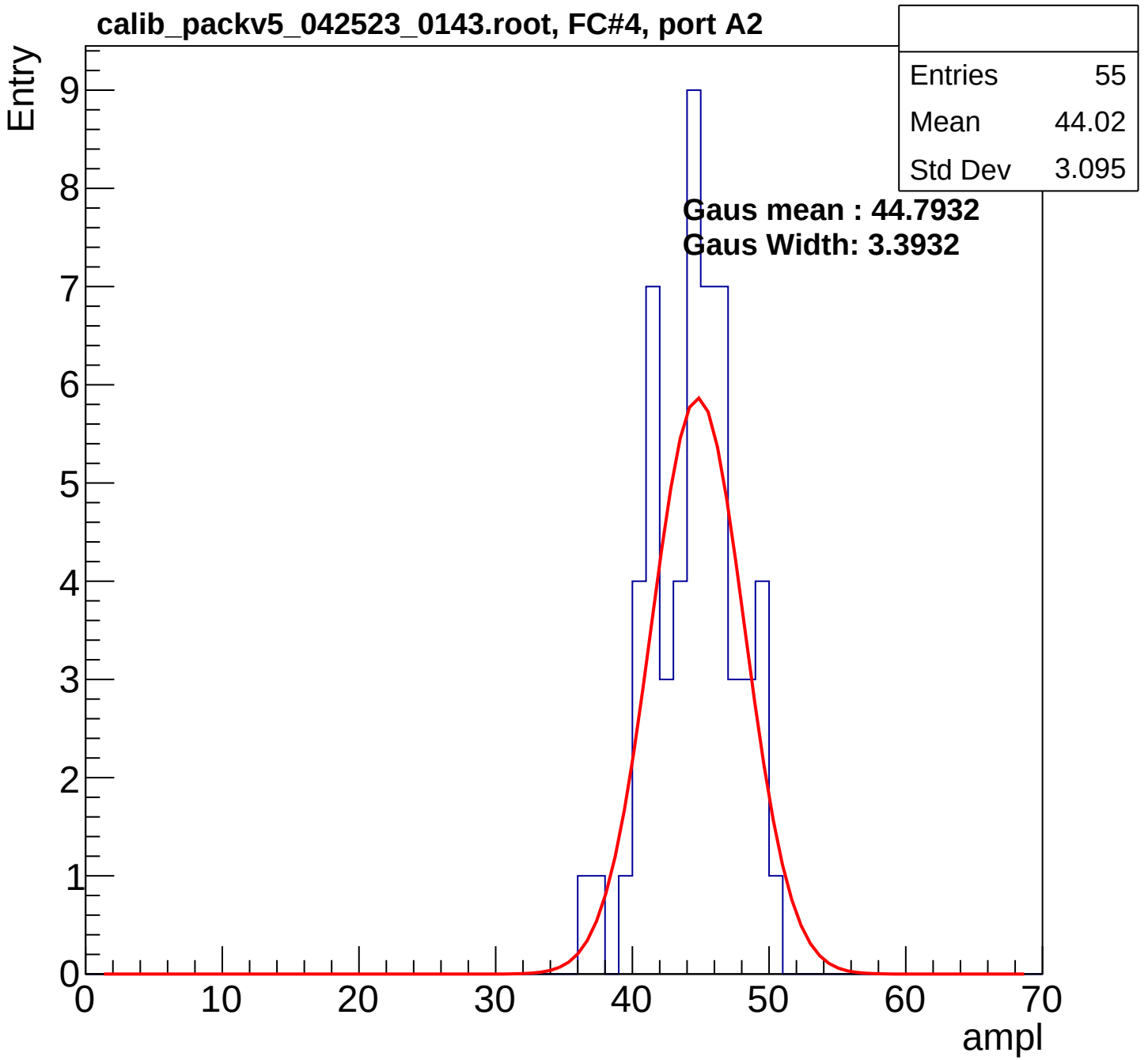
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 37.66
Std Dev 4.141
Gaus mean : 38.0418
Gaus Width: 3.5650
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 44.02
Std Dev 3.095
Gaus mean : 44.7932
Gaus Width: 3.3932
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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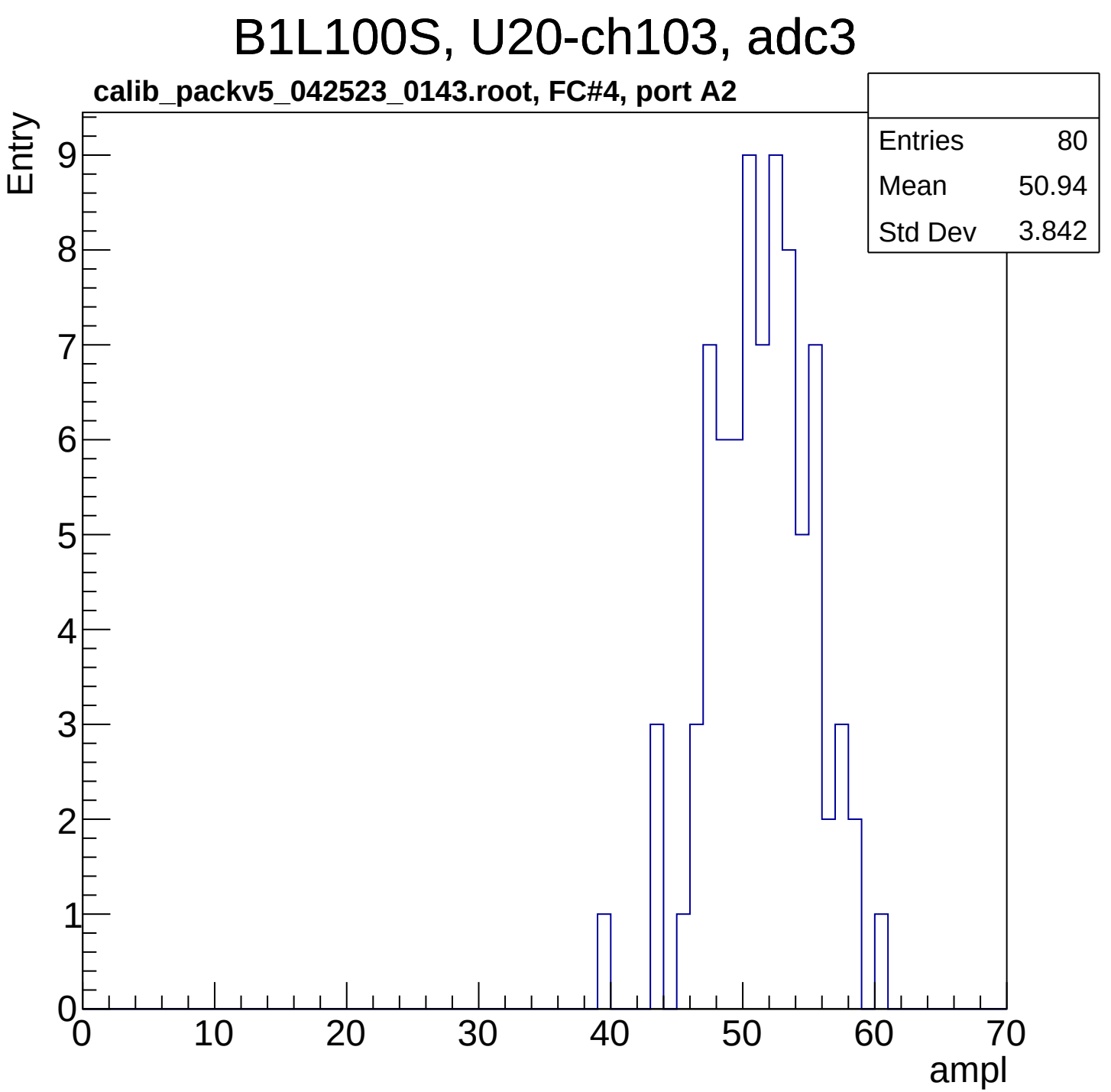
B1L100S, U20-ch103, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 50.94
Std Dev 3.842
Entry
ampl
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2
3
4
5
6
7
8
9
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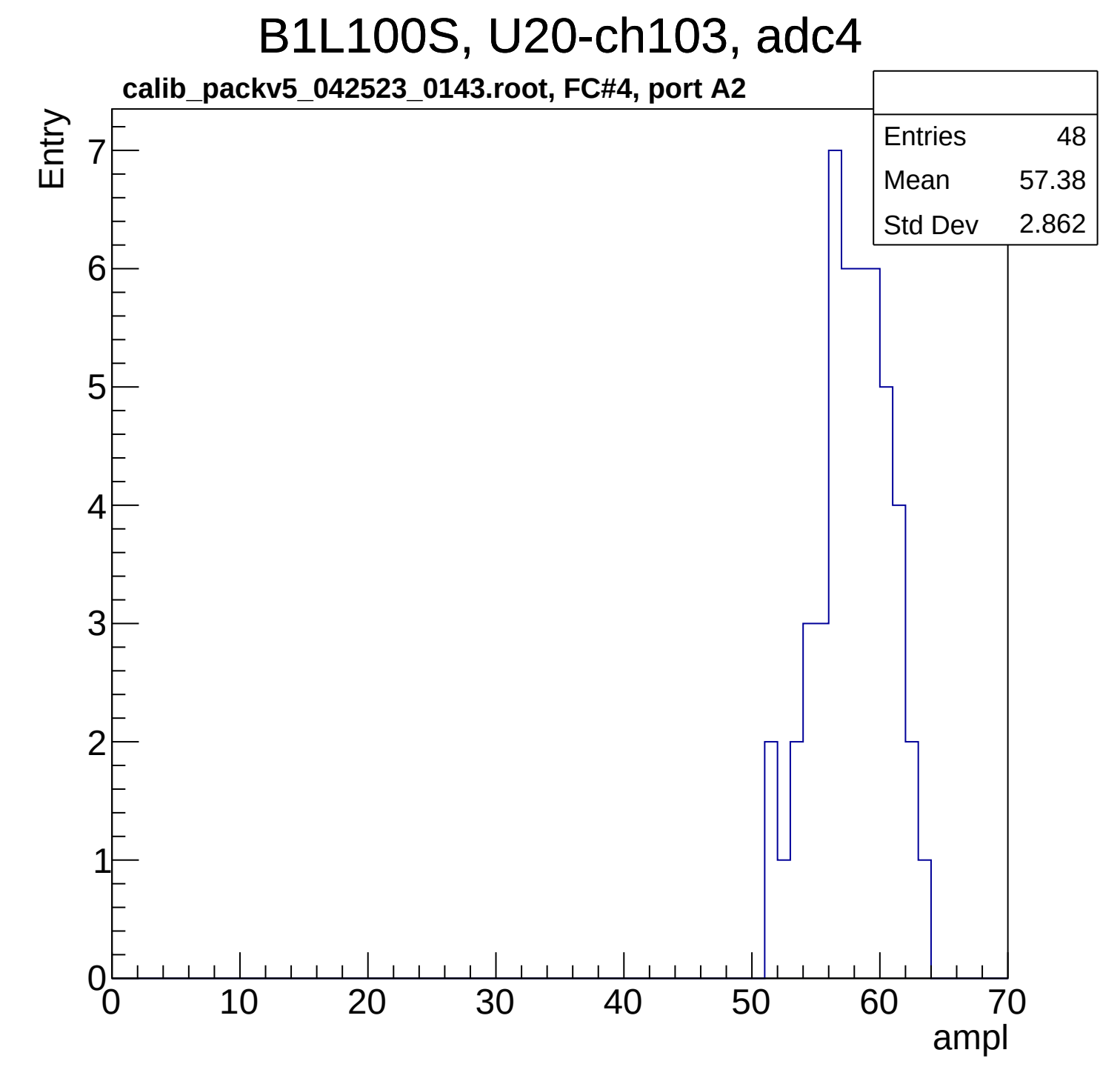
B1L100S, U20-ch103, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 57.38
Std Dev 2.862
Entry
ampl
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2
3
4
5
6
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60
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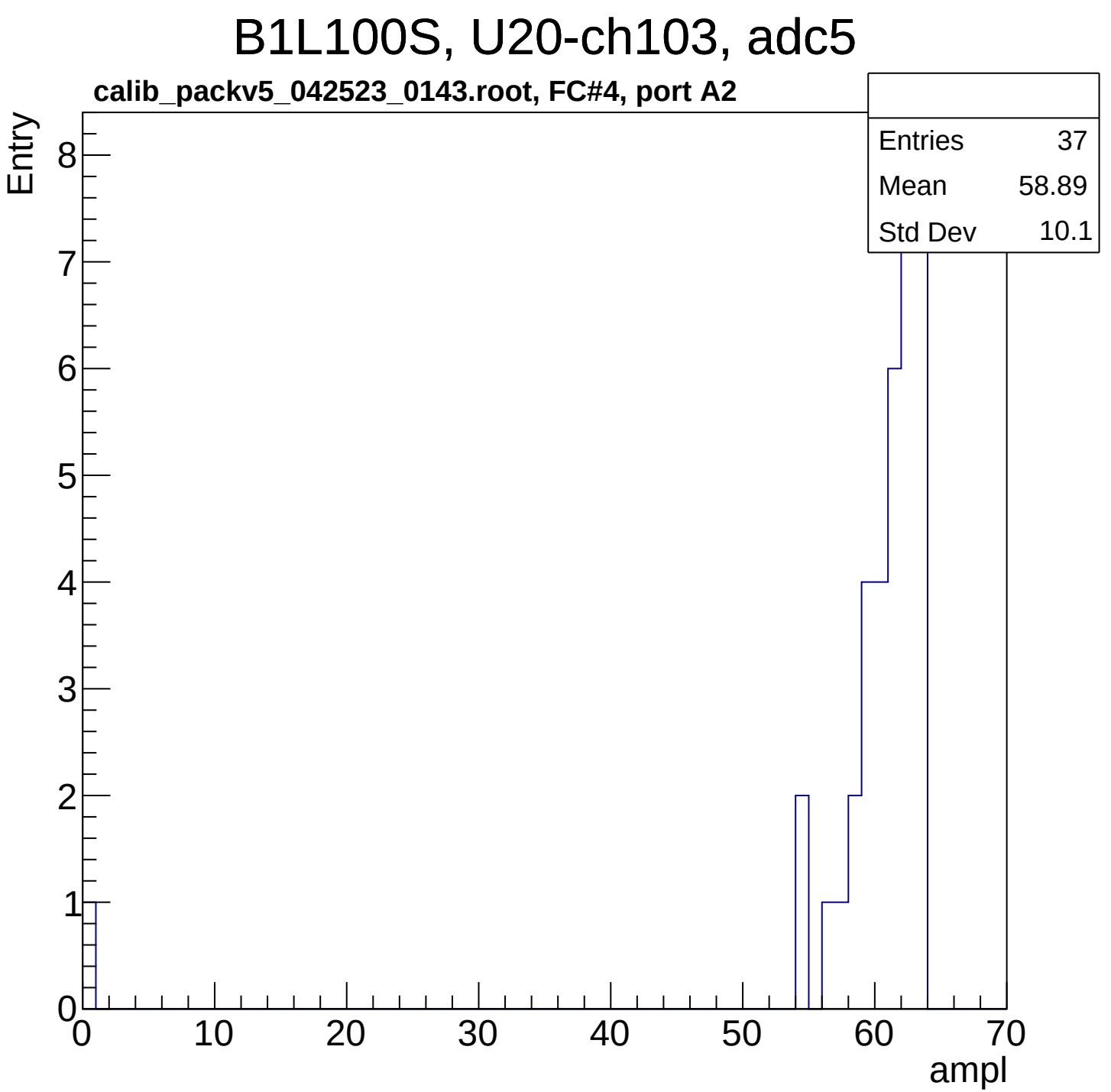
B1L100S, U20-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.89
Std Dev 10.1
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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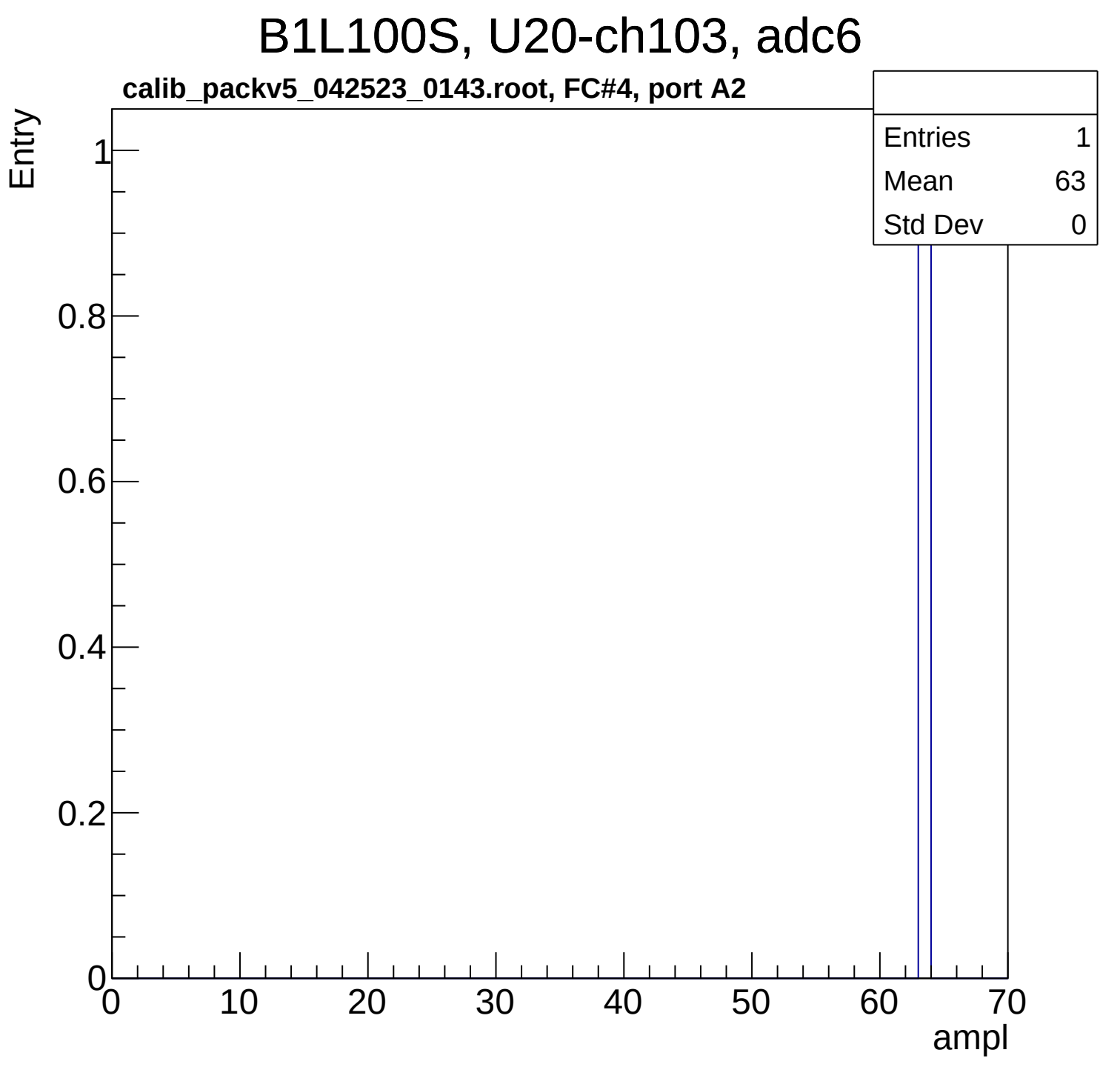
B1L100S, U20-ch103, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
30
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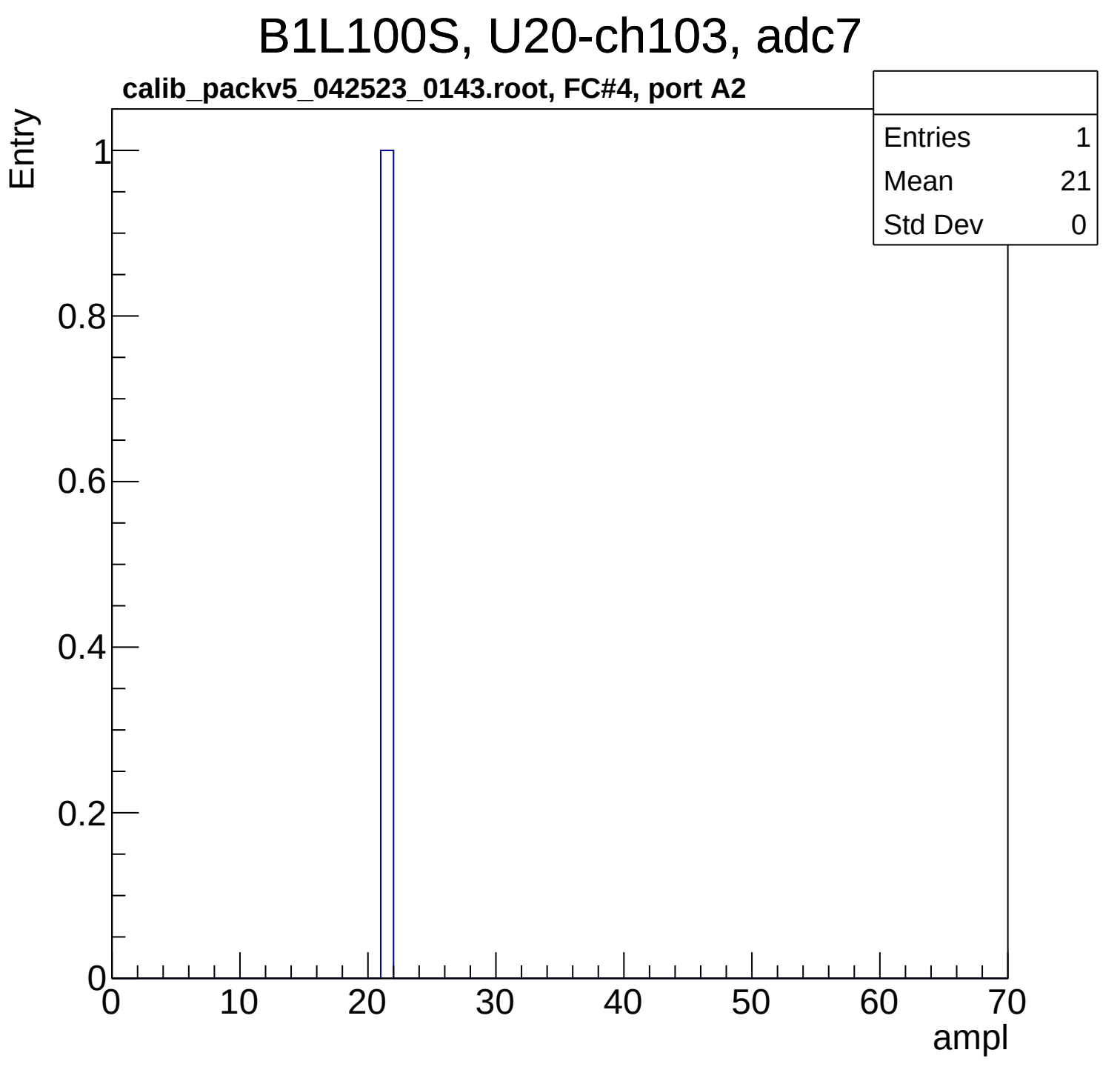
50
60
70
ampl


B1L100S, U20-ch103, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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0.4
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0
10
20
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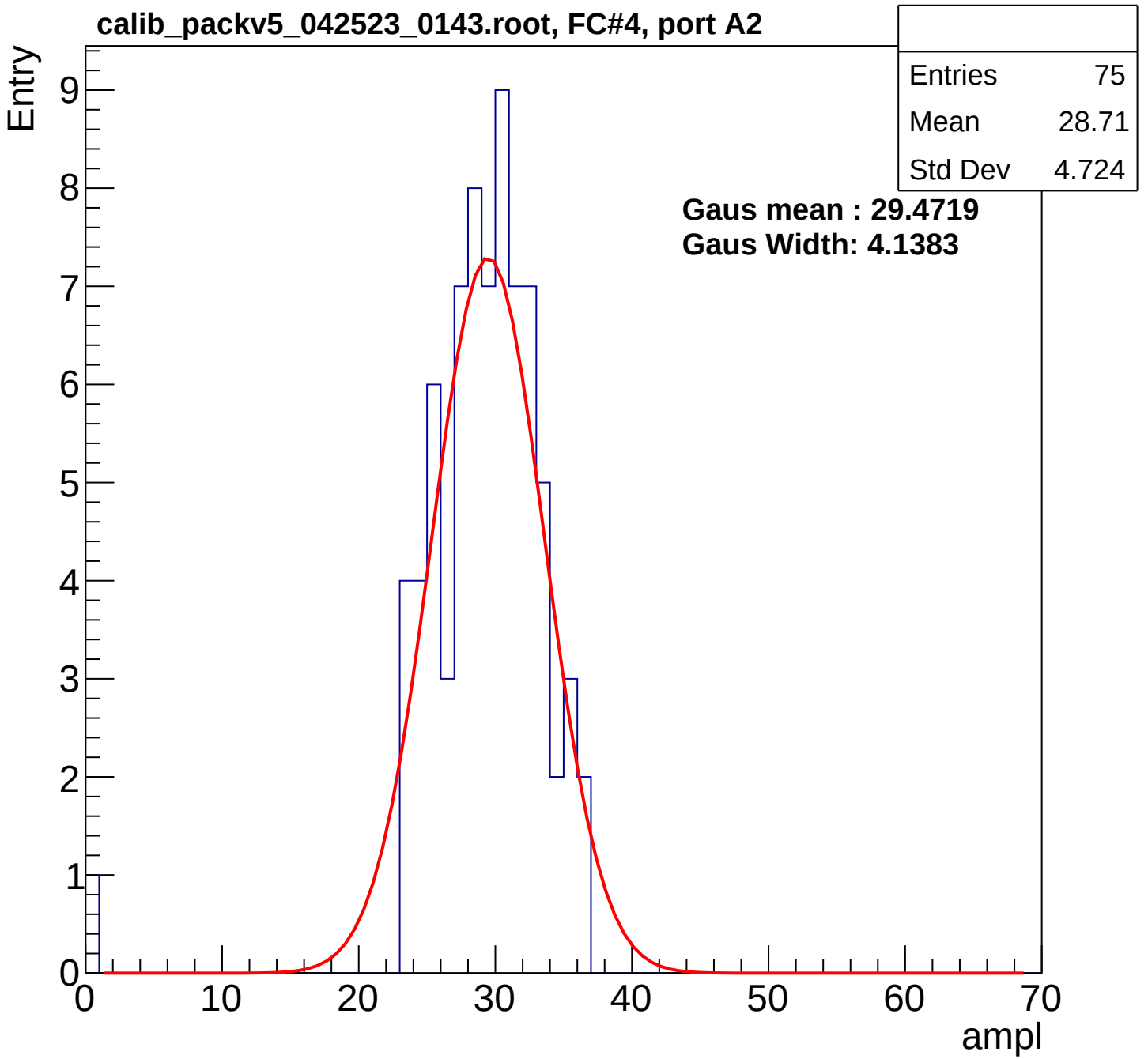
50
60
70
ampl


B1L100S, U20-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.71
Std Dev 4.724
Gaus mean : 29.4719
Gaus Width: 4.1383
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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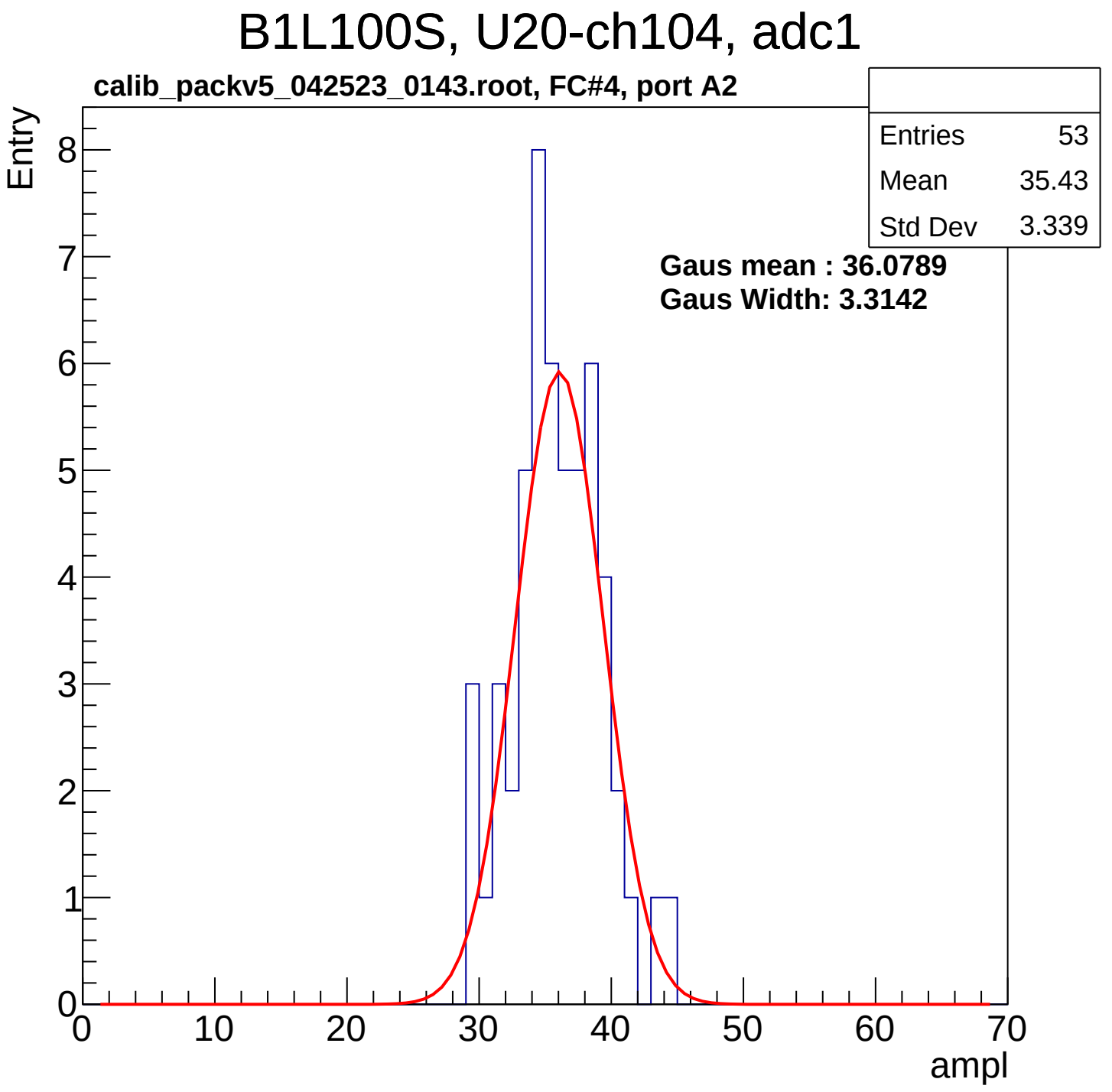
40
50
60
70


B1L100S, U20-ch104, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 35.43
Std Dev 3.339
Gaus mean : 36.0789
Gaus Width: 3.3142
Entry
ampl
0
1
2
3
4
5
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7
8
0
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20
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40
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60
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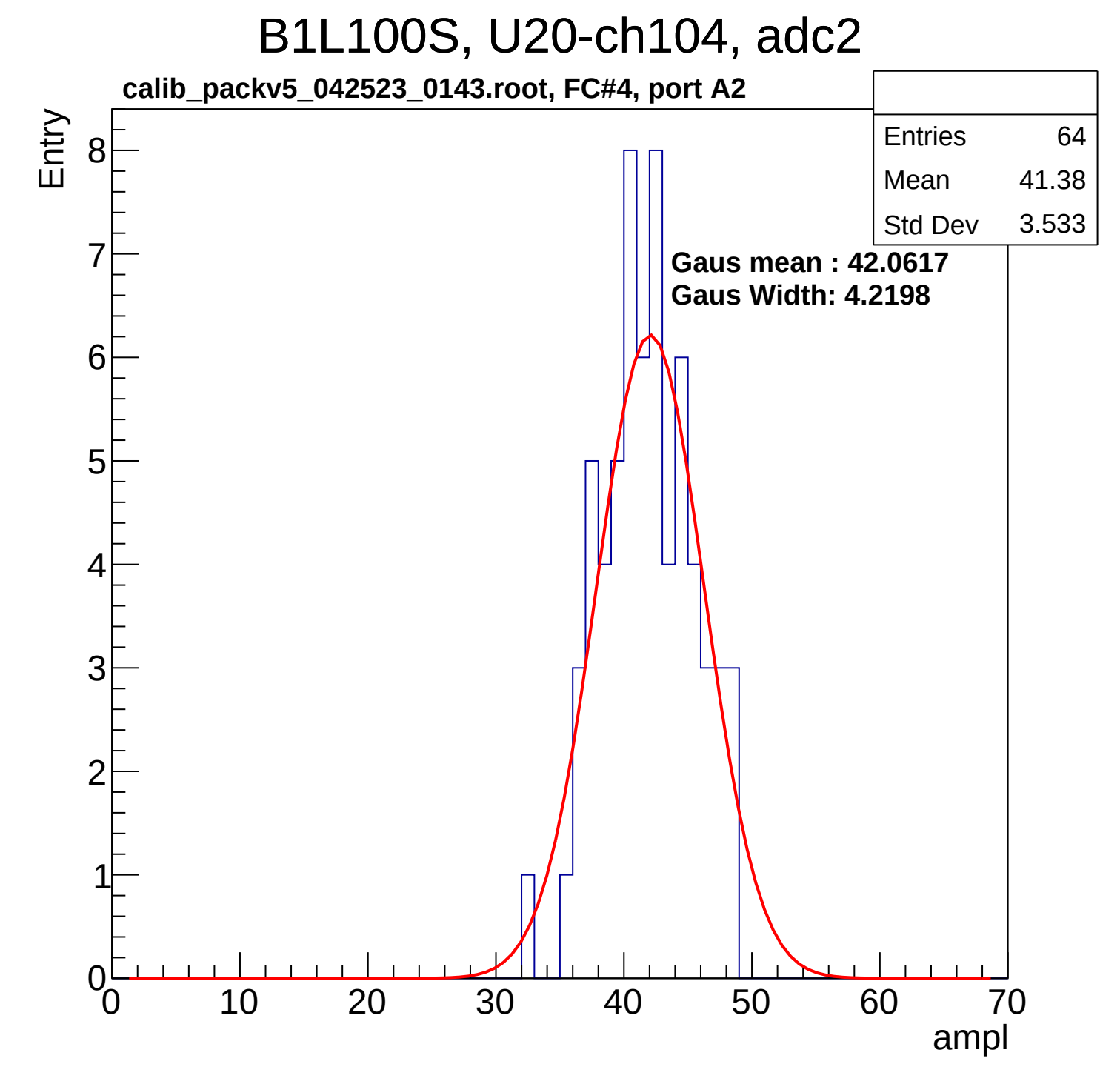
B1L100S, U20-ch104, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 41.38
Std Dev 3.533
Gaus mean : 42.0617
Gaus Width: 4.2198
Entry
ampl
0
1
2
3
4
5
6
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0
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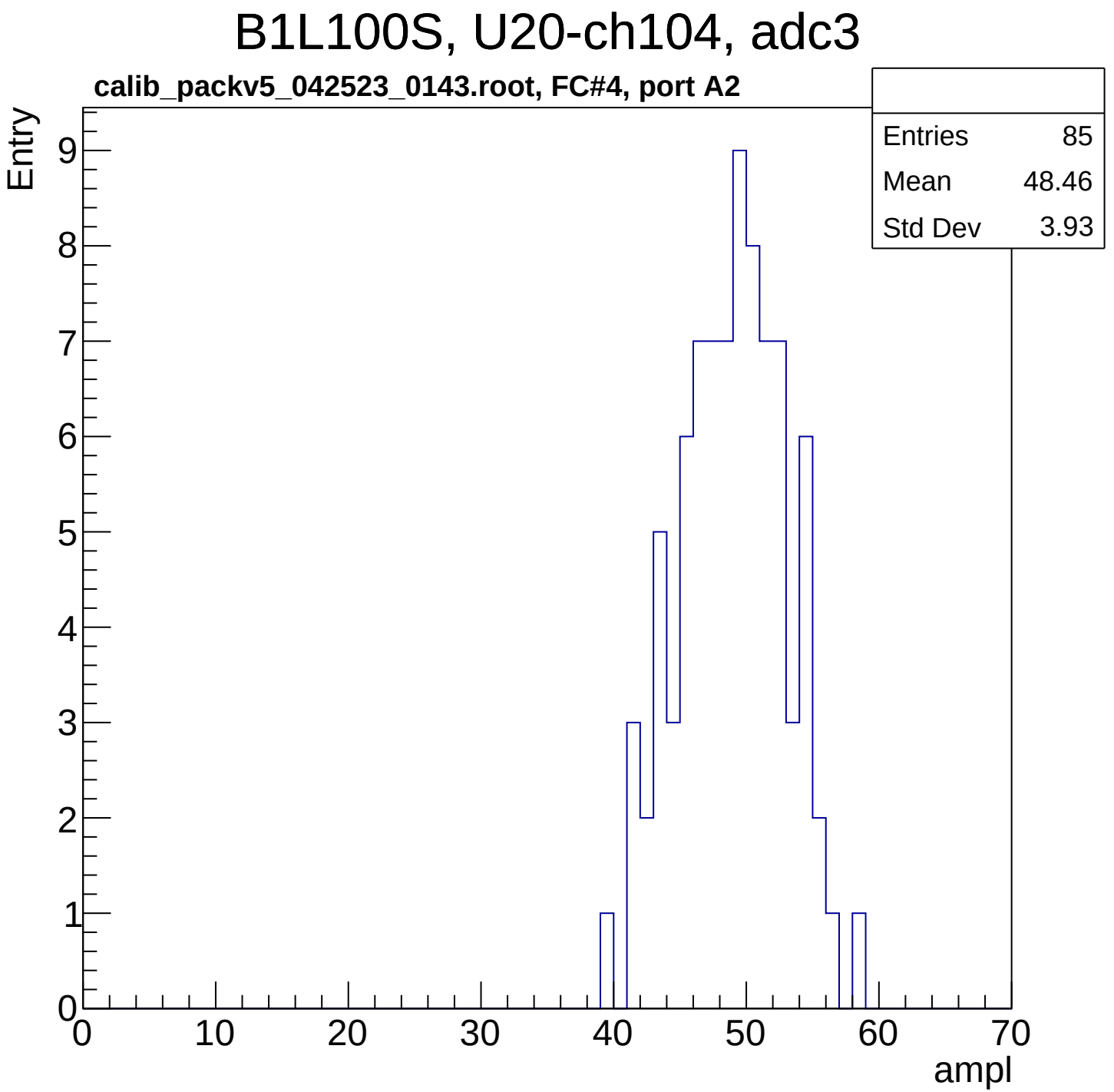
B1L100S, U20-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 48.46
Std Dev 3.93
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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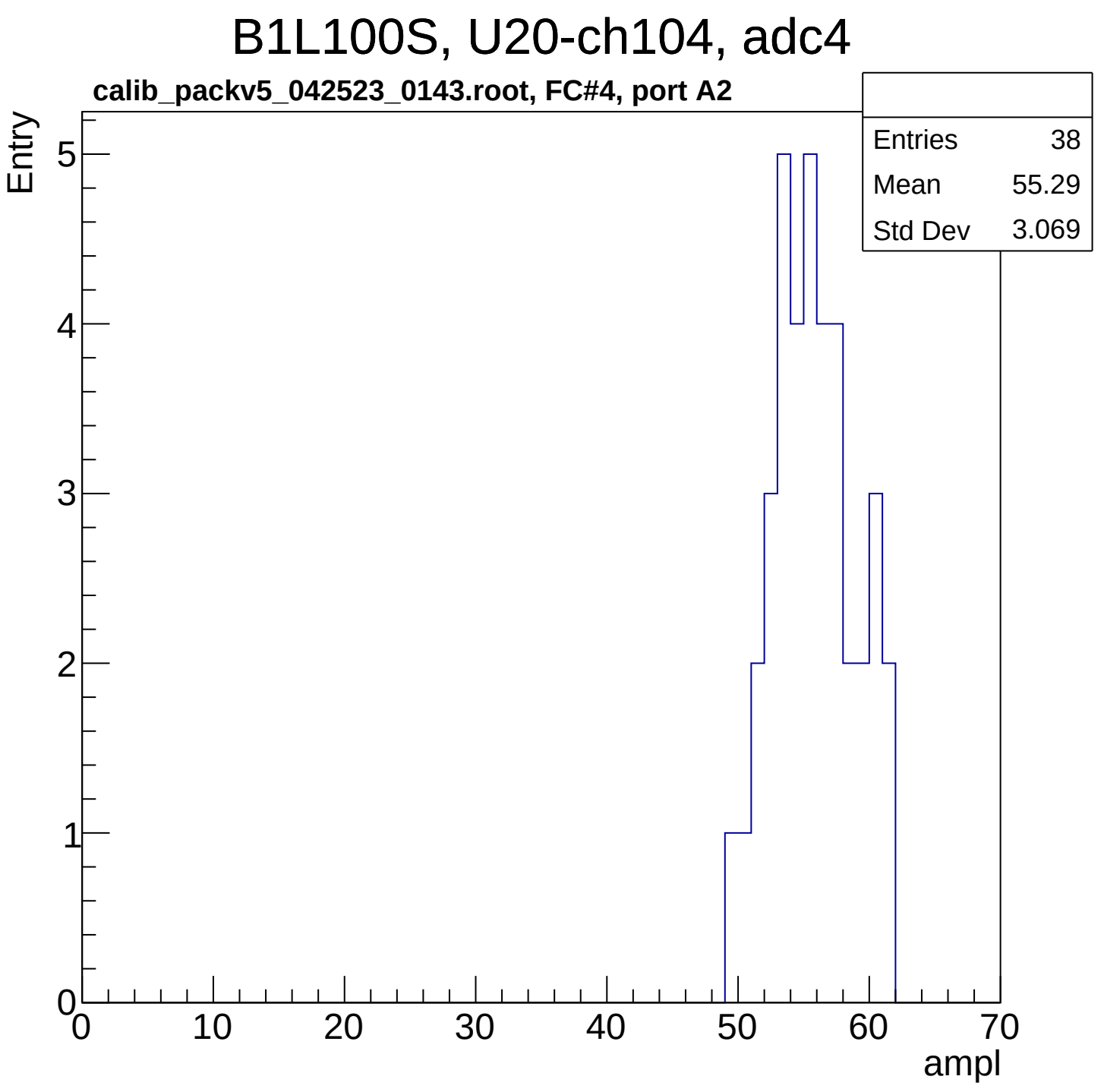
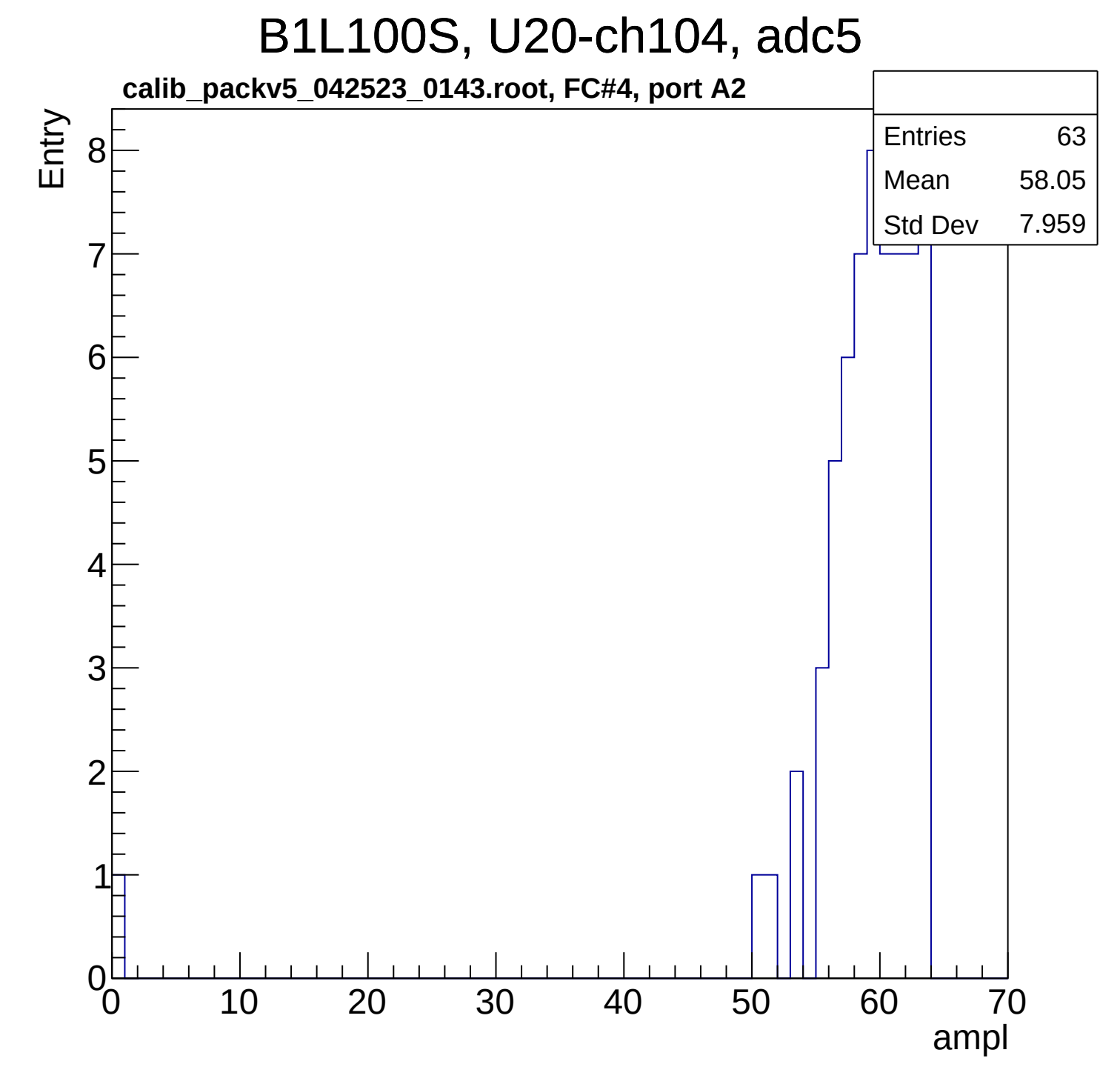

B1L100S, U20-ch104, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 55.29
Std Dev 3.069
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch104, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 58.05
Std Dev 7.959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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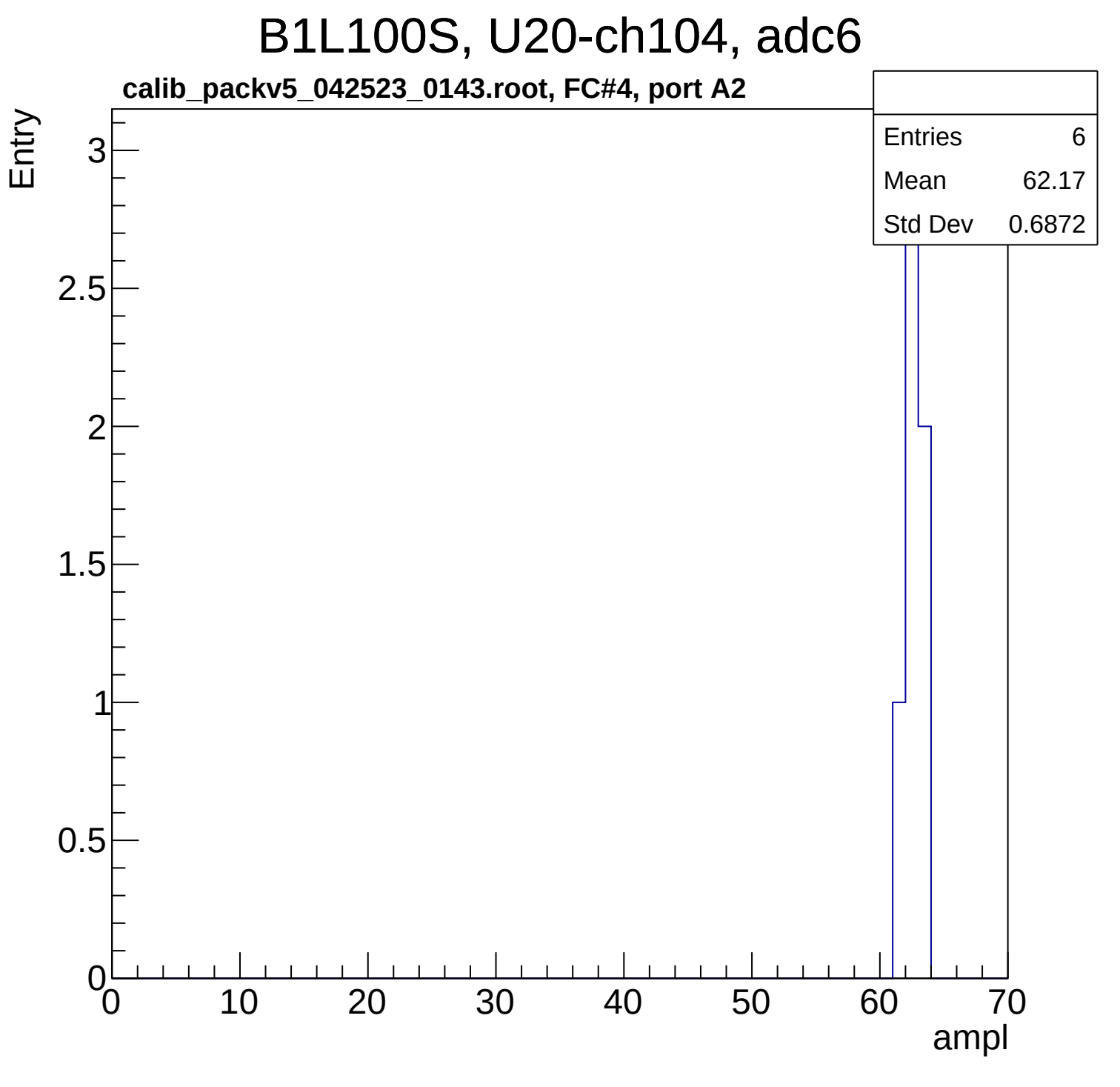
B1L100S, U20-ch104, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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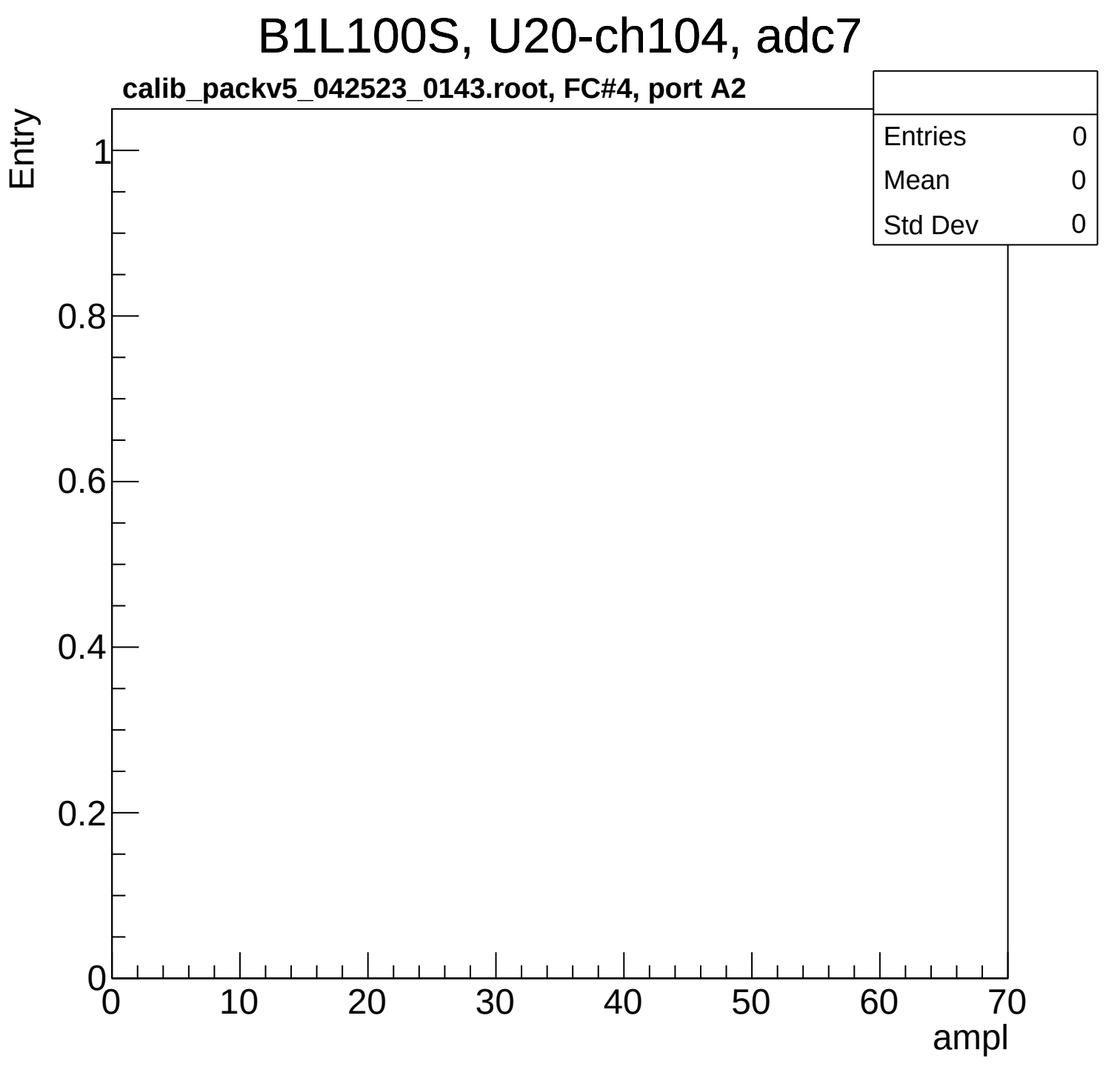
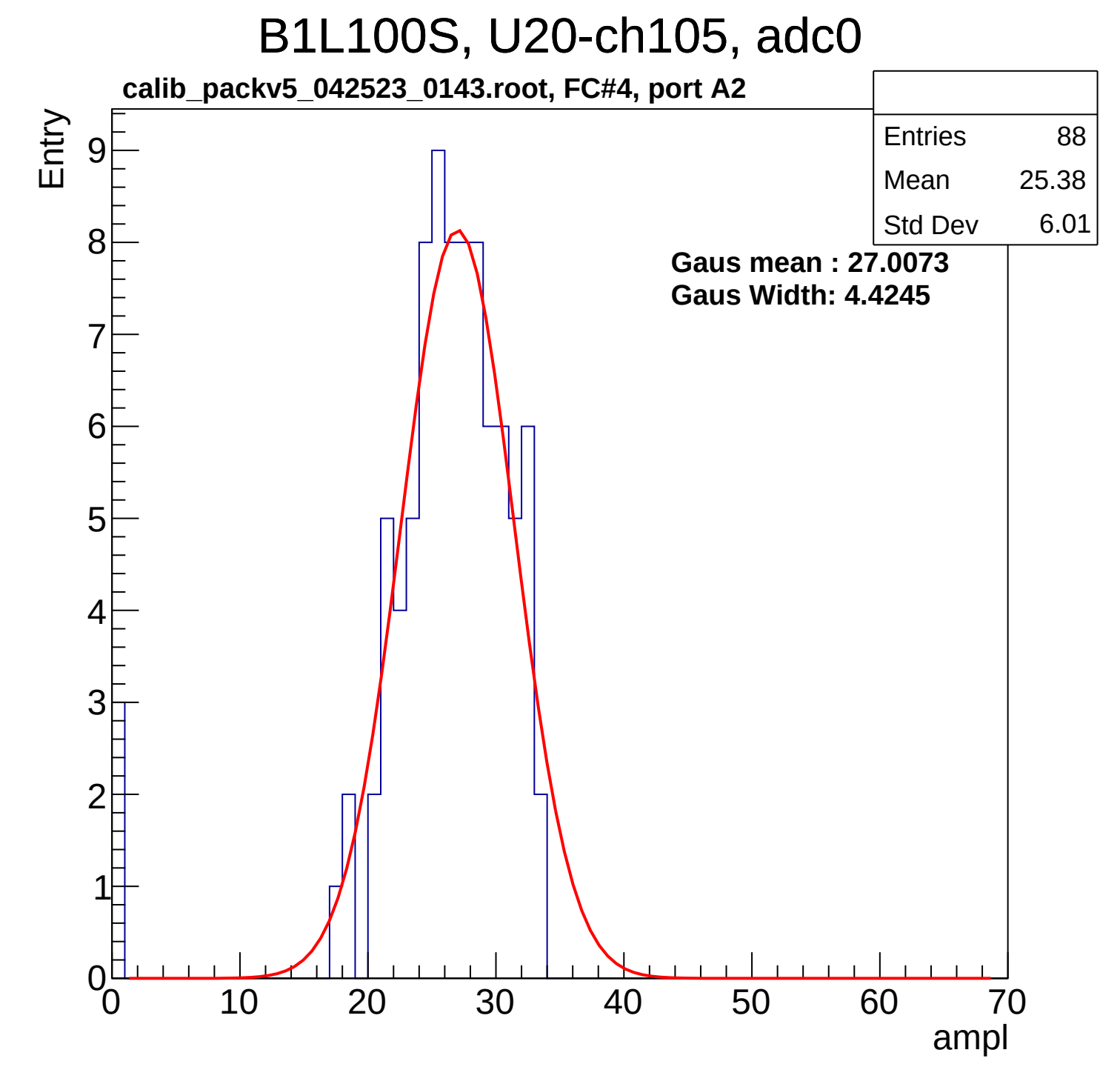
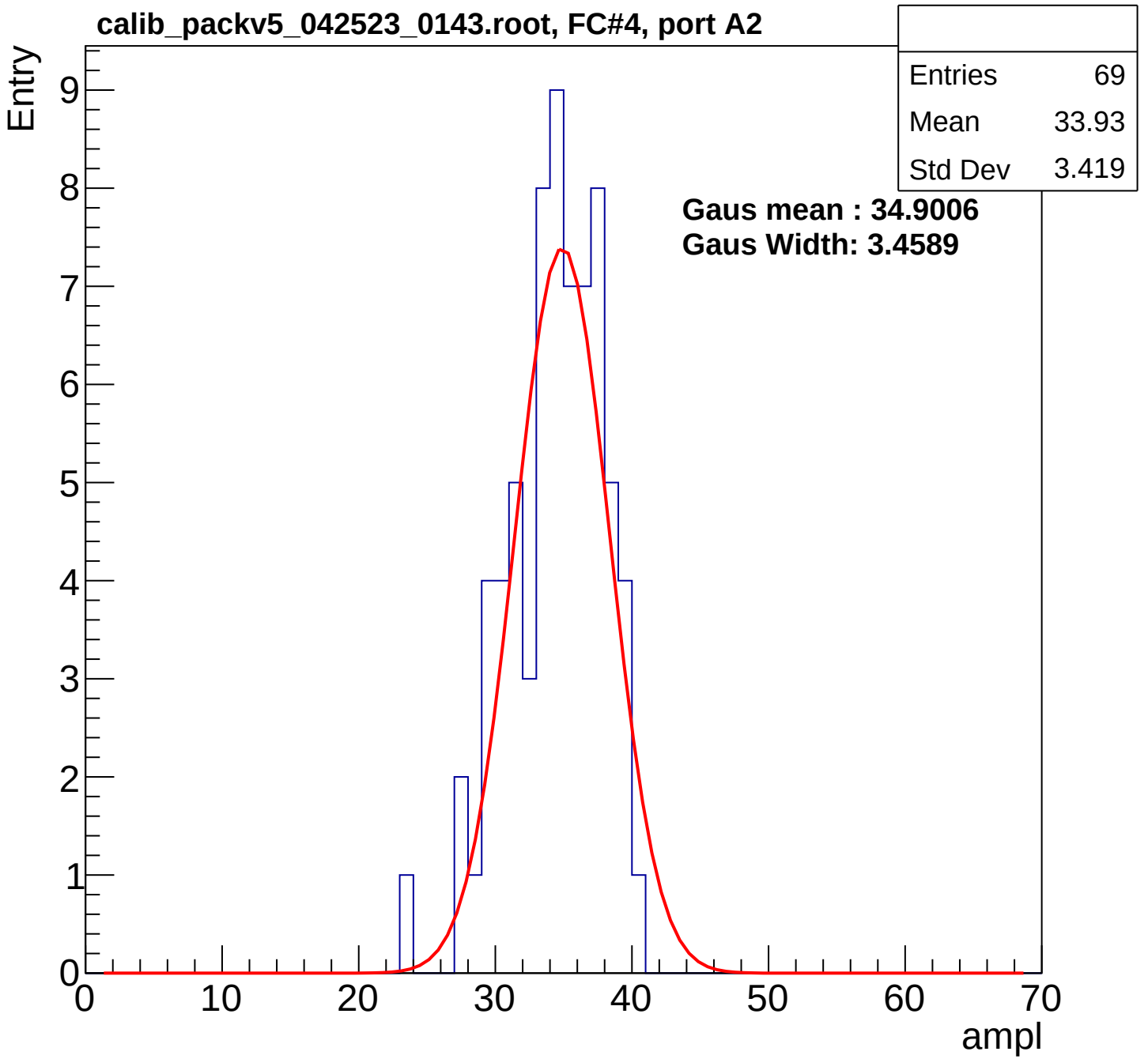

B1L100S, U20-ch104, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 25.38
Std Dev 6.01
Gaus mean : 27.0073
Gaus Width: 4.4245
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 33.93
Std Dev 3.419
Gaus mean : 34.9006
Gaus Width: 3.4589
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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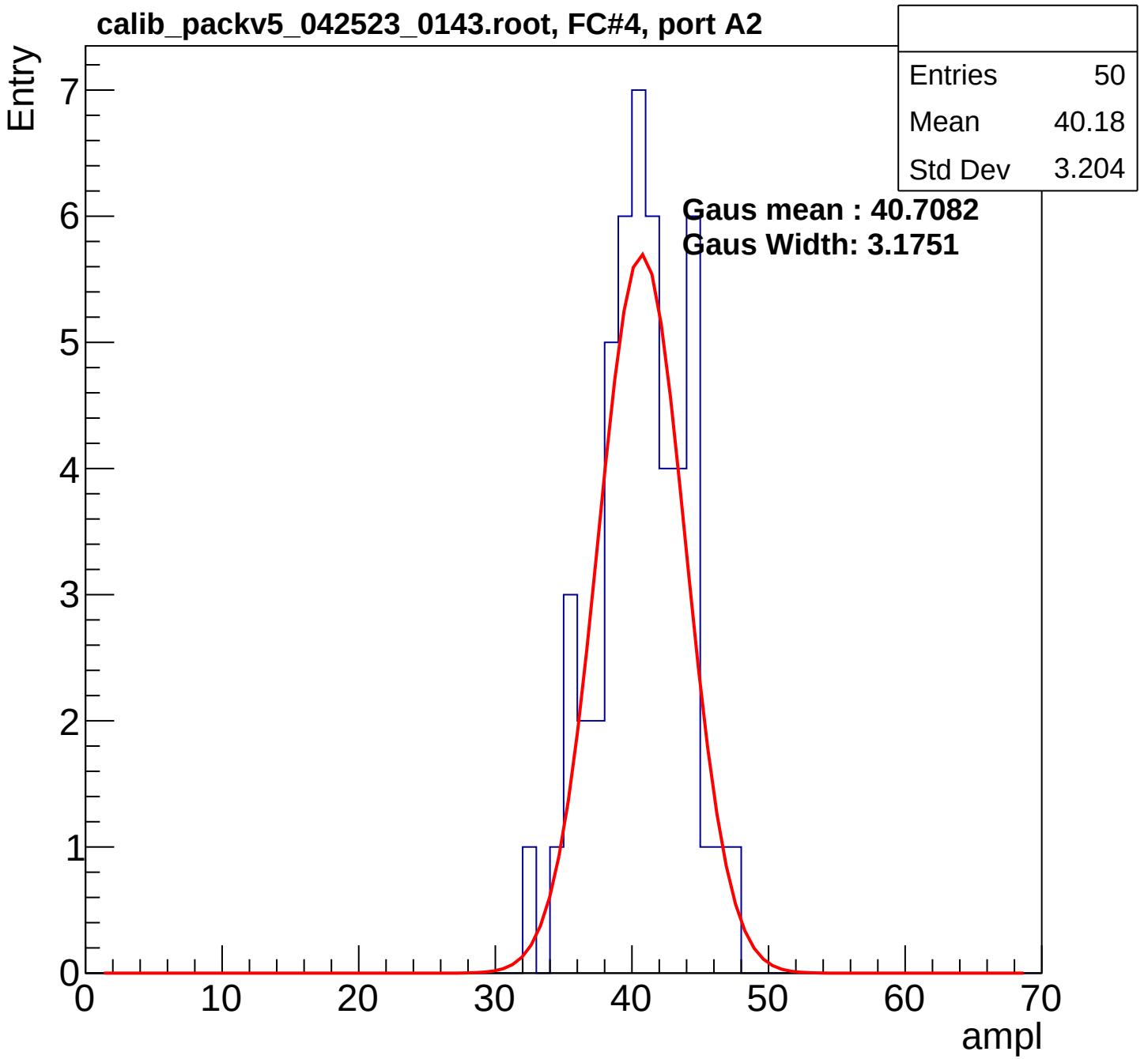
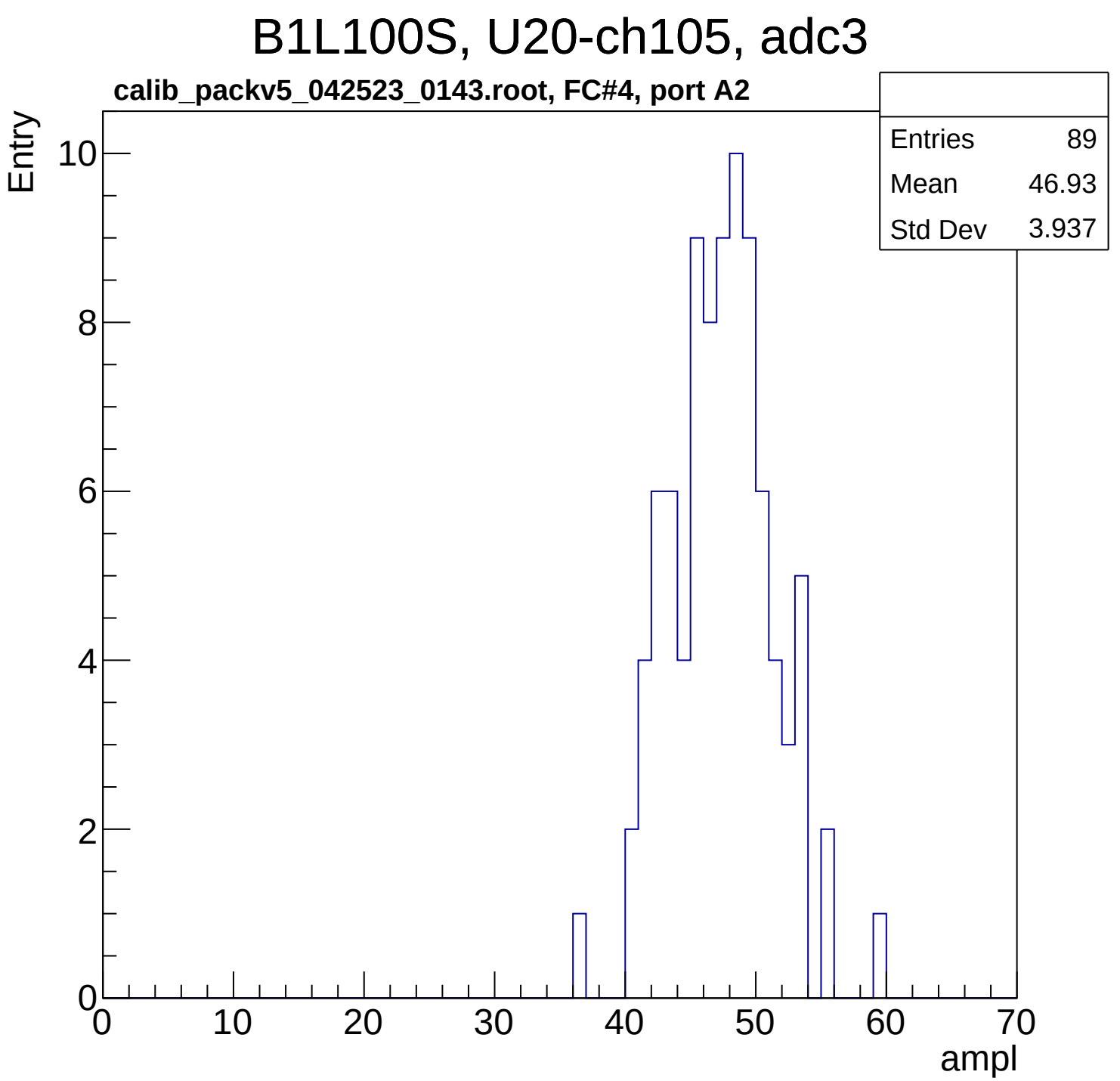
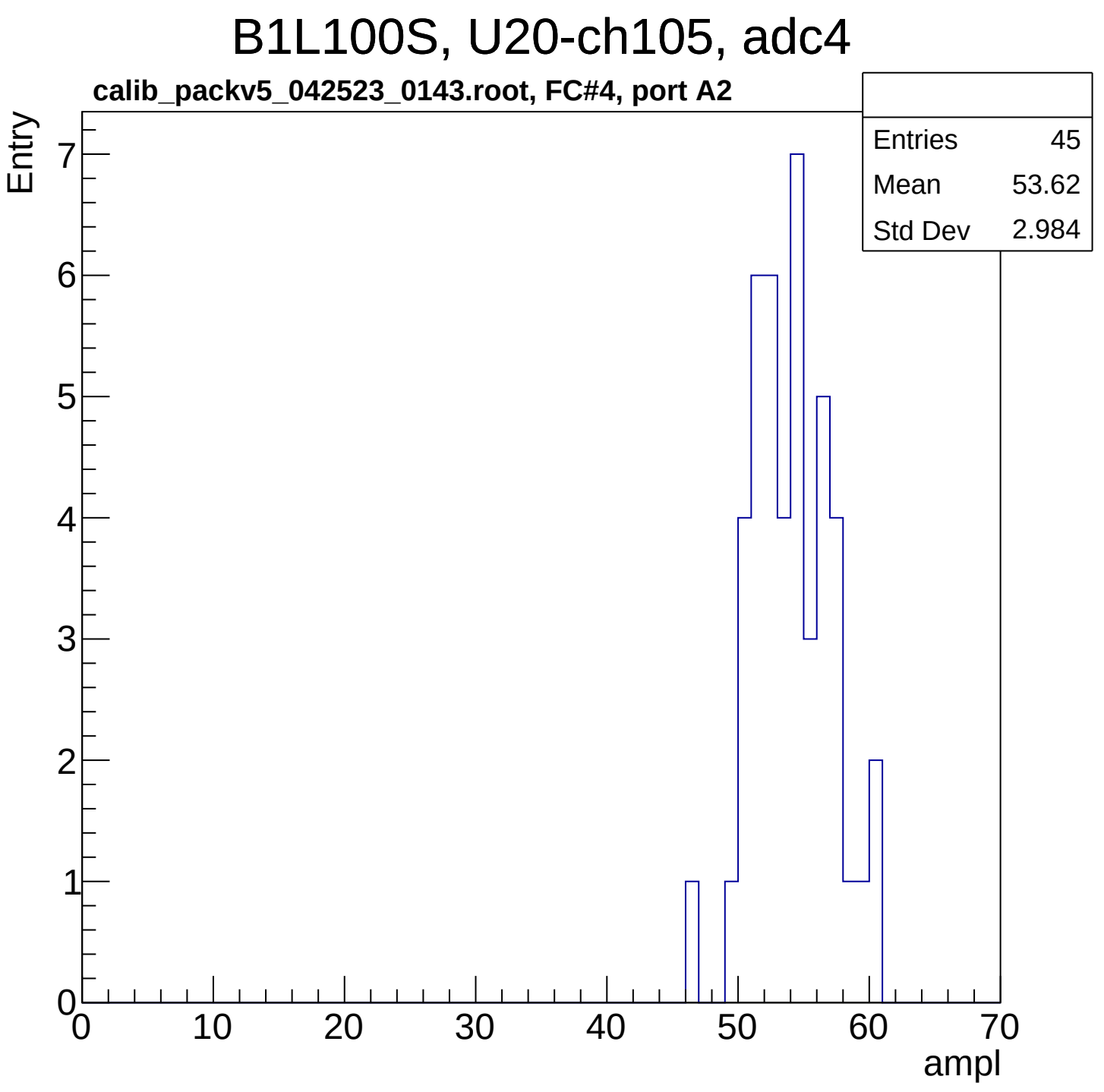
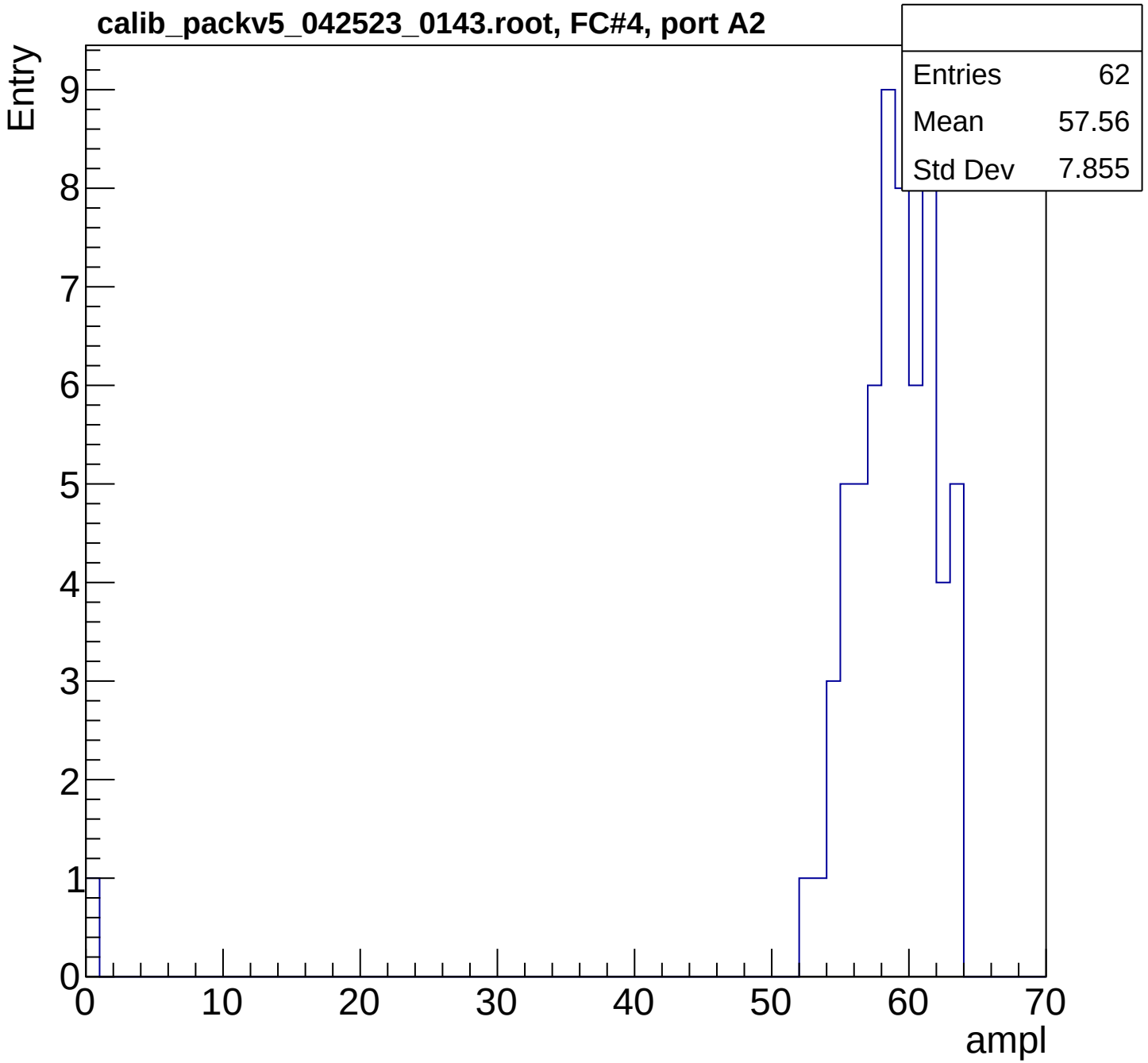
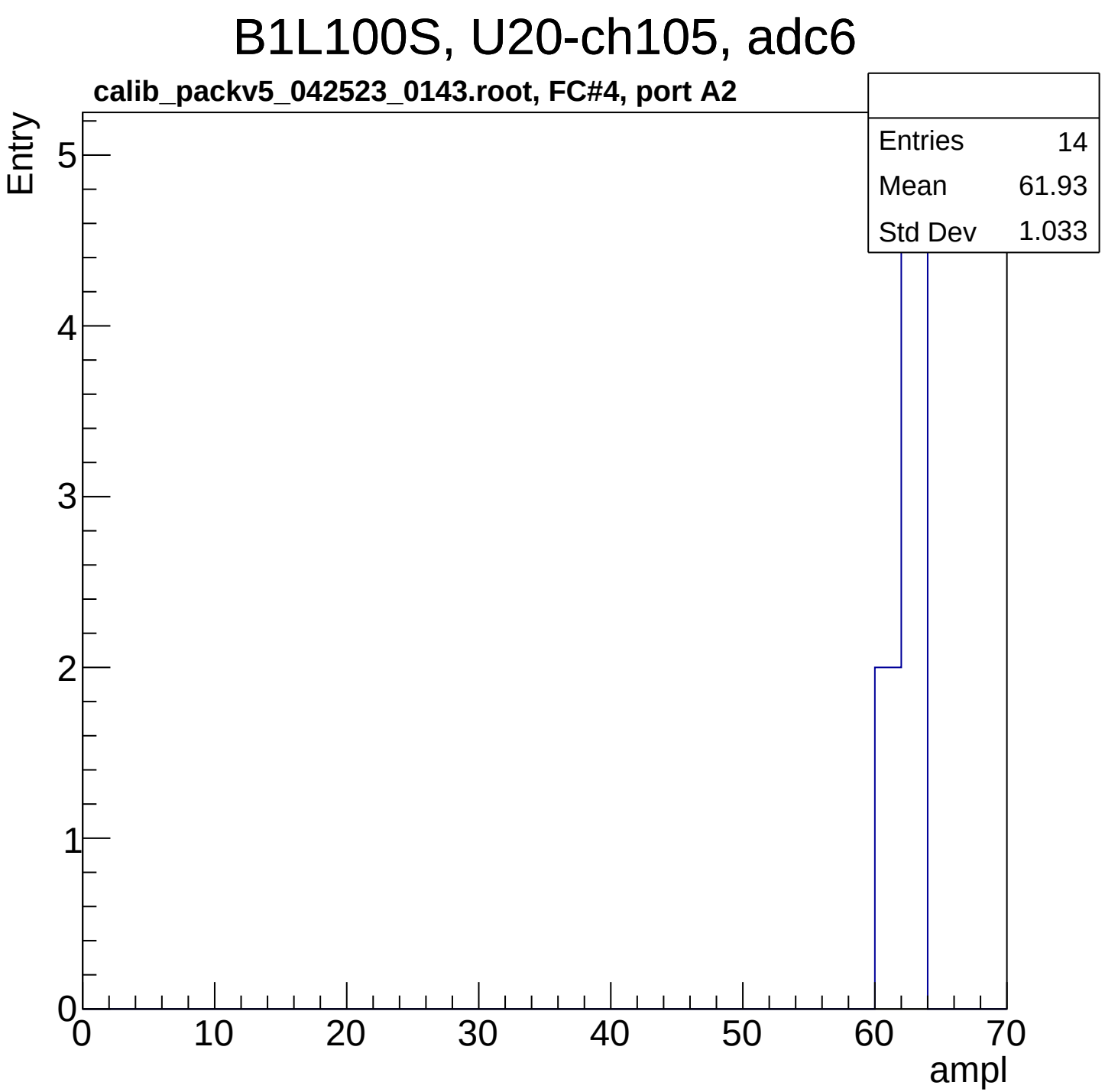
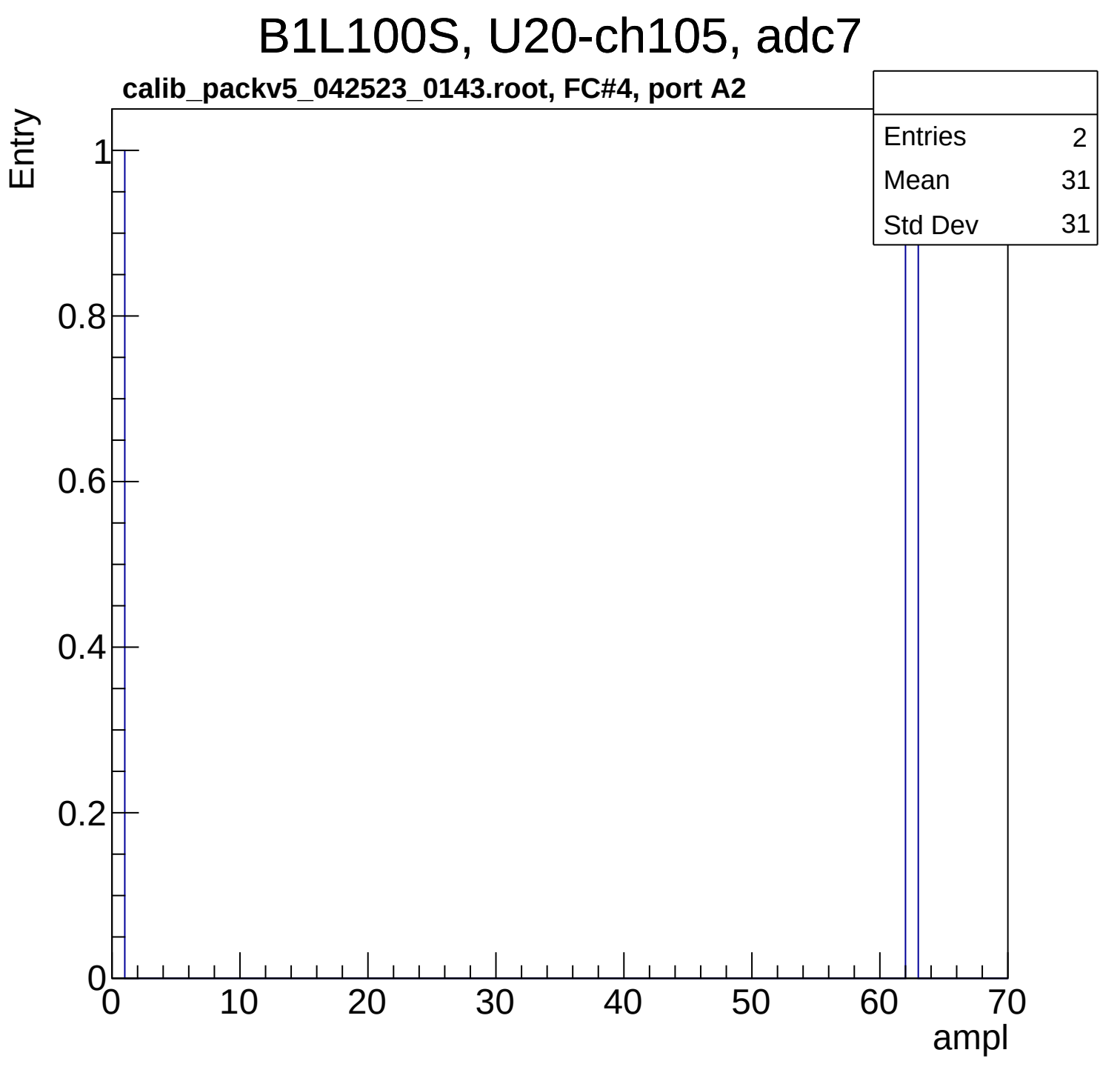

B1L100S, U20-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 40.18
Std Dev 3.204
Gaus mean : 40.7082
Gaus Width: 3.1751
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch105, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 46.93
Std Dev 3.937
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 53.62
Std Dev 2.984
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch105, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.56
Std Dev 7.855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 61.93
Std Dev 1.033
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
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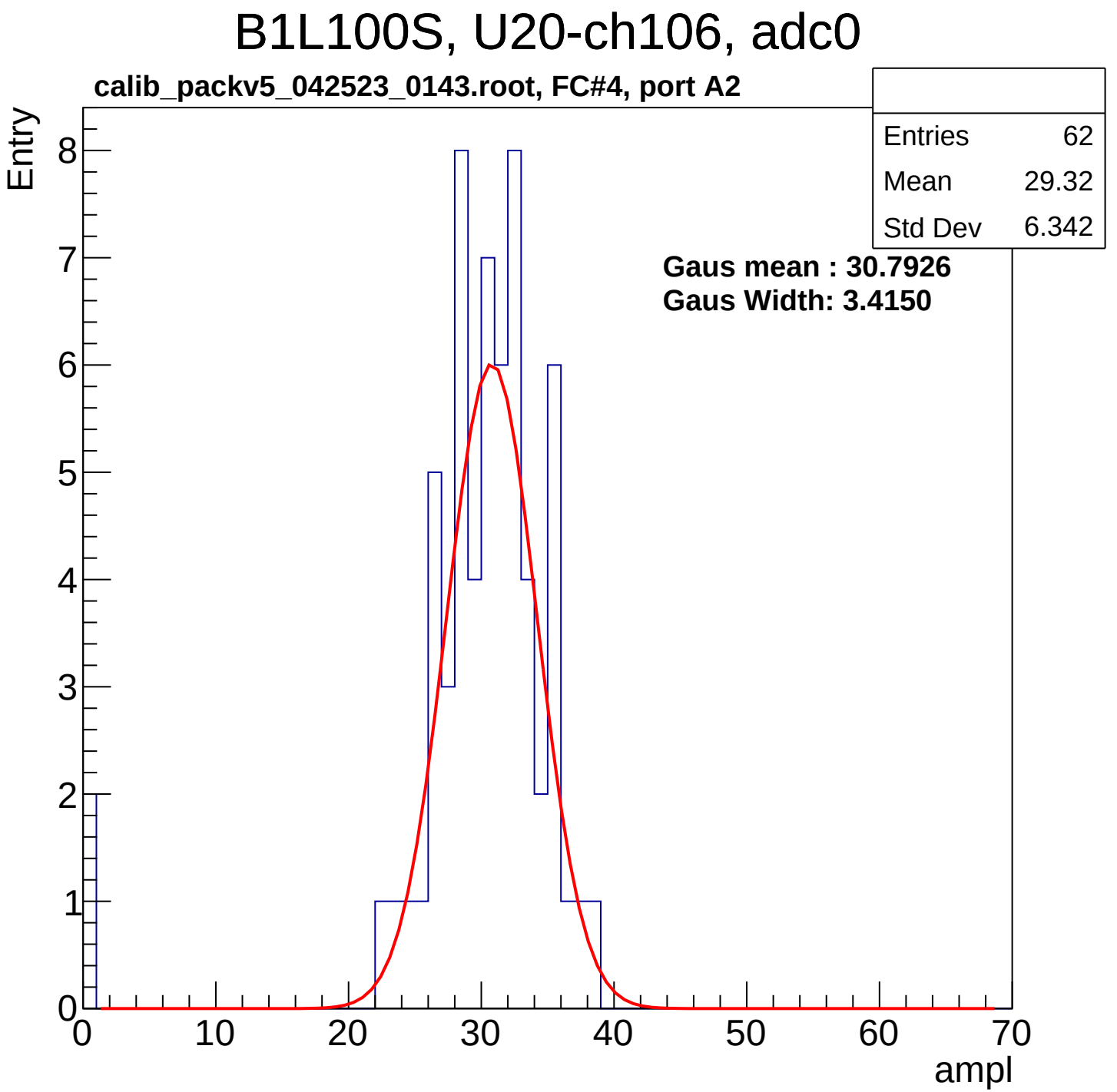
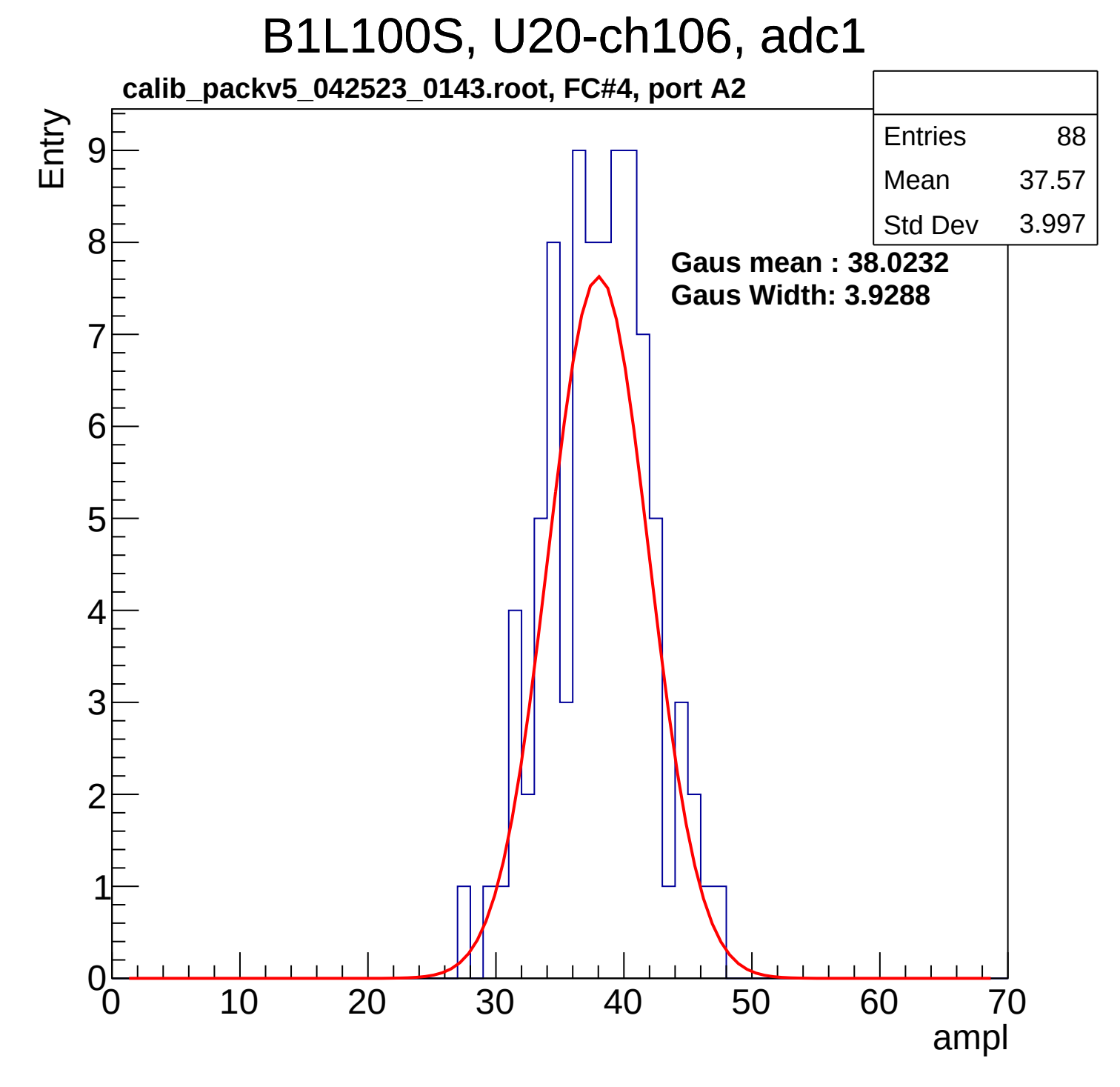
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.32
Std Dev 6.342
Gaus mean : 30.7926
Gaus Width: 3.4150
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch106, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 37.57
Std Dev 3.997
Gaus mean : 38.0232
Gaus Width: 3.9288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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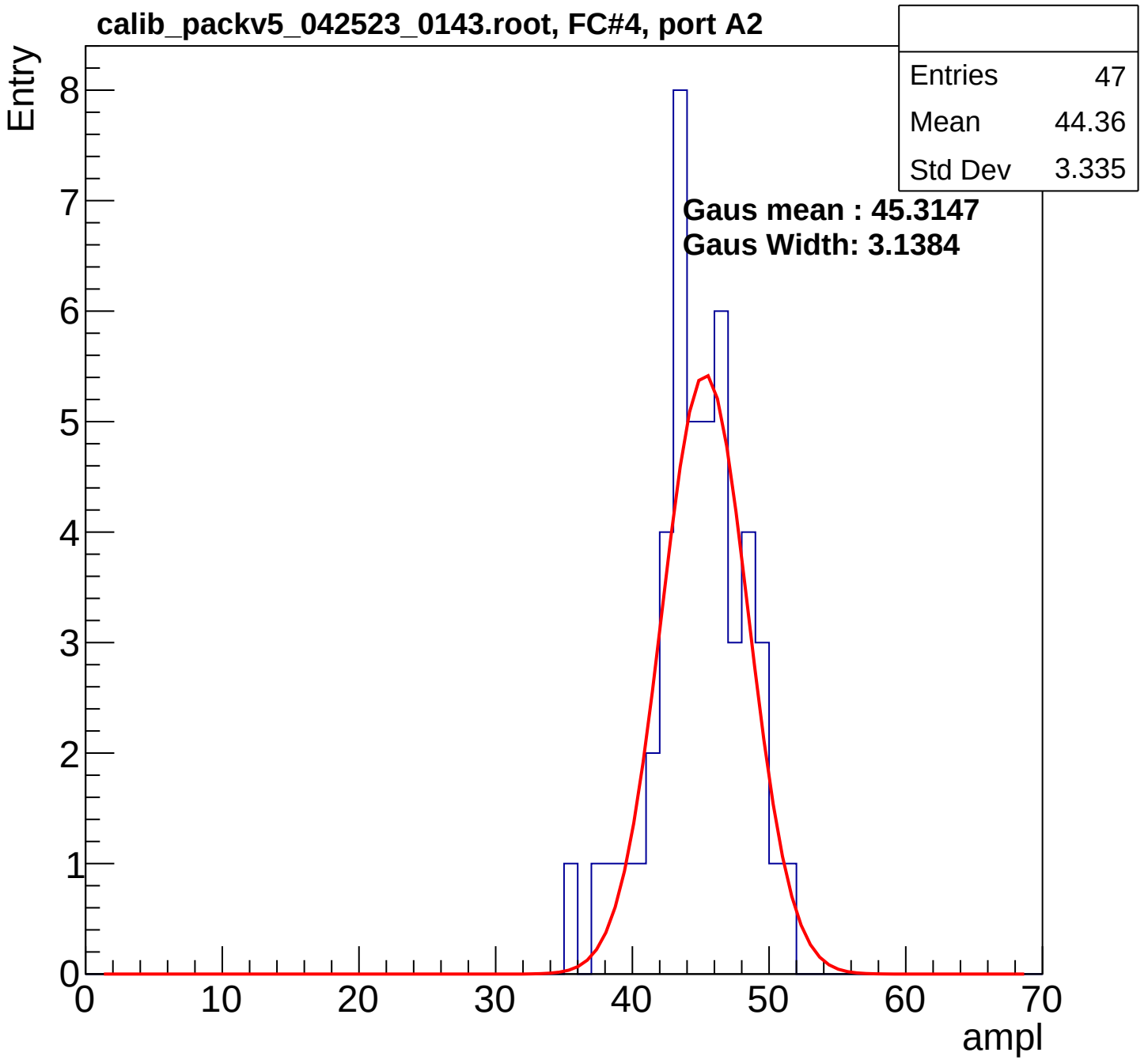
B1L100S, U20-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 44.36
Std Dev 3.335
Gaus mean : 45.3147
Gaus Width: 3.1384
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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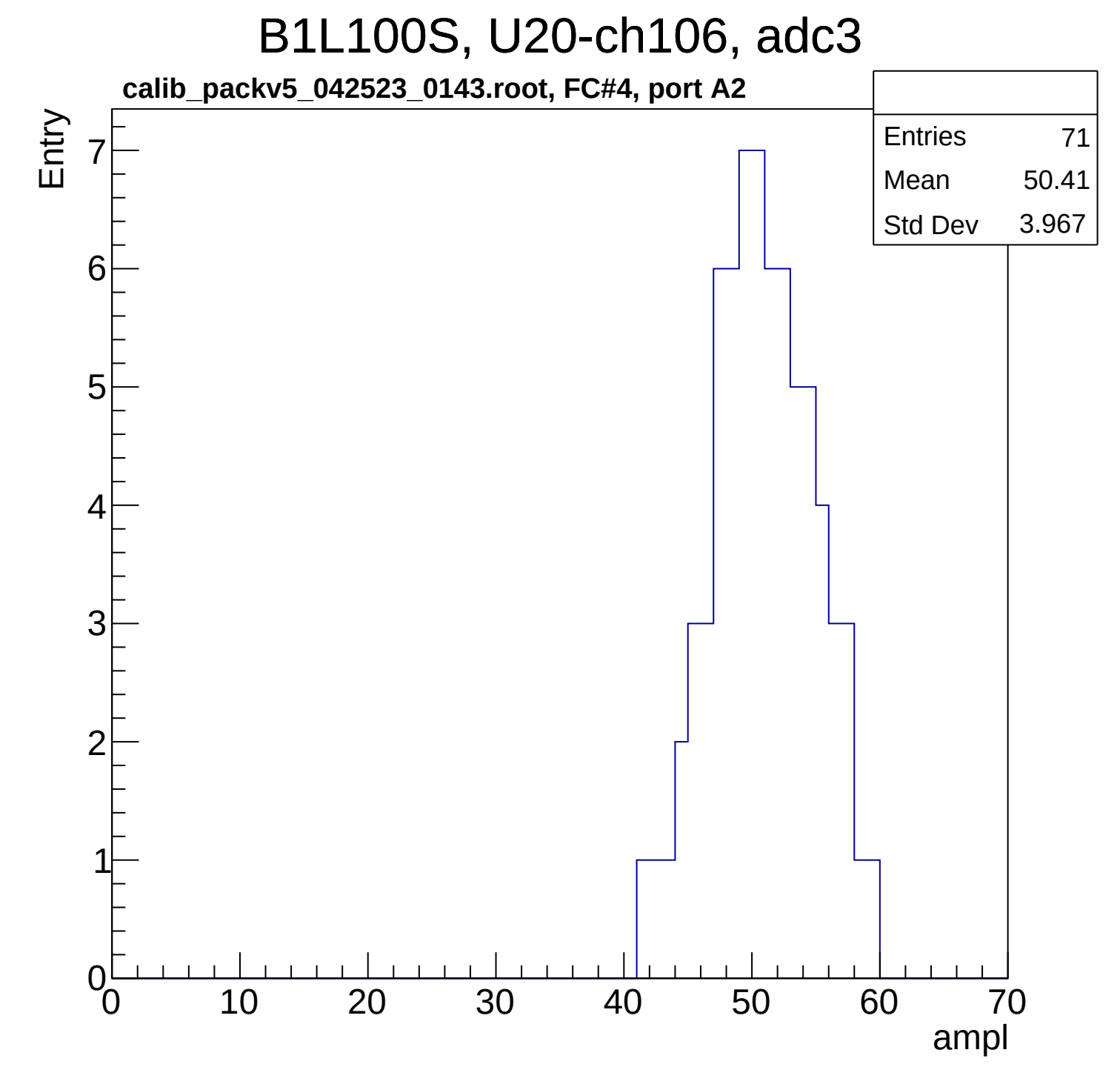
B1L100S, U20-ch106, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.41
Std Dev 3.967
Entry
ampl
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1
2
3
4
5
6
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0
10
20
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40
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60
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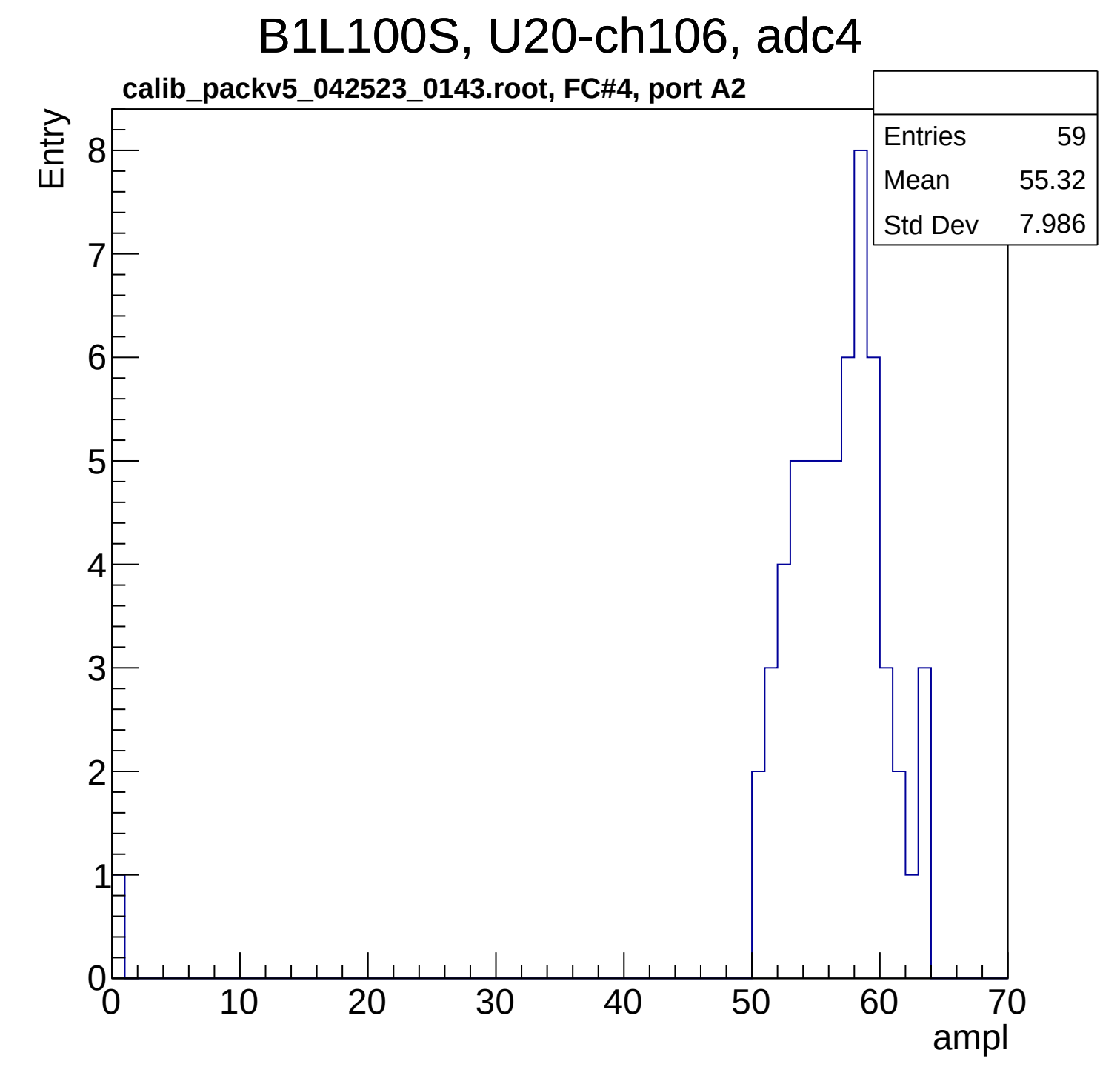
B1L100S, U20-ch106, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.32
Std Dev 7.986
Entry
ampl
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1
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4
5
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7
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0
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60
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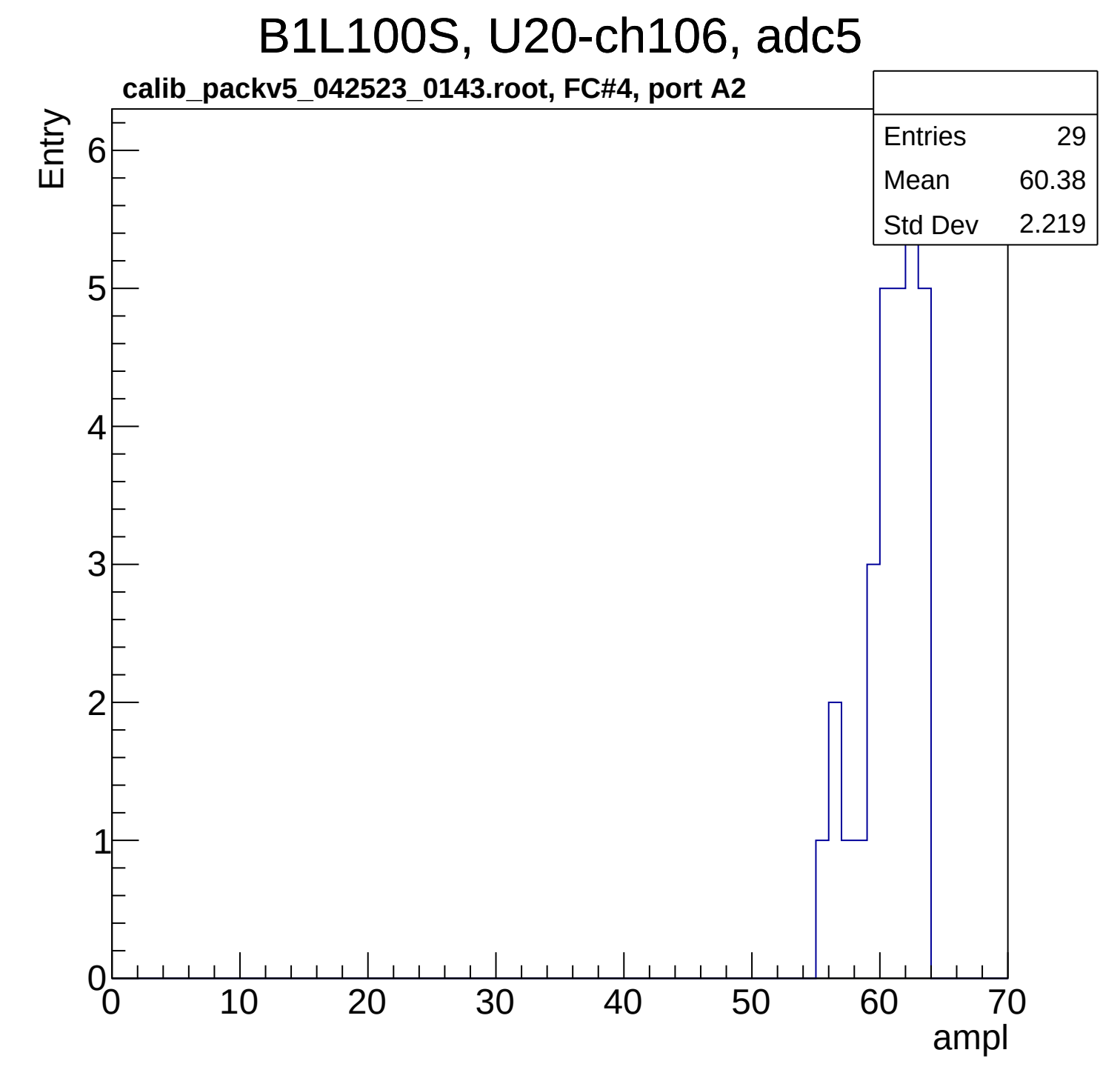
B1L100S, U20-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 60.38
Std Dev 2.219
Entry
ampl
0
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0
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60
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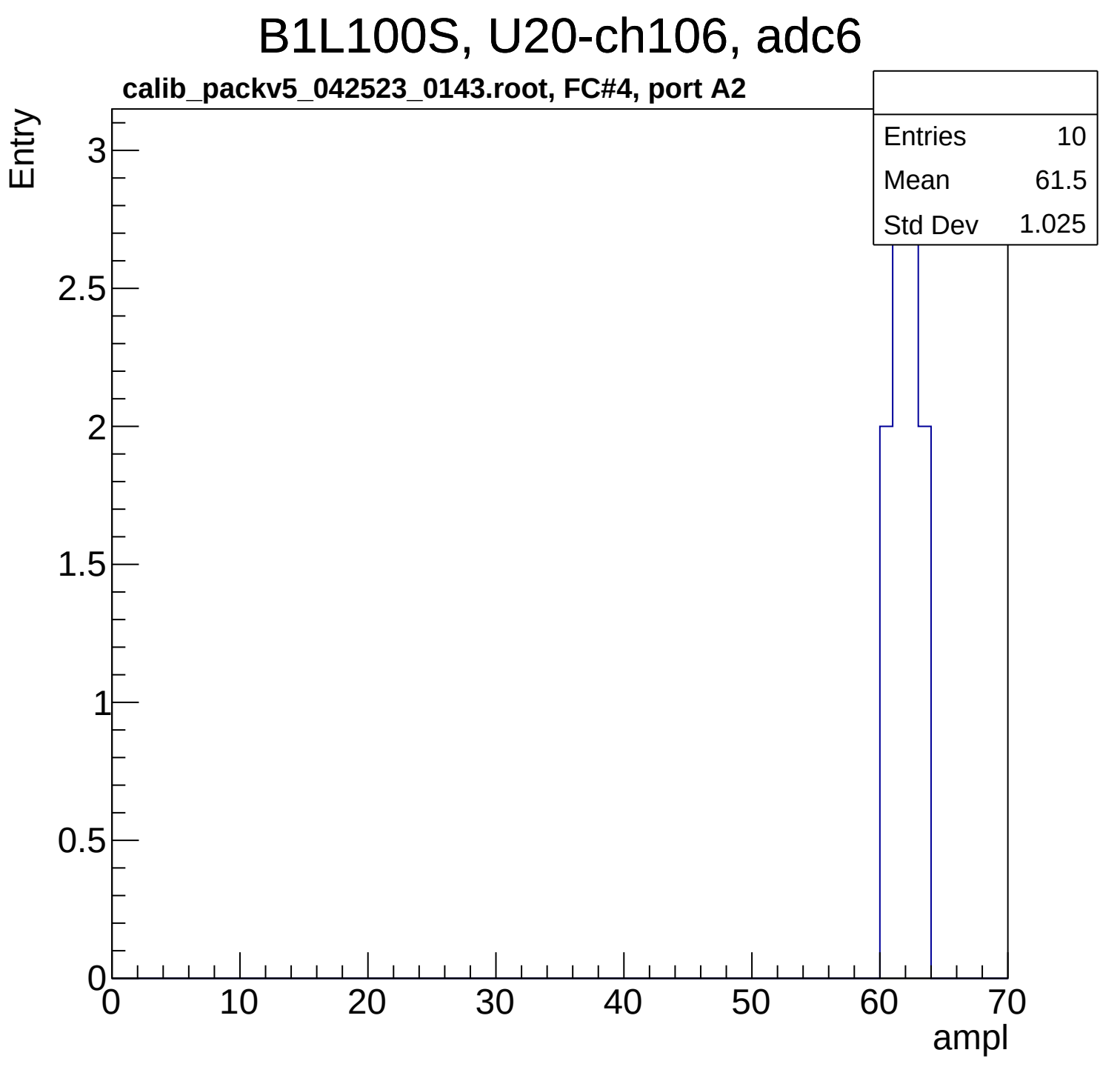
B1L100S, U20-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.5
Std Dev 1.025
Entry
ampl
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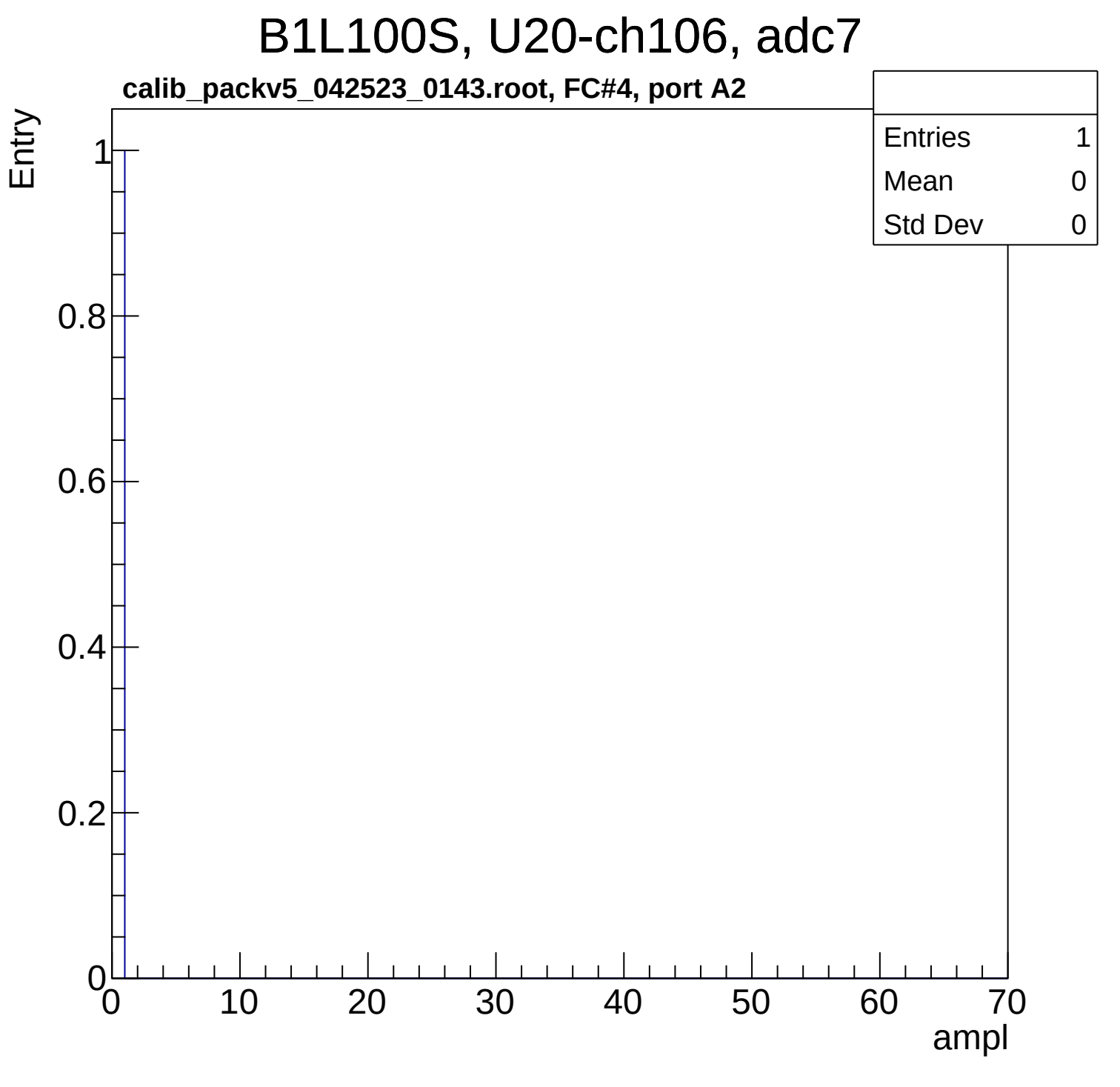
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
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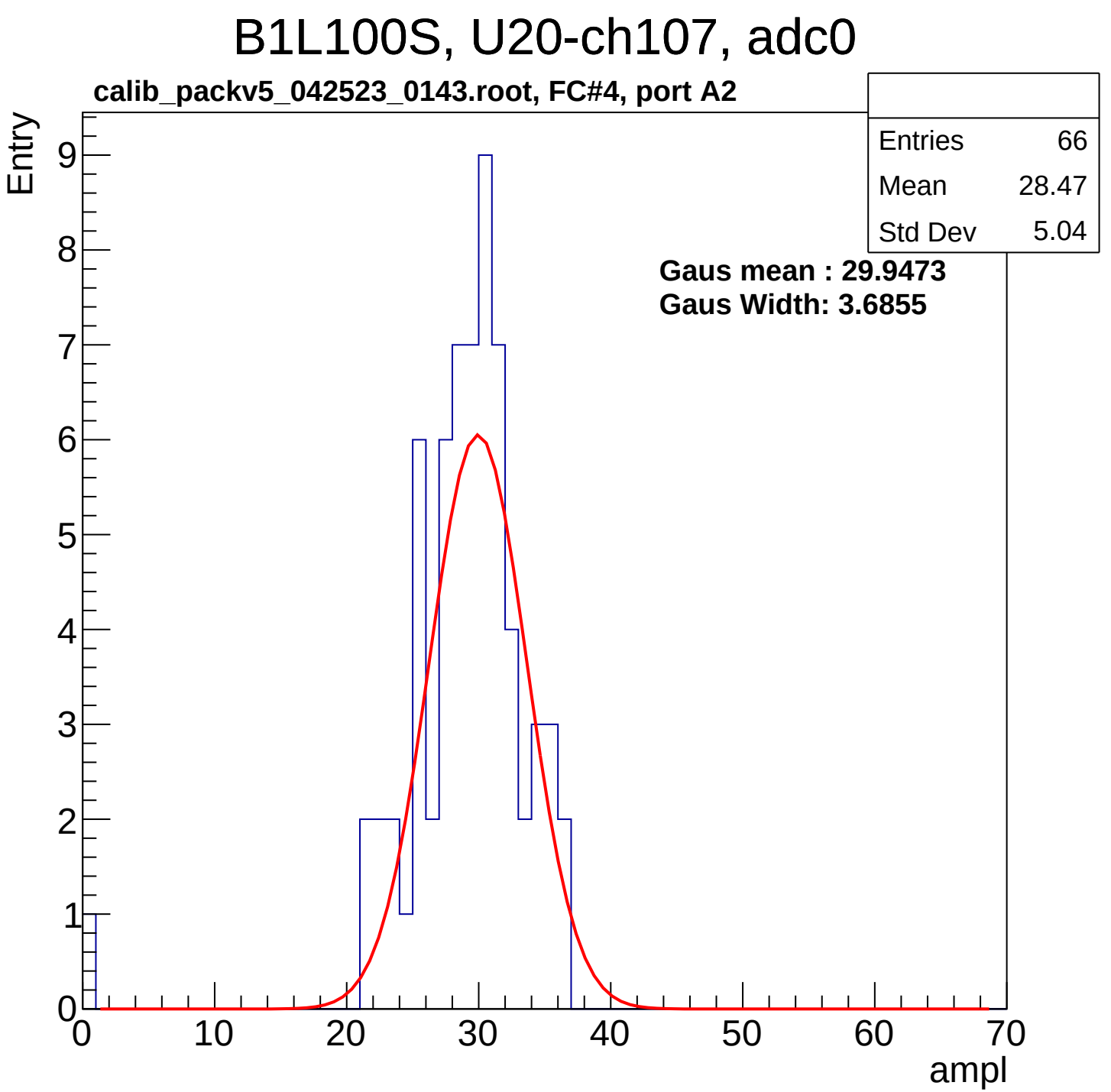
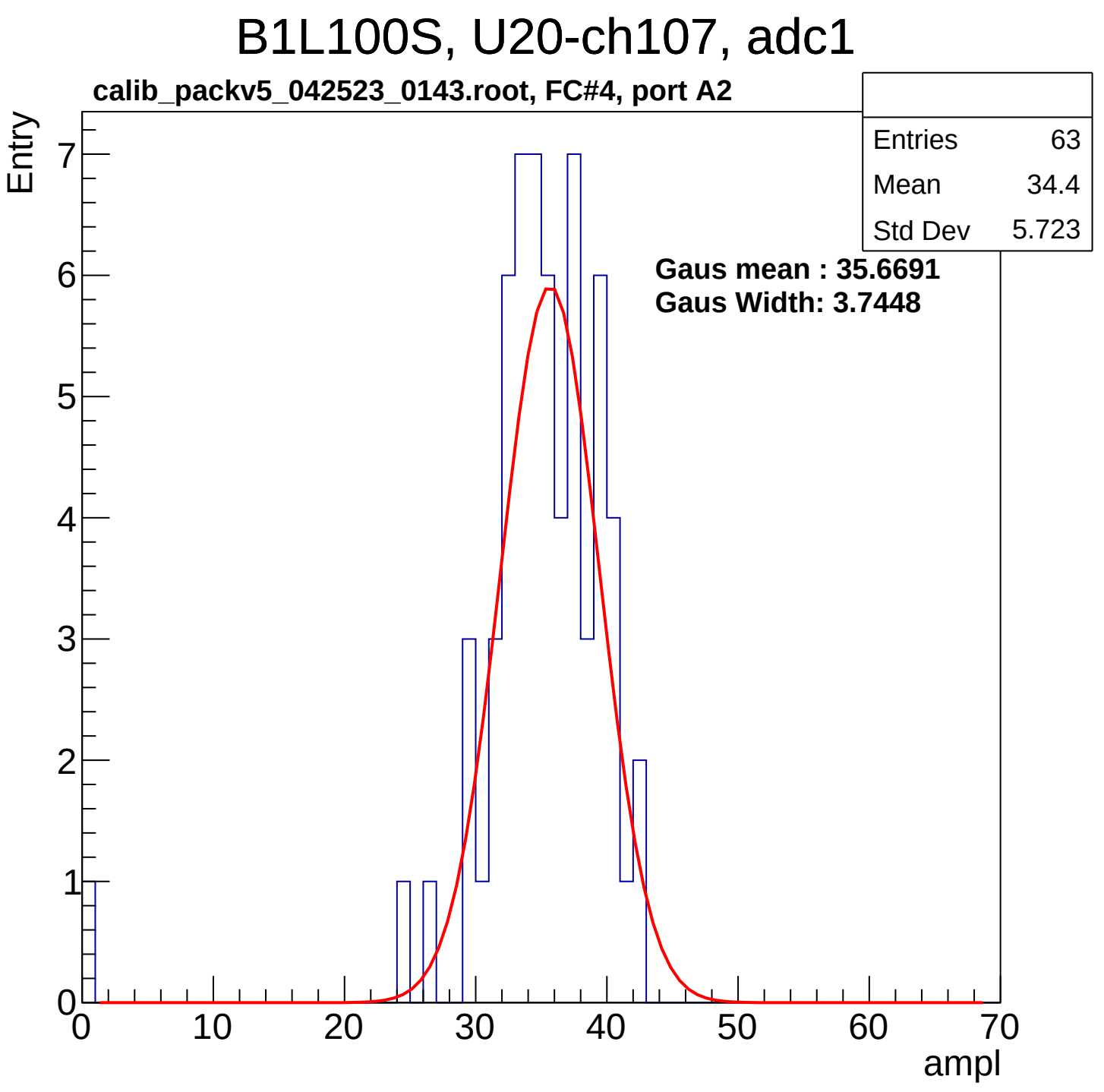
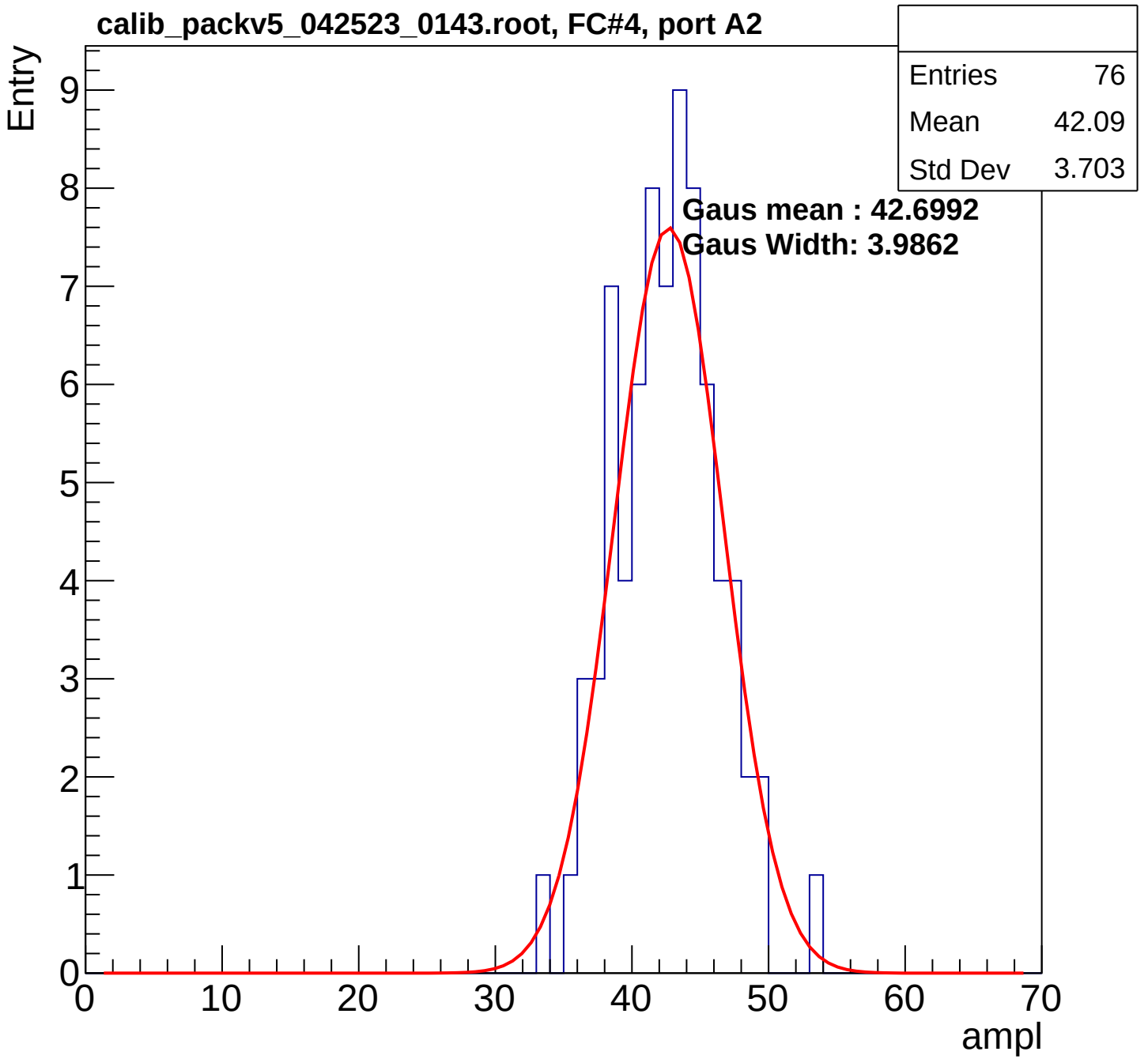
50
60
70
ampl


B1L100S, U20-ch107, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.47
Std Dev 5.04
Gaus mean : 29.9473
Gaus Width: 3.6855
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 34.4
Std Dev 5.723
Gaus mean : 35.6691
Gaus Width: 3.7448
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 42.09
Std Dev 3.703
Gaus mean : 42.6992
Gaus Width: 3.9862
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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50
60
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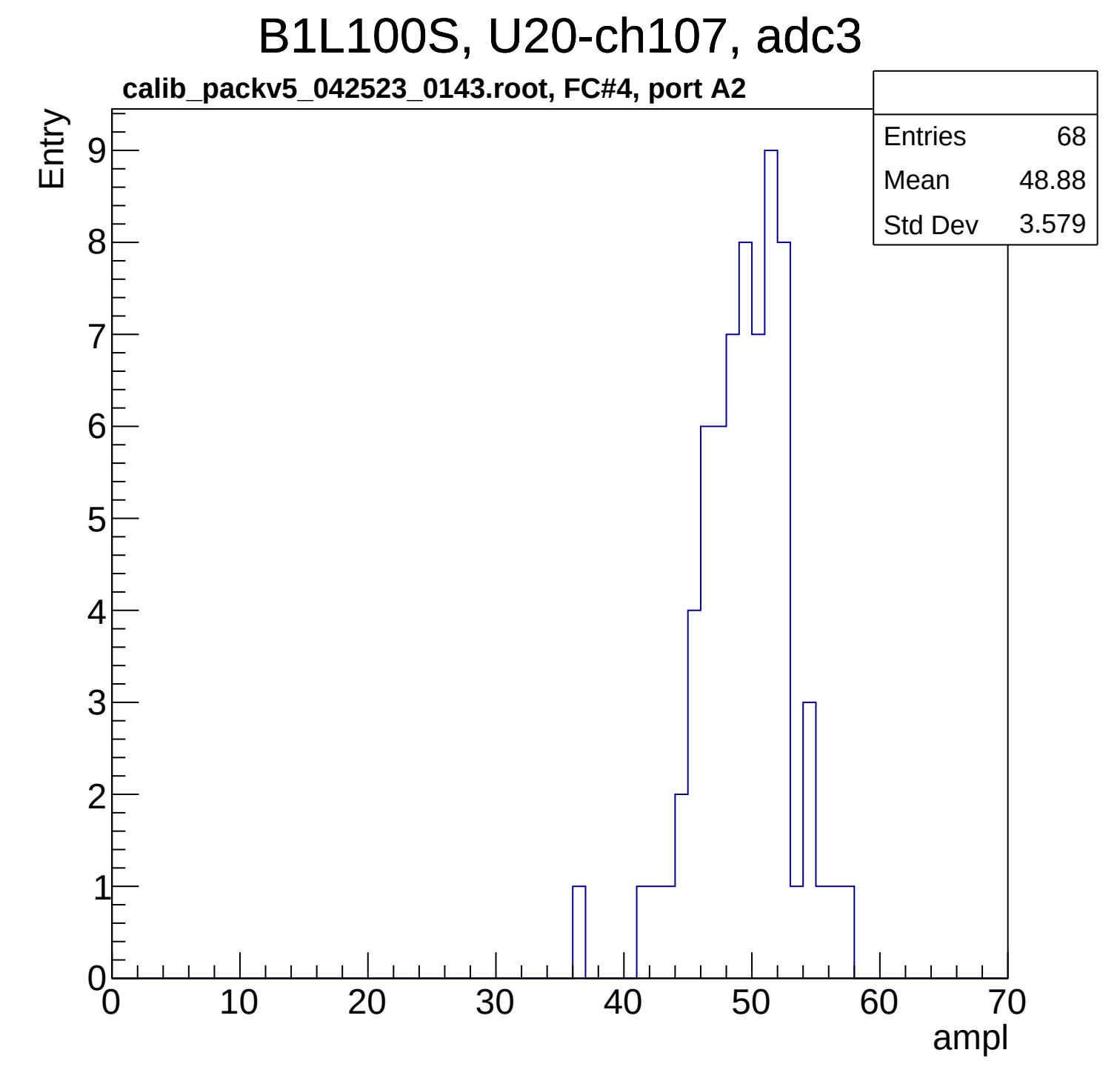
B1L100S, U20-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.88
Std Dev 3.579
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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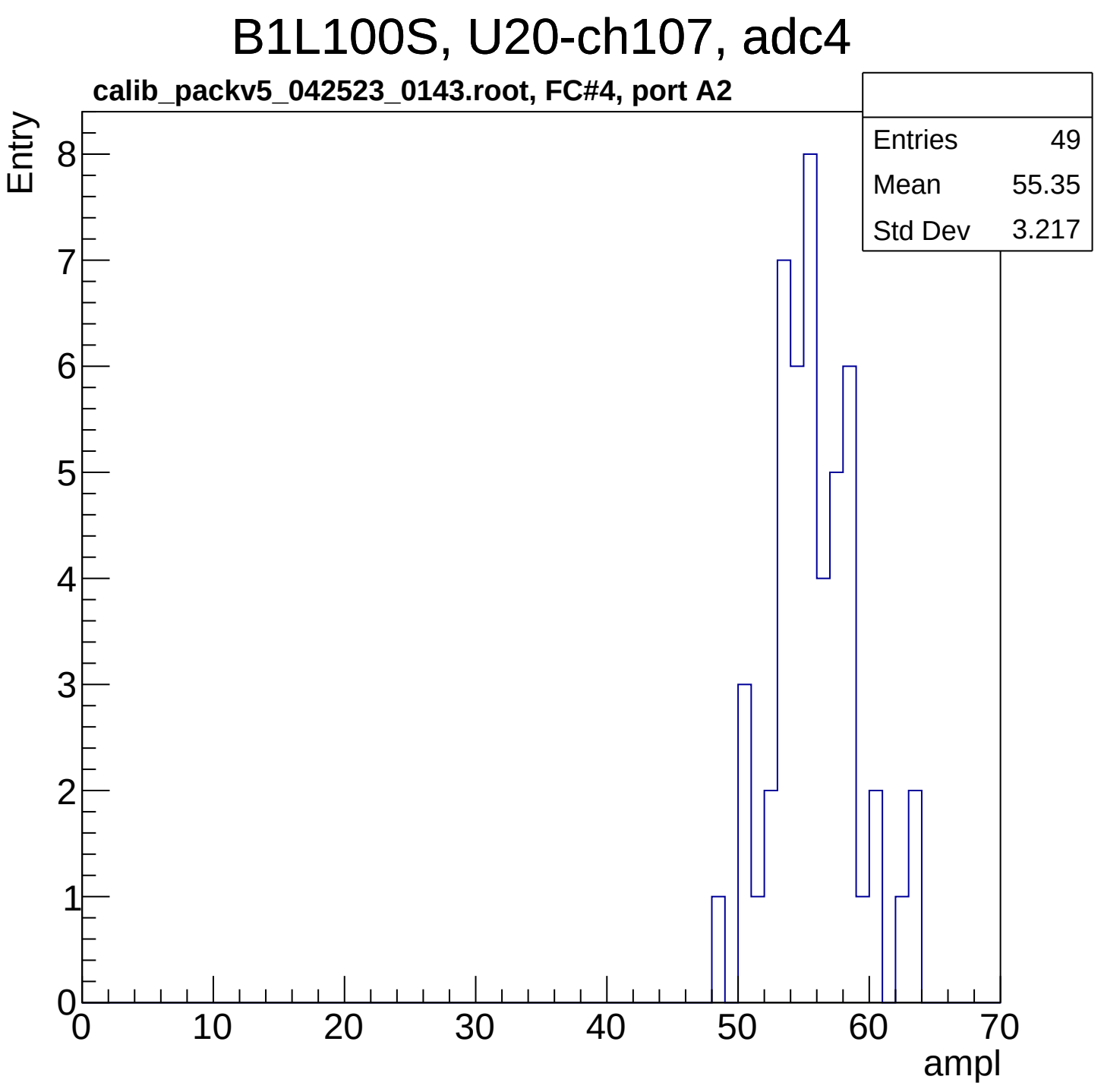
B1L100S, U20-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.35
Std Dev 3.217
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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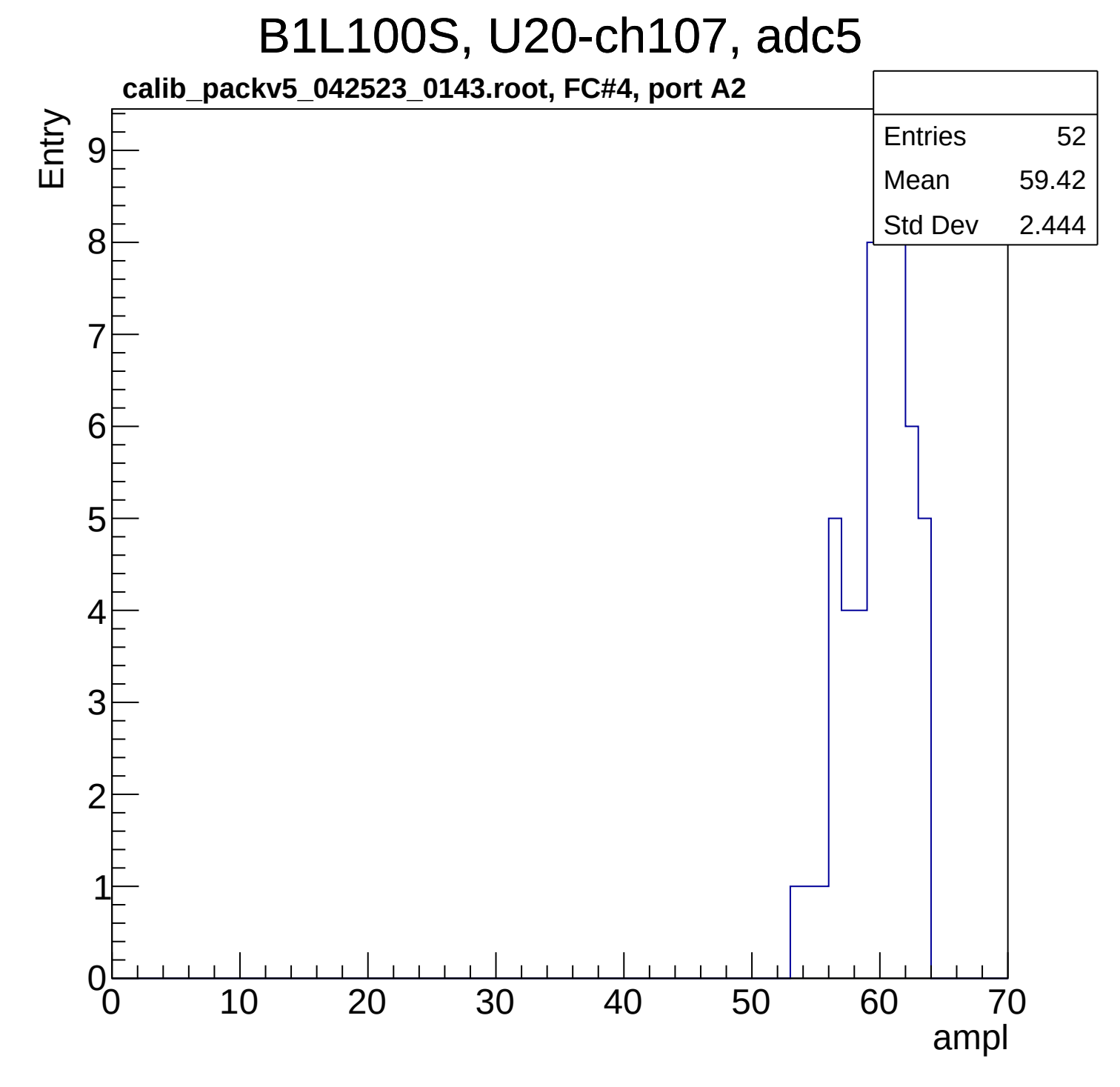
B1L100S, U20-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 59.42
Std Dev 2.444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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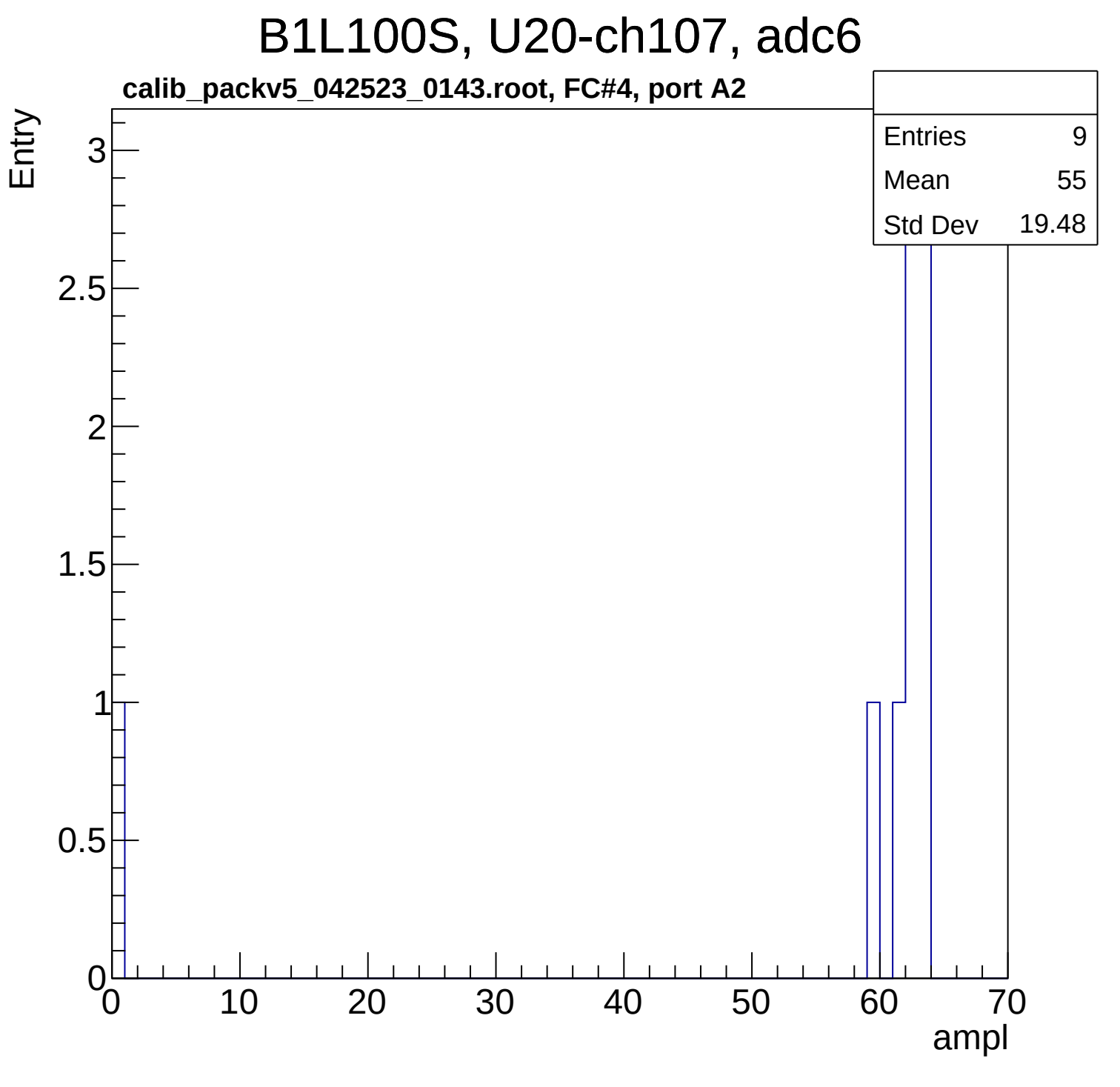
B1L100S, U20-ch107, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 55
Std Dev 19.48
Entry
ampl
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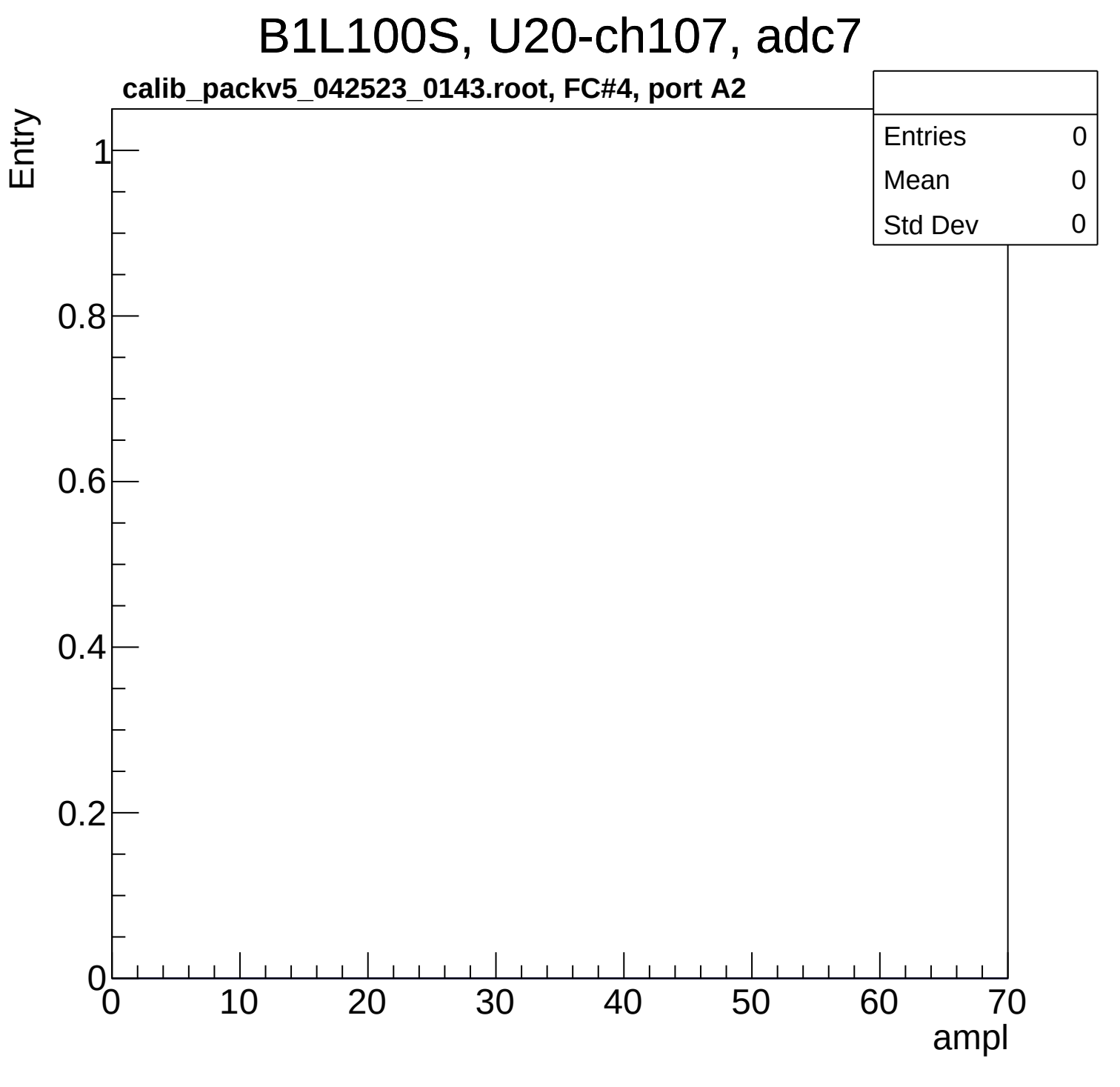
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch107, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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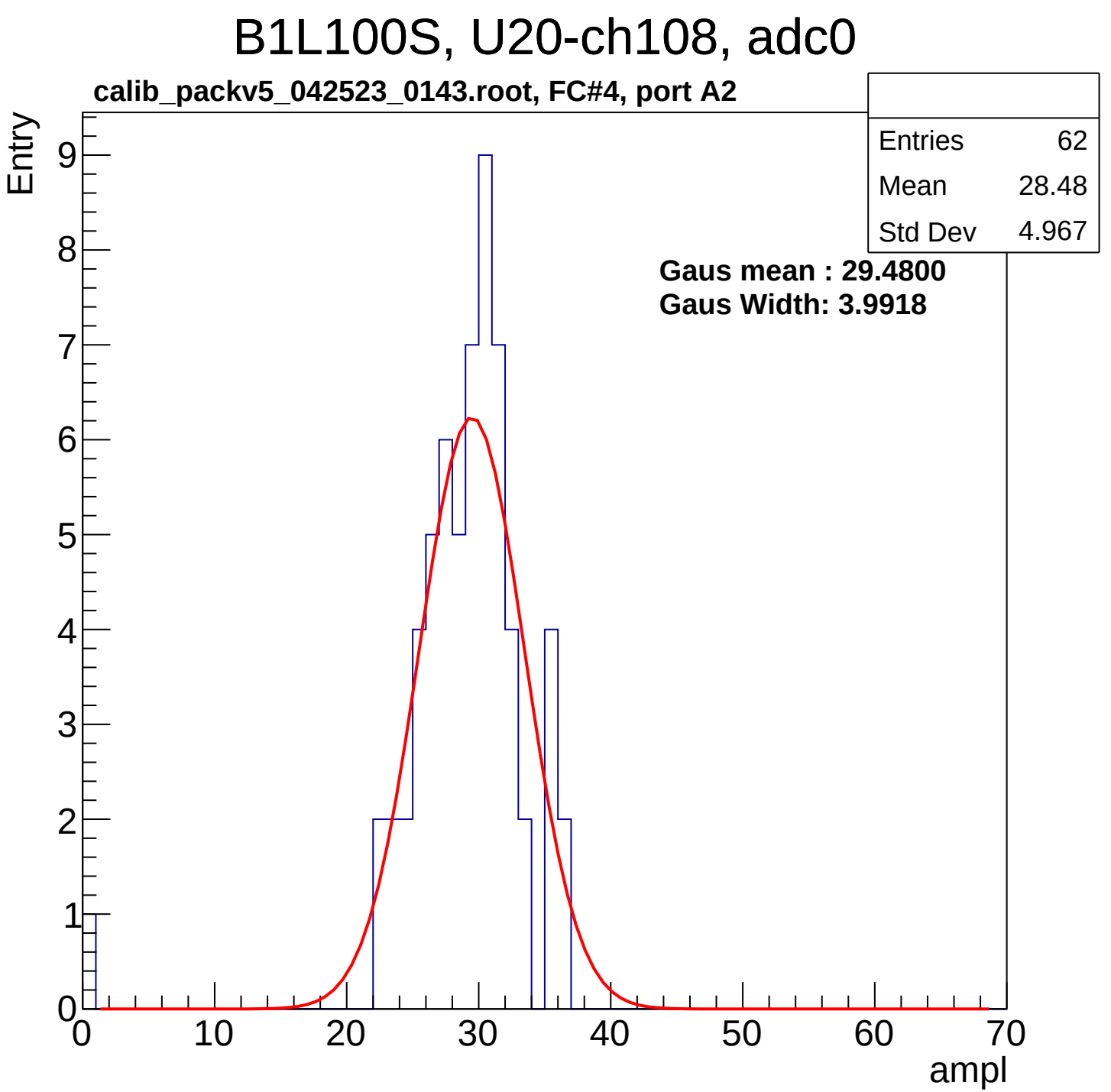
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.48
Std Dev 4.967
Gaus mean : 29.4800
Gaus Width: 3.9918
Entry
ampl
0
1
2
3
4
5
6
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0
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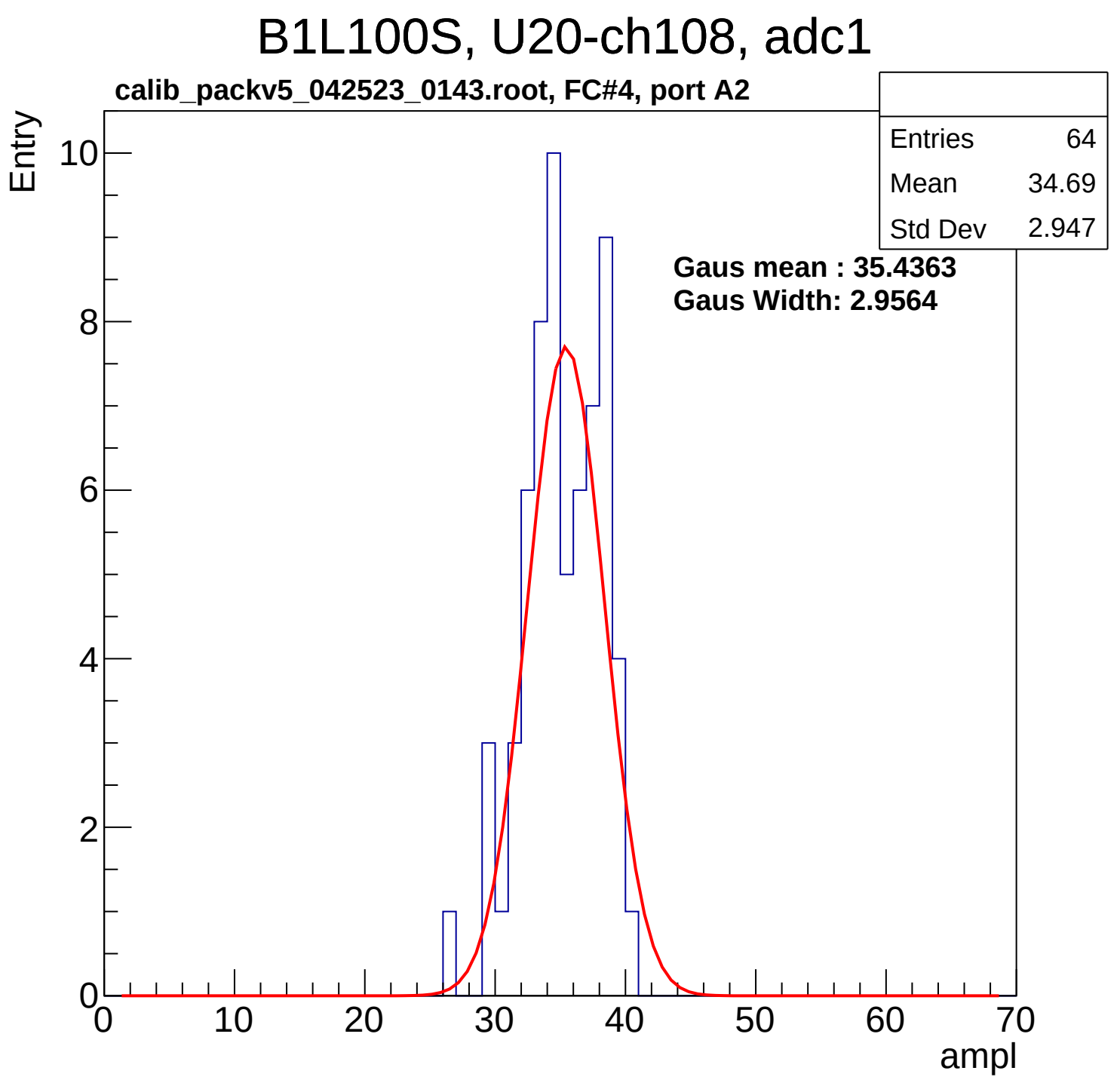
B1L100S, U20-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 34.69
Std Dev 2.947
Gaus mean : 35.4363
Gaus Width: 2.9564
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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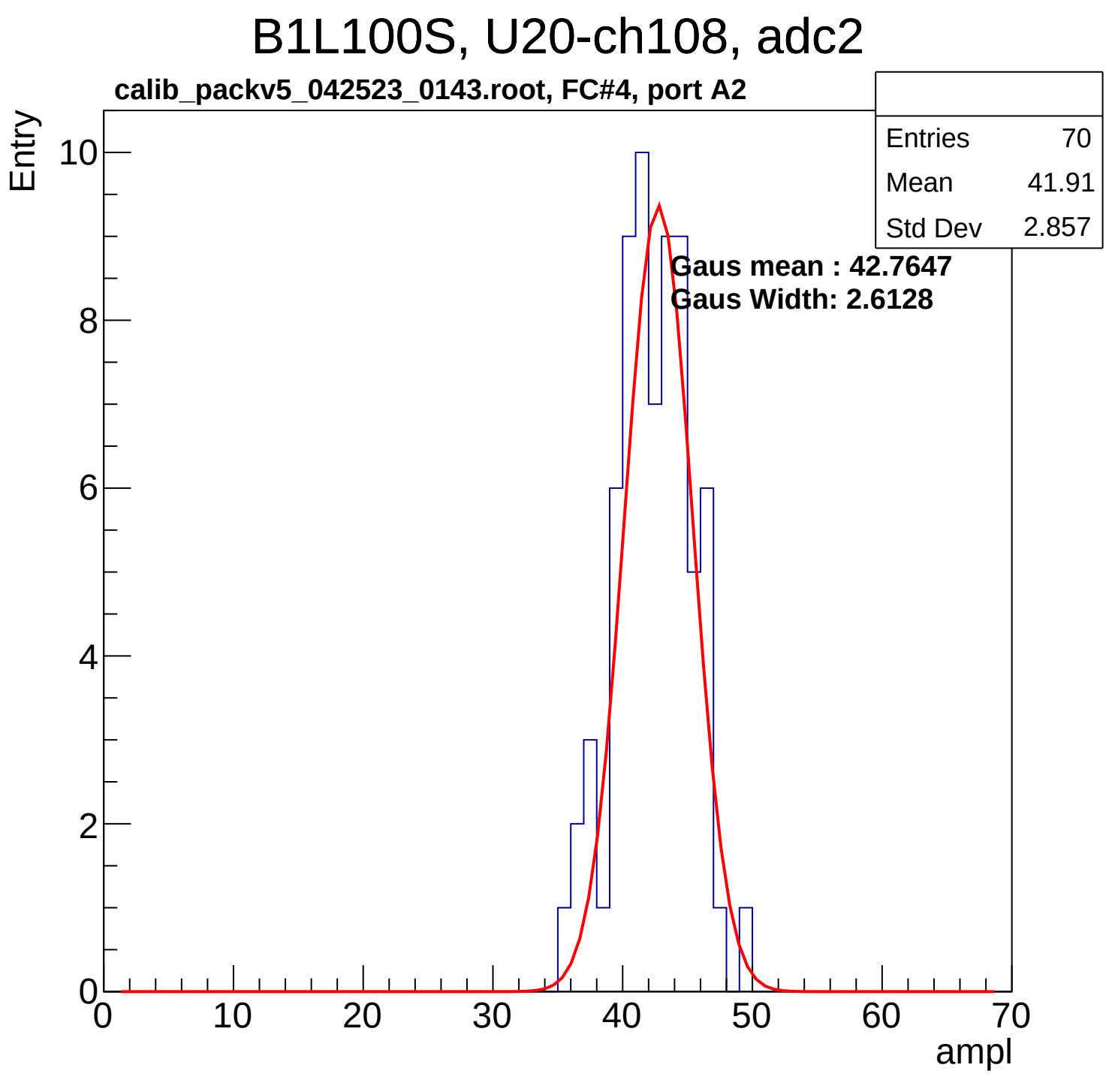
B1L100S, U20-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 41.91
Std Dev 2.857
Gaus mean : 42.7647
Gaus Width: 2.6128
Entry
ampl
0
2
4
6
8
10
0
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30
40
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60
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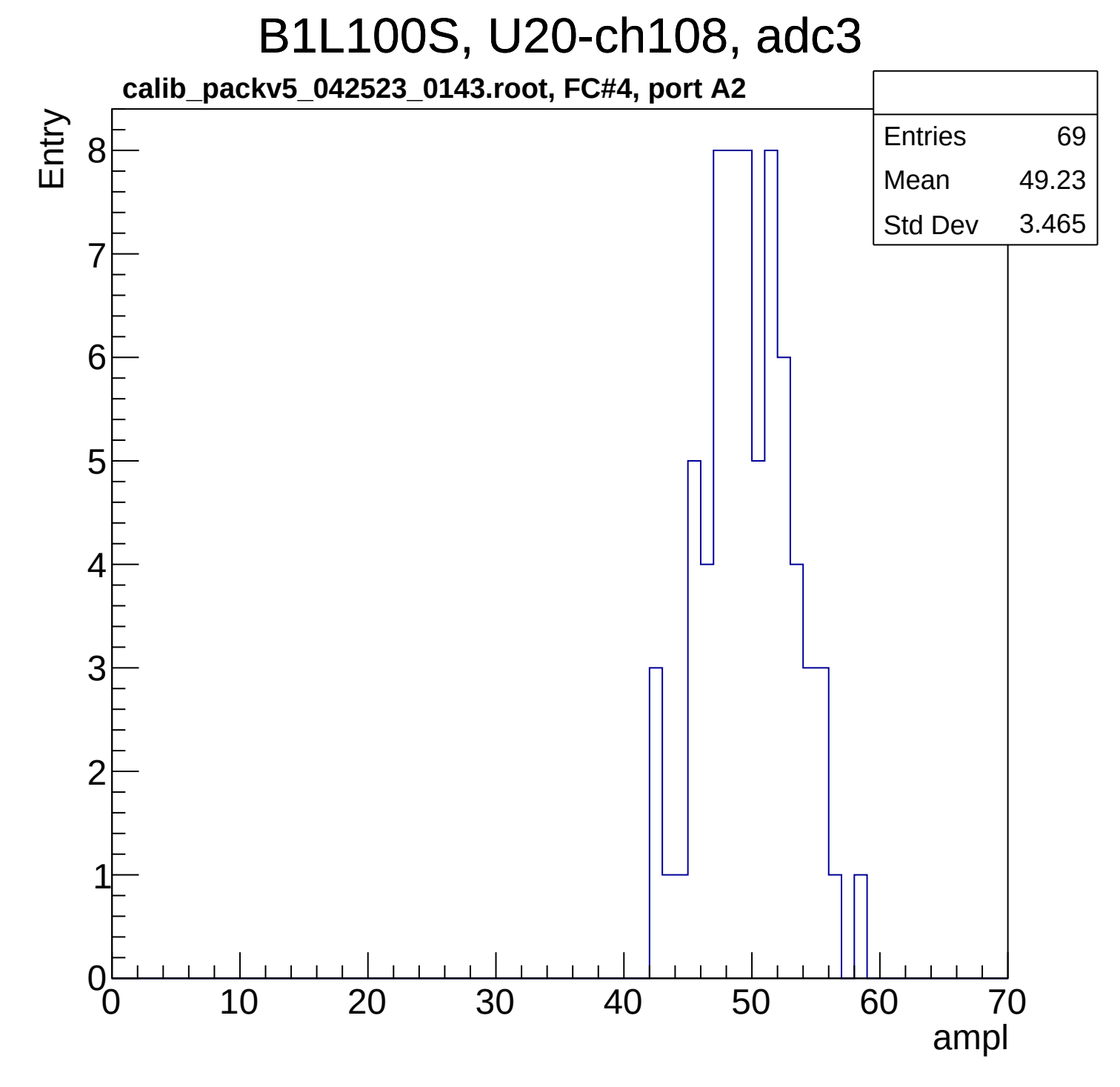
B1L100S, U20-ch108, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.23
Std Dev 3.465
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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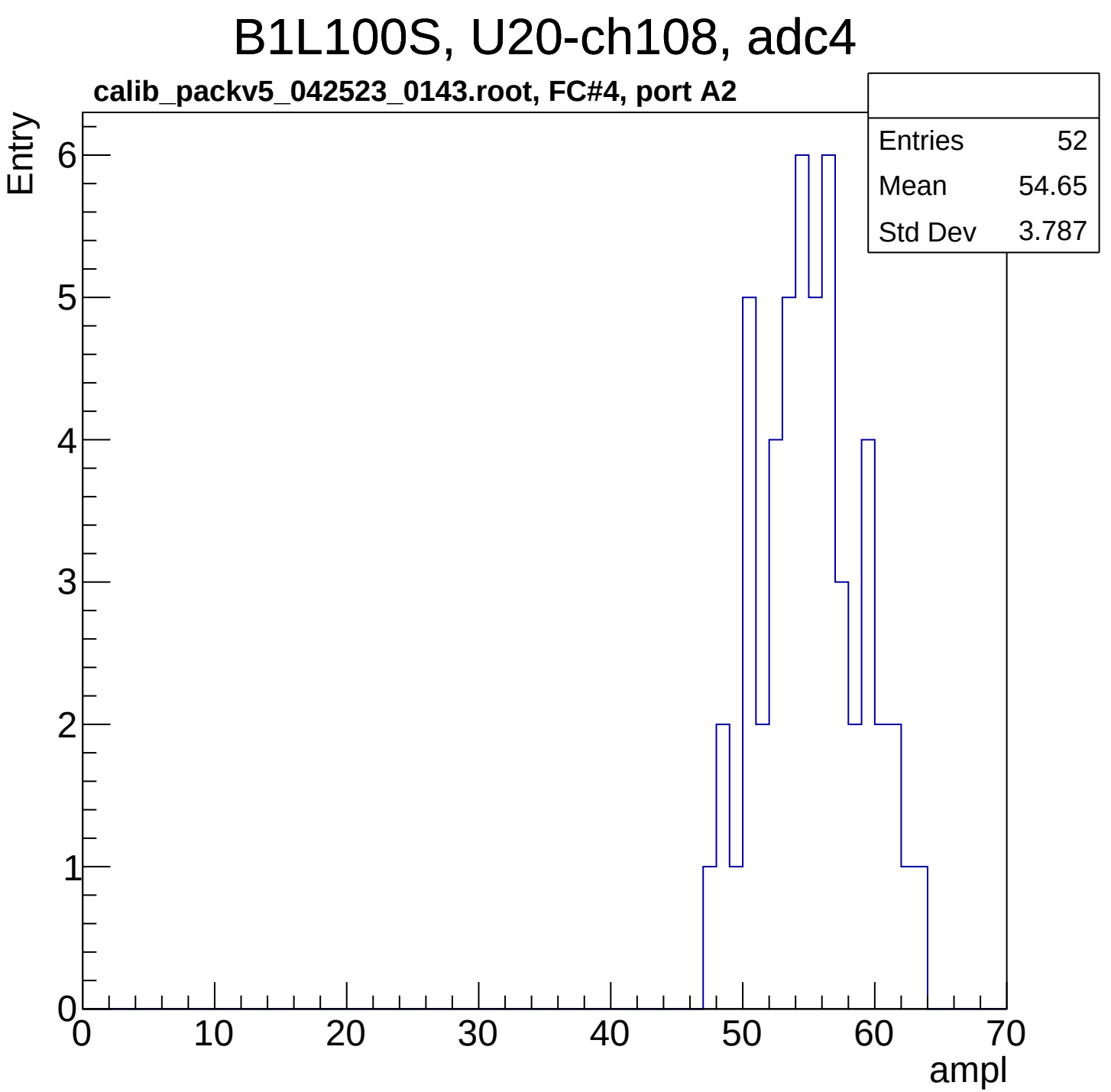
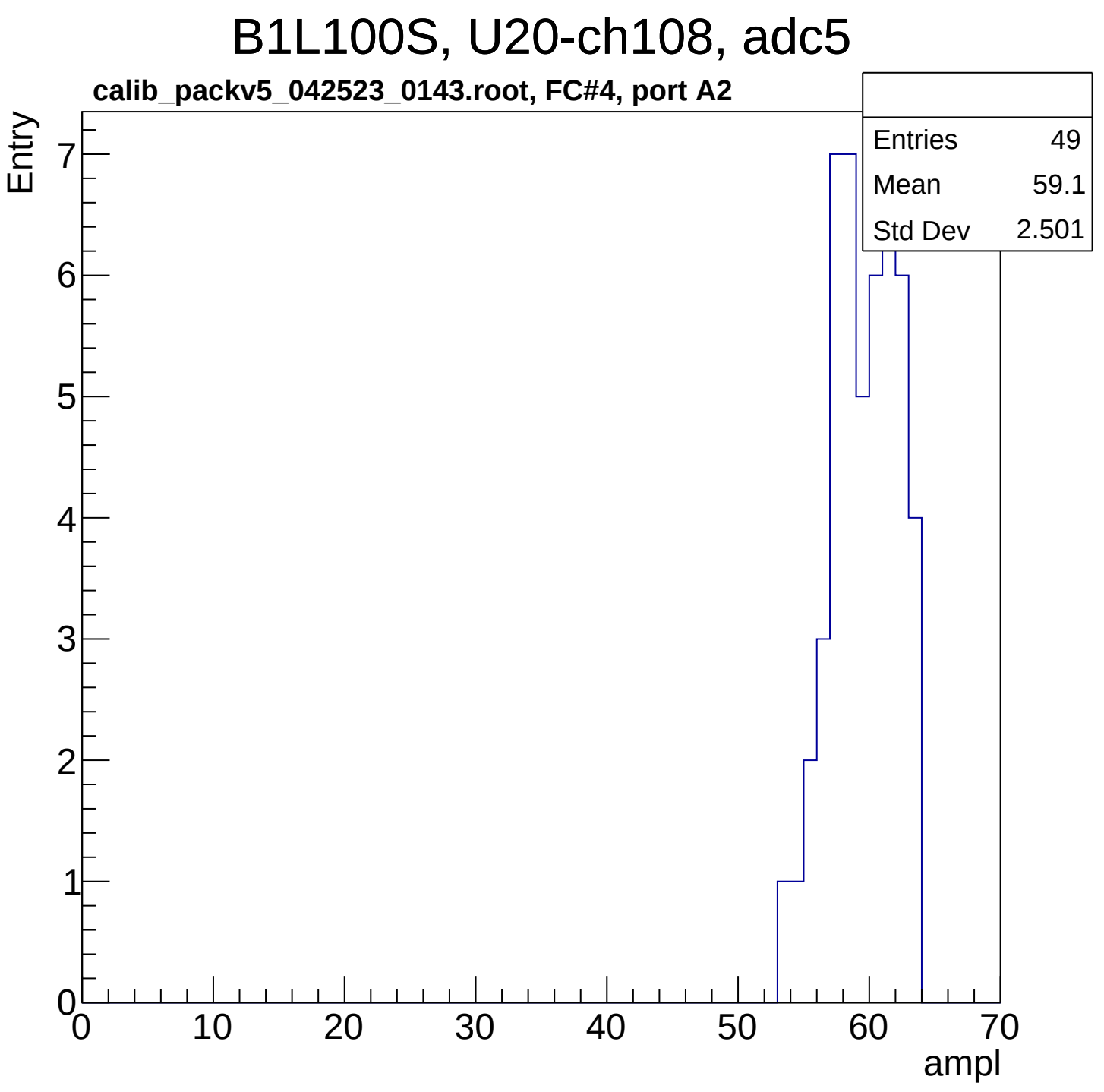

B1L100S, U20-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 54.65
Std Dev 3.787
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 59.1
Std Dev 2.501
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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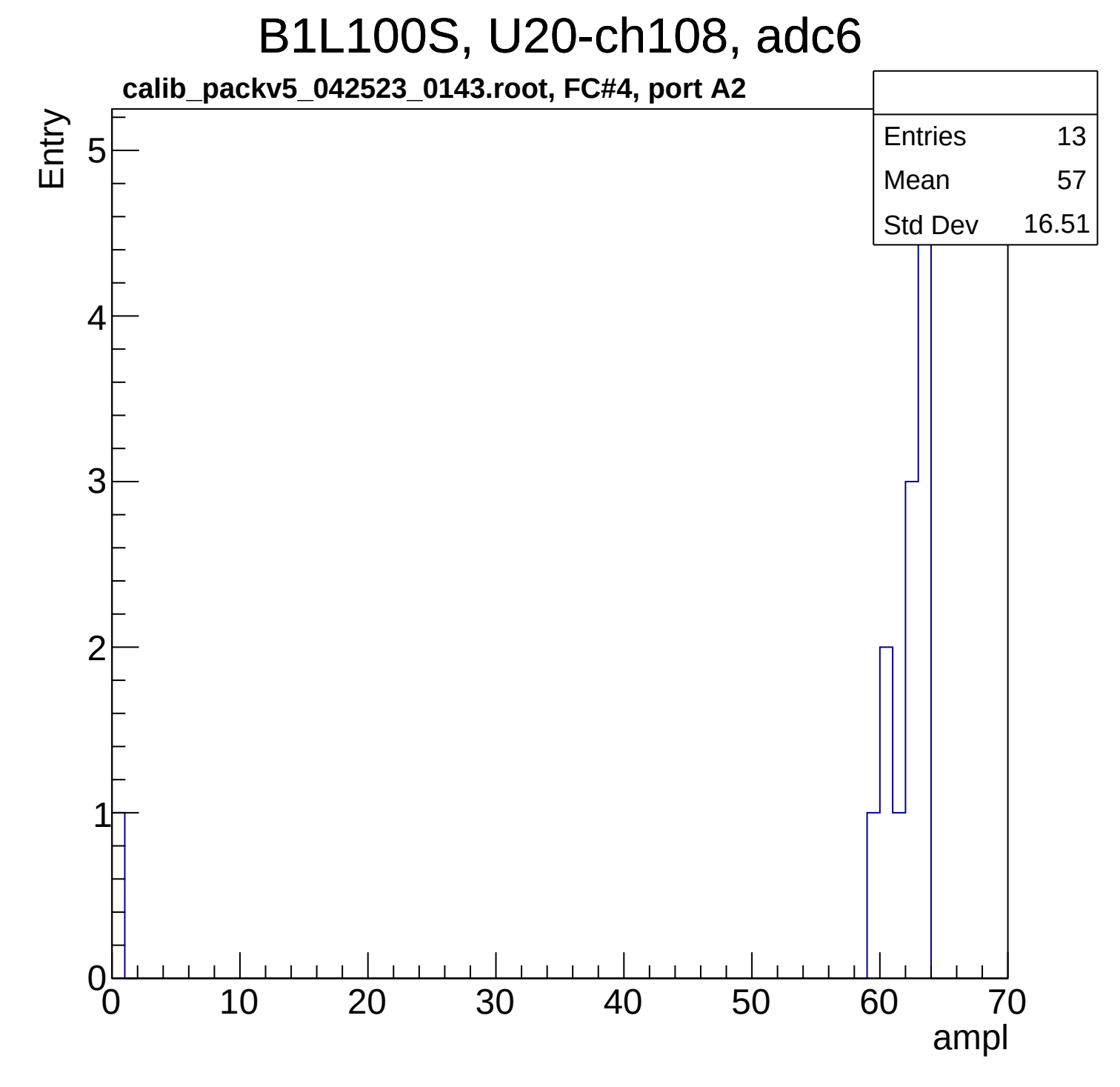
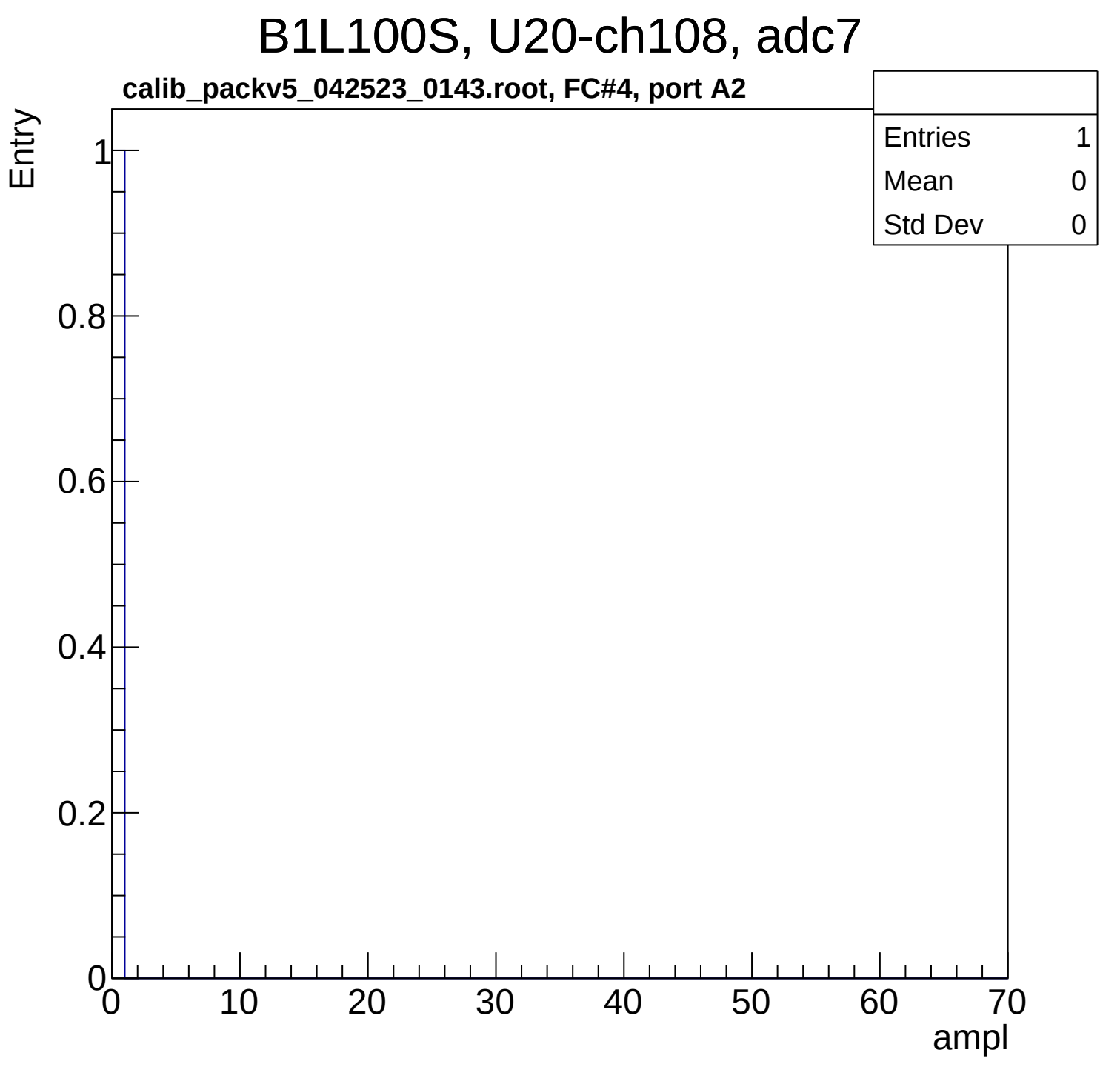
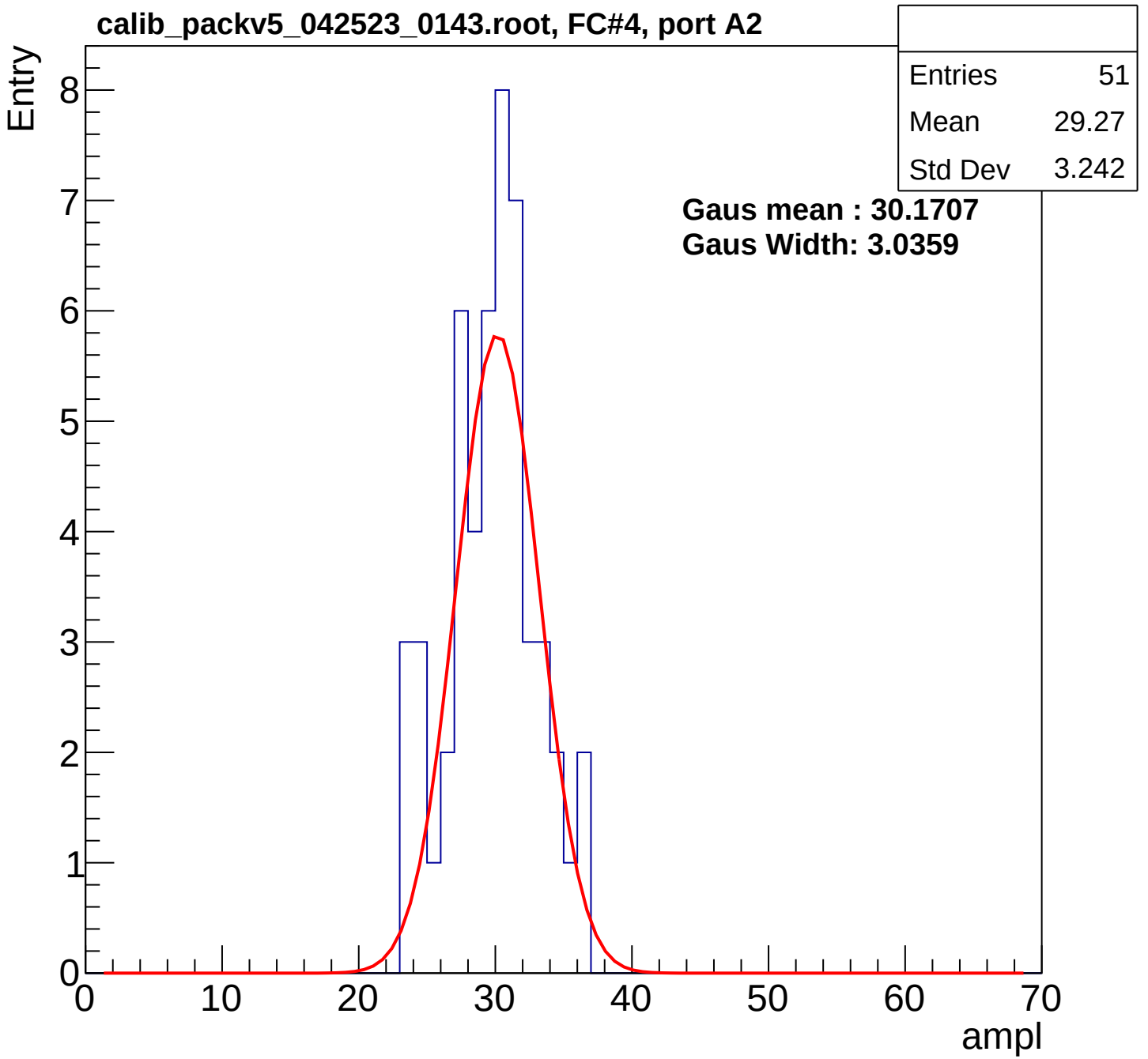
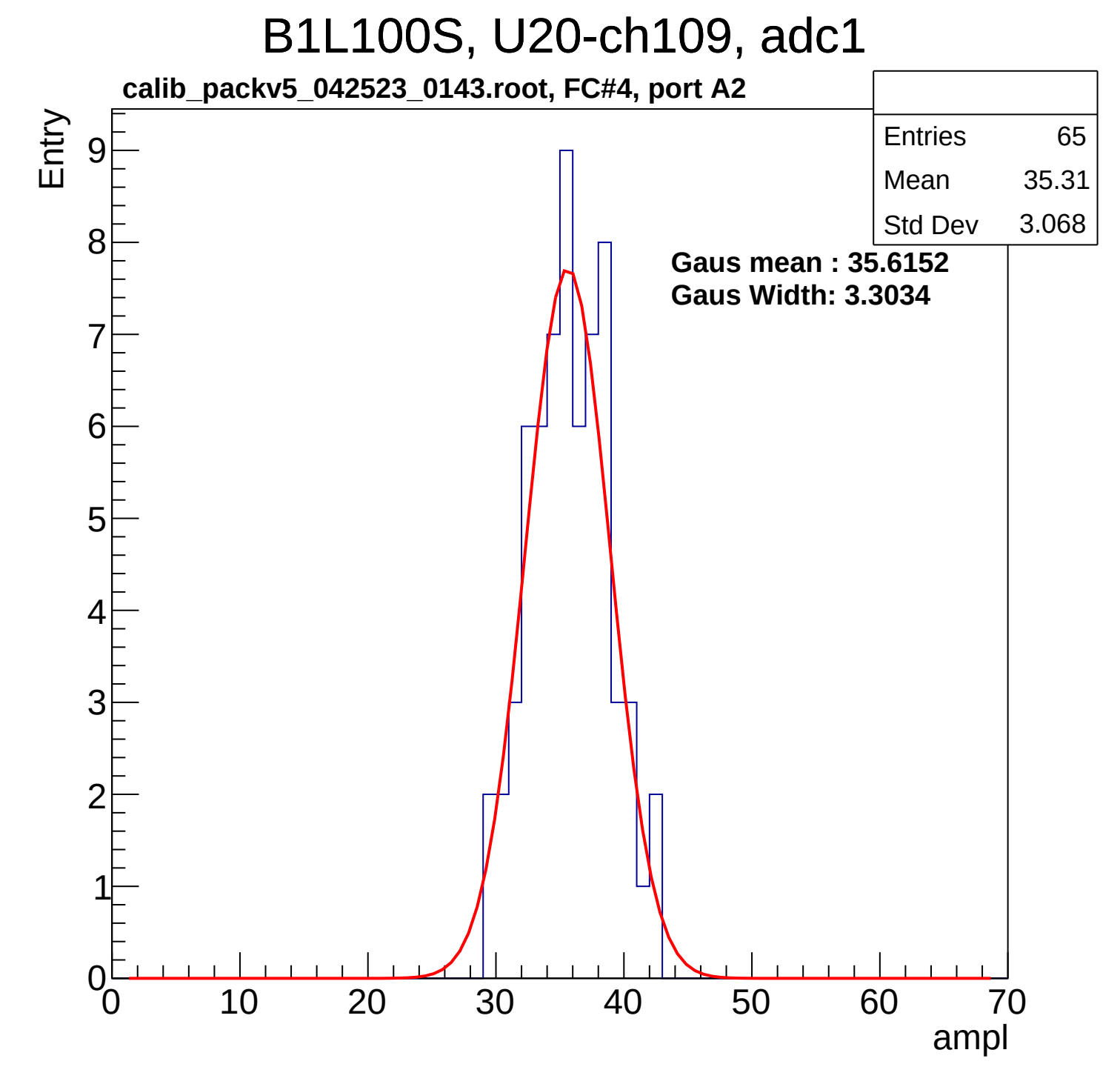
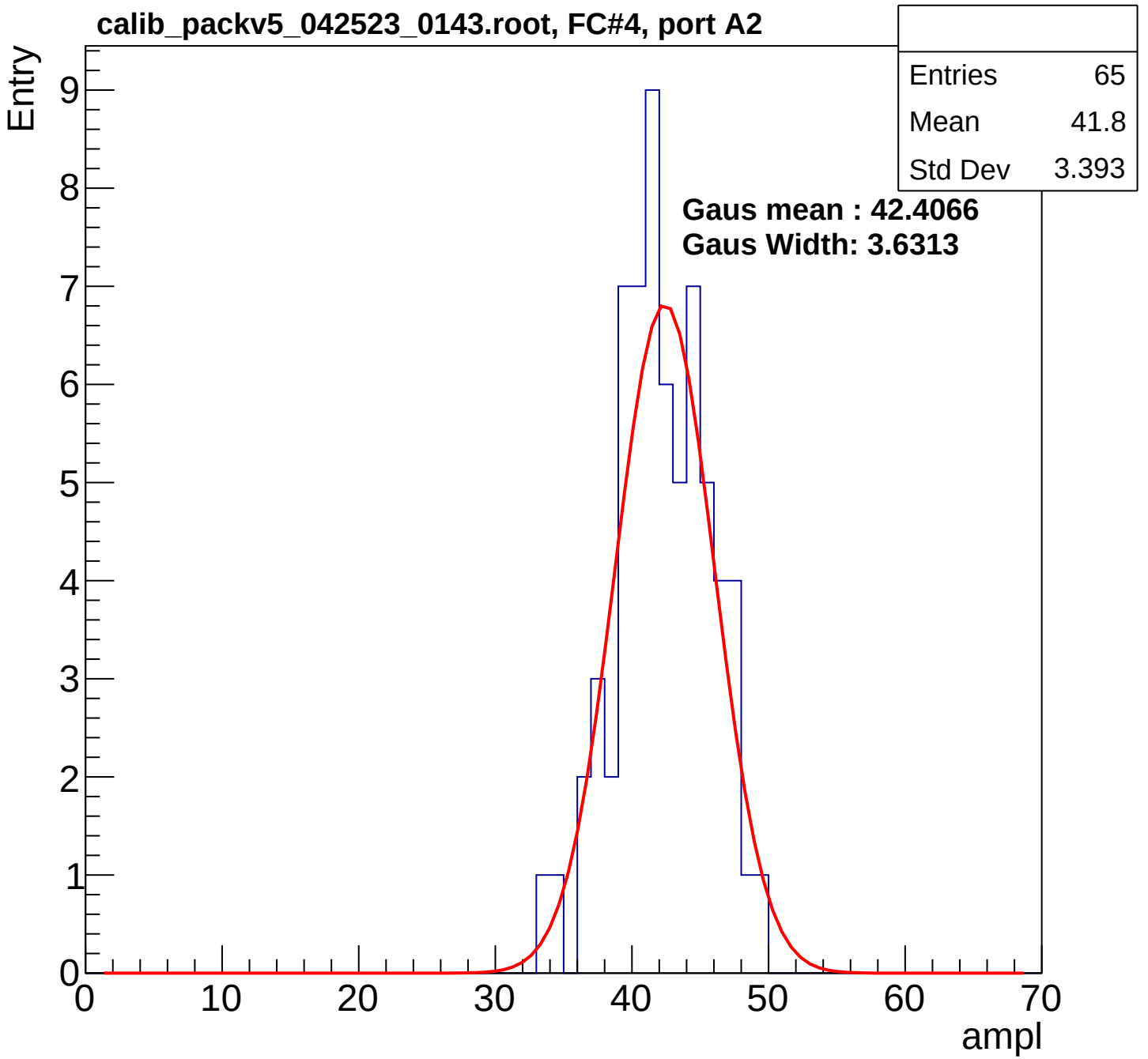
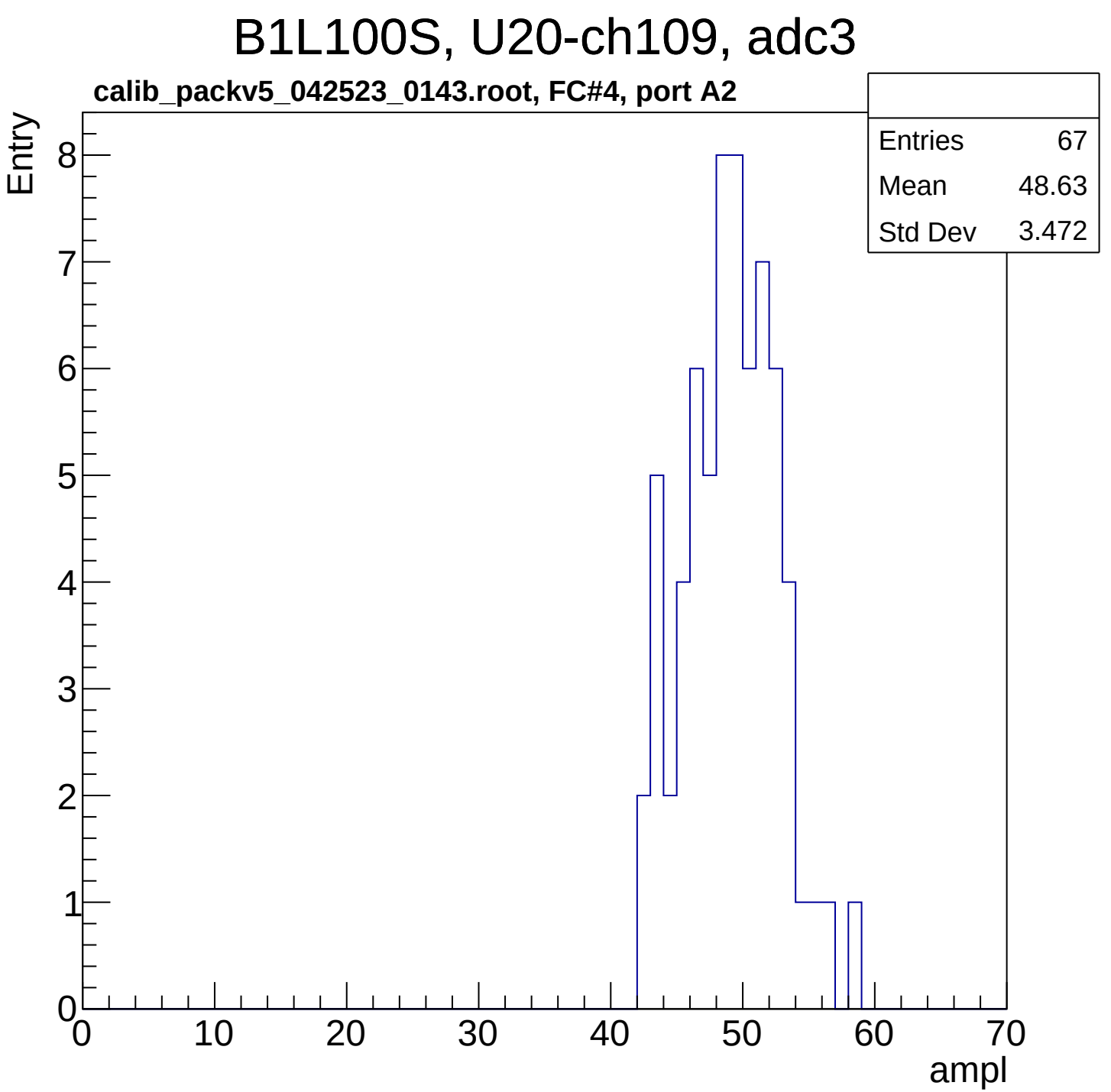

B1L100S, U20-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 57
Std Dev 16.51
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U20-ch108, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 29.27
Std Dev 3.242
Gaus mean : 30.1707
Gaus Width: 3.0359
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 35.31
Std Dev 3.068
Gaus mean : 35.6152
Gaus Width: 3.3034
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 41.8
Std Dev 3.393
Gaus mean : 42.4066
Gaus Width: 3.6313
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch109, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 48.63
Std Dev 3.472
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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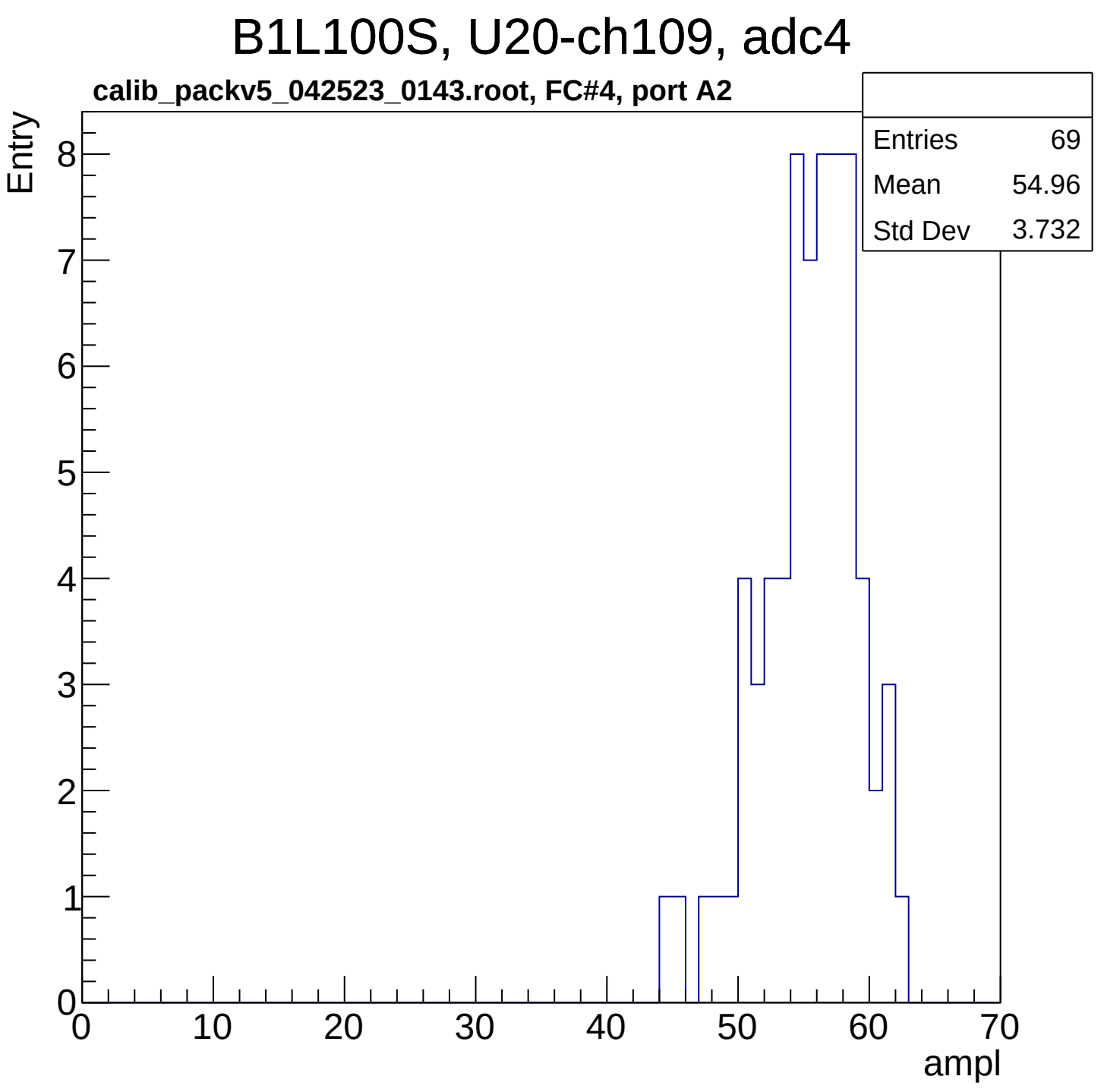
B1L100S, U20-ch109, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 54.96
Std Dev 3.732
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
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60
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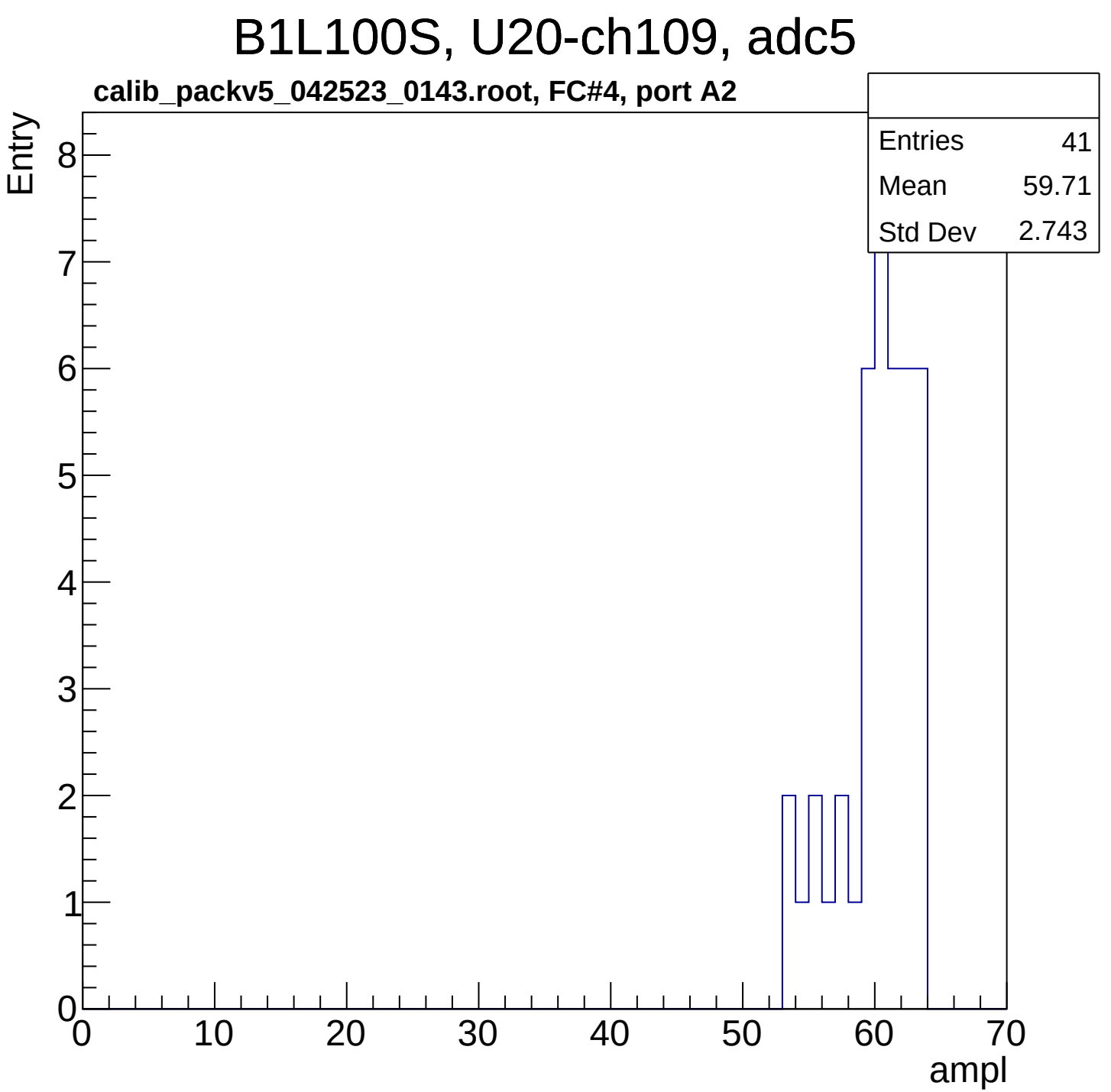
B1L100S, U20-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.71
Std Dev 2.743
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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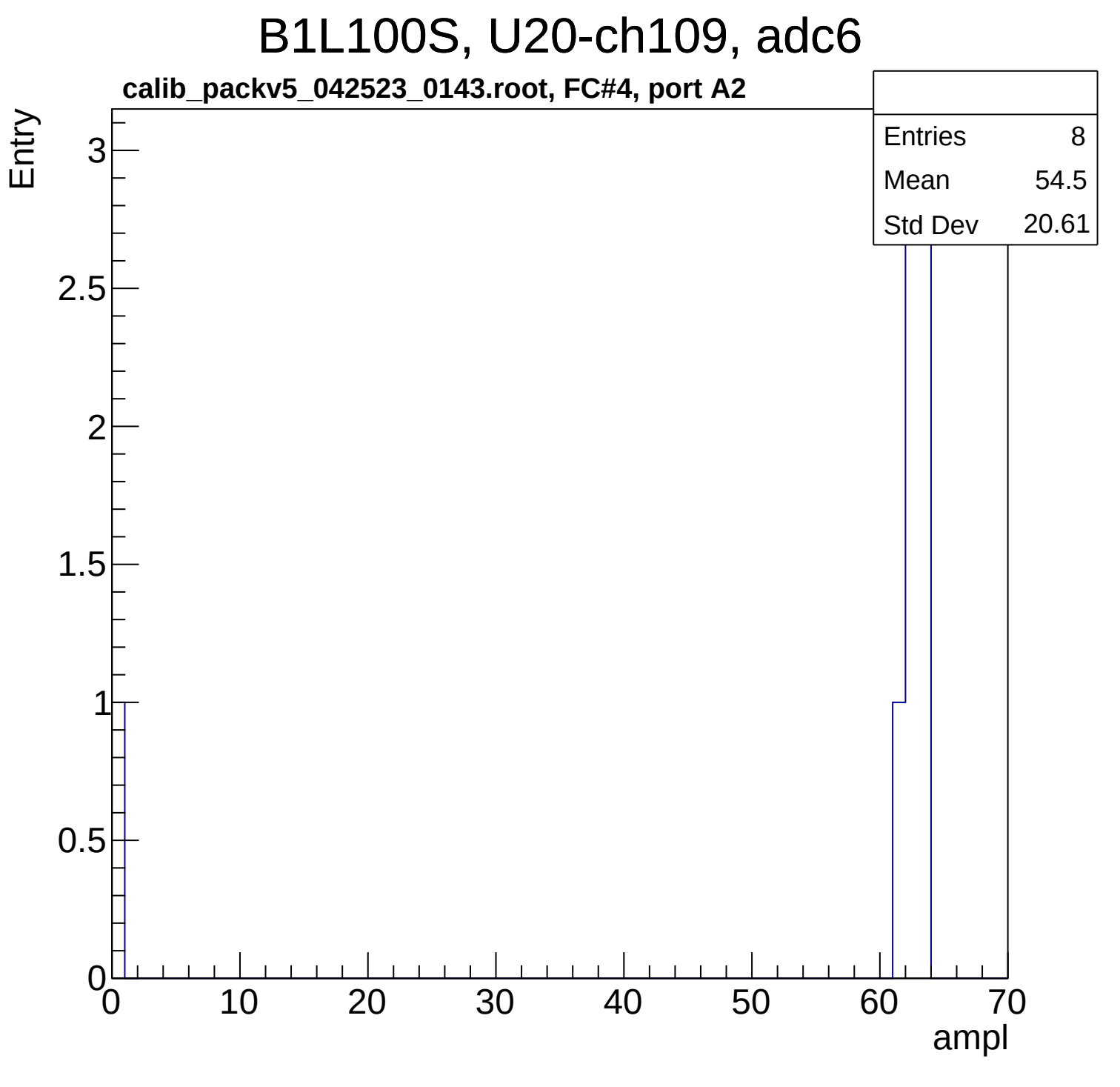
B1L100S, U20-ch109, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54.5
Std Dev 20.61
Entry
ampl
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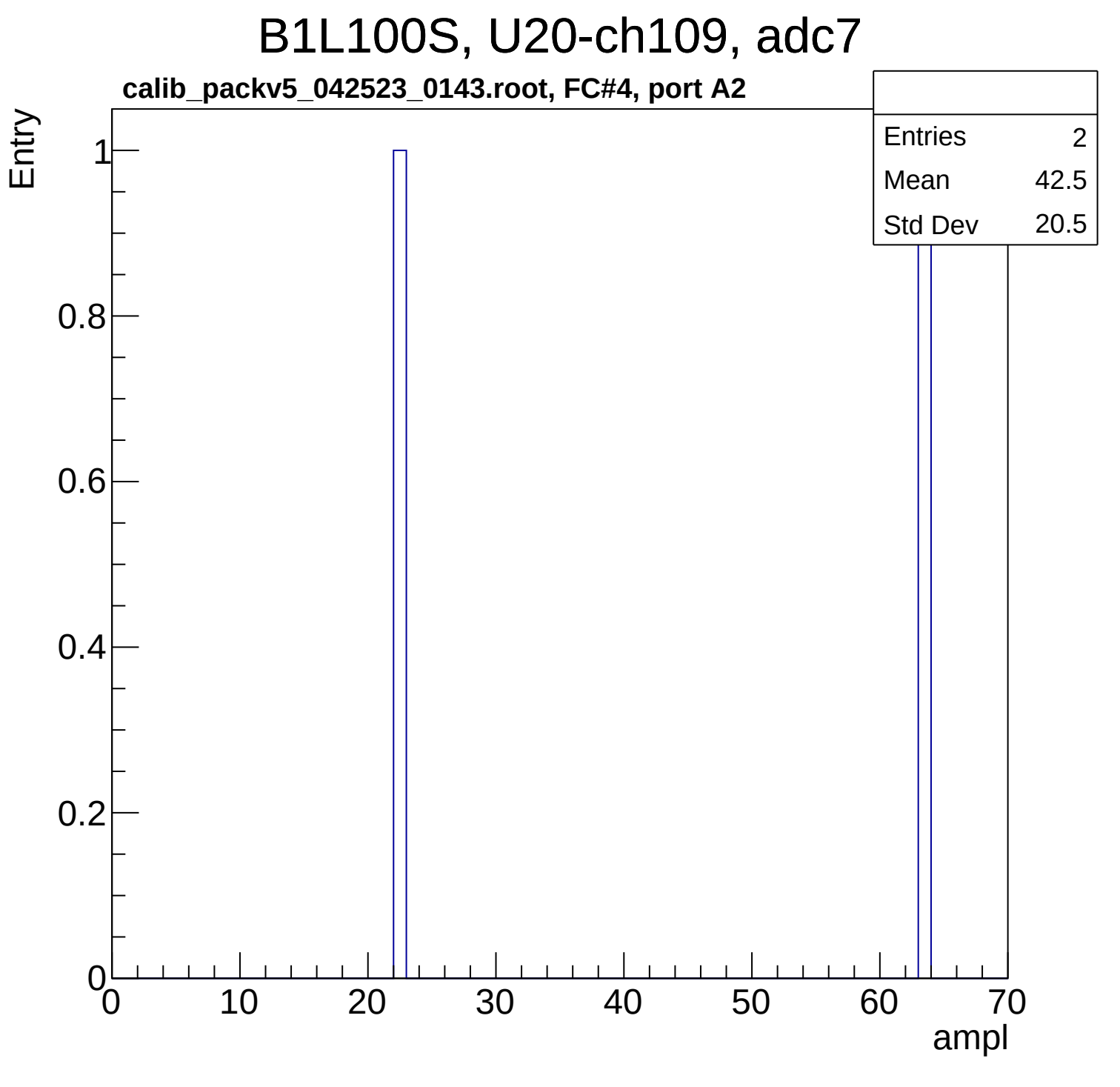
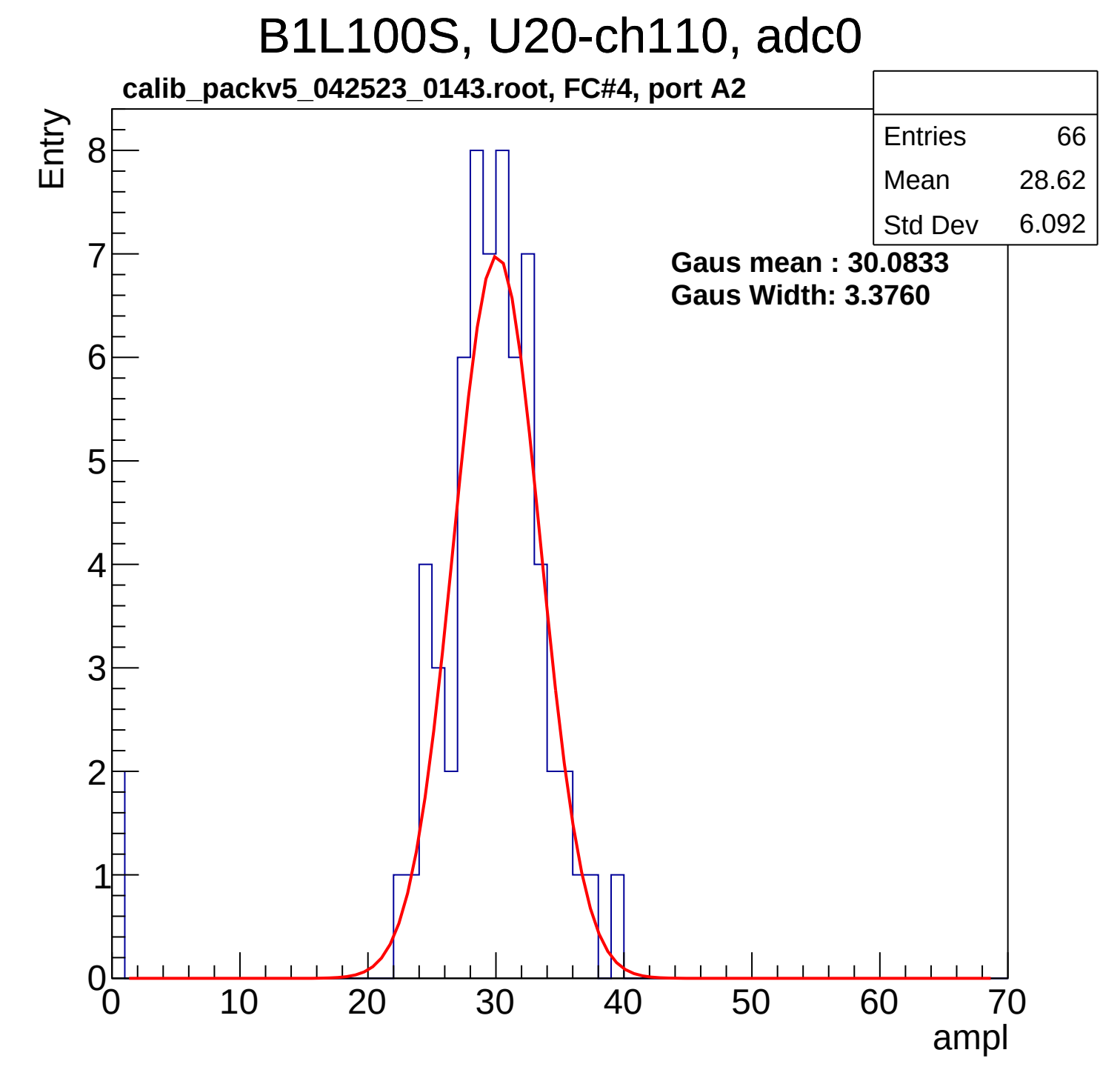
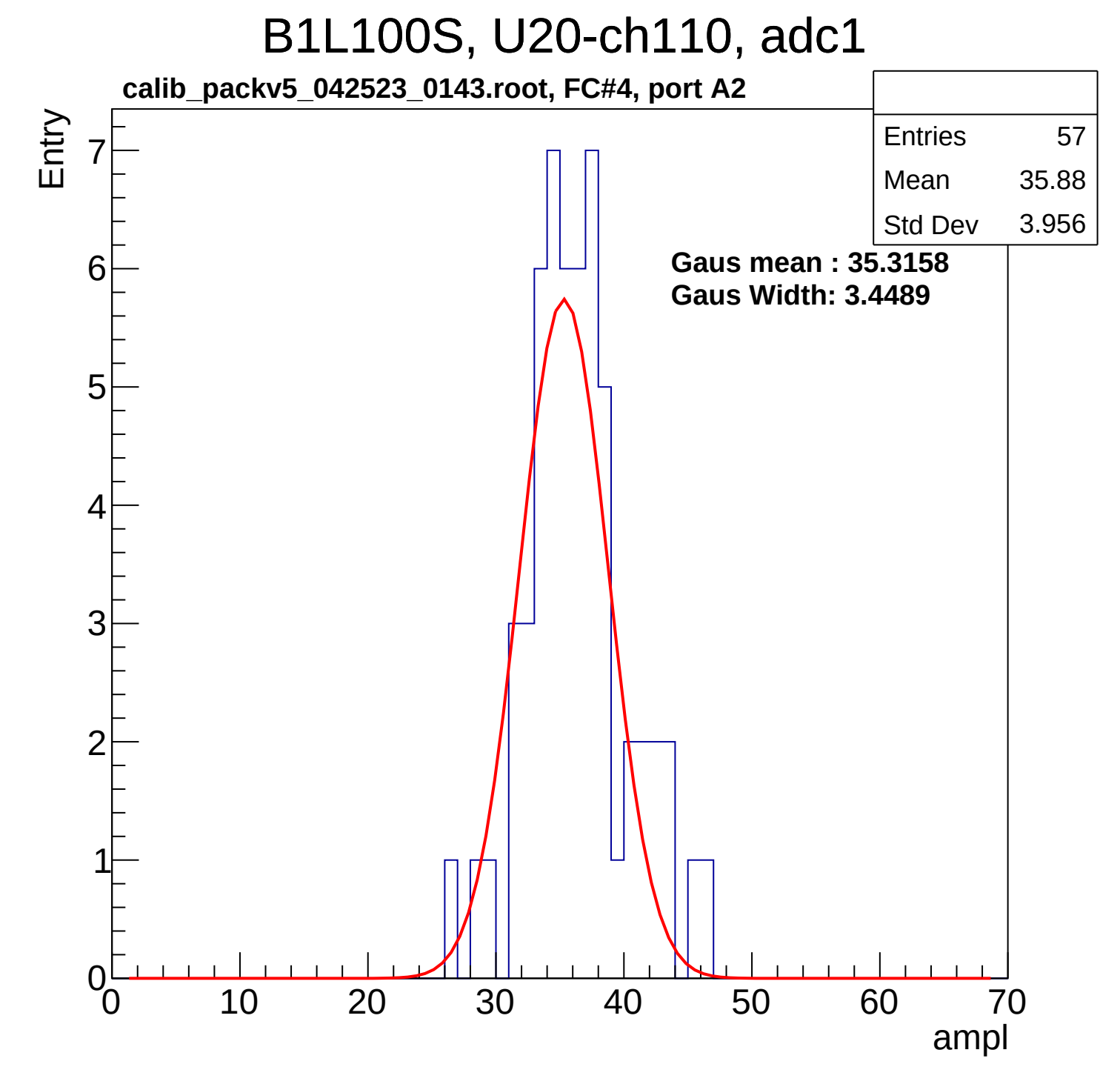
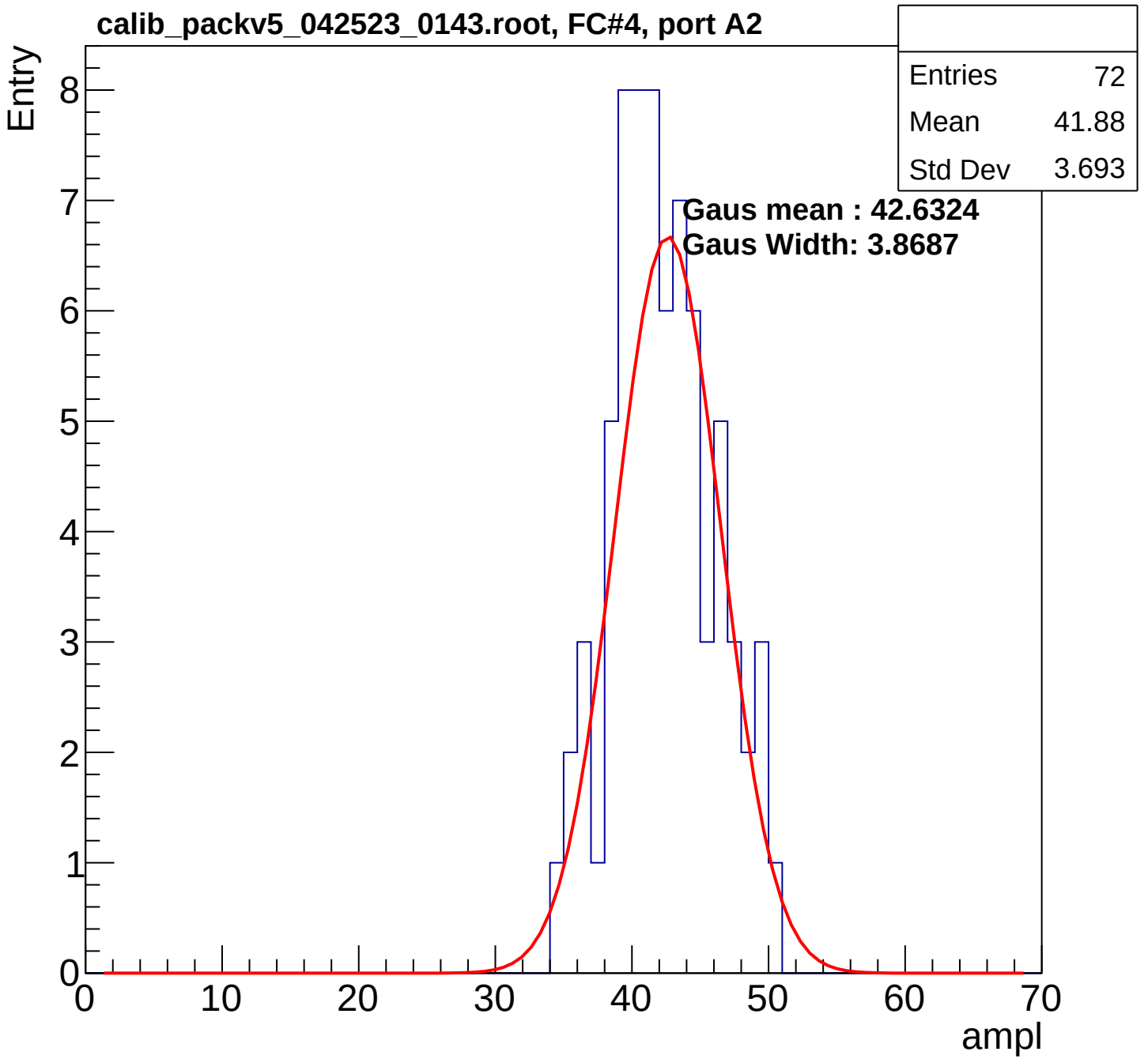
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch109, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 42.5
Std Dev 20.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.62
Std Dev 6.092
Gaus mean : 30.0833
Gaus Width: 3.3760
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 35.88
Std Dev 3.956
Gaus mean : 35.3158
Gaus Width: 3.4489
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 41.88
Std Dev 3.693
Gaus mean : 42.6324
Gaus Width: 3.8687
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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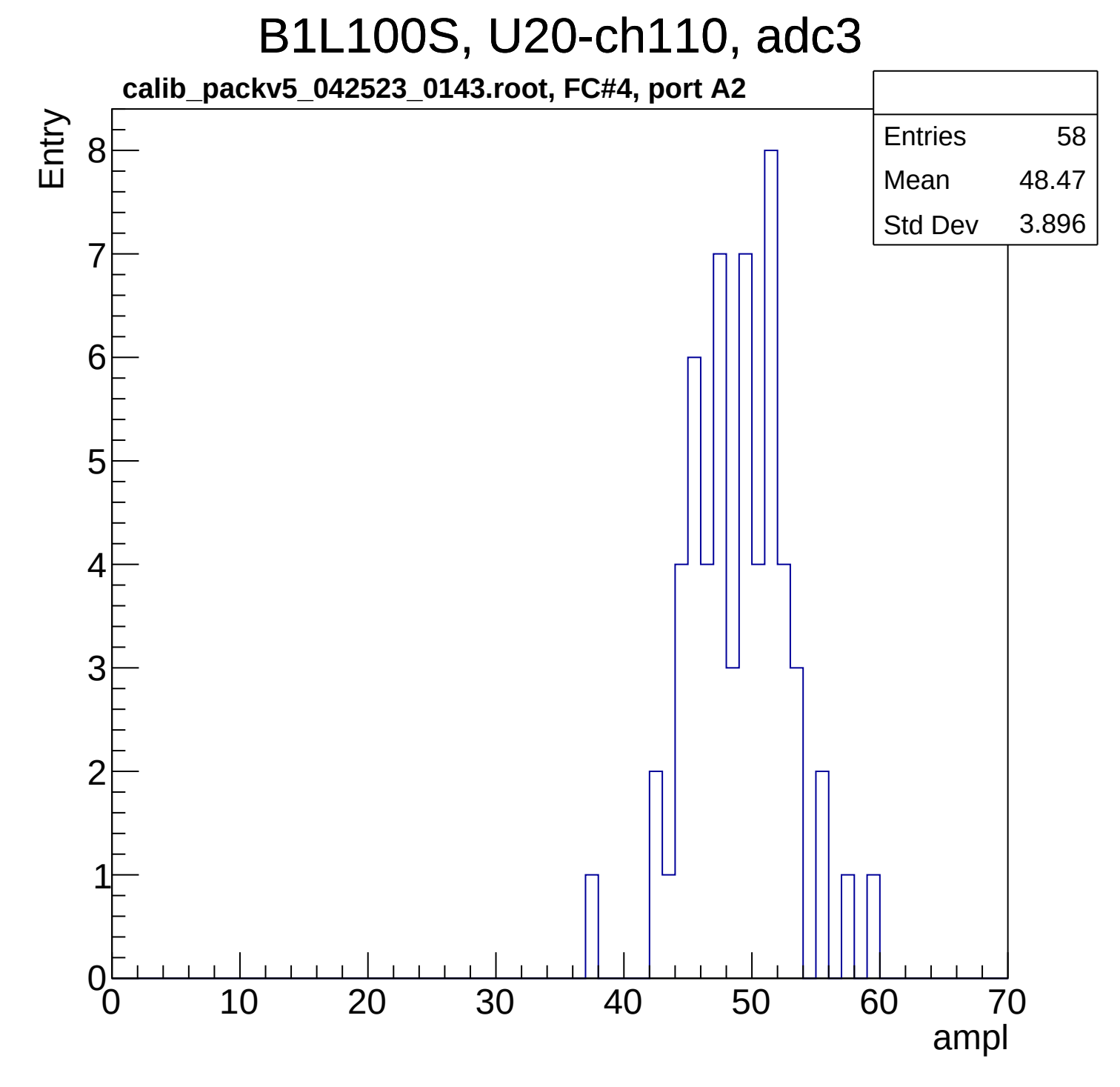
B1L100S, U20-ch110, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 48.47
Std Dev 3.896
Entry
ampl
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1
2
3
4
5
6
7
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0
10
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60
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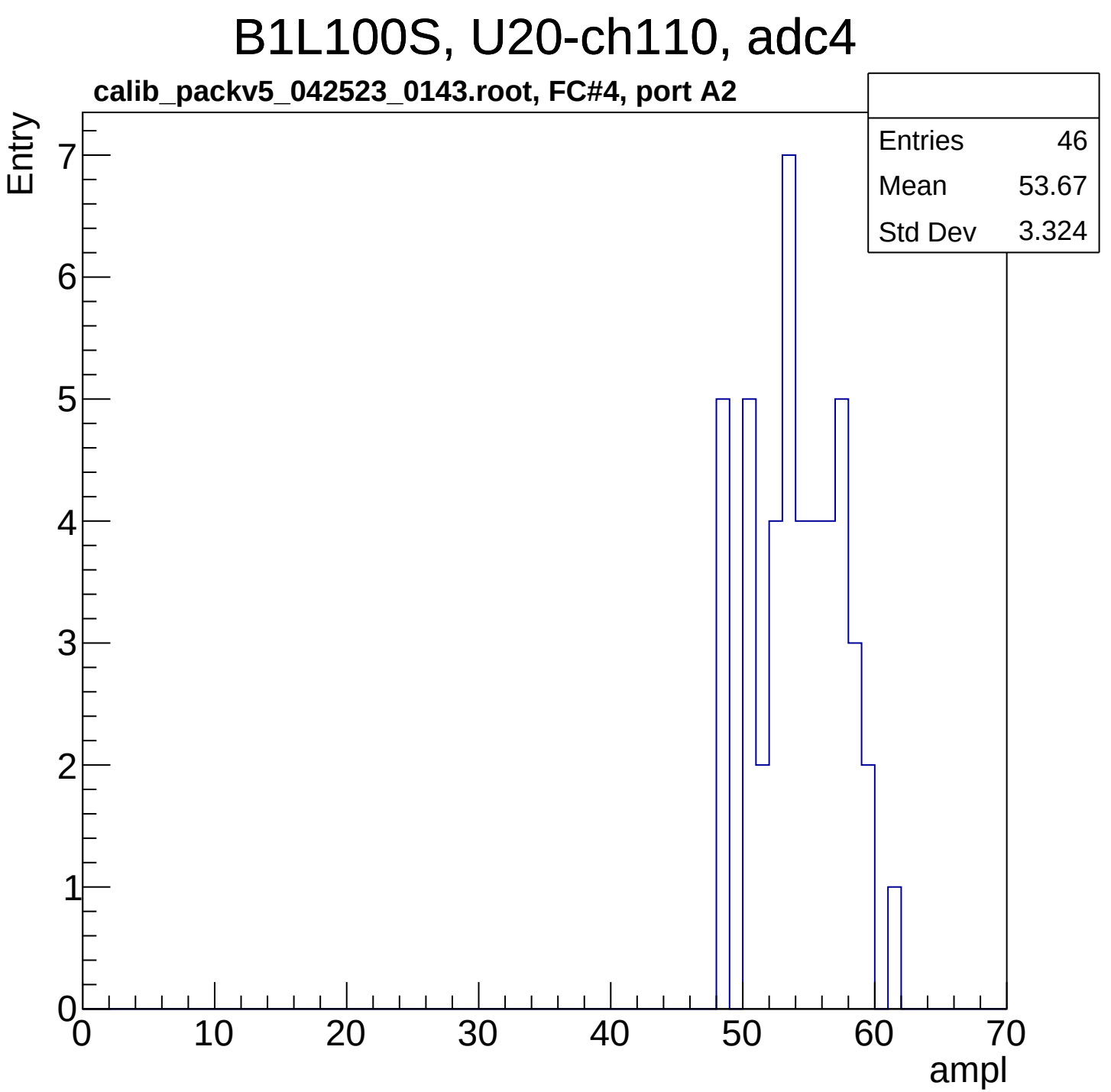
B1L100S, U20-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 53.67
Std Dev 3.324
Entry
ampl
0
1
2
3
4
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6
7
0
10
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40
50
60
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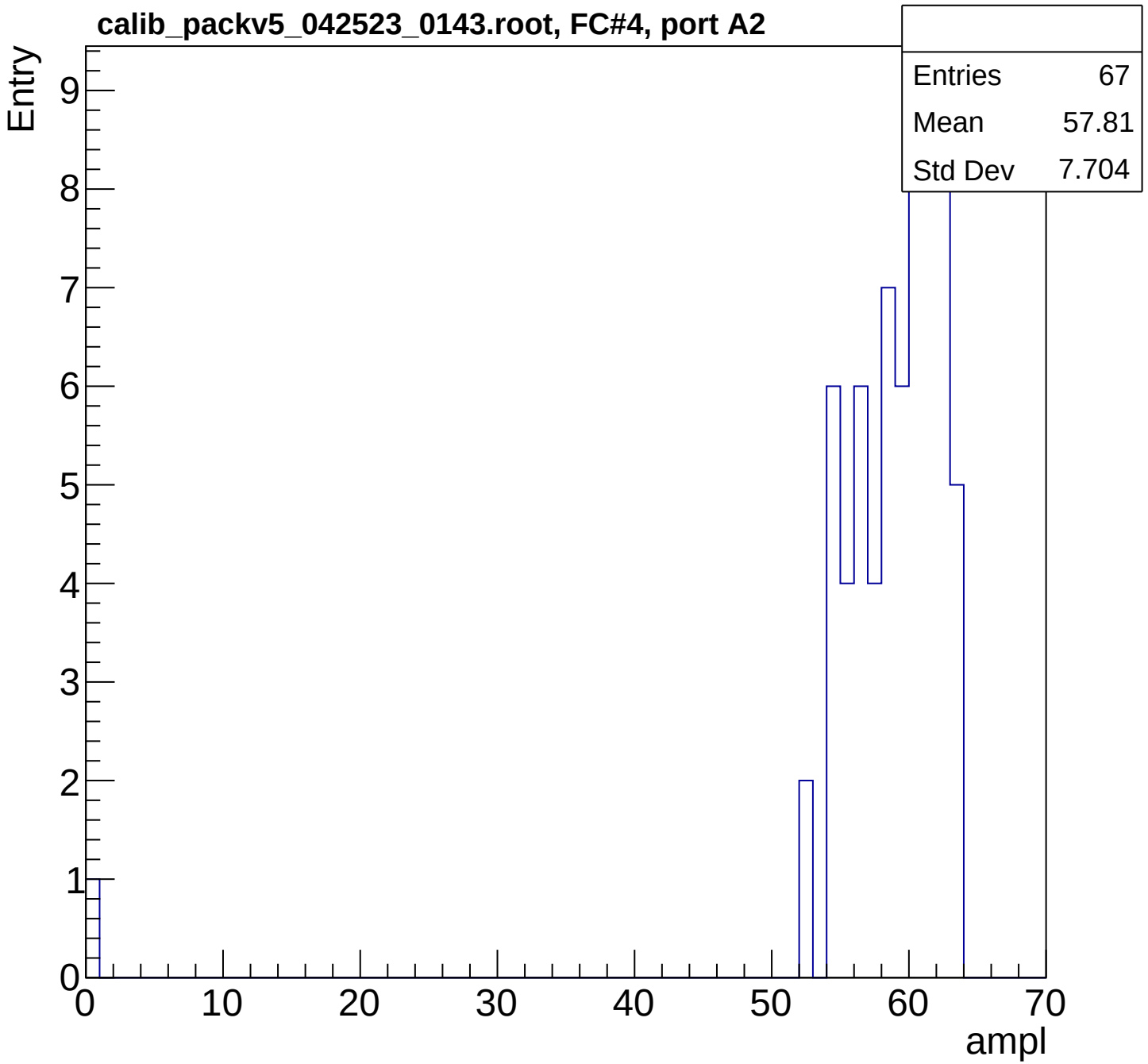
B1L100S, U20-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 57.81
Std Dev 7.704
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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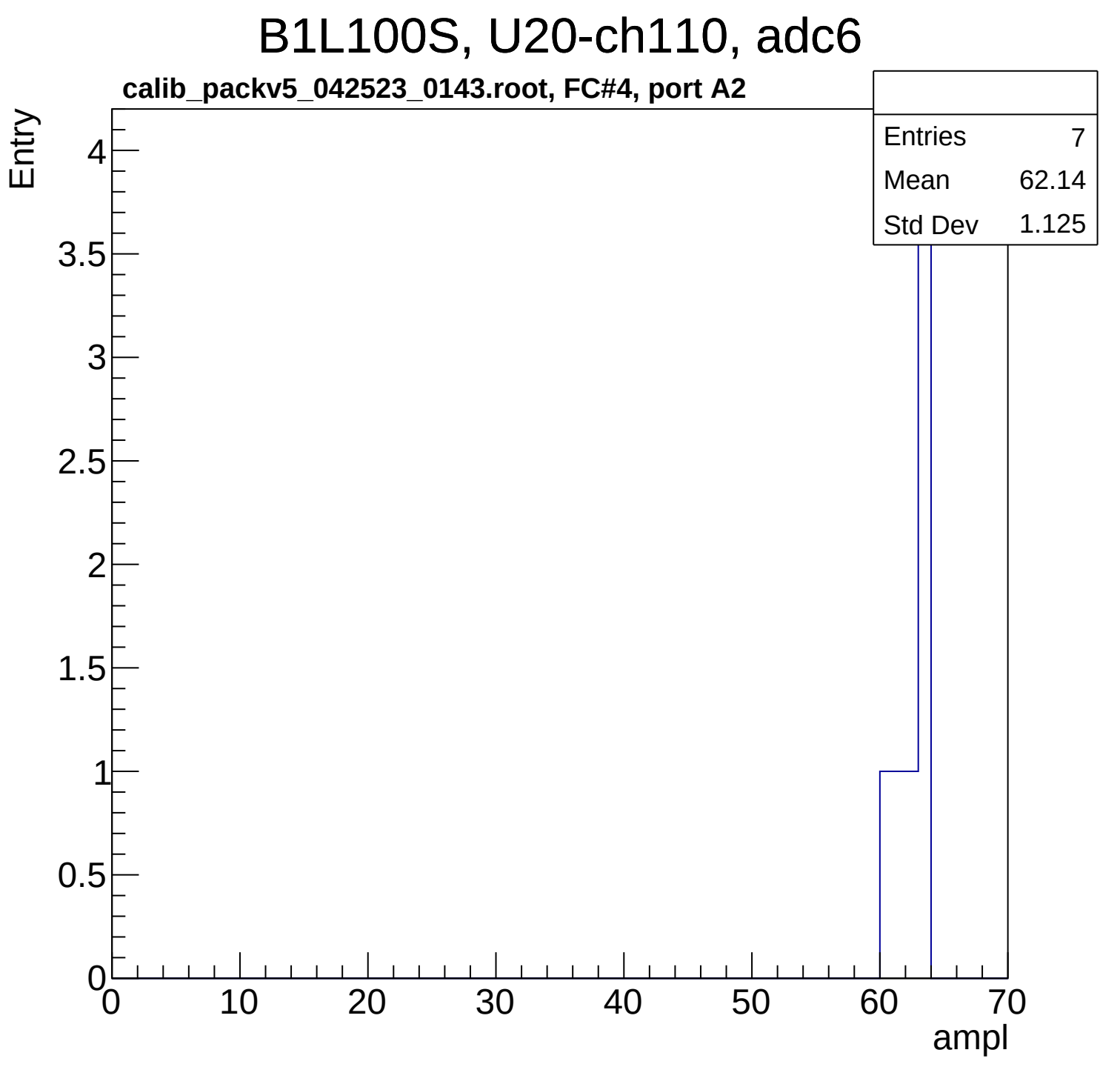
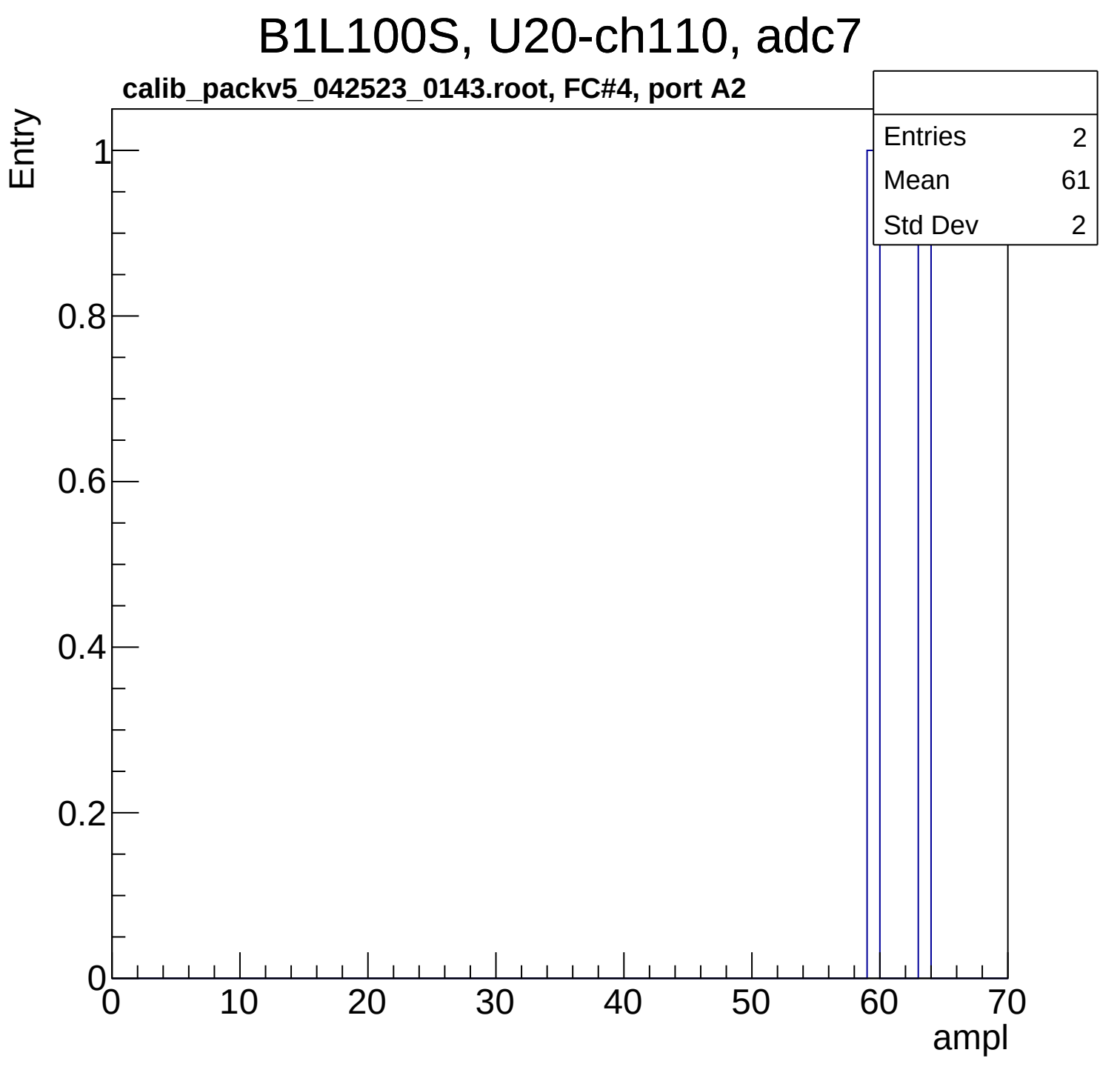

B1L100S, U20-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.14
Std Dev 1.125
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 2
Entry
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0.8
0.6
0.4
0.2
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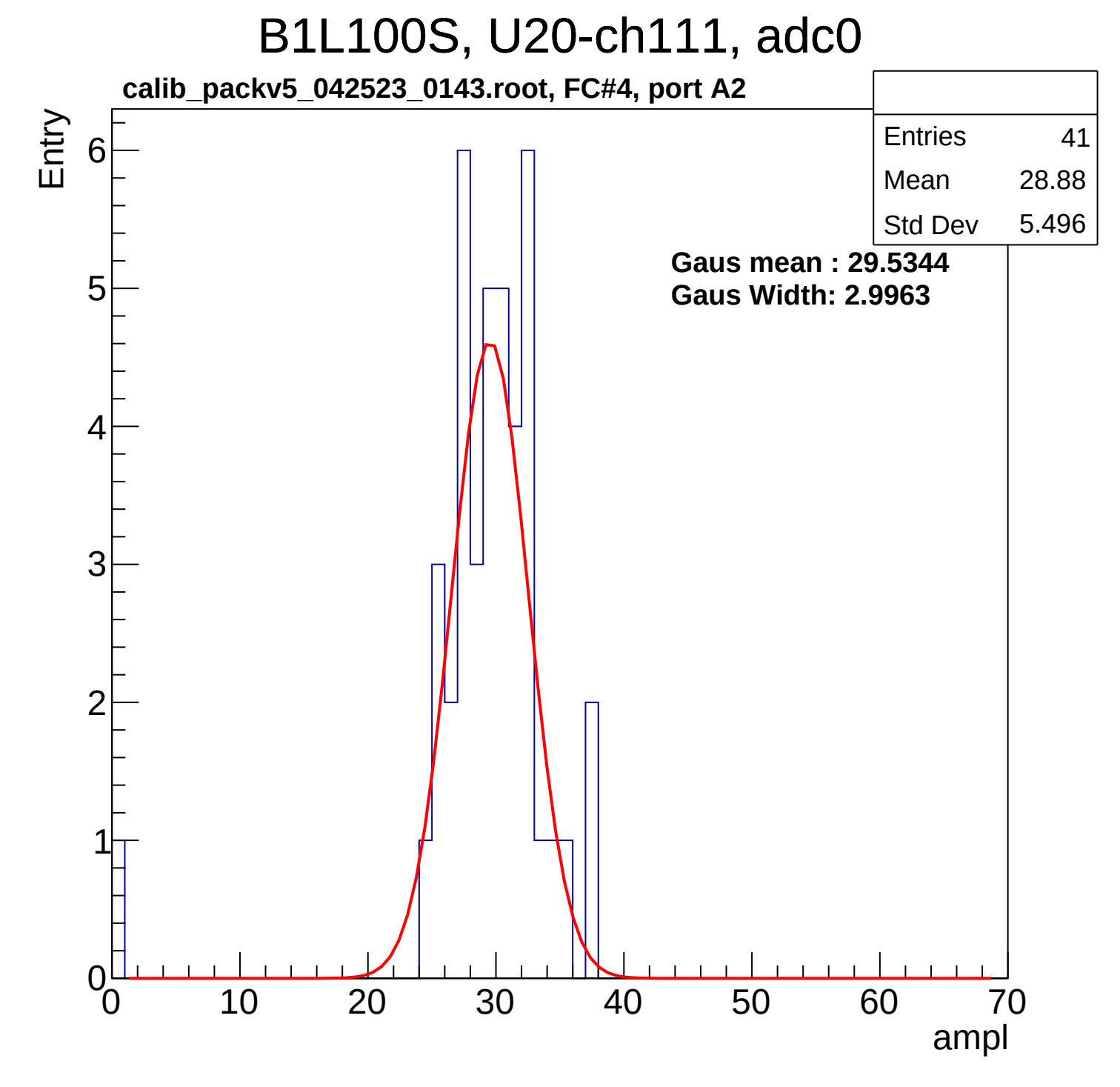
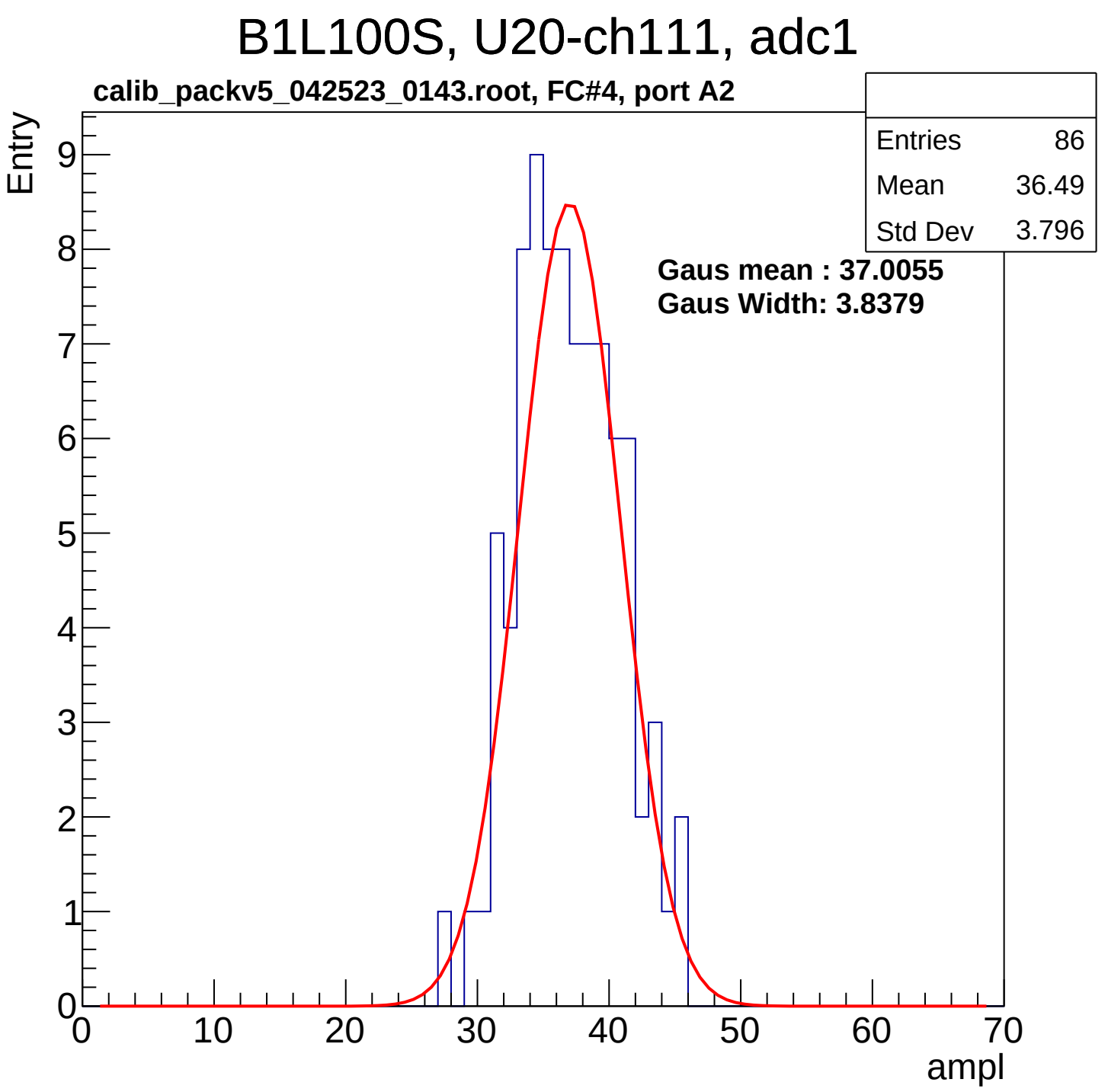
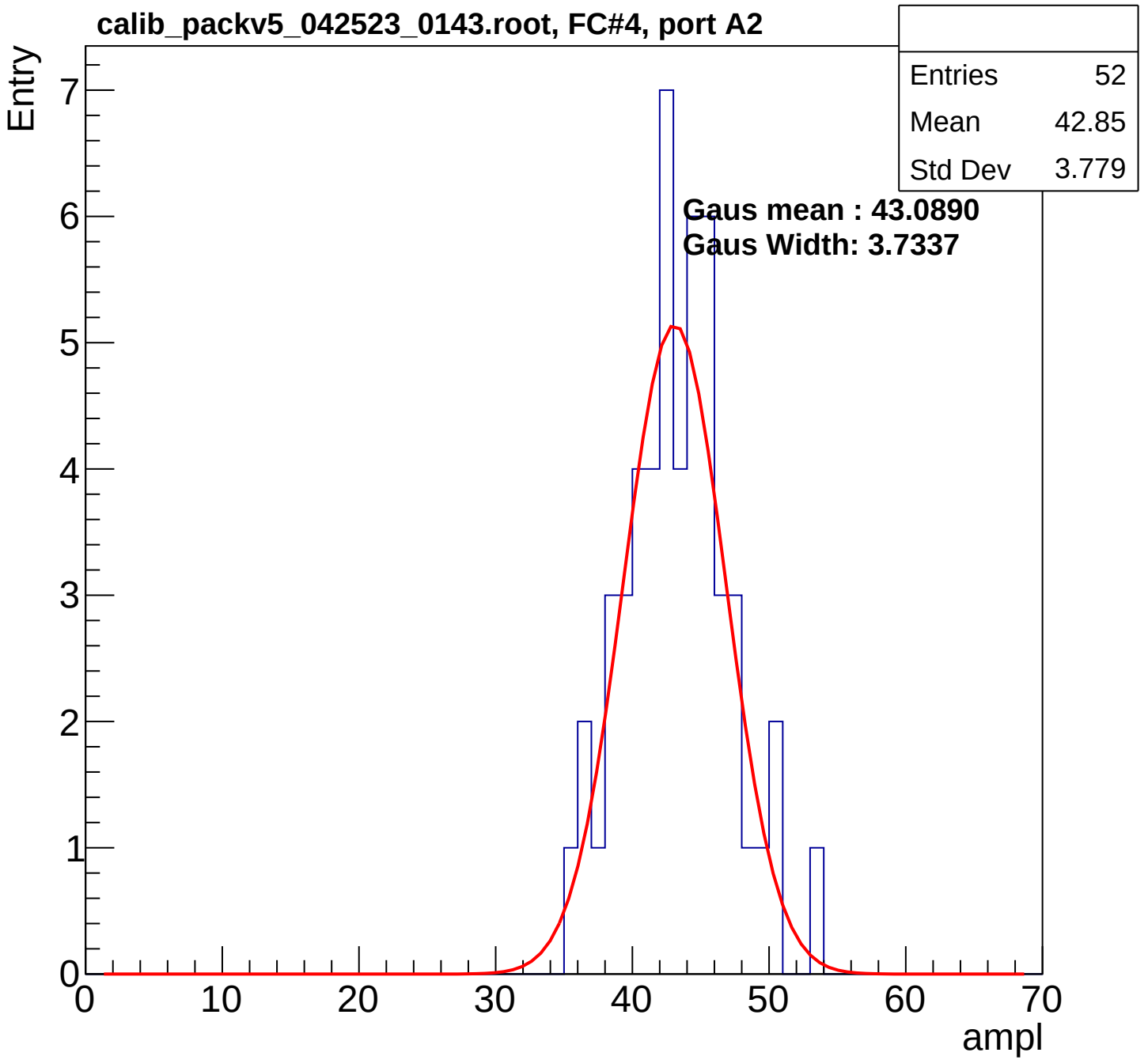
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch111, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 28.88
Std Dev 5.496
Gaus mean : 29.5344
Gaus Width: 2.9963
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U20-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 36.49
Std Dev 3.796
Gaus mean : 37.0055
Gaus Width: 3.8379
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 42.85
Std Dev 3.779
Gaus mean : 43.0890
Gaus Width: 3.7337
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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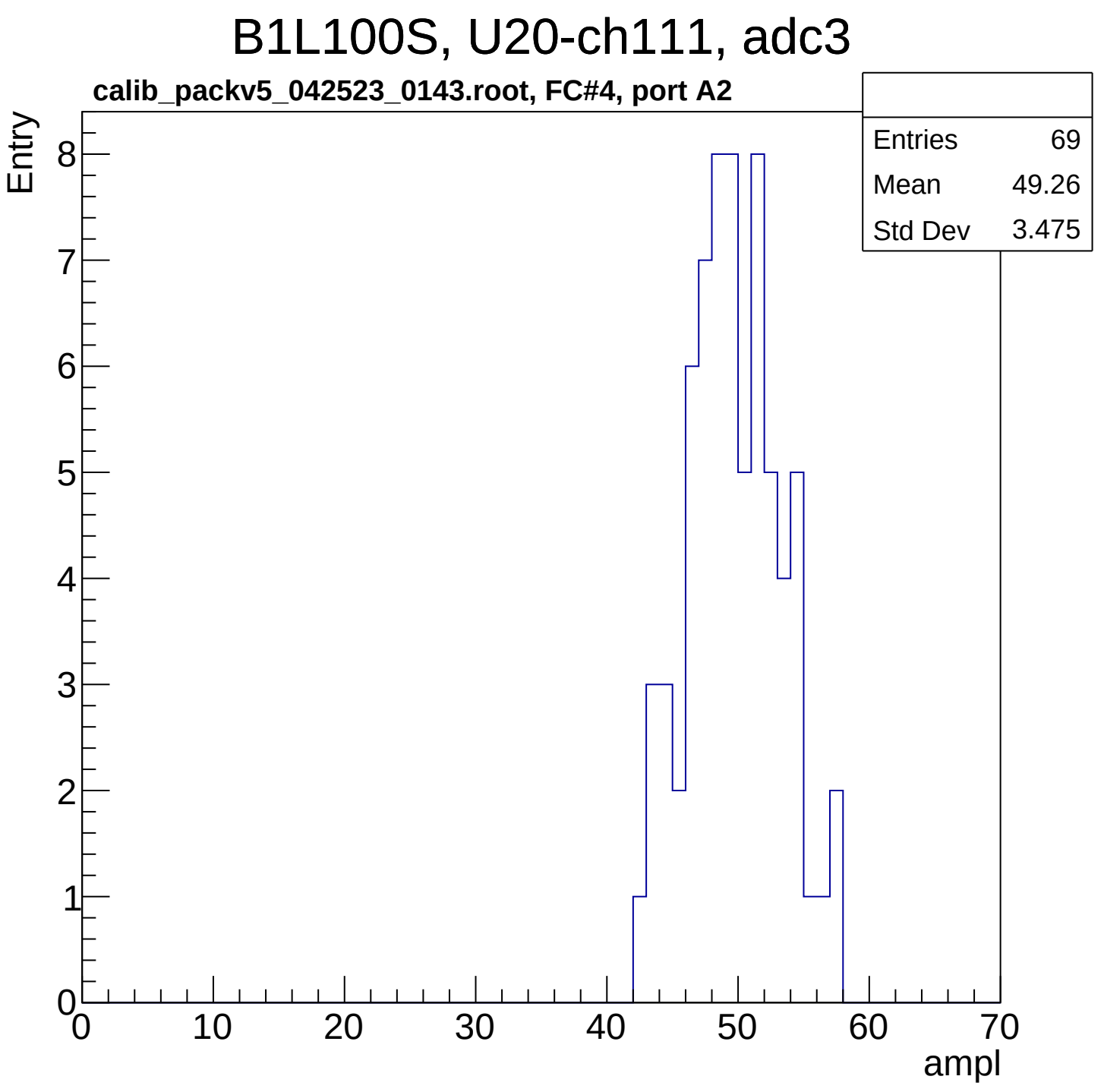
B1L100S, U20-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.26
Std Dev 3.475
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
50
60
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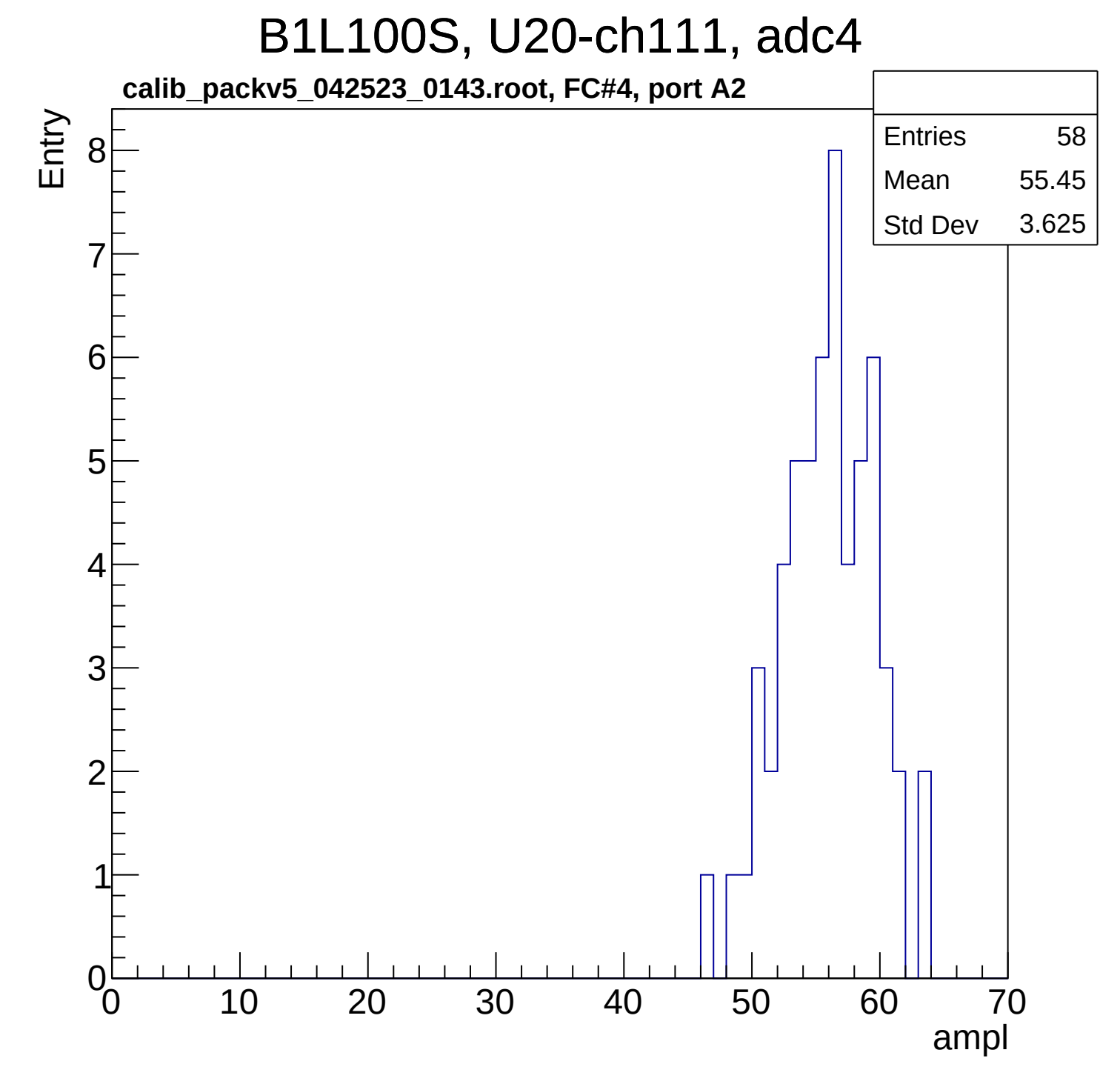
B1L100S, U20-ch111, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.45
Std Dev 3.625
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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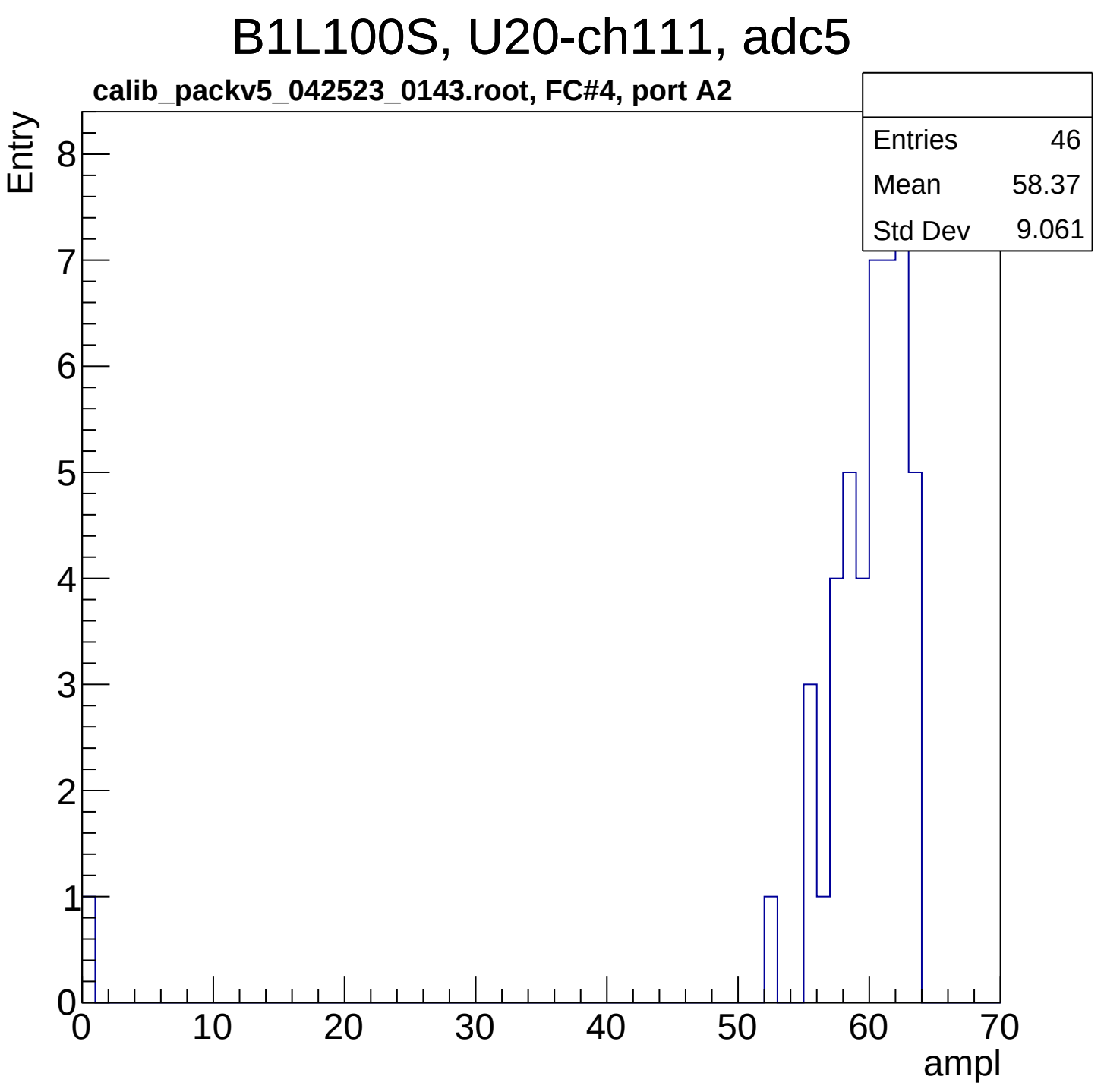
B1L100S, U20-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 58.37
Std Dev 9.061
Entry
ampl
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7
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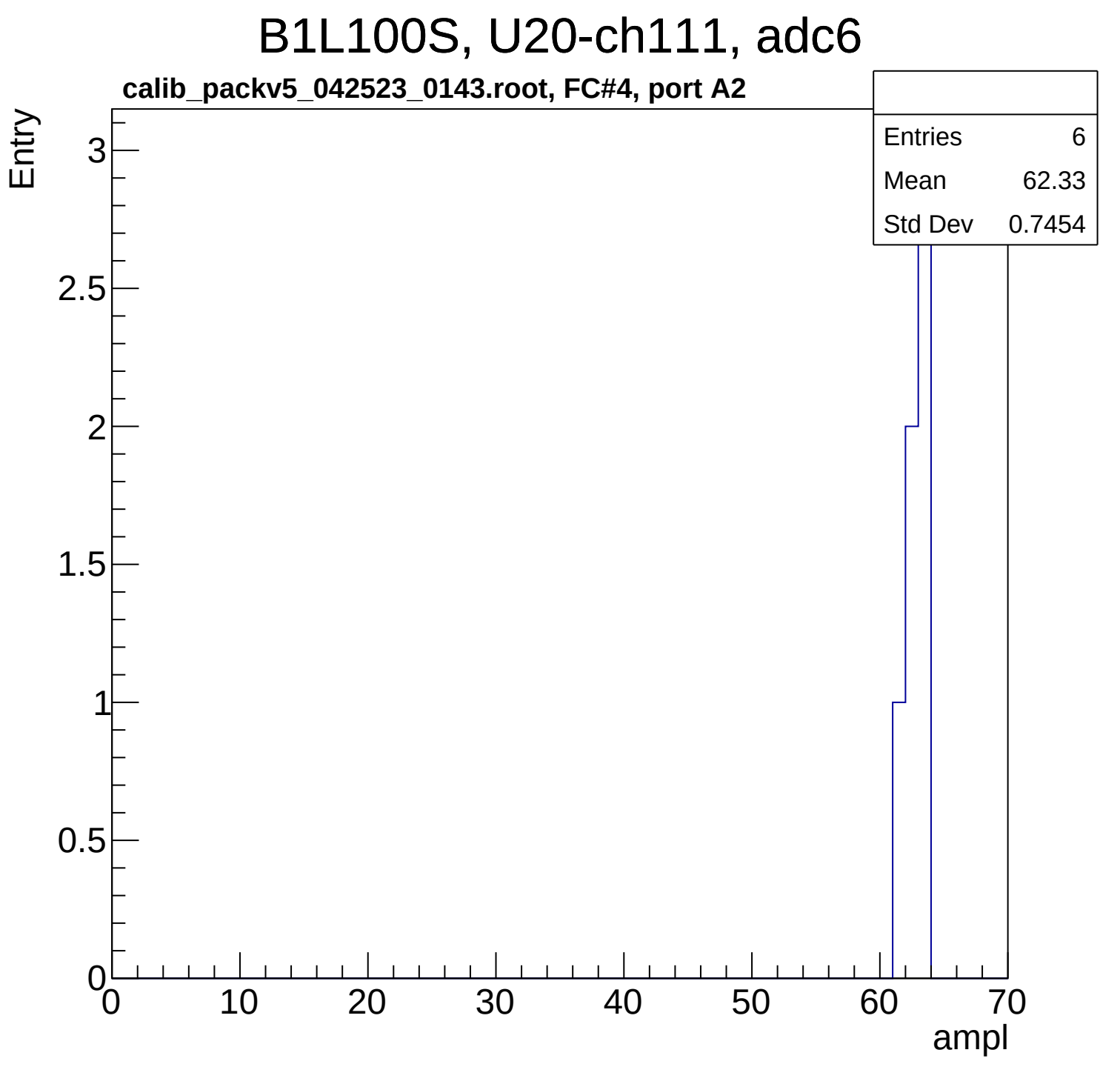
B1L100S, U20-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.33
Std Dev 0.7454
Entry
ampl
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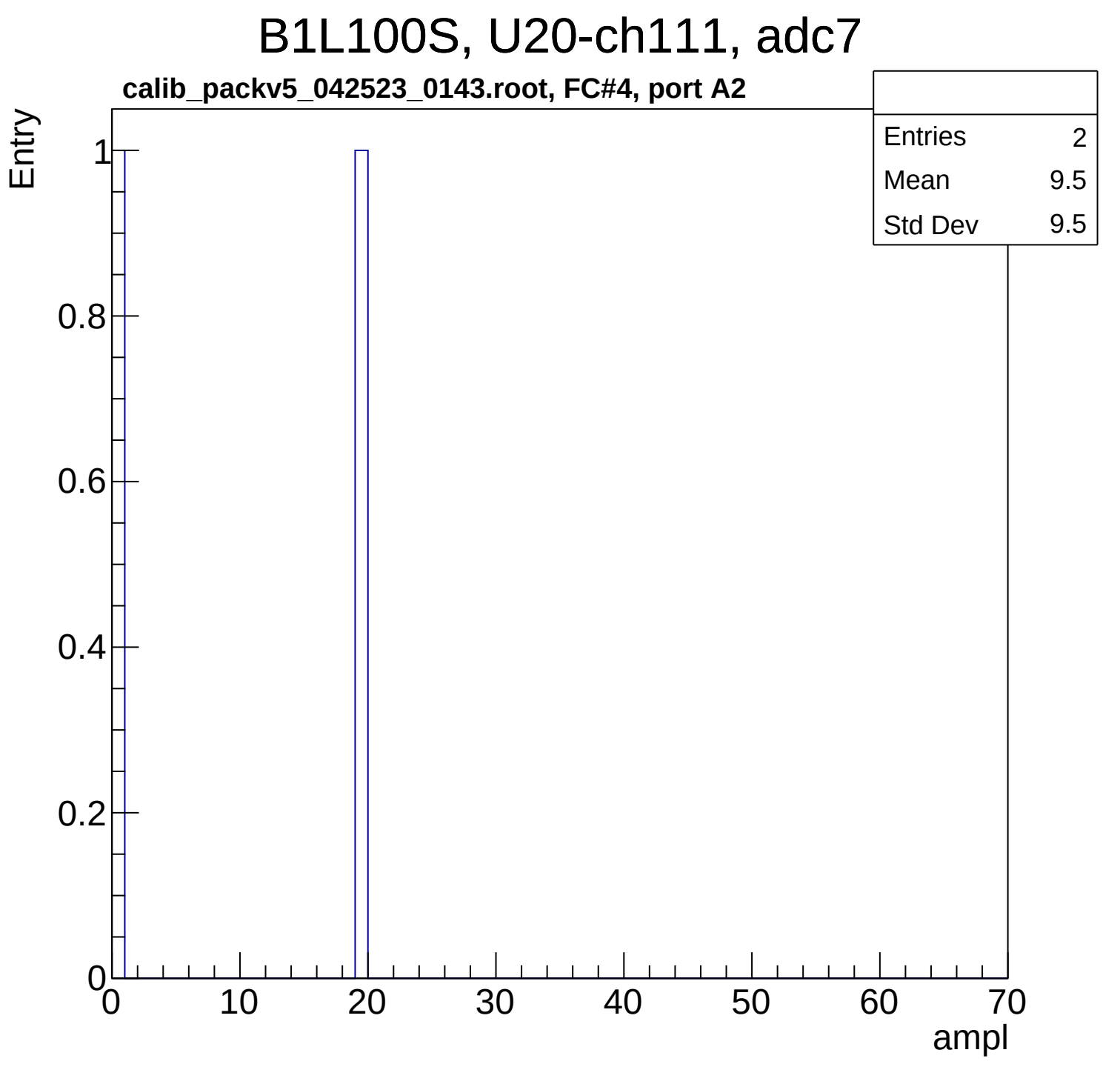
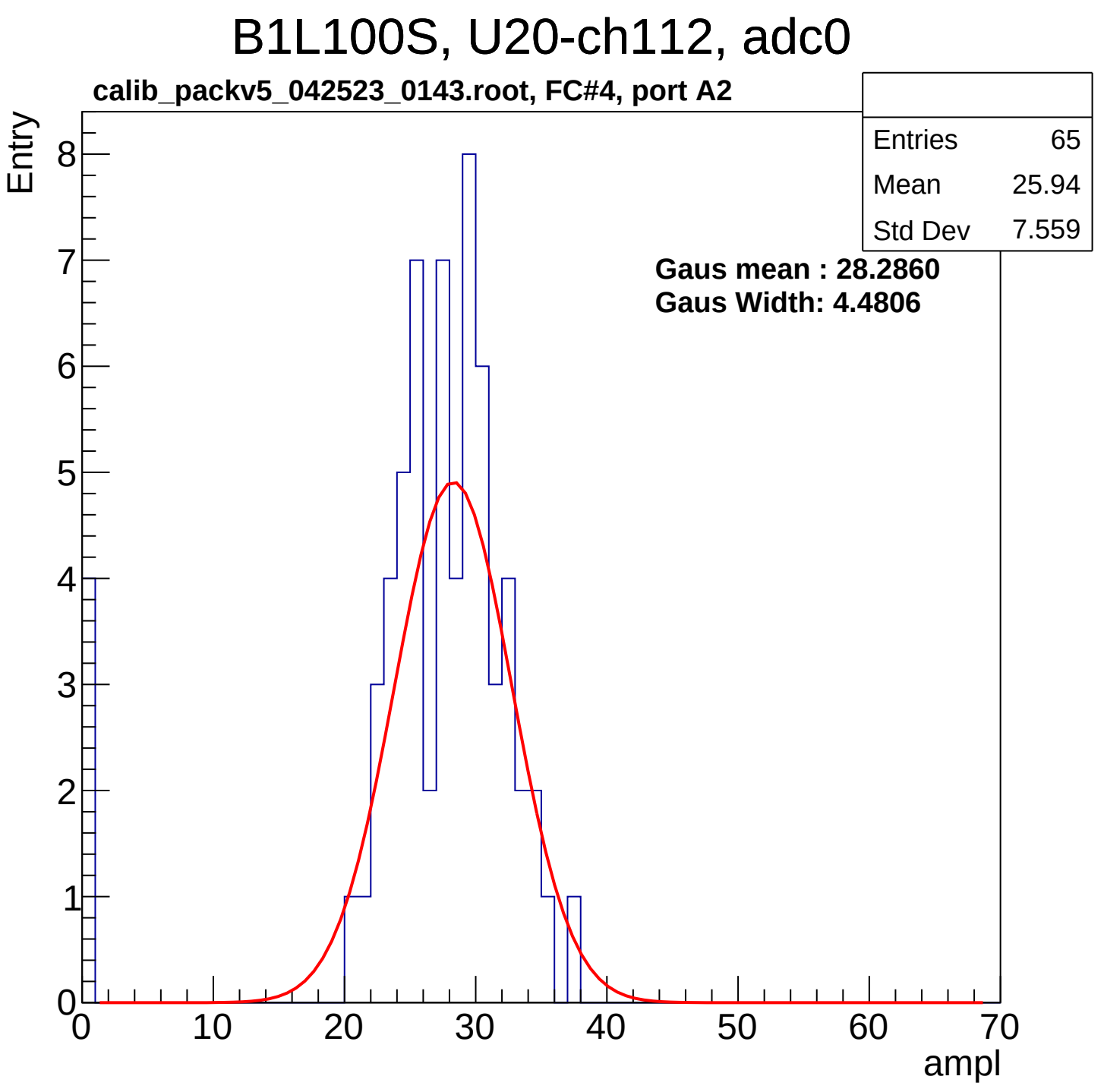
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch111, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 25.94
Std Dev 7.559
Gaus mean : 28.2860
Gaus Width: 4.4806
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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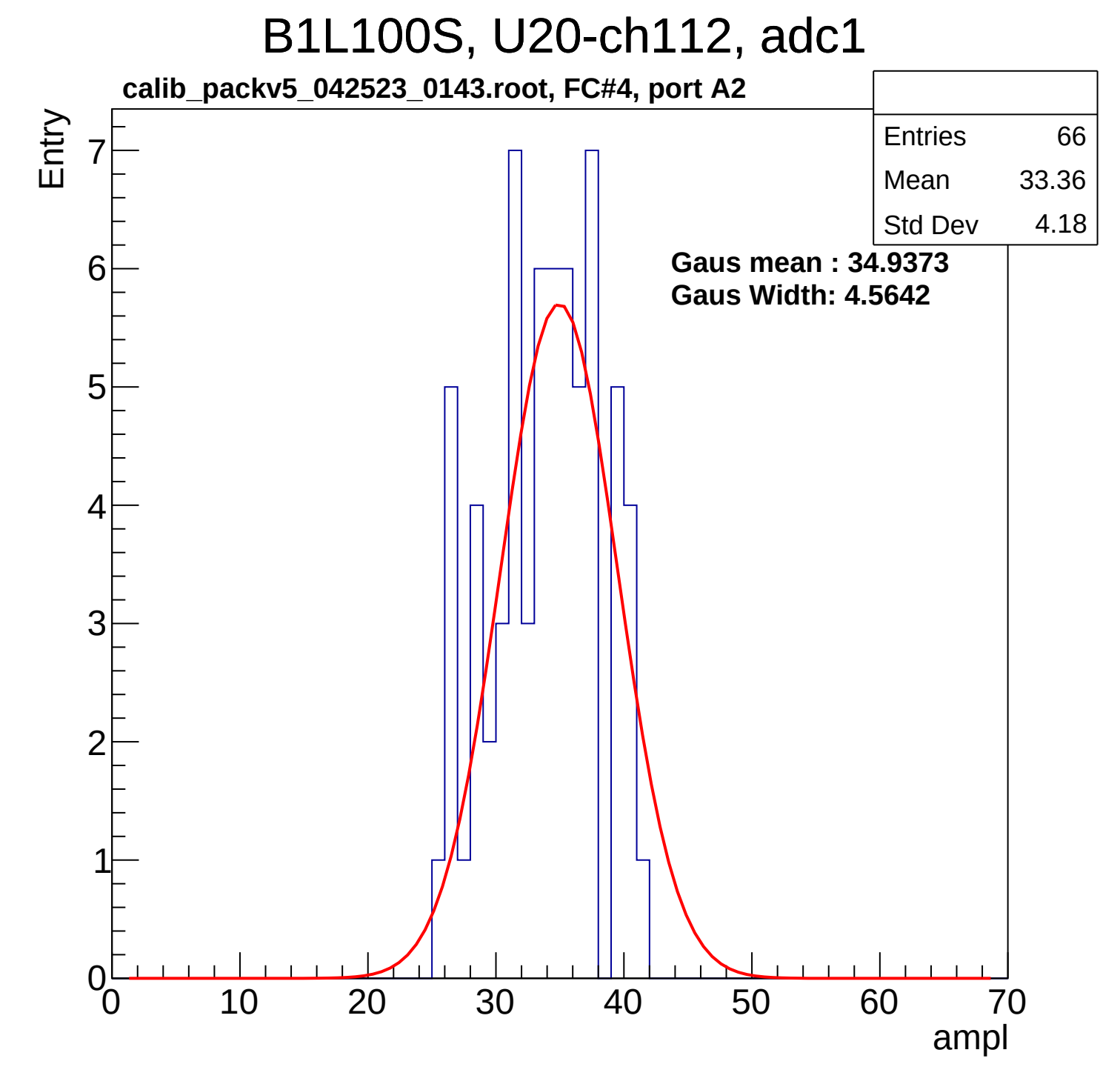
B1L100S, U20-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 33.36
Std Dev 4.18
Gaus mean : 34.9373
Gaus Width: 4.5642
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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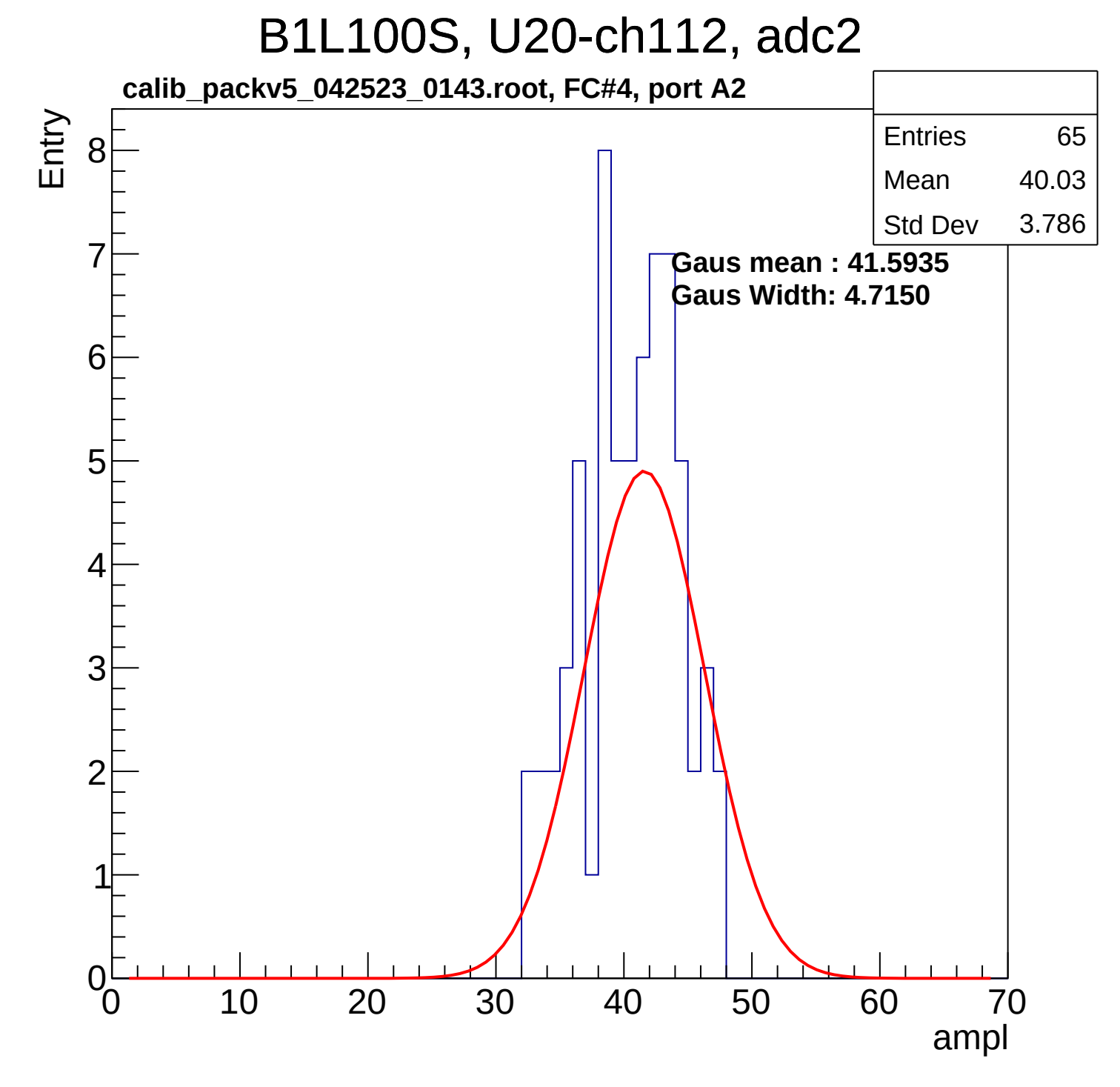
B1L100S, U20-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 40.03
Std Dev 3.786
Gaus mean : 41.5935
Gaus Width: 4.7150
Entry
ampl
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1
2
3
4
5
6
7
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0
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40
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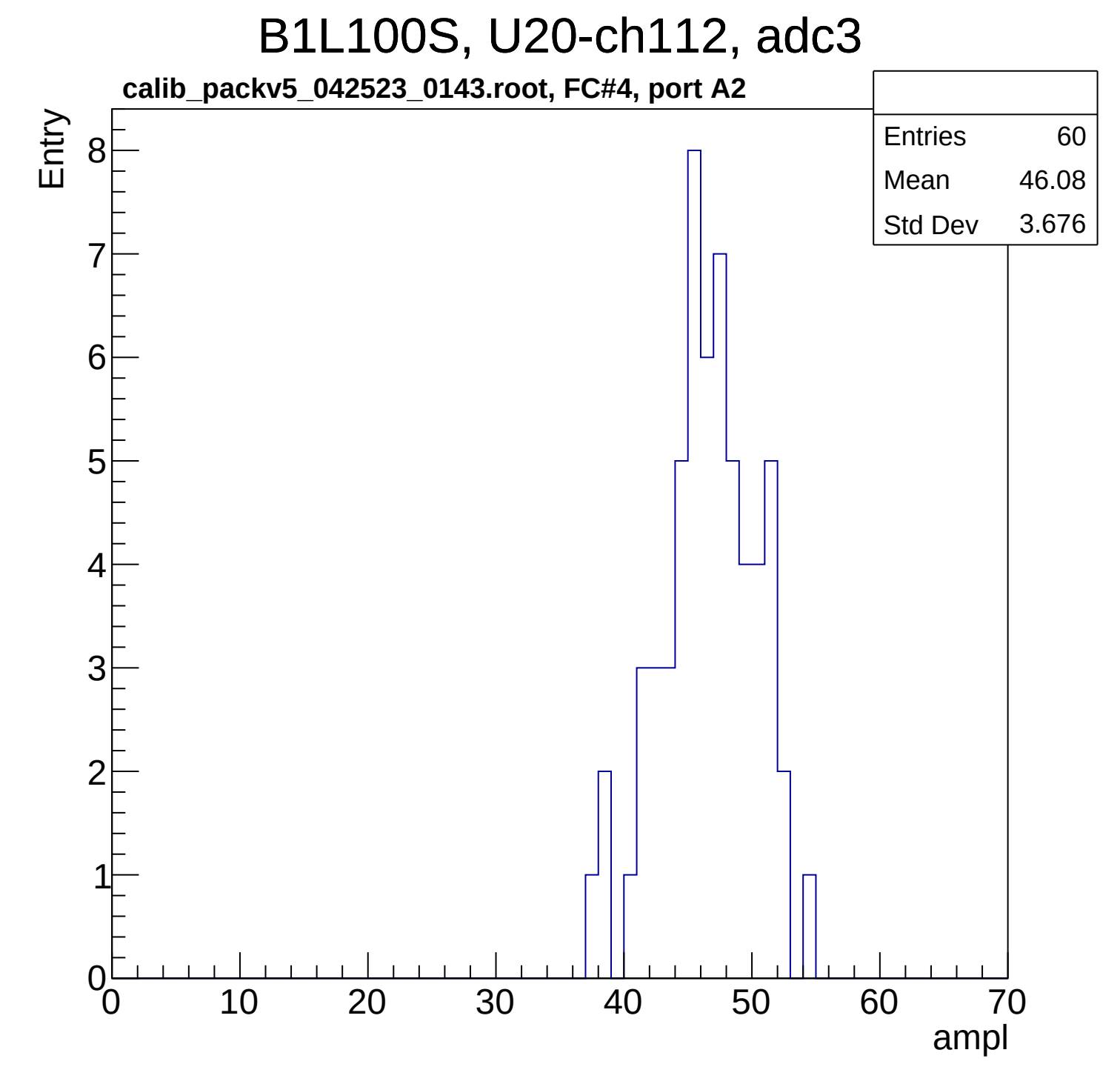
B1L100S, U20-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 46.08
Std Dev 3.676
Entry
ampl
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1
2
3
4
5
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0
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60
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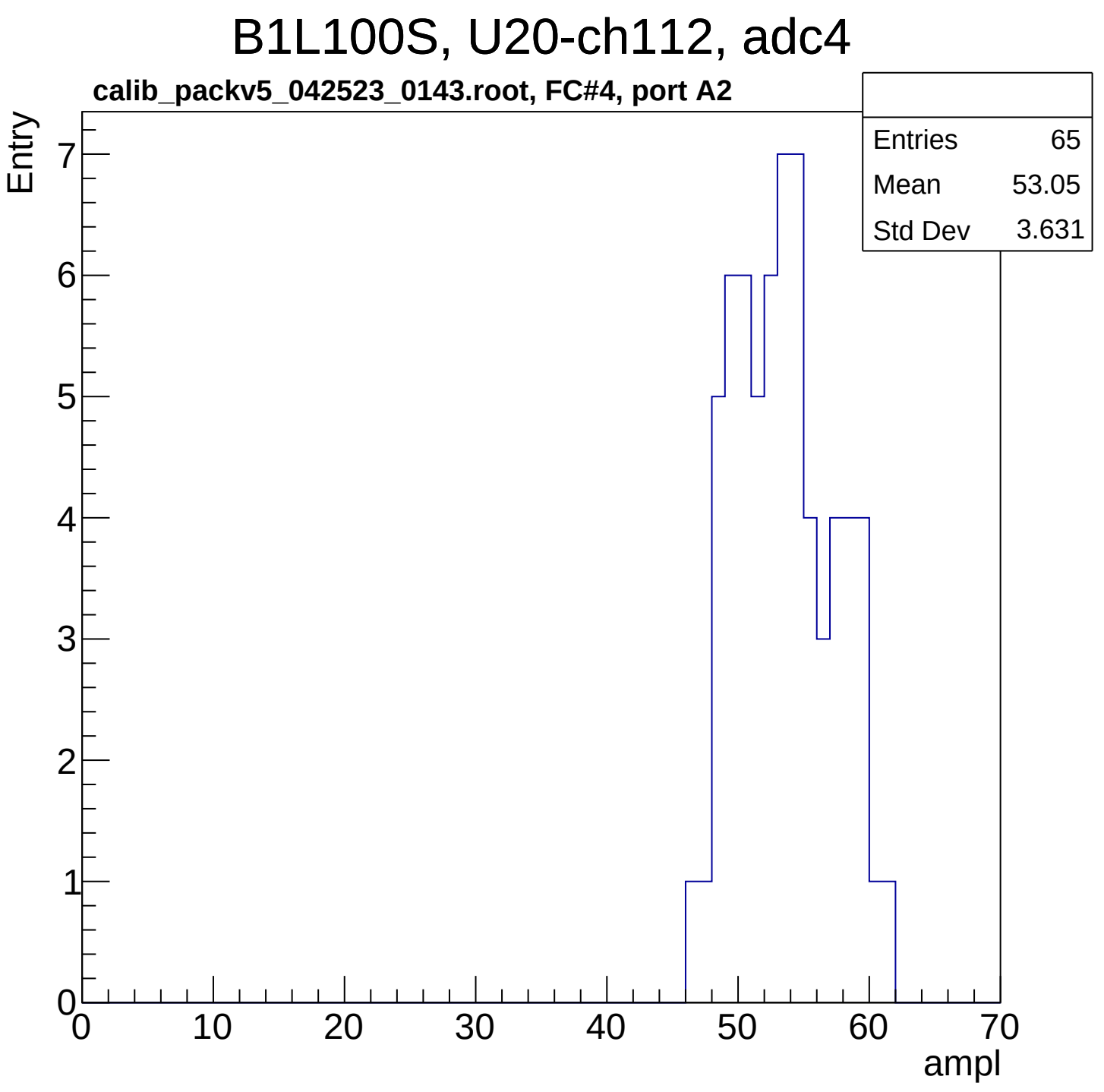
B1L100S, U20-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 53.05
Std Dev 3.631
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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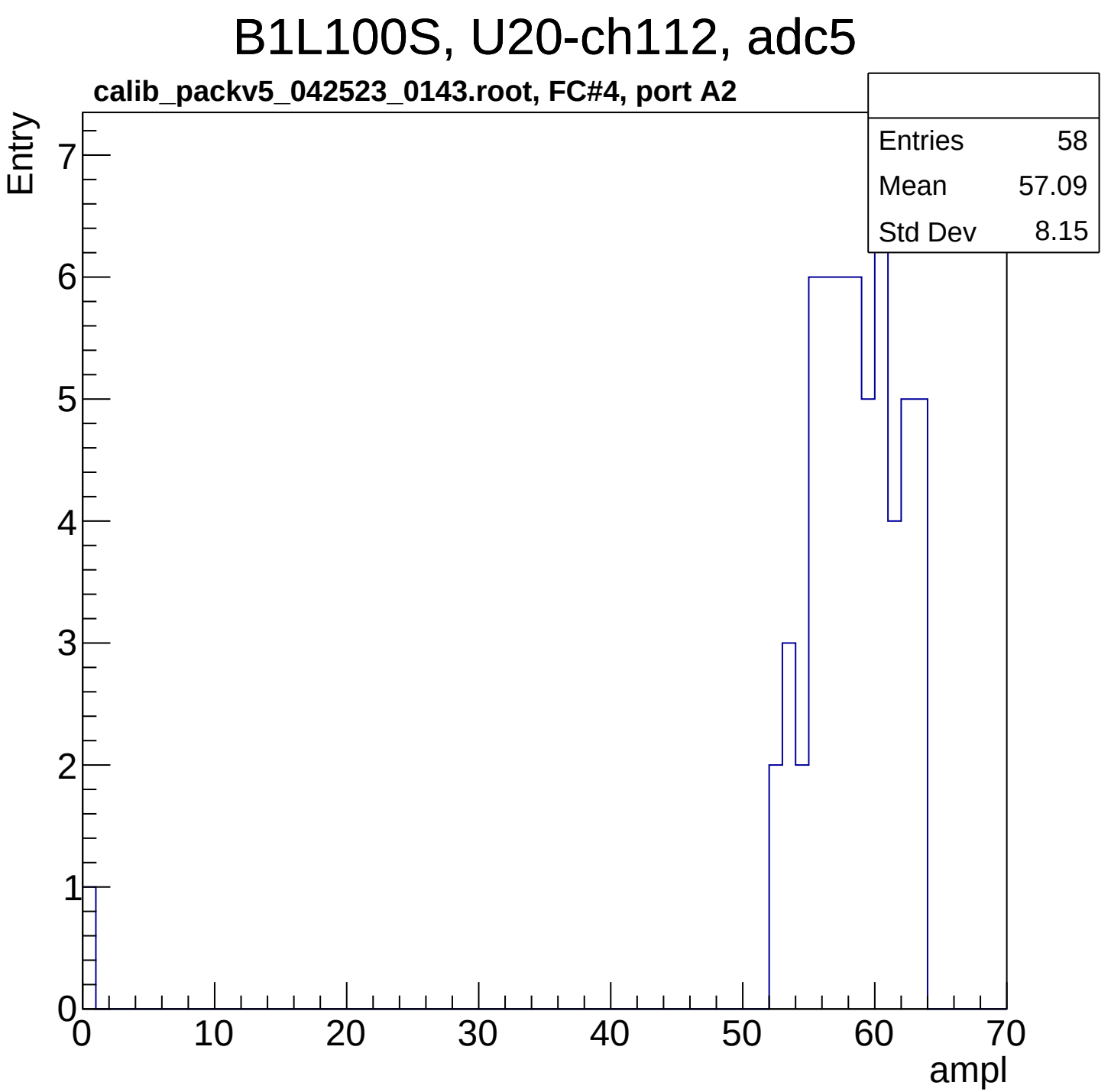
B1L100S, U20-ch112, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 57.09
Std Dev 8.15
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
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60
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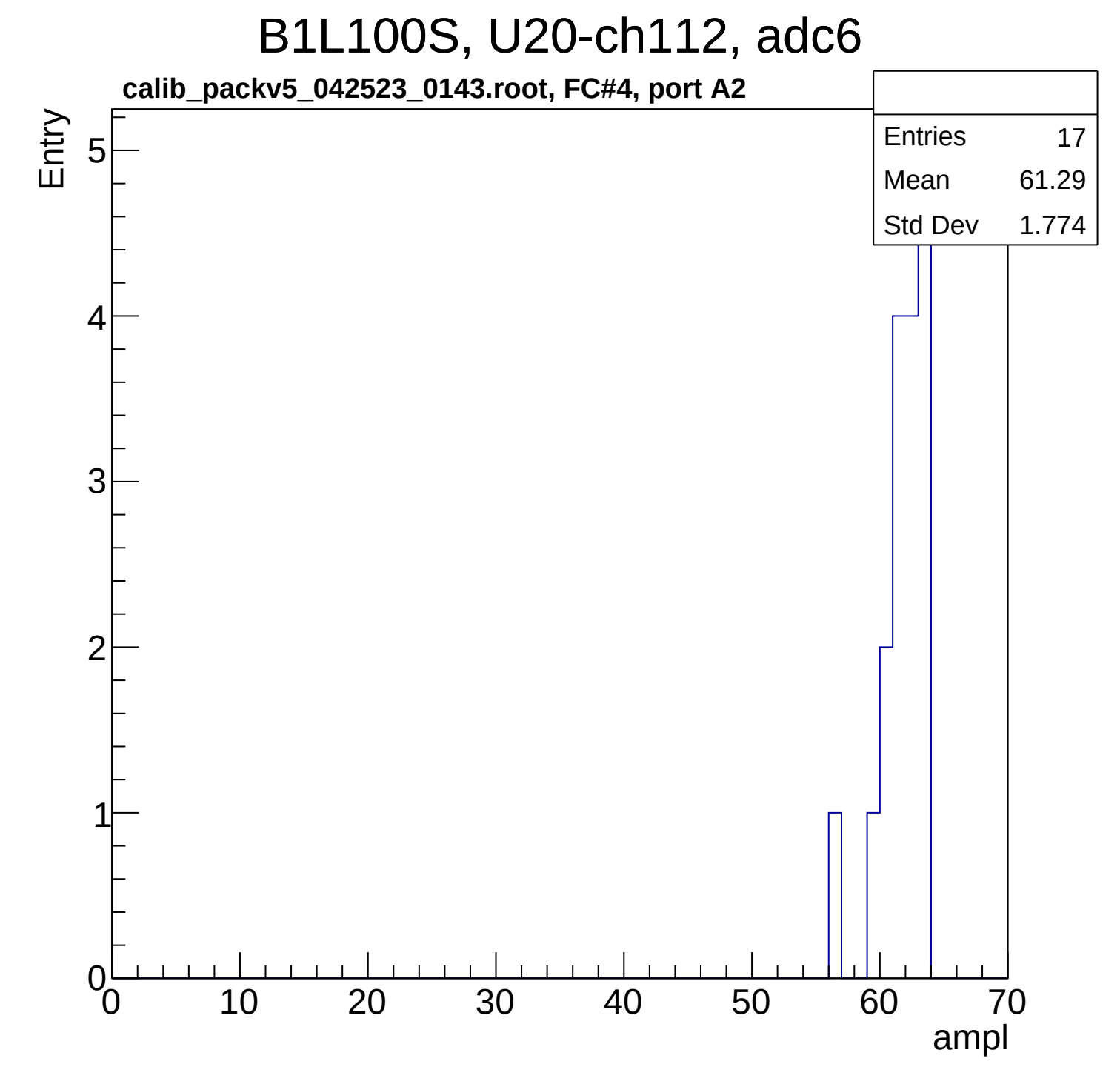
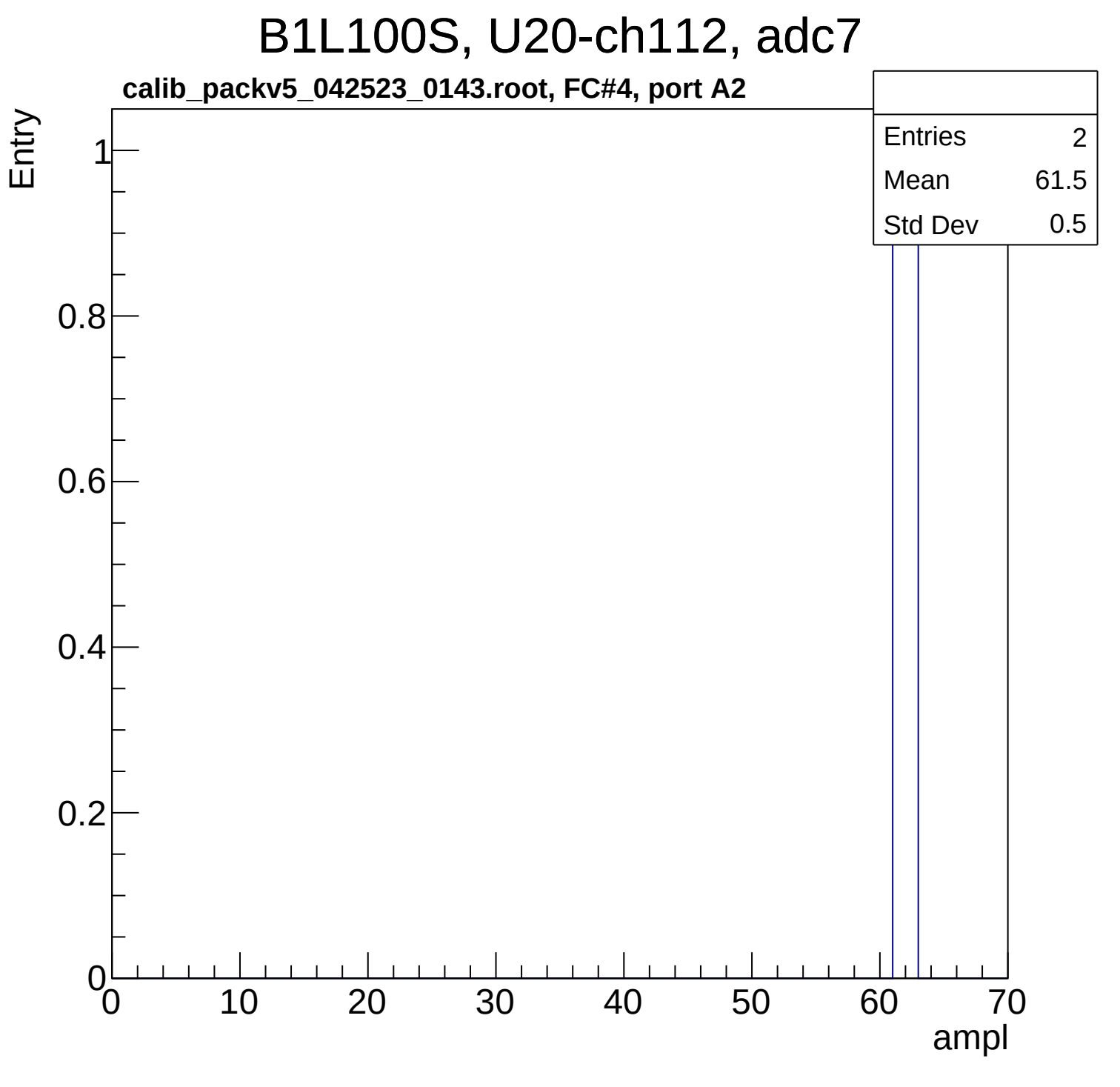

B1L100S, U20-ch112, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 17
Mean 61.29
Std Dev 1.774
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U20-ch112, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
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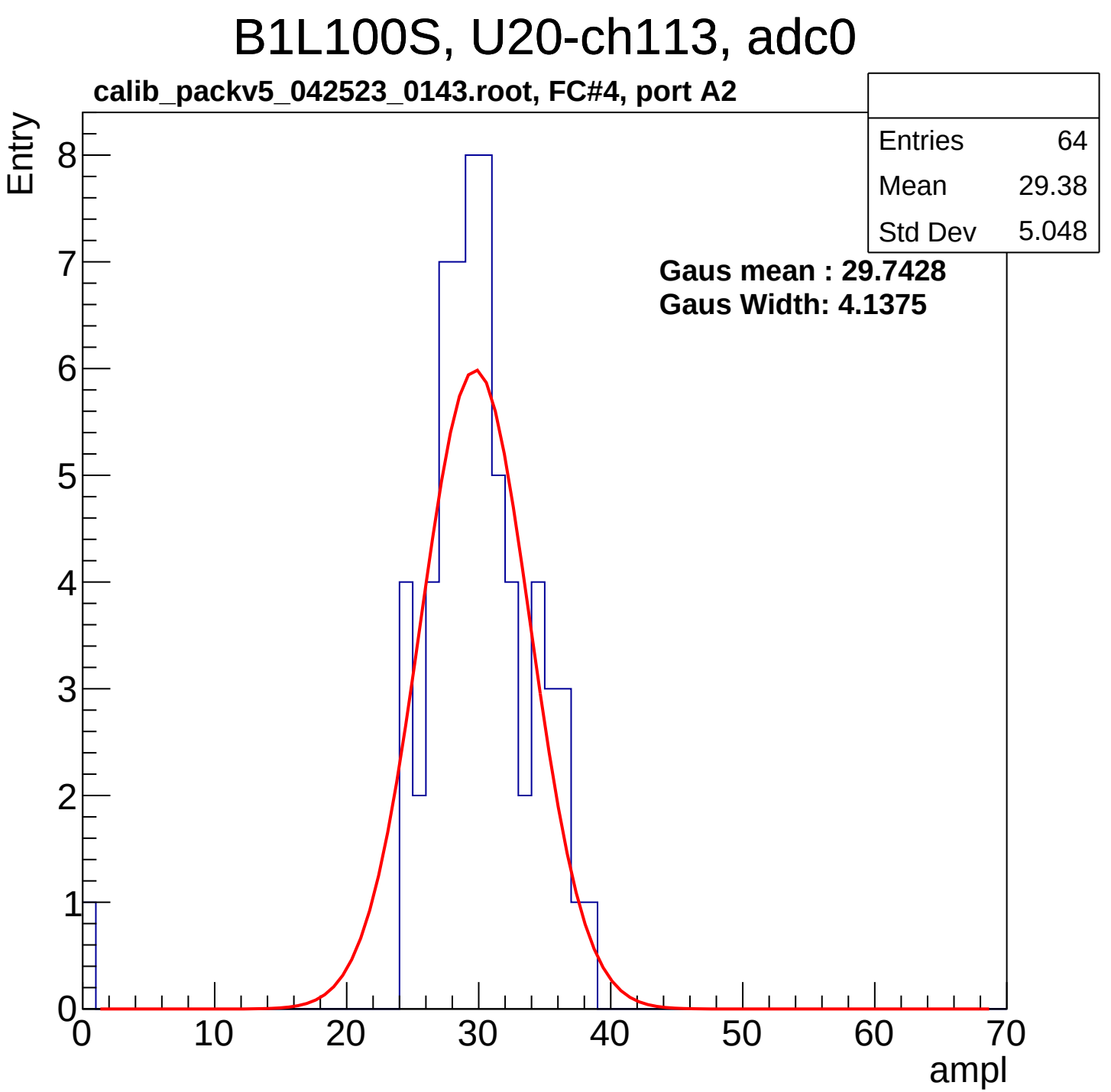
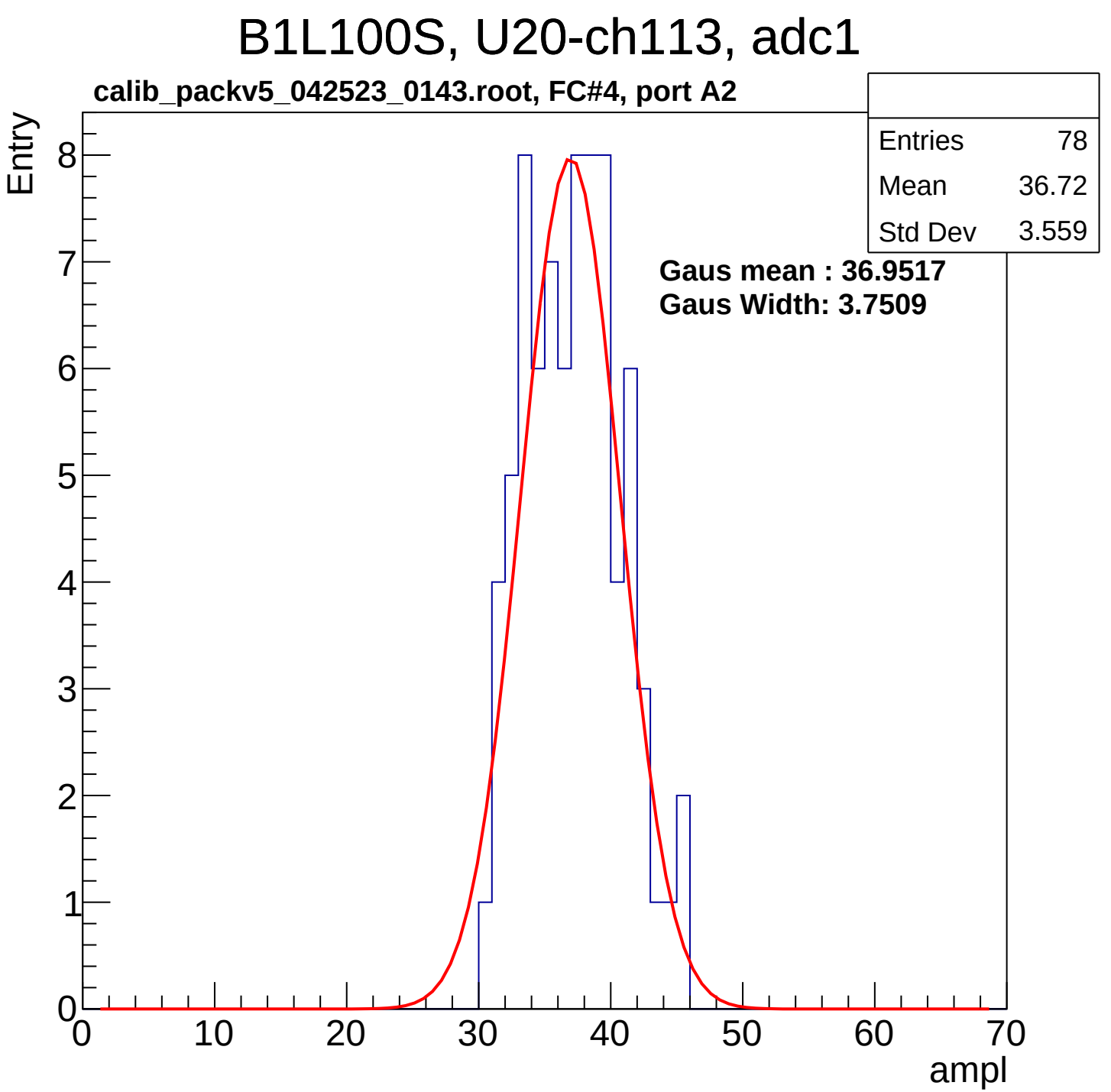
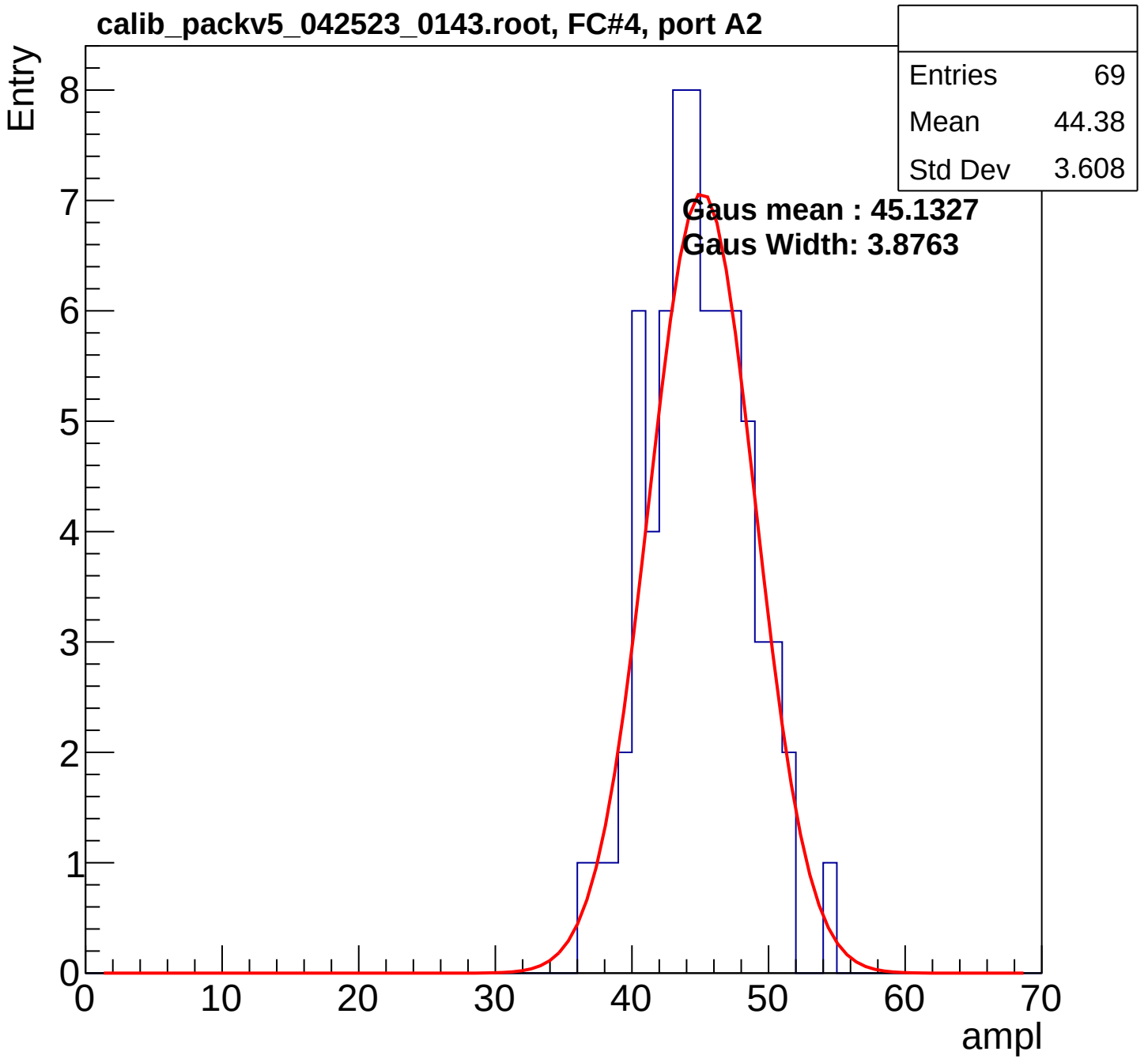
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.38
Std Dev 5.048
Gaus mean : 29.7428
Gaus Width: 4.1375
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.72
Std Dev 3.559
Gaus mean : 36.9517
Gaus Width: 3.7509
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.38
Std Dev 3.608
Gaus mean : 45.1327
Gaus Width: 3.8763
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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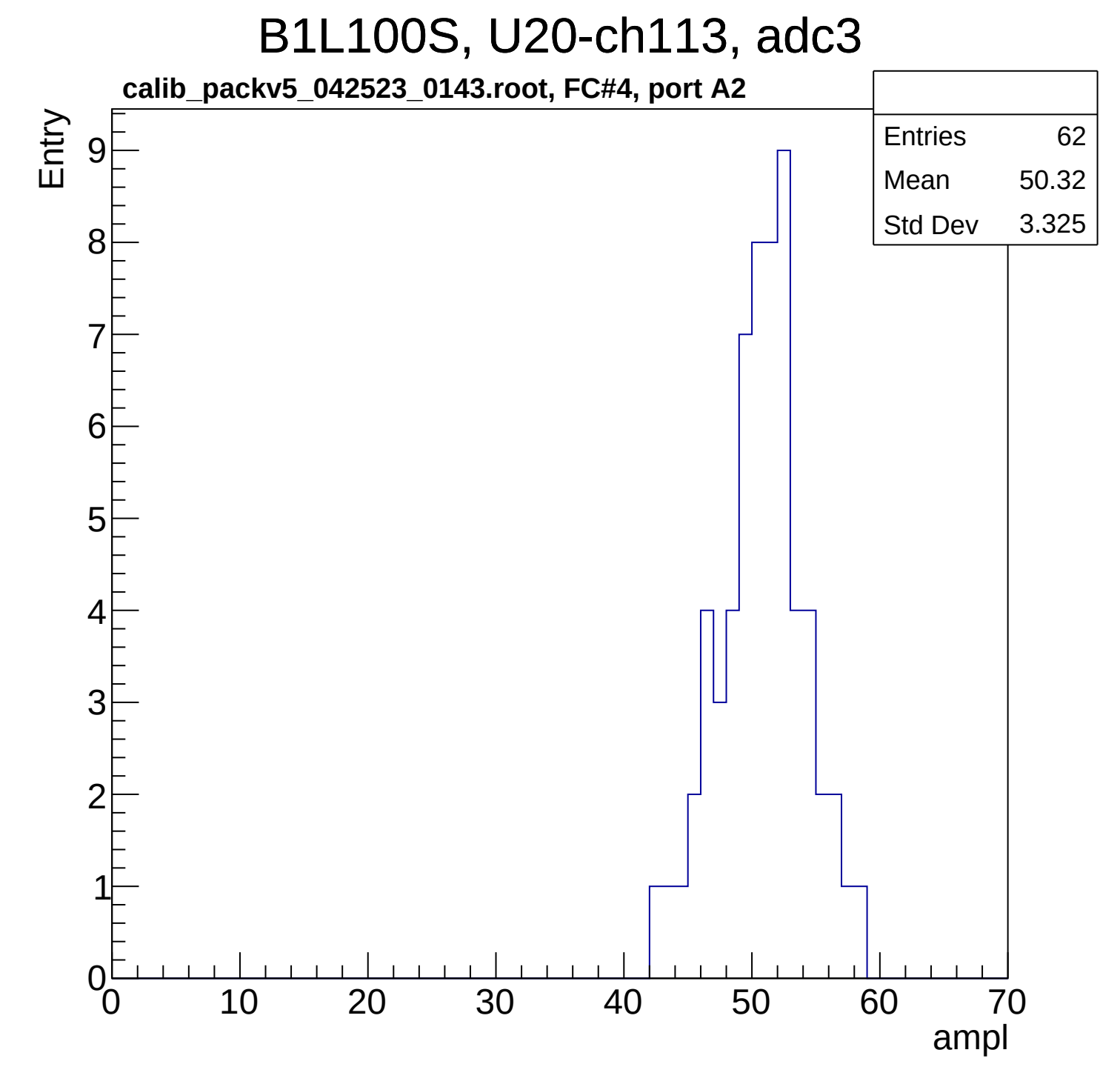
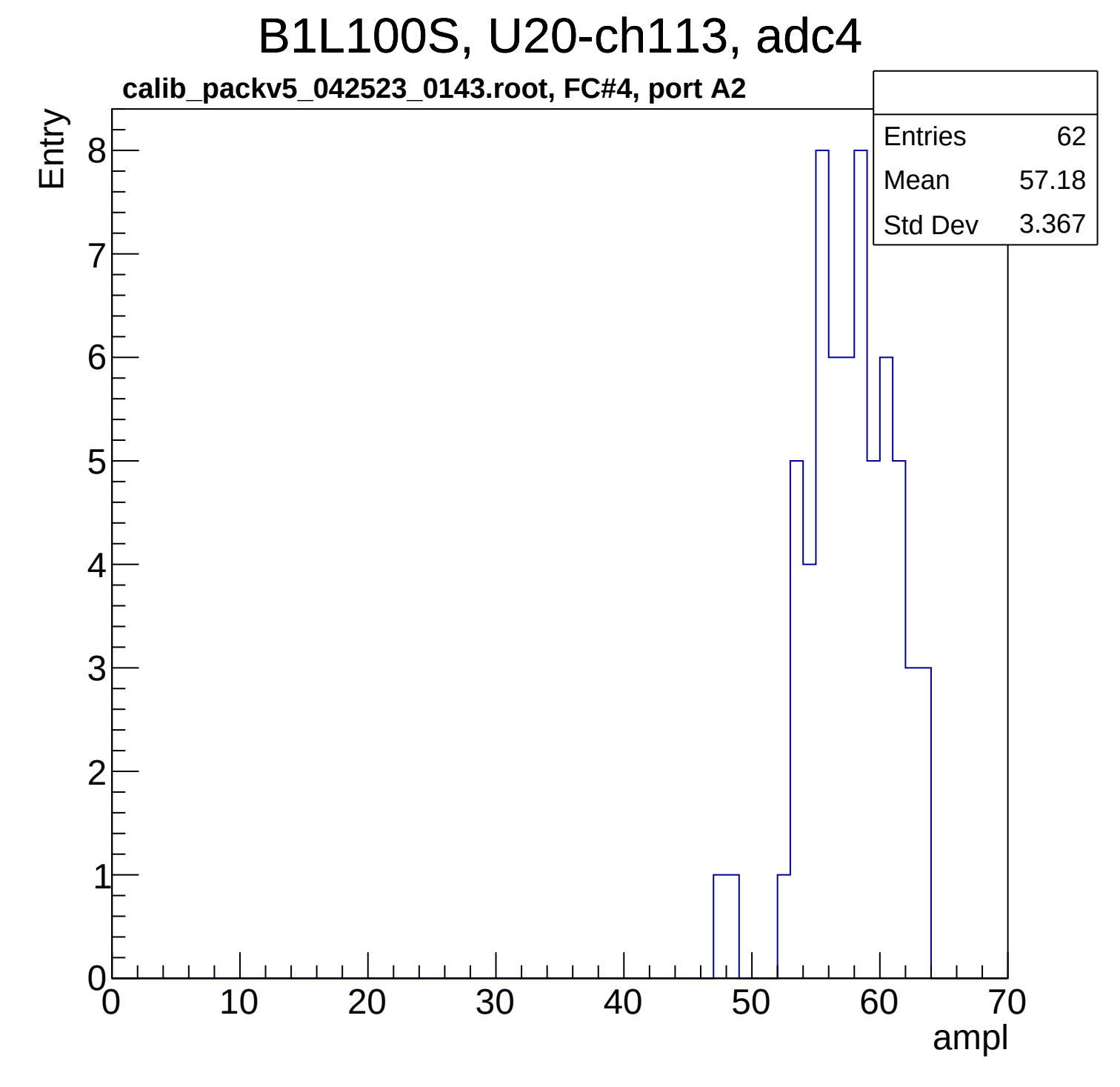
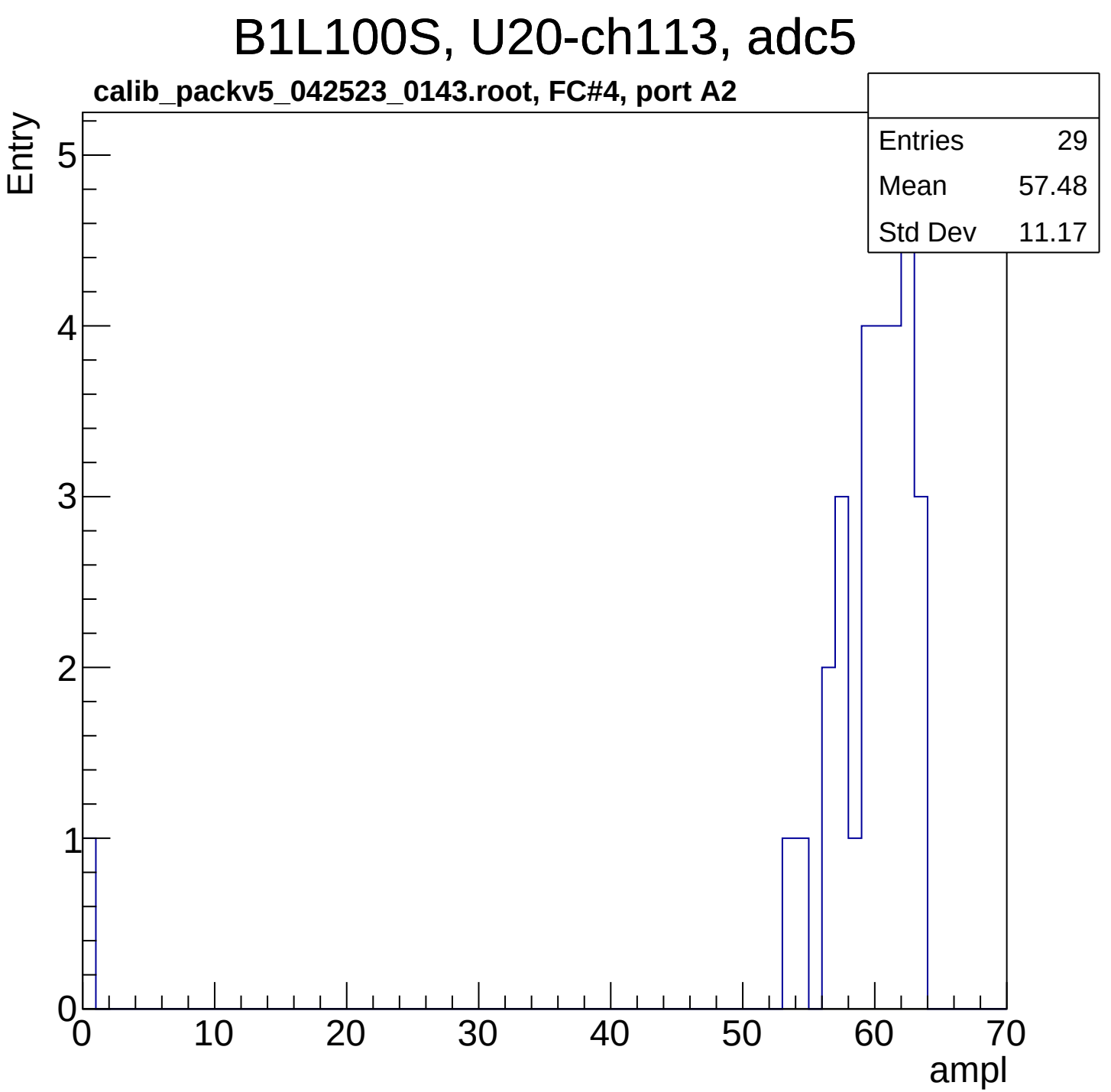
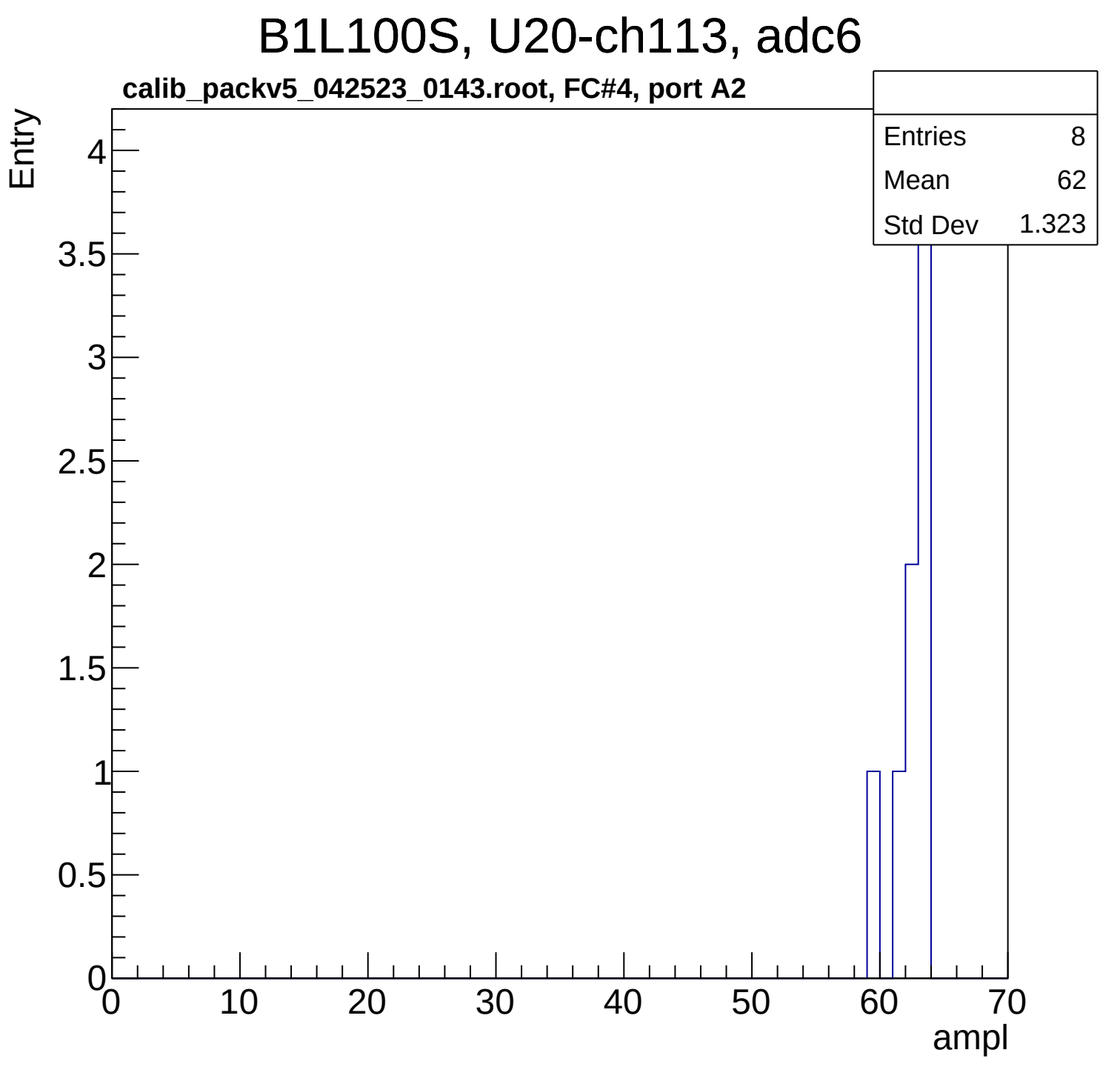

B1L100S, U20-ch113, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.32
Std Dev 3.325
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 57.18
Std Dev 3.367
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 57.48
Std Dev 11.17
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62
Std Dev 1.323
Entry
ampl
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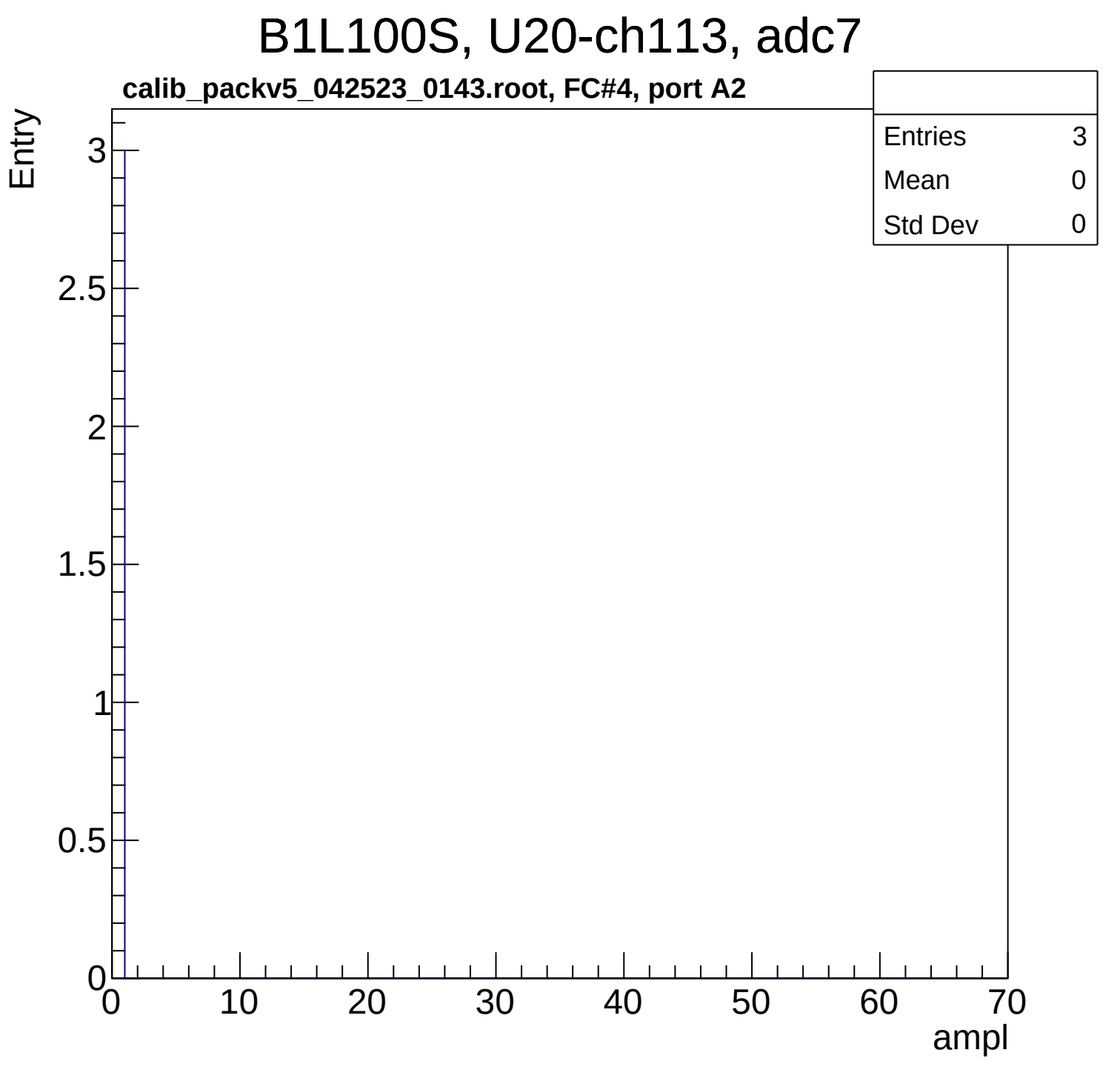
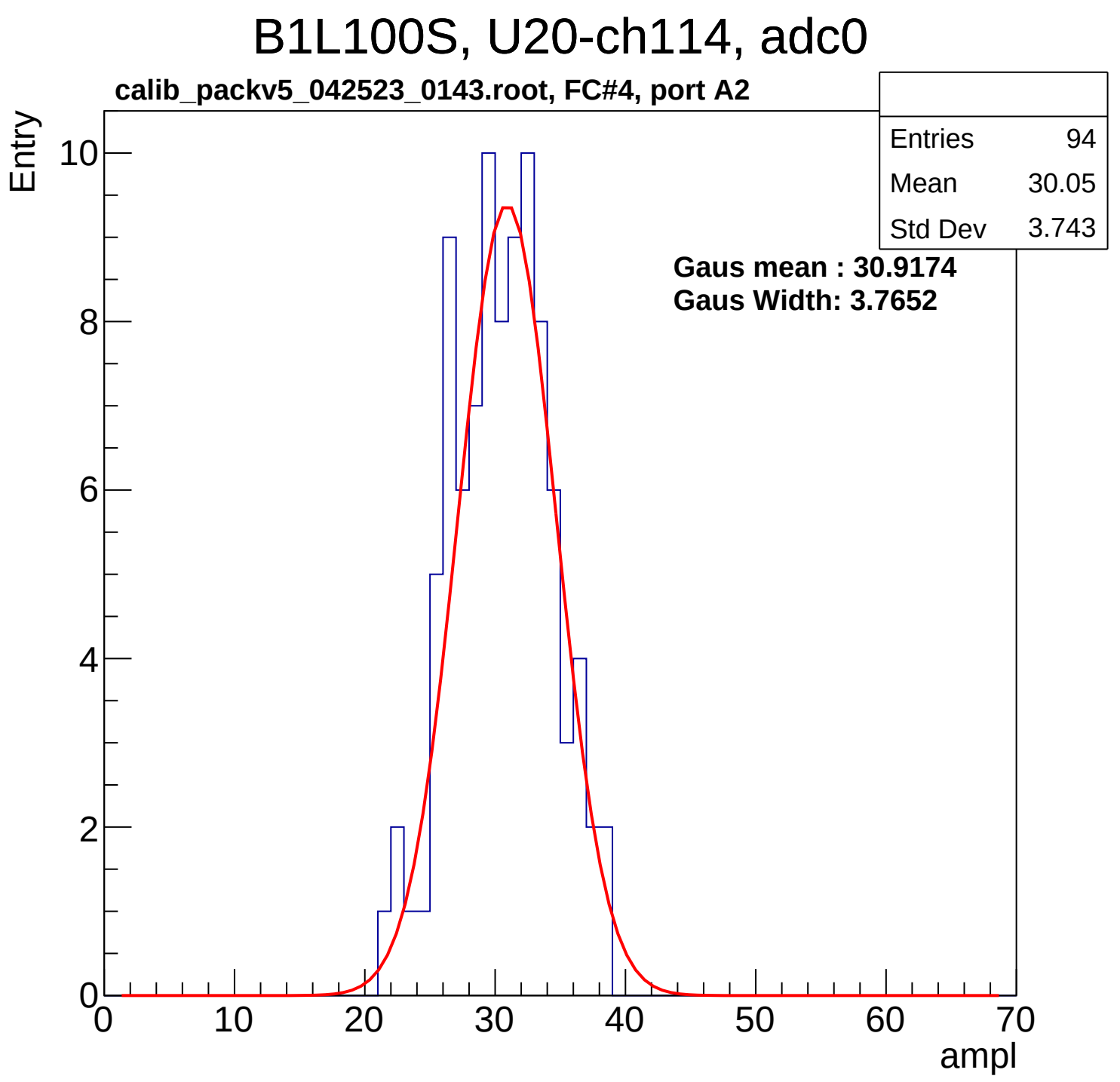
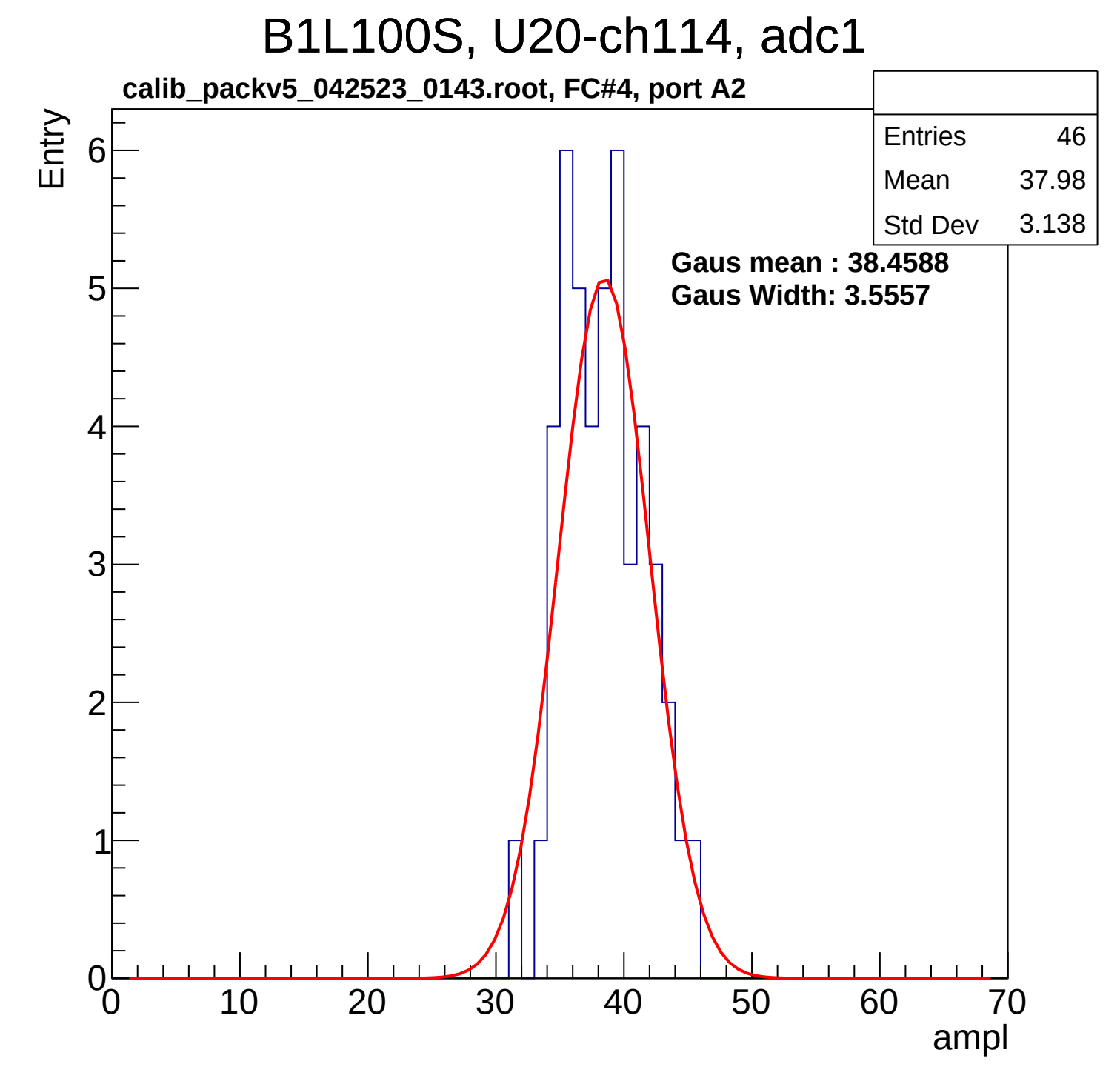
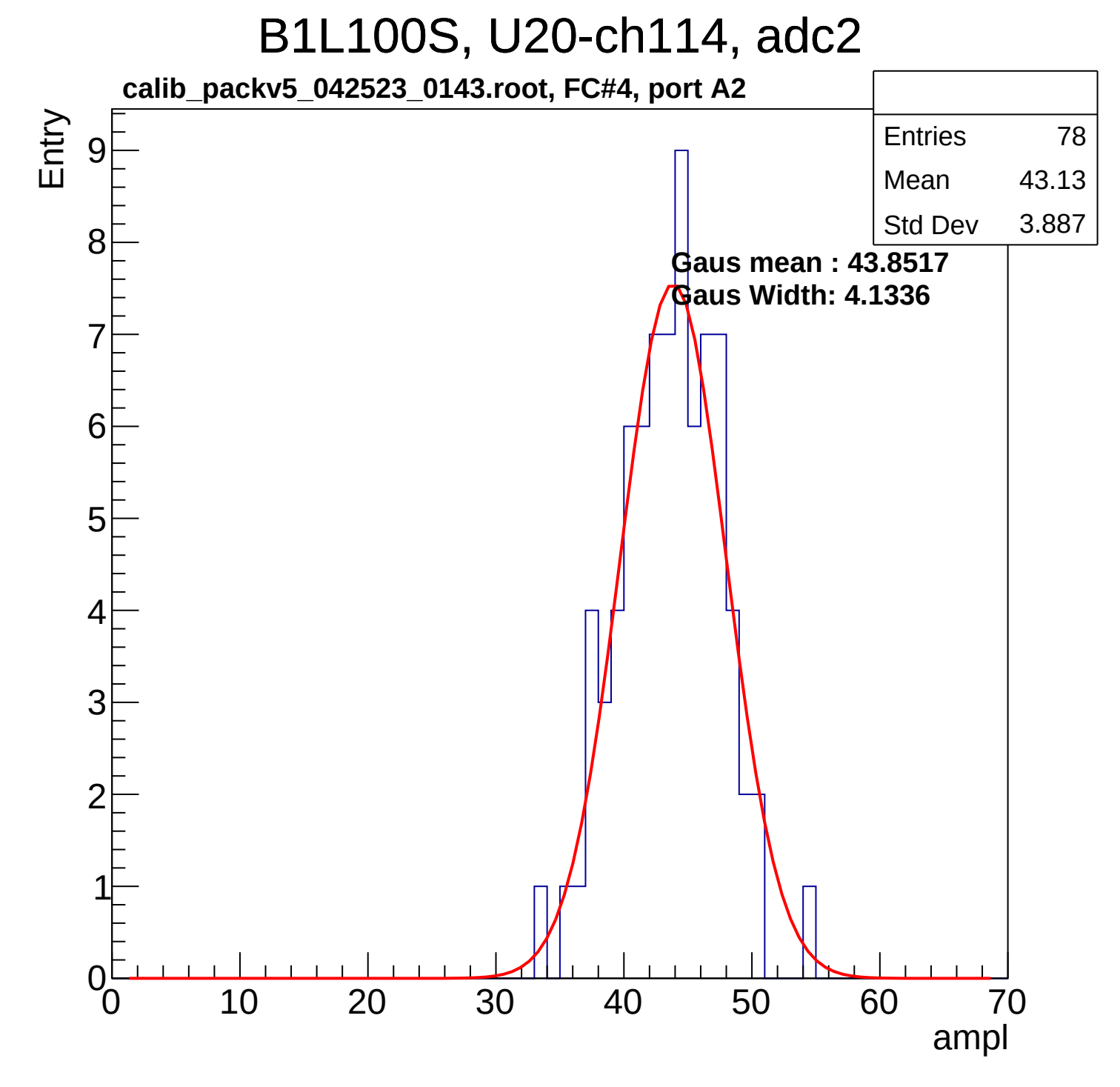
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch113, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch114, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 94
Mean 30.05
Std Dev 3.743
Gaus mean : 30.9174
Gaus Width: 3.7652
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 37.98
Std Dev 3.138
Gaus mean : 38.4588
Gaus Width: 3.5557
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U20-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.13
Std Dev 3.887
Gaus mean : 43.8517
Gaus Width: 4.1336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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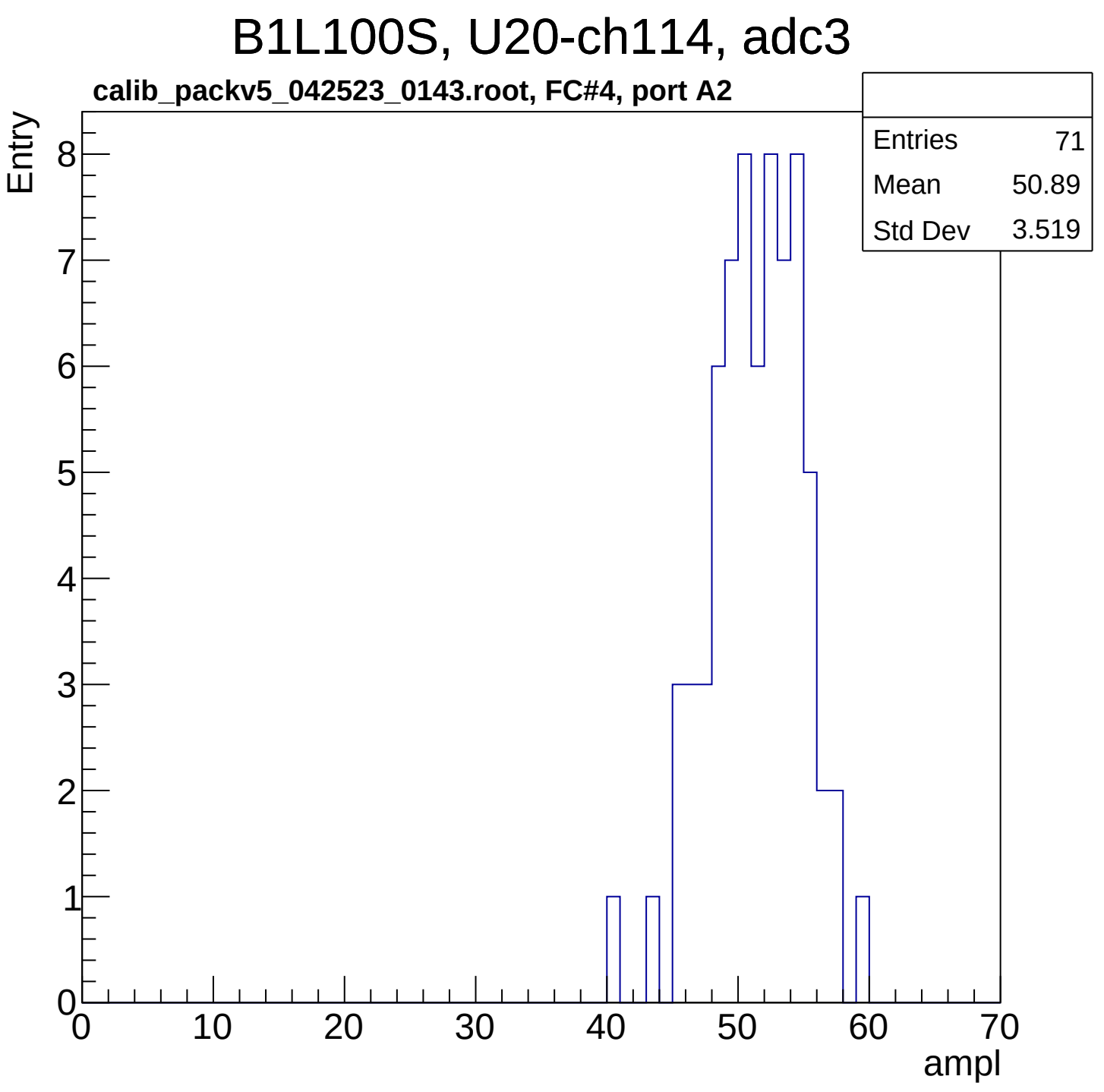
B1L100S, U20-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.89
Std Dev 3.519
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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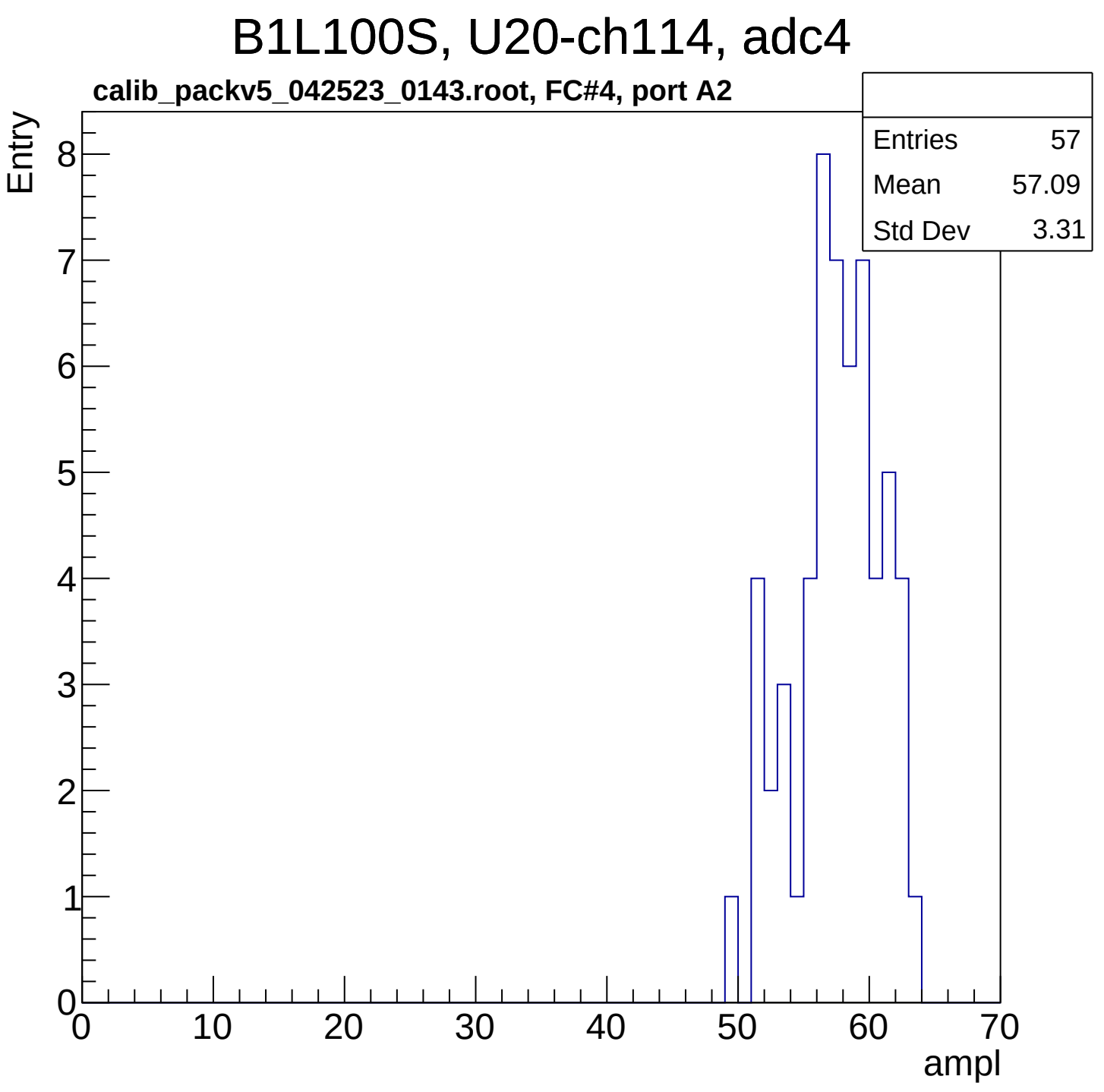
B1L100S, U20-ch114, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.09
Std Dev 3.31
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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60
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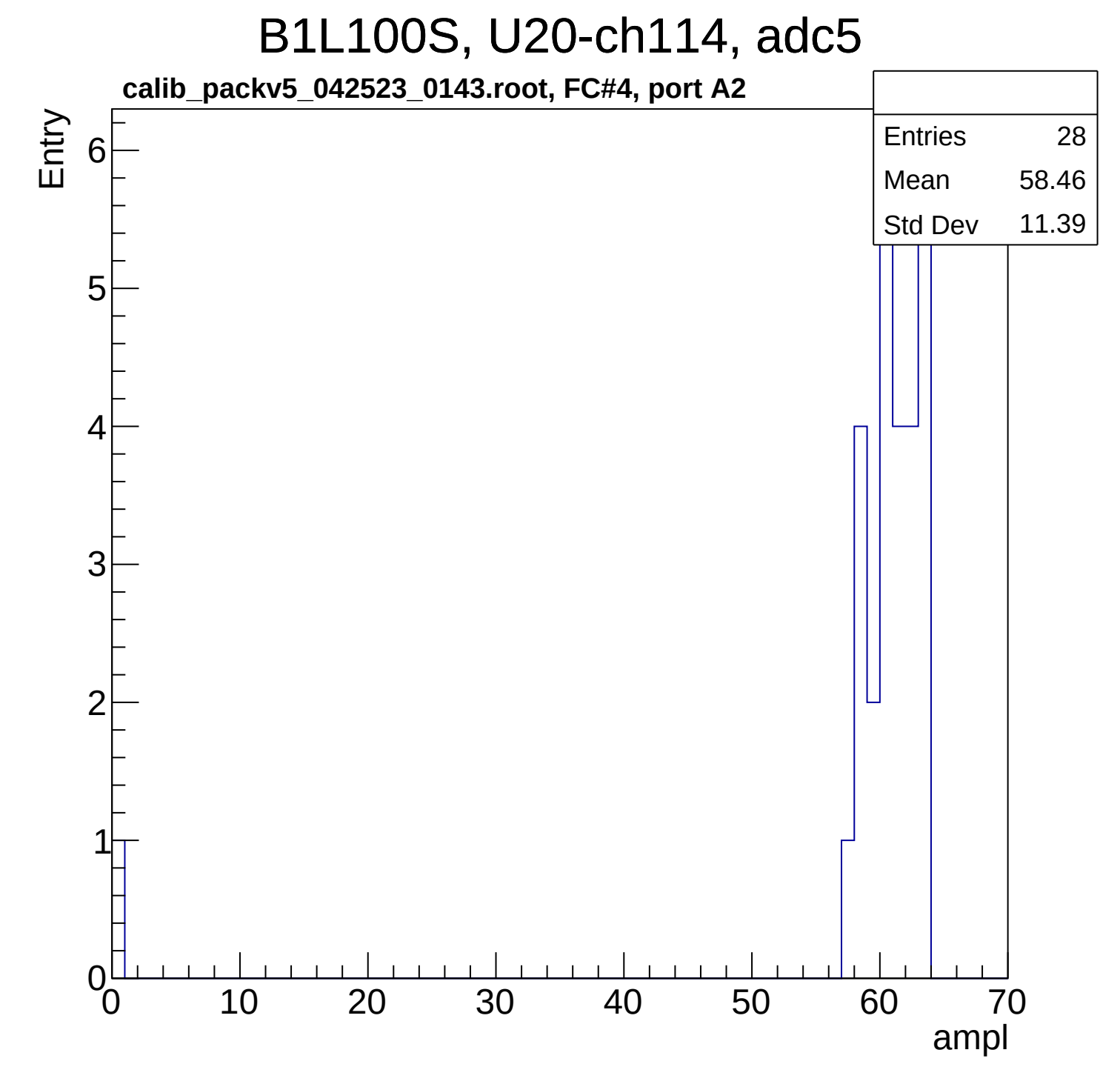
B1L100S, U20-ch114, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.46
Std Dev 11.39
Entry
ampl
0
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2
3
4
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6
0
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60
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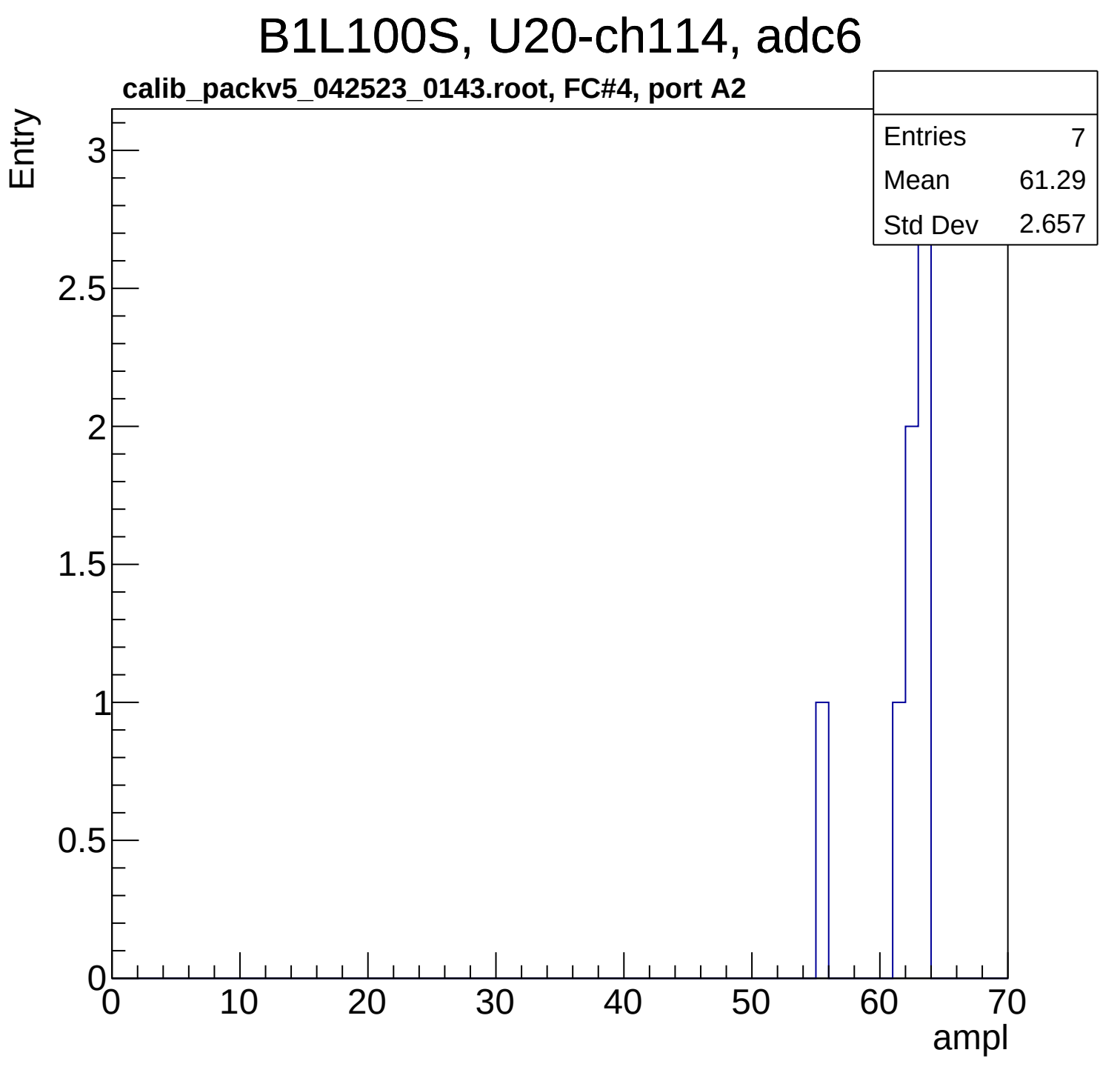
B1L100S, U20-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.29
Std Dev 2.657
Entry
ampl
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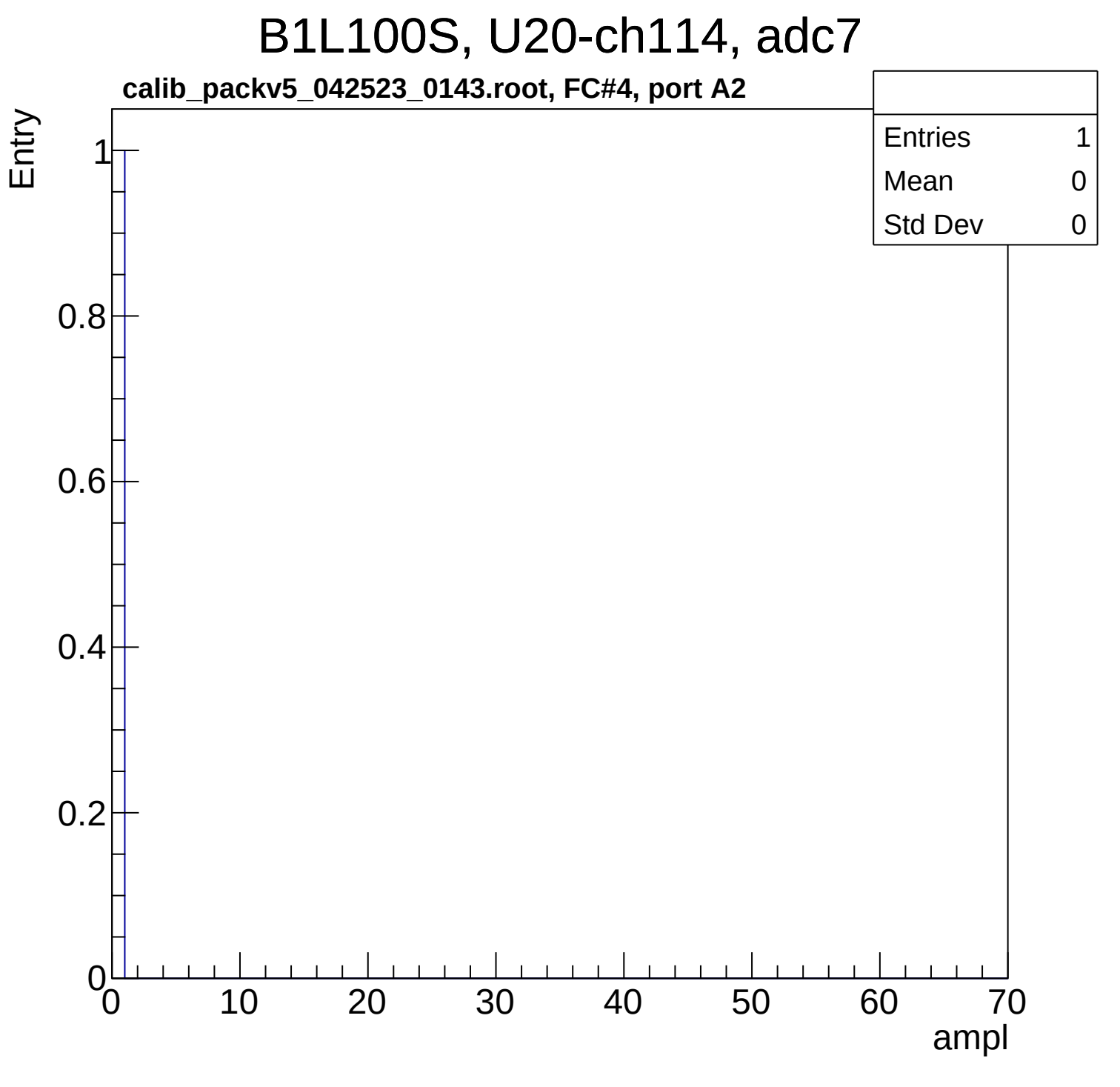
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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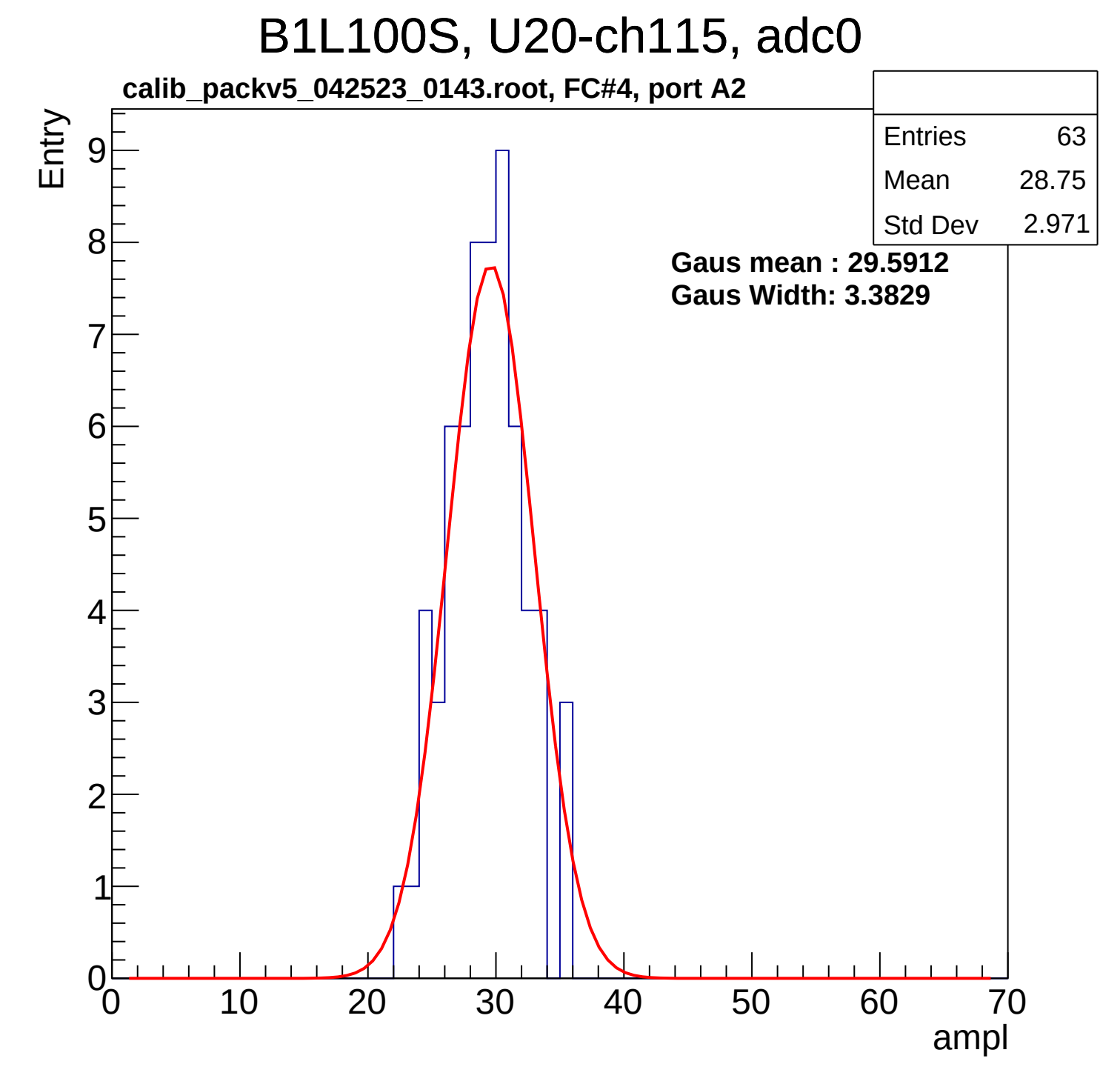
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch115, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.75
Std Dev 2.971
Gaus mean : 29.5912
Gaus Width: 3.3829
Entry
ampl
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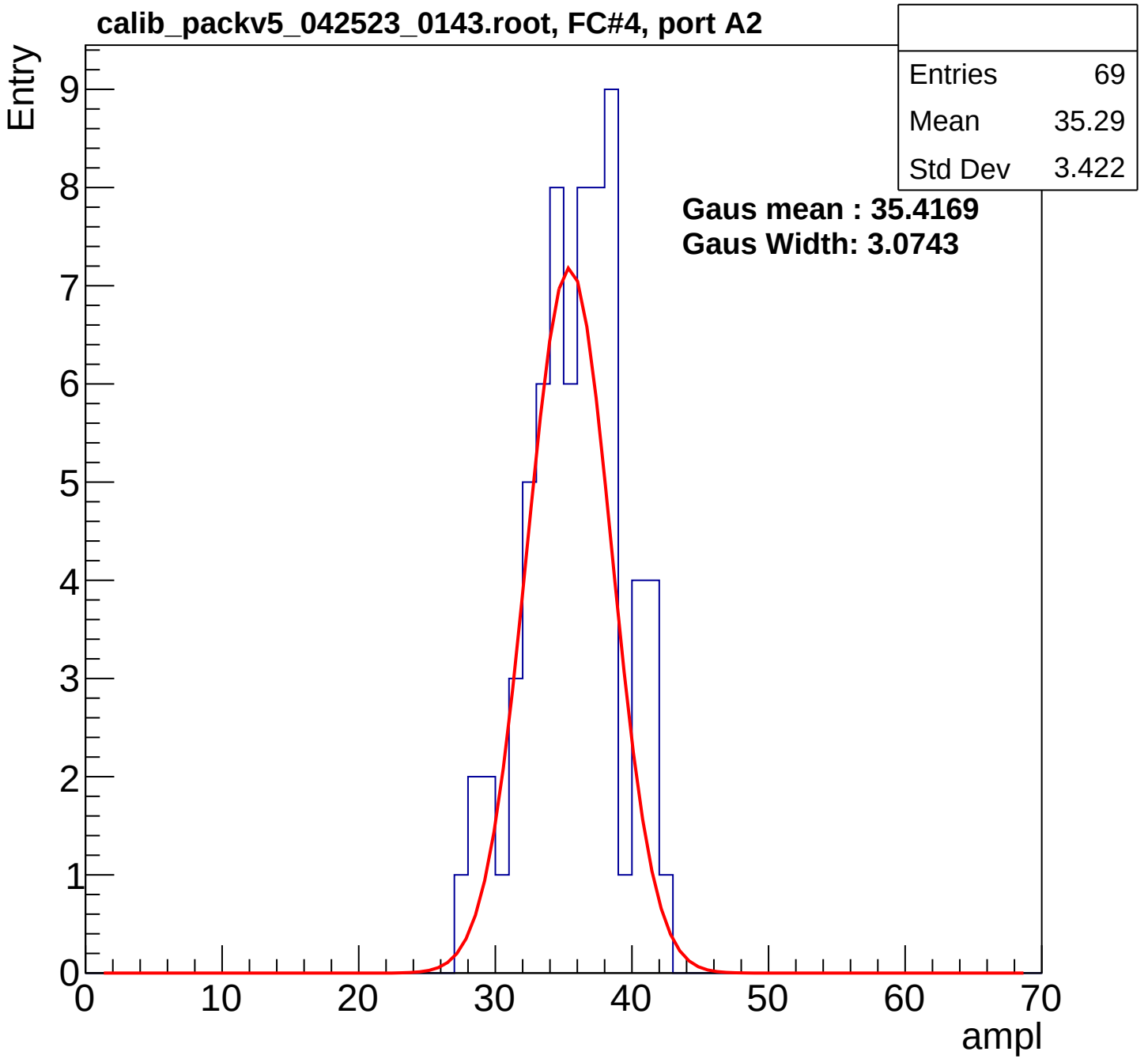
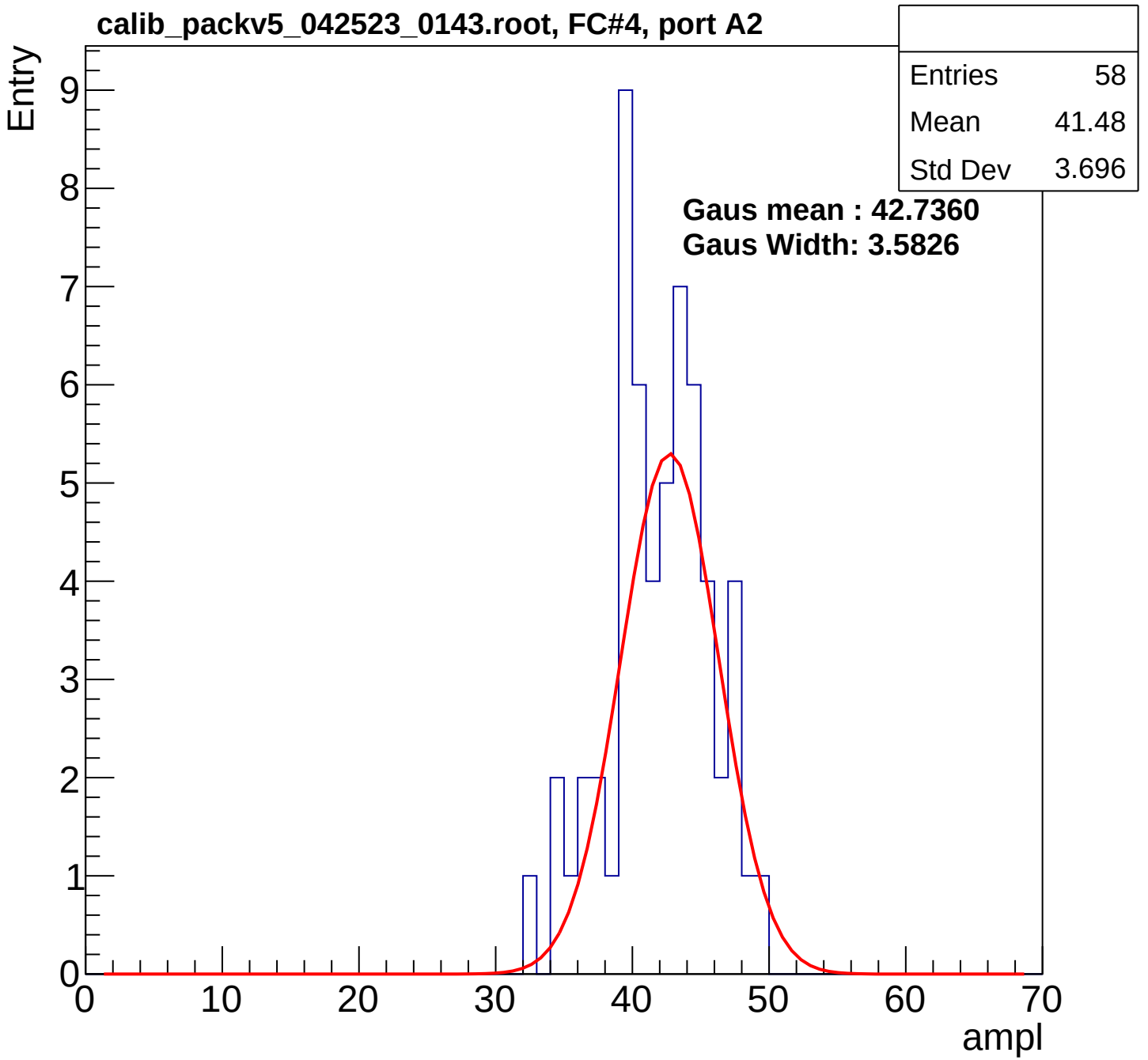
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 35.29
Std Dev 3.422
Gaus mean : 35.4169
Gaus Width: 3.0743
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 41.48
Std Dev 3.696
Gaus mean : 42.7360
Gaus Width: 3.5826
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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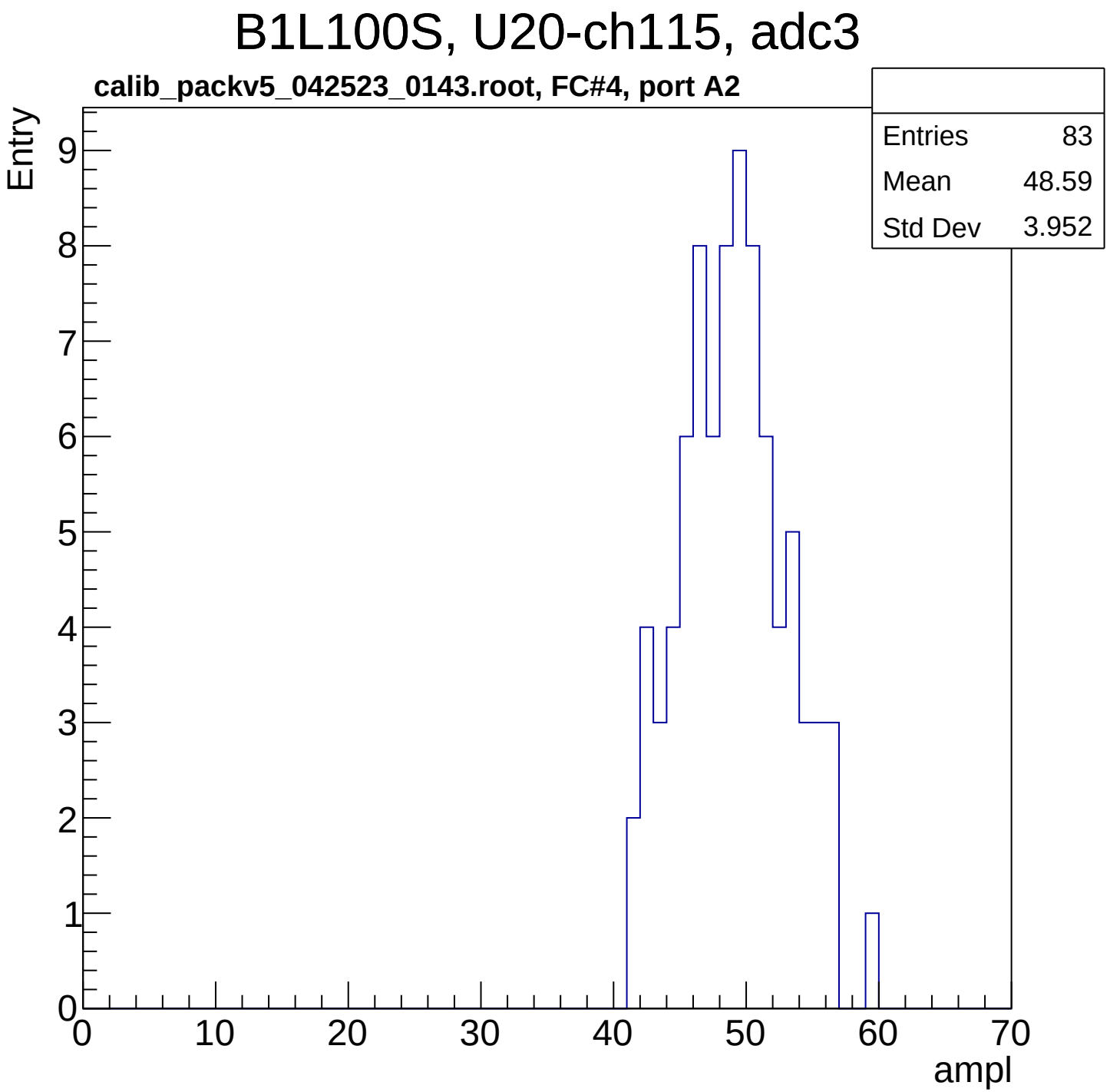
B1L100S, U20-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 48.59
Std Dev 3.952
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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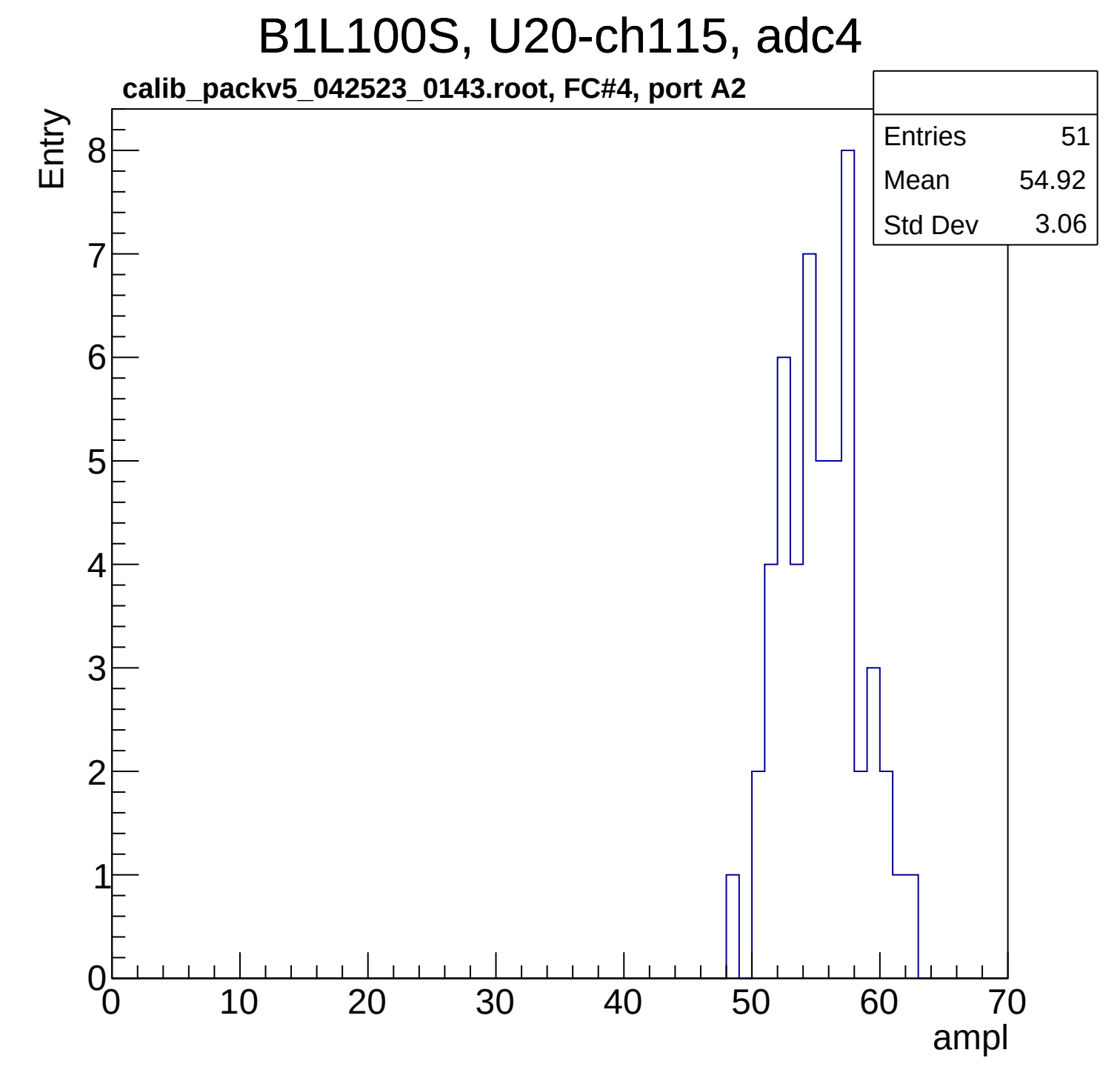
B1L100S, U20-ch115, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.92
Std Dev 3.06
Entry
ampl
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2
3
4
5
6
7
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0
10
20
30
40
50
60
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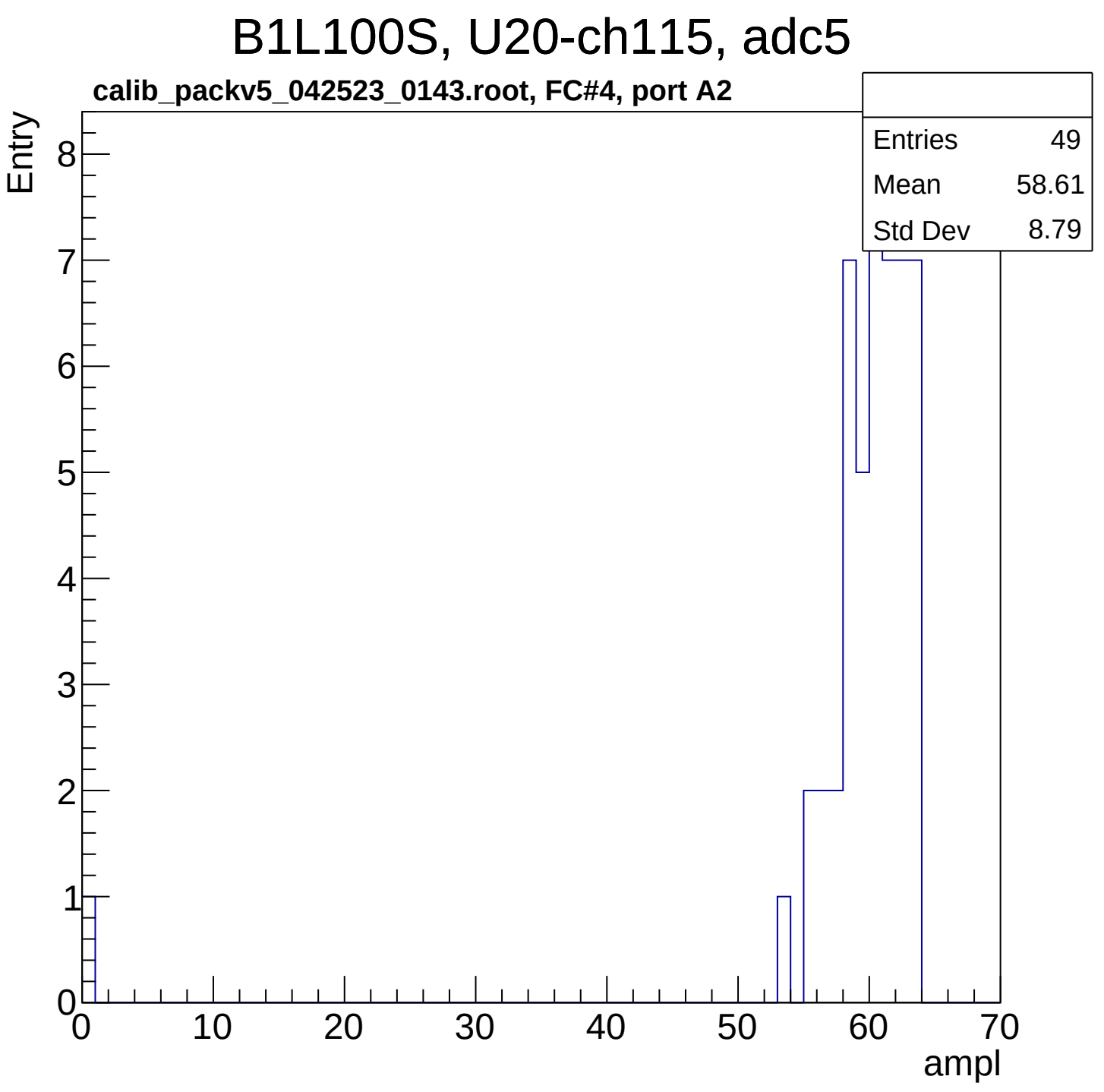
B1L100S, U20-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.61
Std Dev 8.79
Entry
ampl
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4
5
6
7
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0
10
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40
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60
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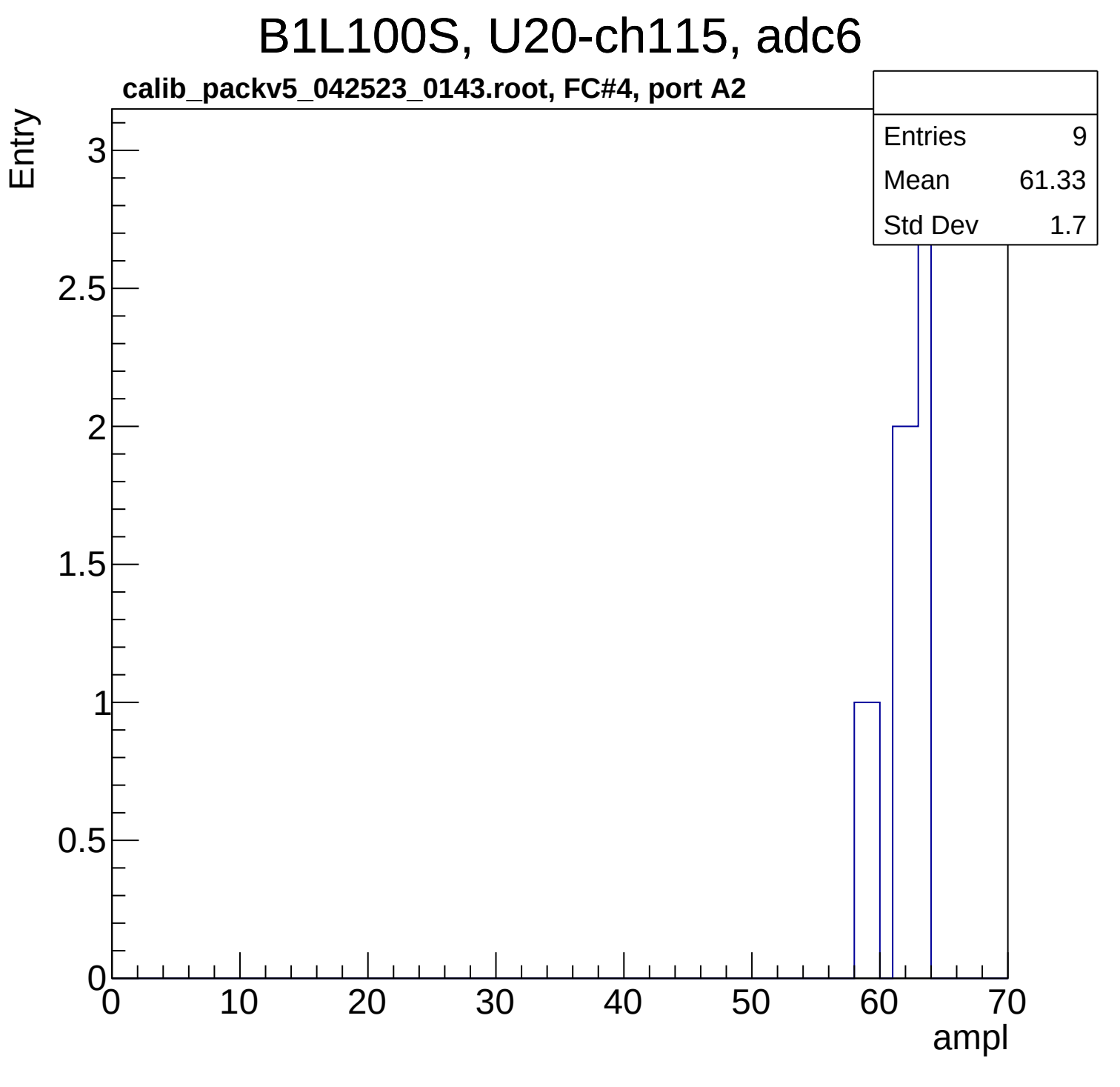
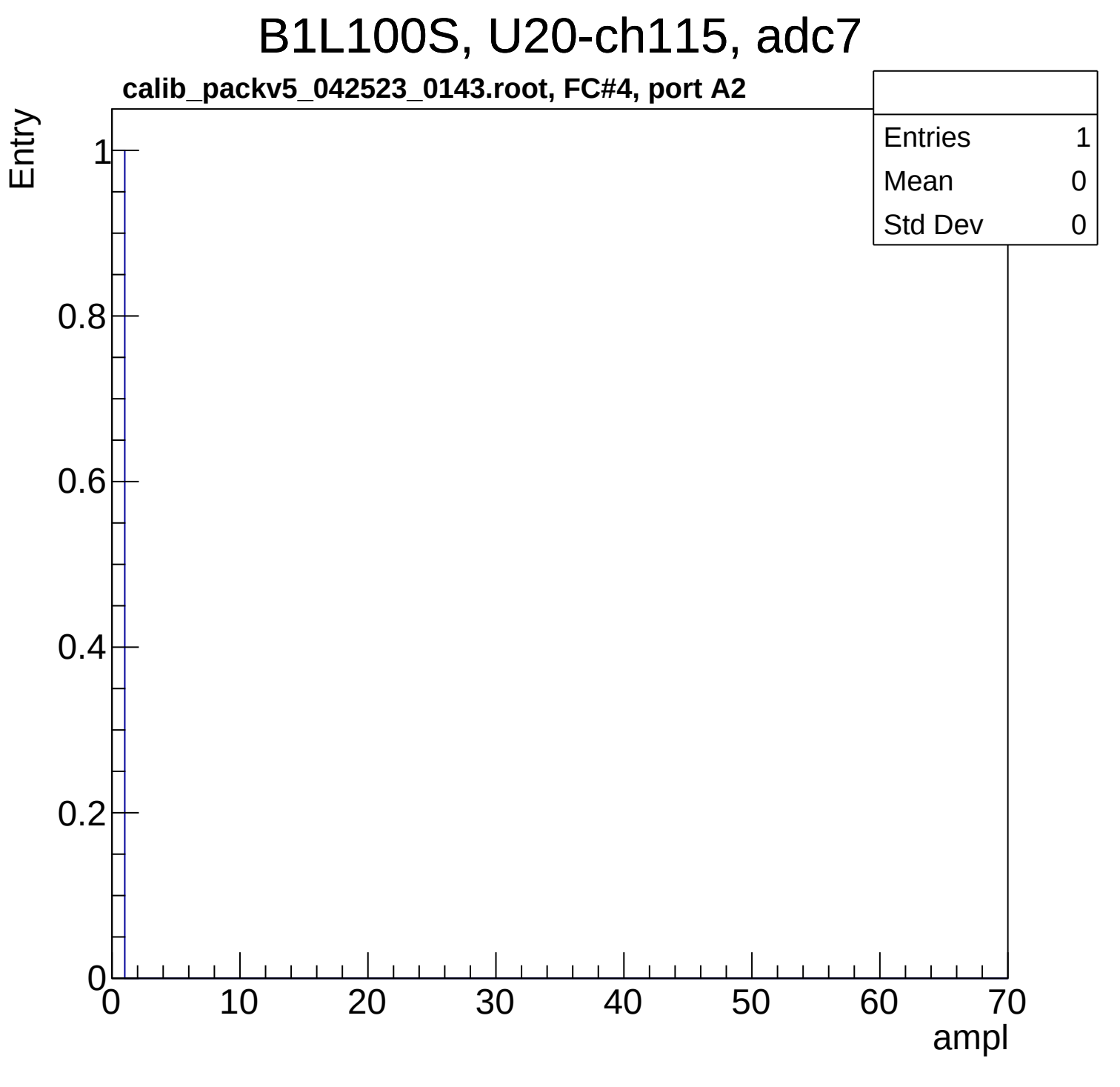

B1L100S, U20-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
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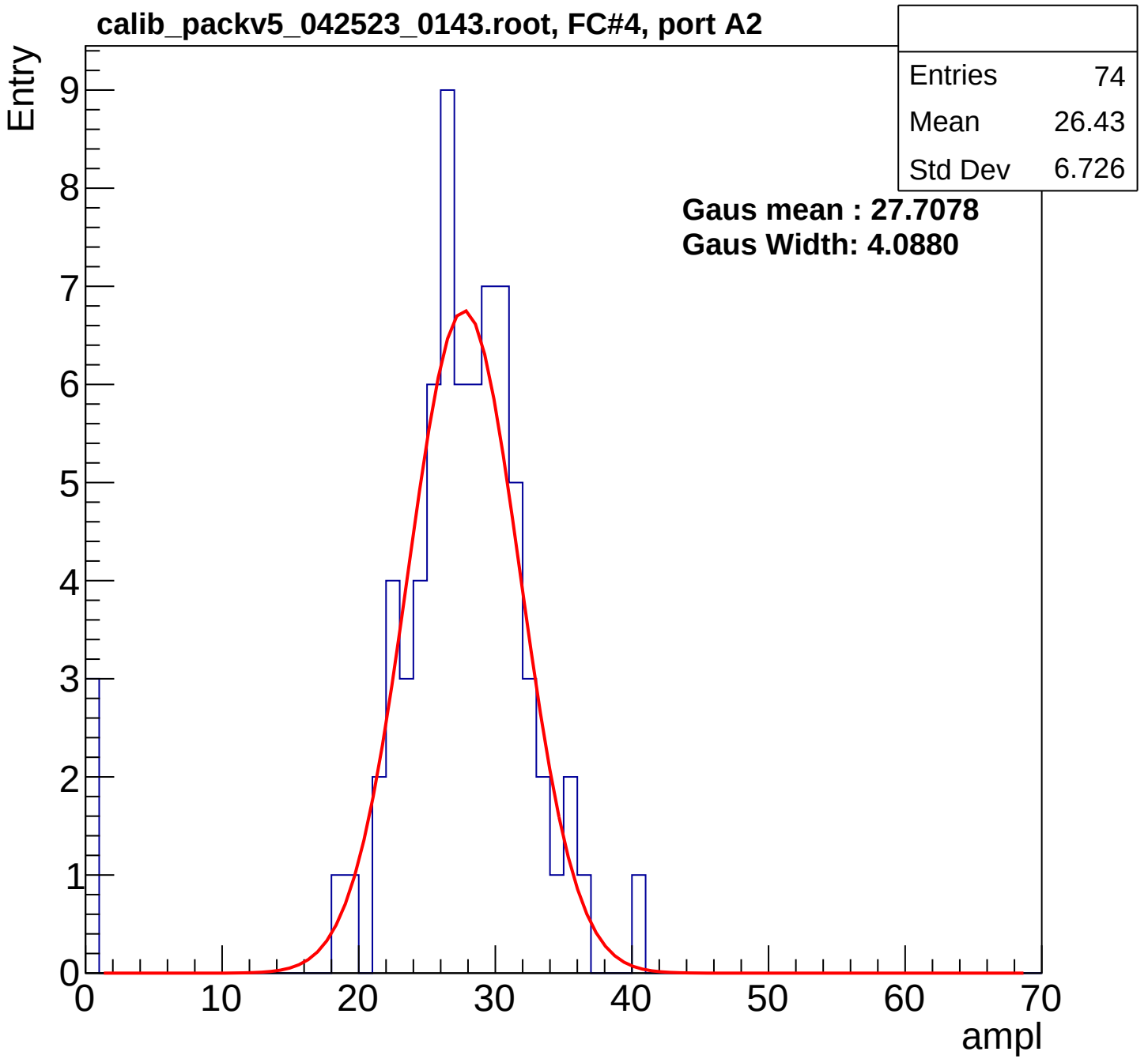
40
50
60
70
ampl


B1L100S, U20-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 26.43
Std Dev 6.726
Gaus mean : 27.7078
Gaus Width: 4.0880
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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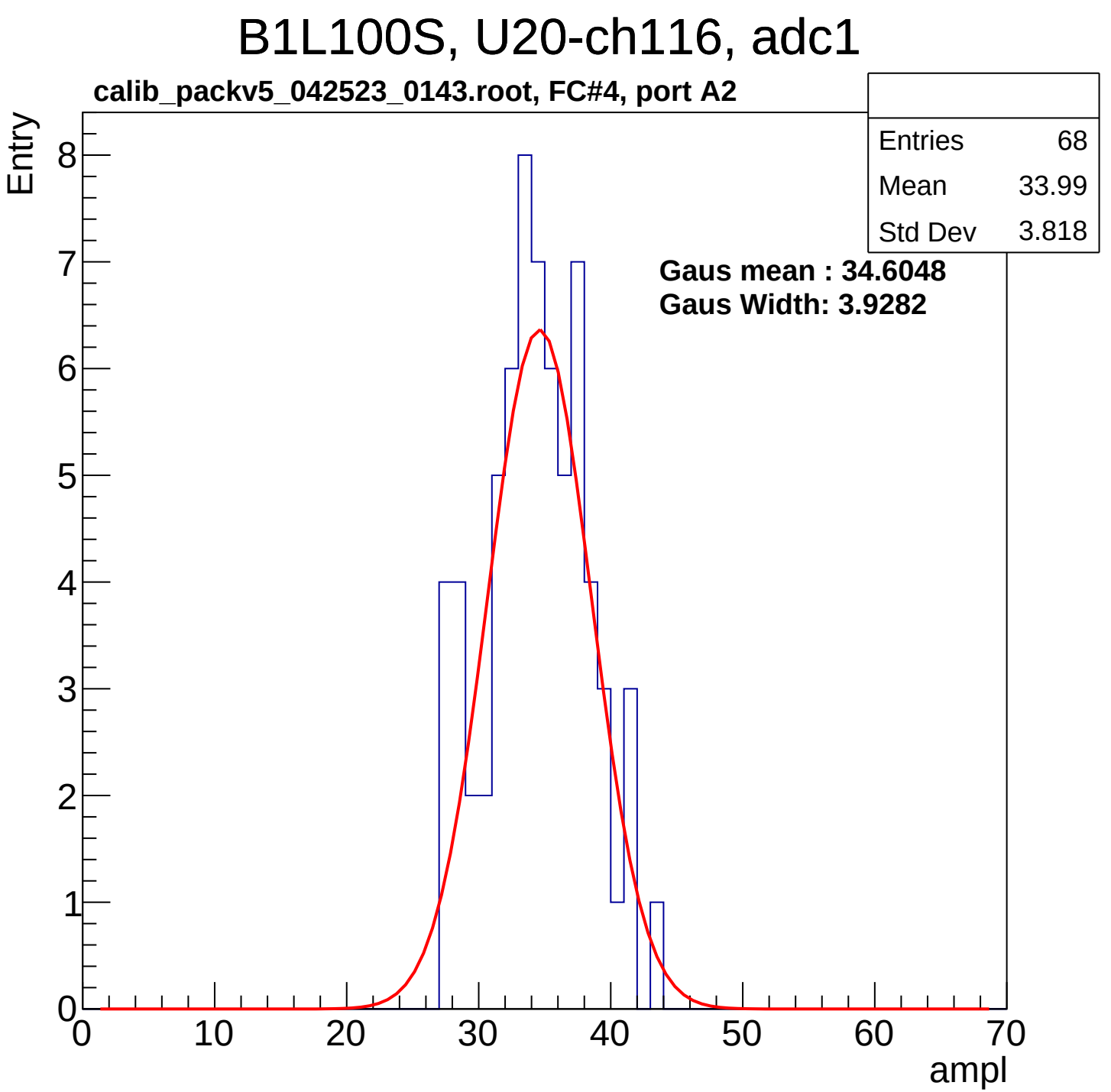
B1L100S, U20-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 33.99
Std Dev 3.818
Gaus mean : 34.6048
Gaus Width: 3.9282
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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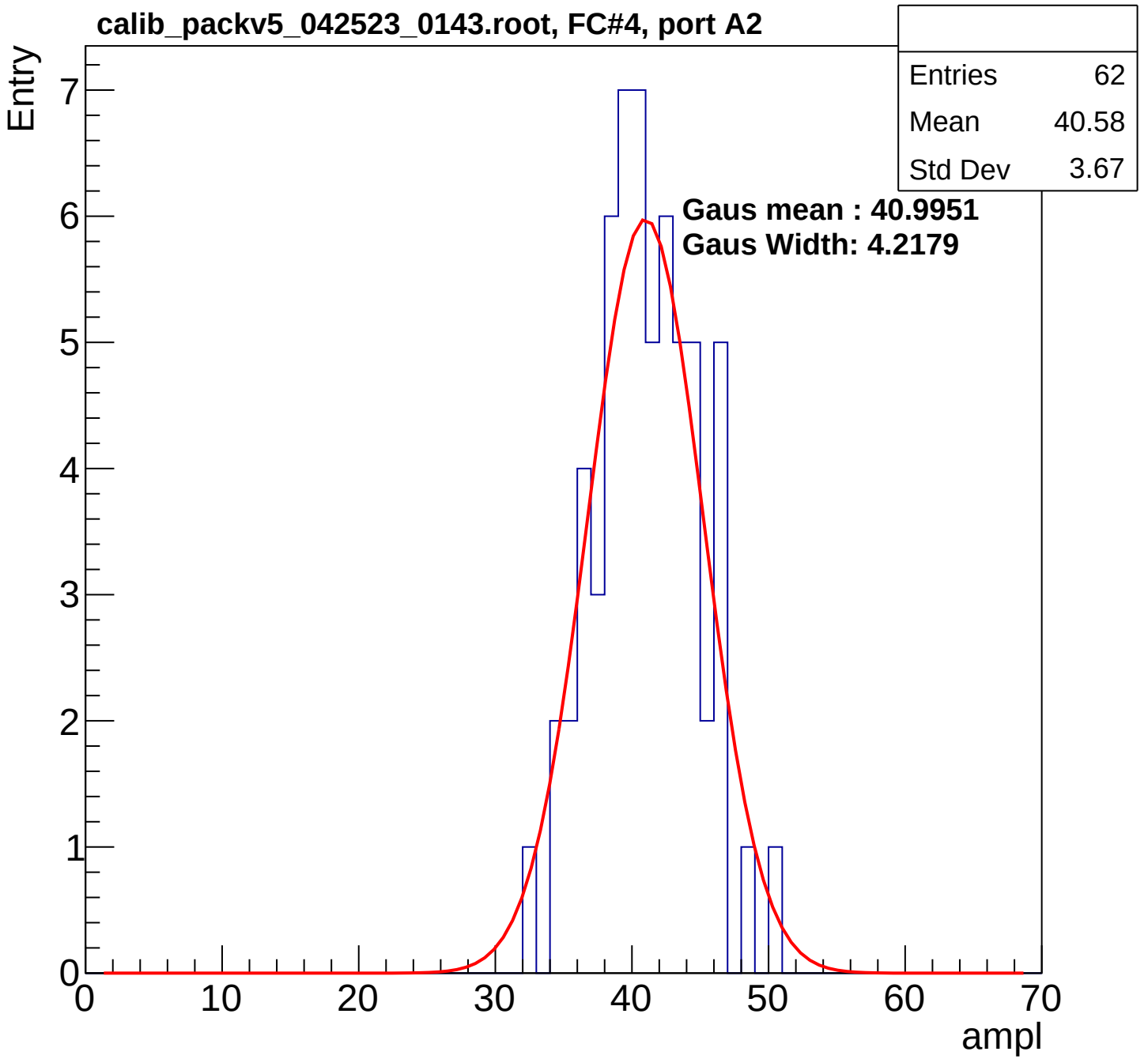
B1L100S, U20-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 40.58
Std Dev 3.67
Gaus mean : 40.9951
Gaus Width: 4.2179
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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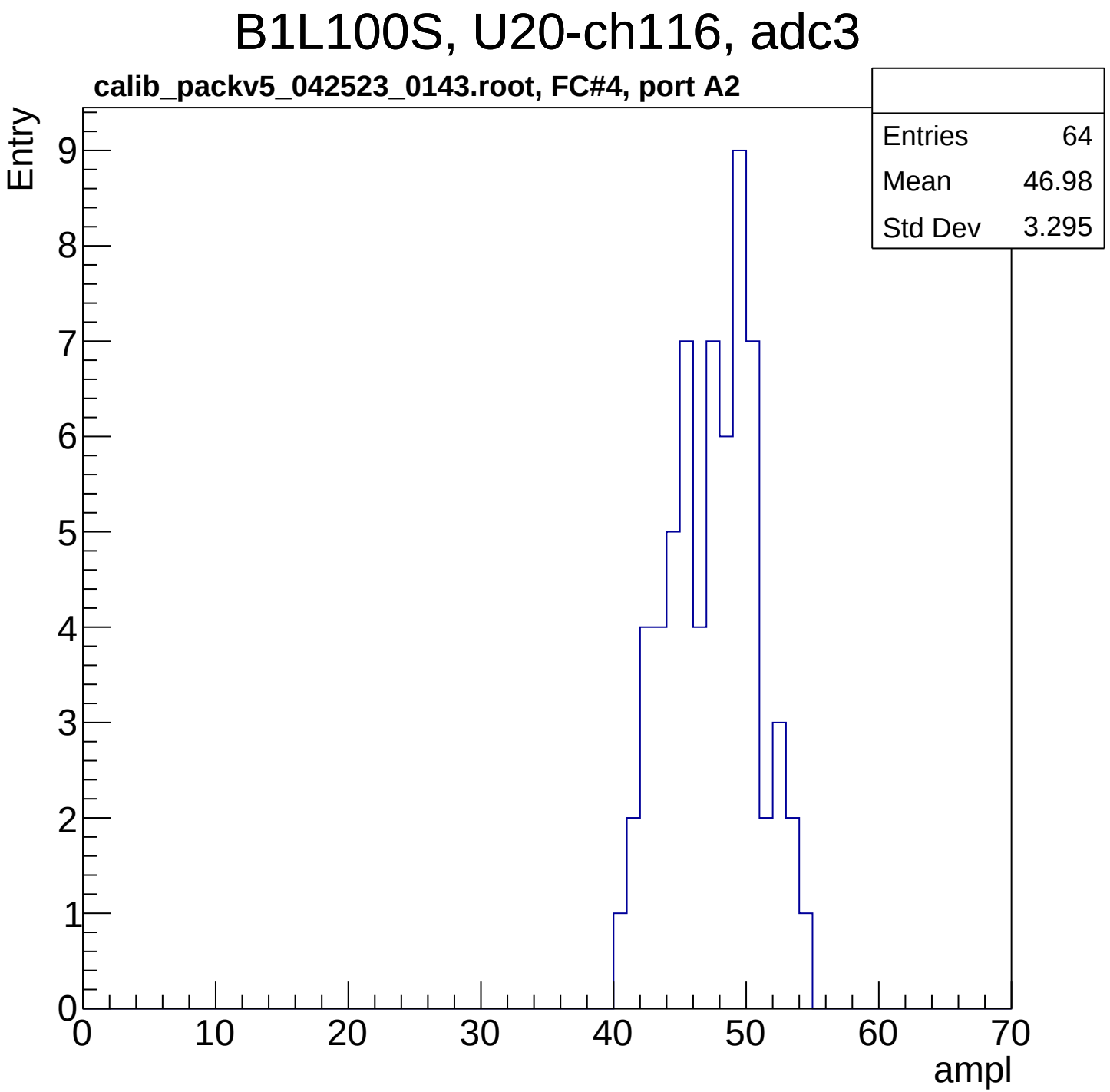
B1L100S, U20-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 46.98
Std Dev 3.295
Entry
ampl
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3
4
5
6
7
8
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0
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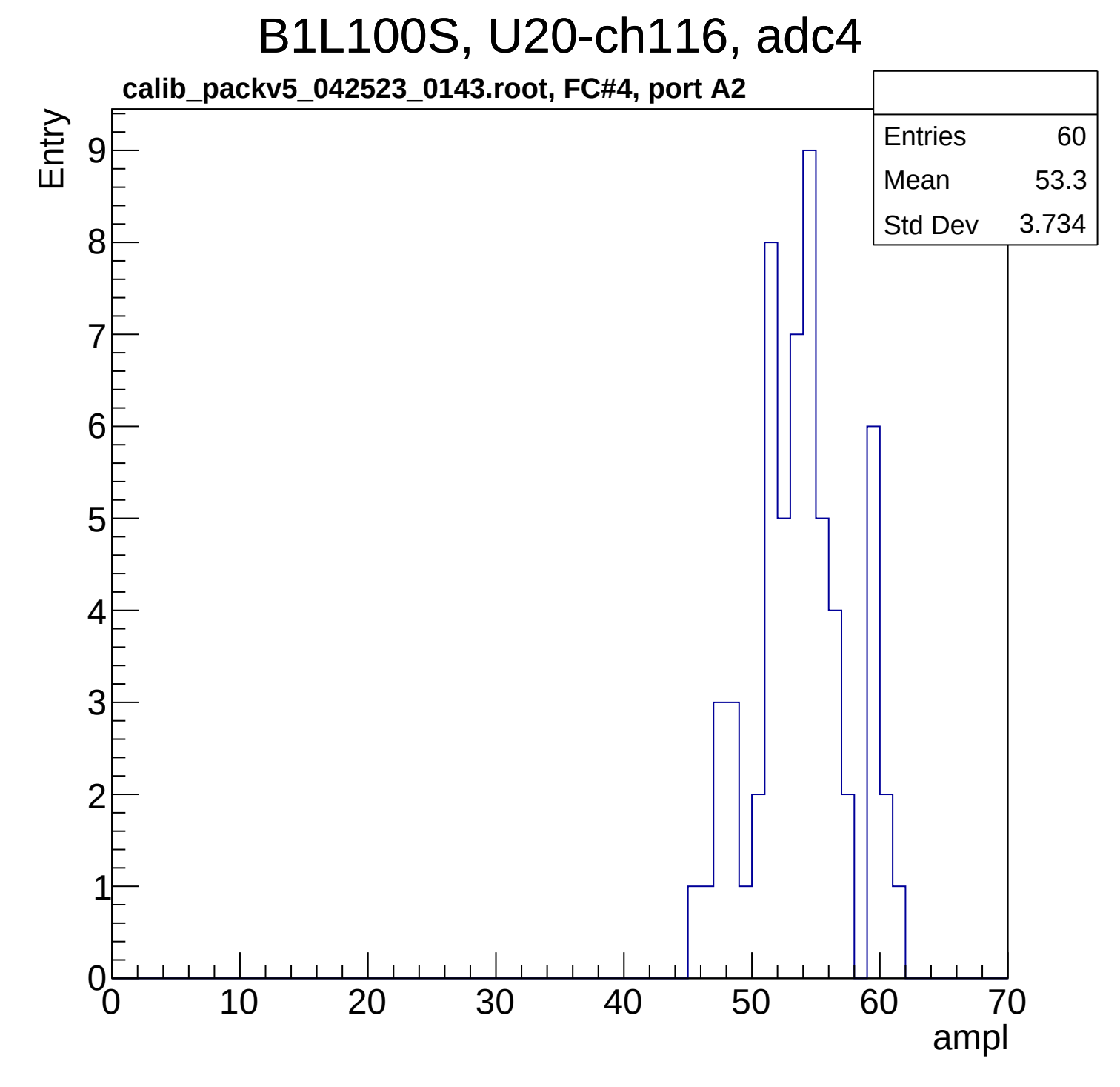
B1L100S, U20-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 53.3
Std Dev 3.734
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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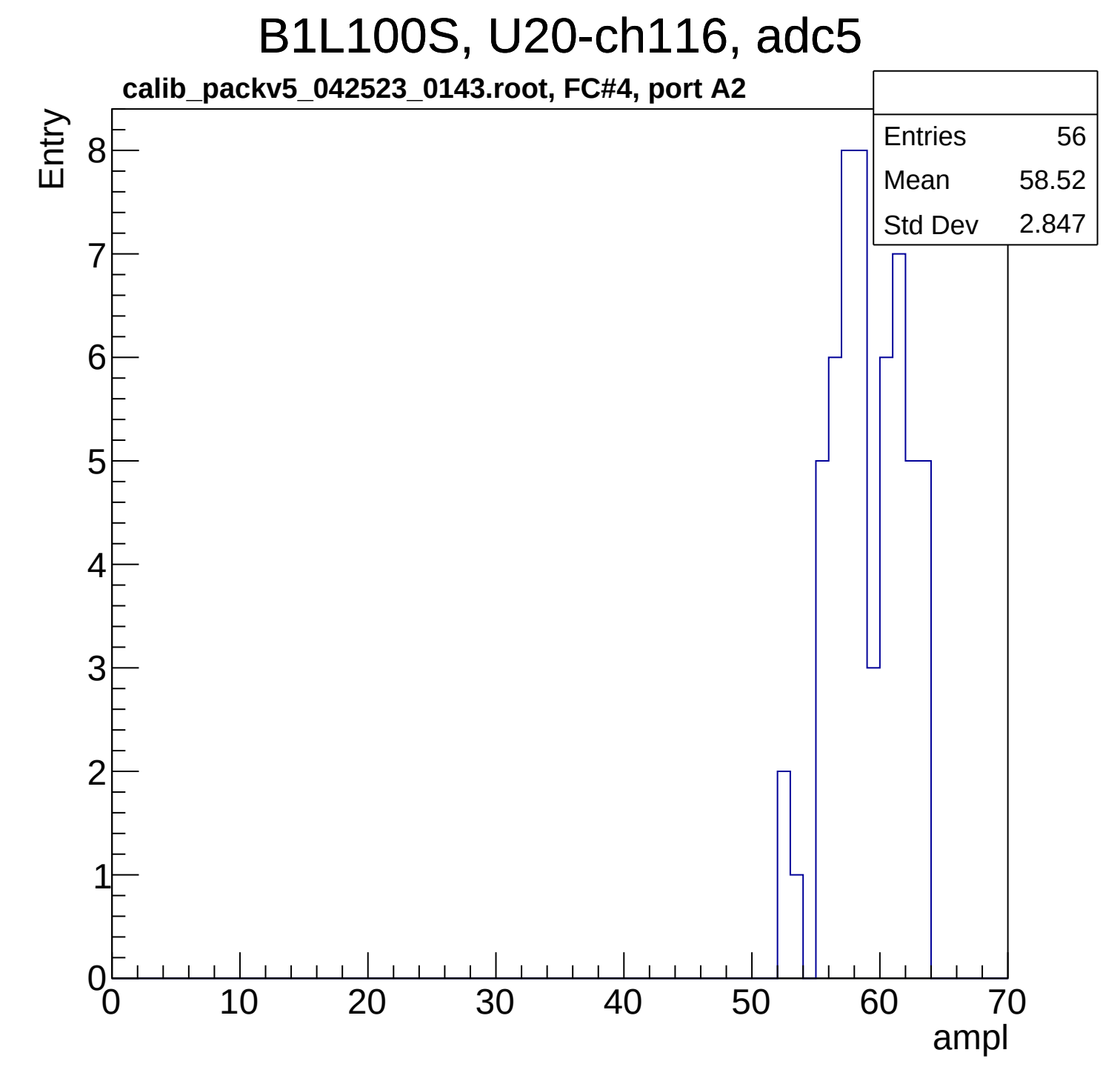
B1L100S, U20-ch116, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.52
Std Dev 2.847
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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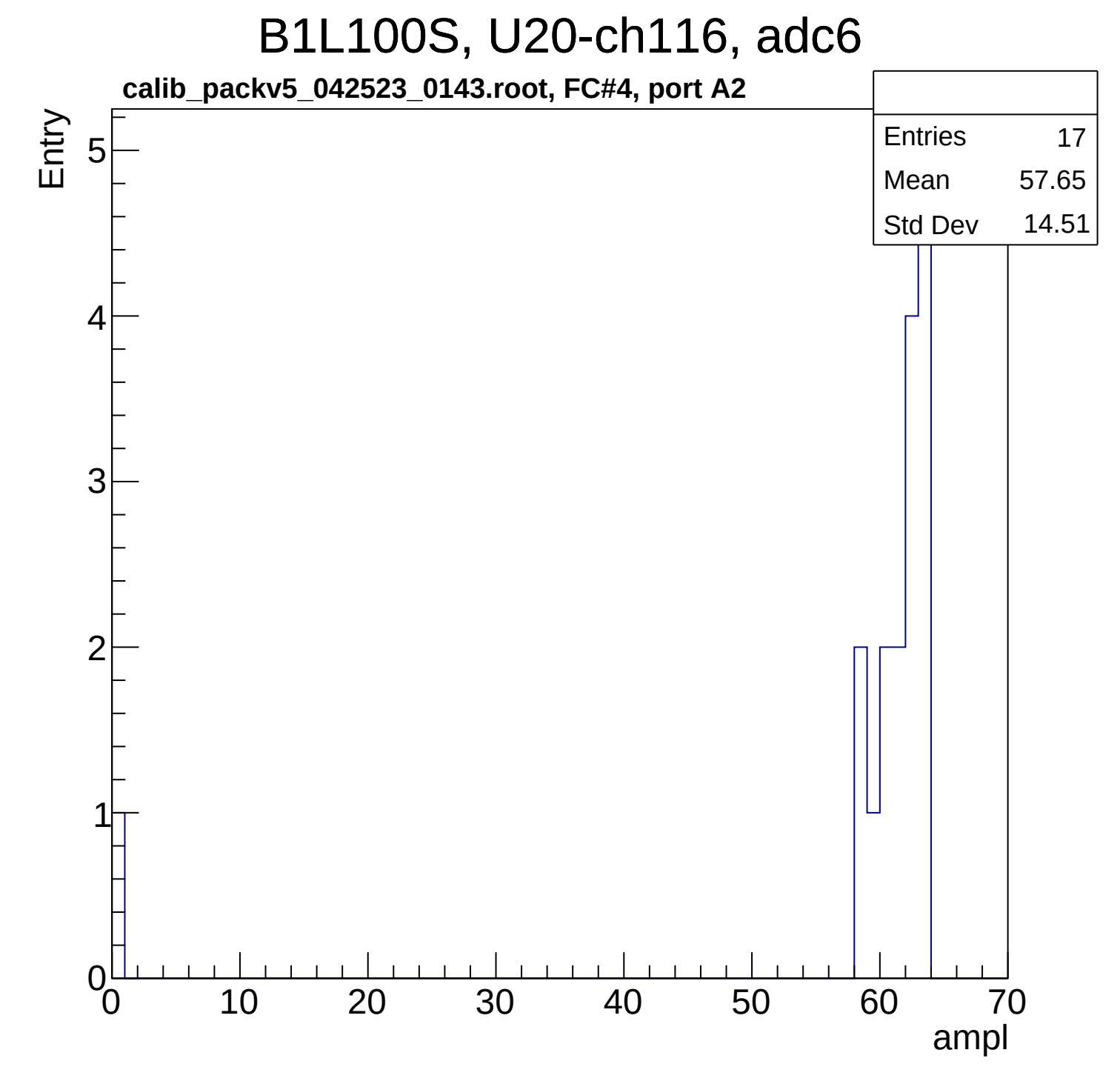
B1L100S, U20-ch116, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 17
Mean 57.65
Std Dev 14.51
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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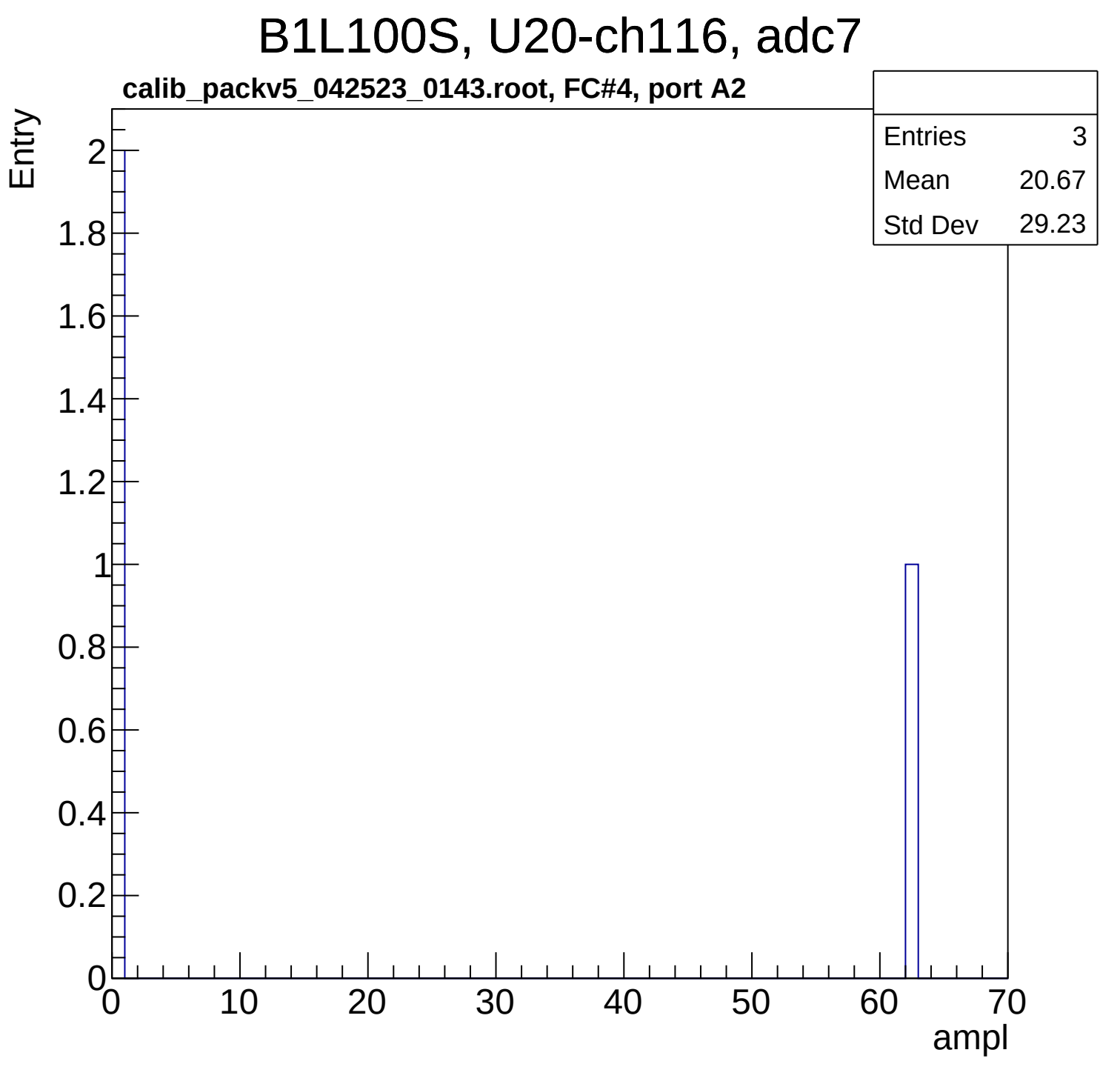
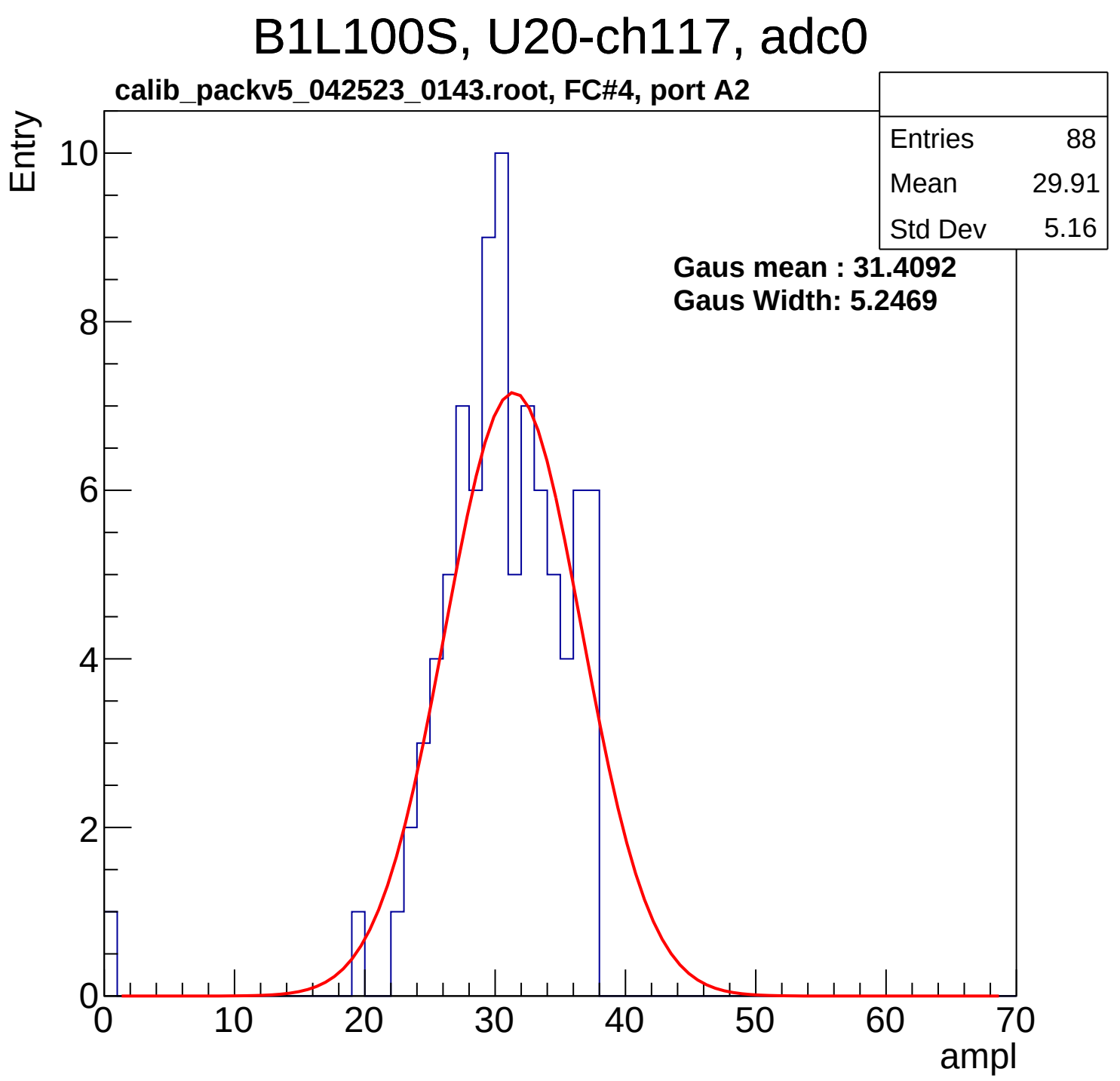
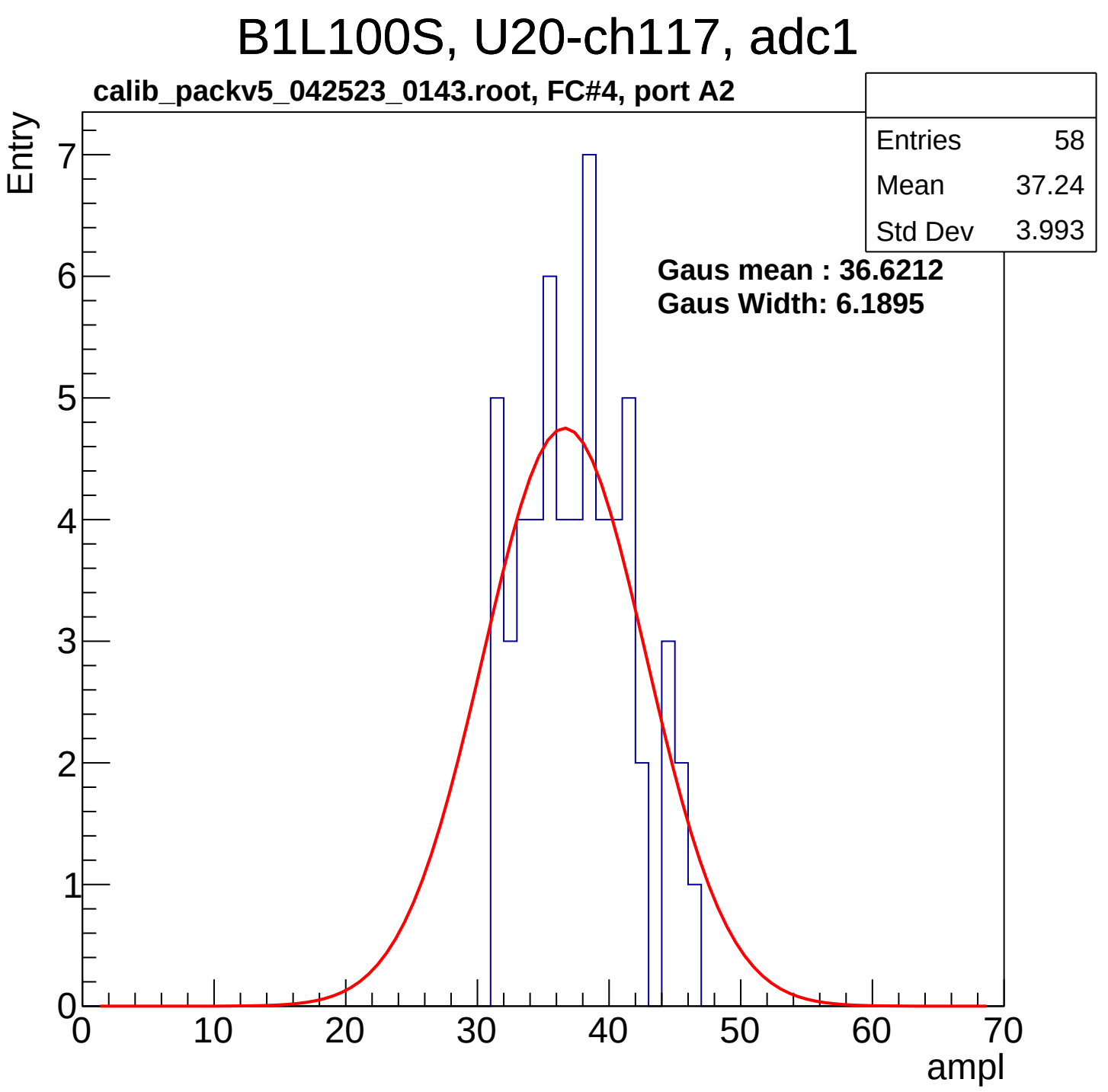

B1L100S, U20-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 20.67
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 29.91
Std Dev 5.16
Gaus mean : 31.4092
Gaus Width: 5.2469
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 37.24
Std Dev 3.993
Gaus mean : 36.6212
Gaus Width: 6.1895
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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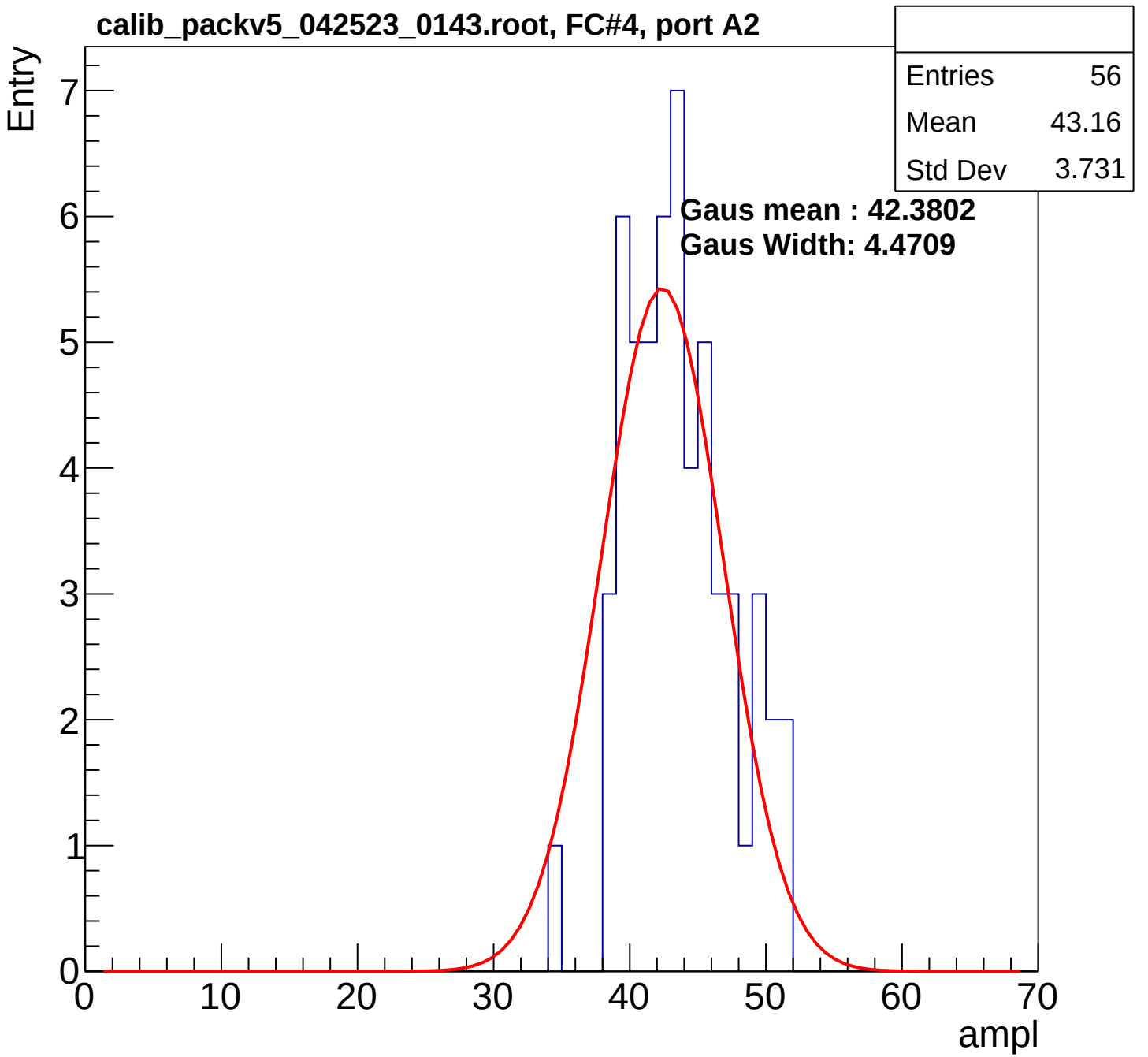
B1L100S, U20-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.16
Std Dev 3.731
Gaus mean : 42.3802
Gaus Width: 4.4709
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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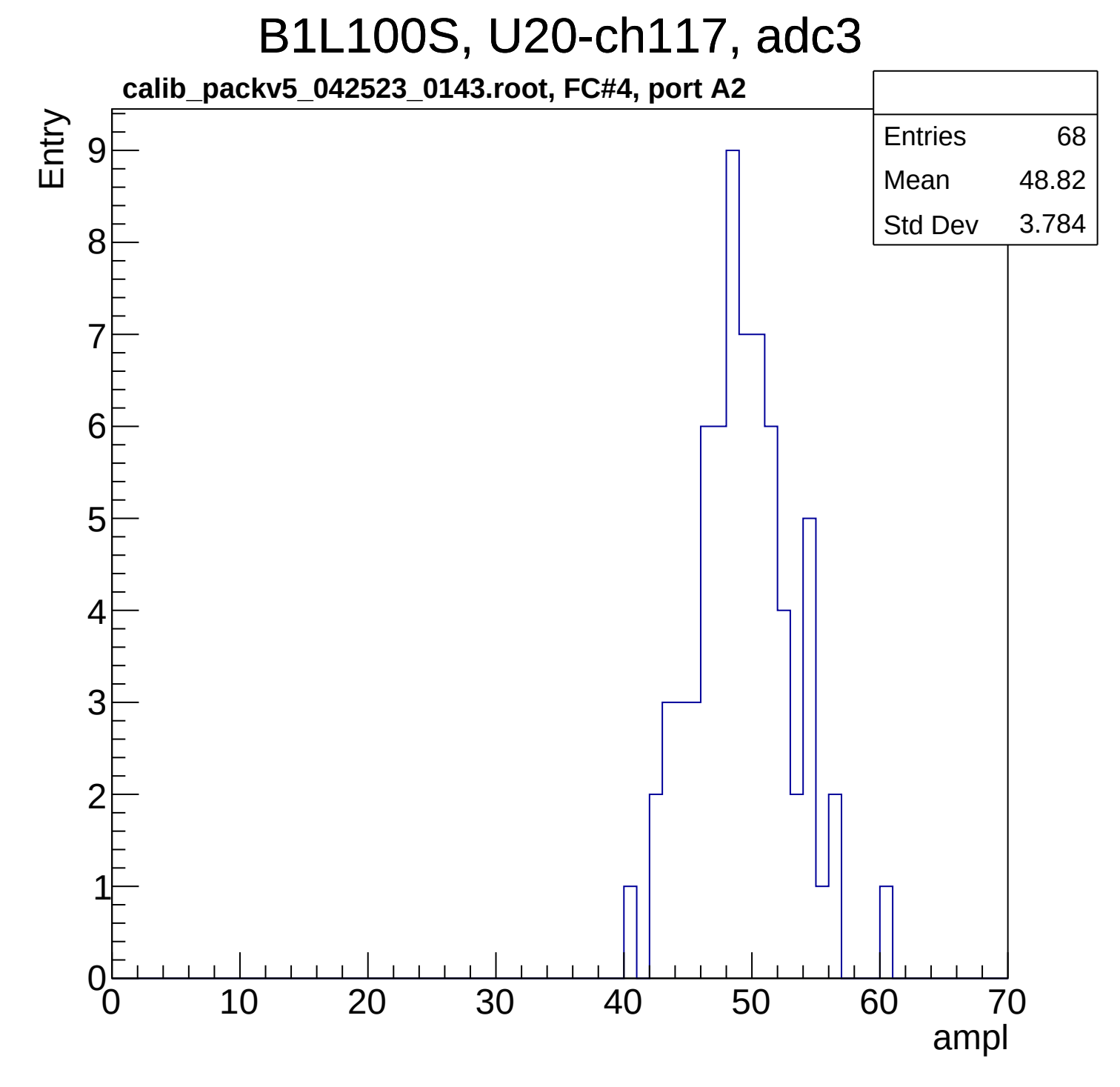
B1L100S, U20-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.82
Std Dev 3.784
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
30
40
50
60
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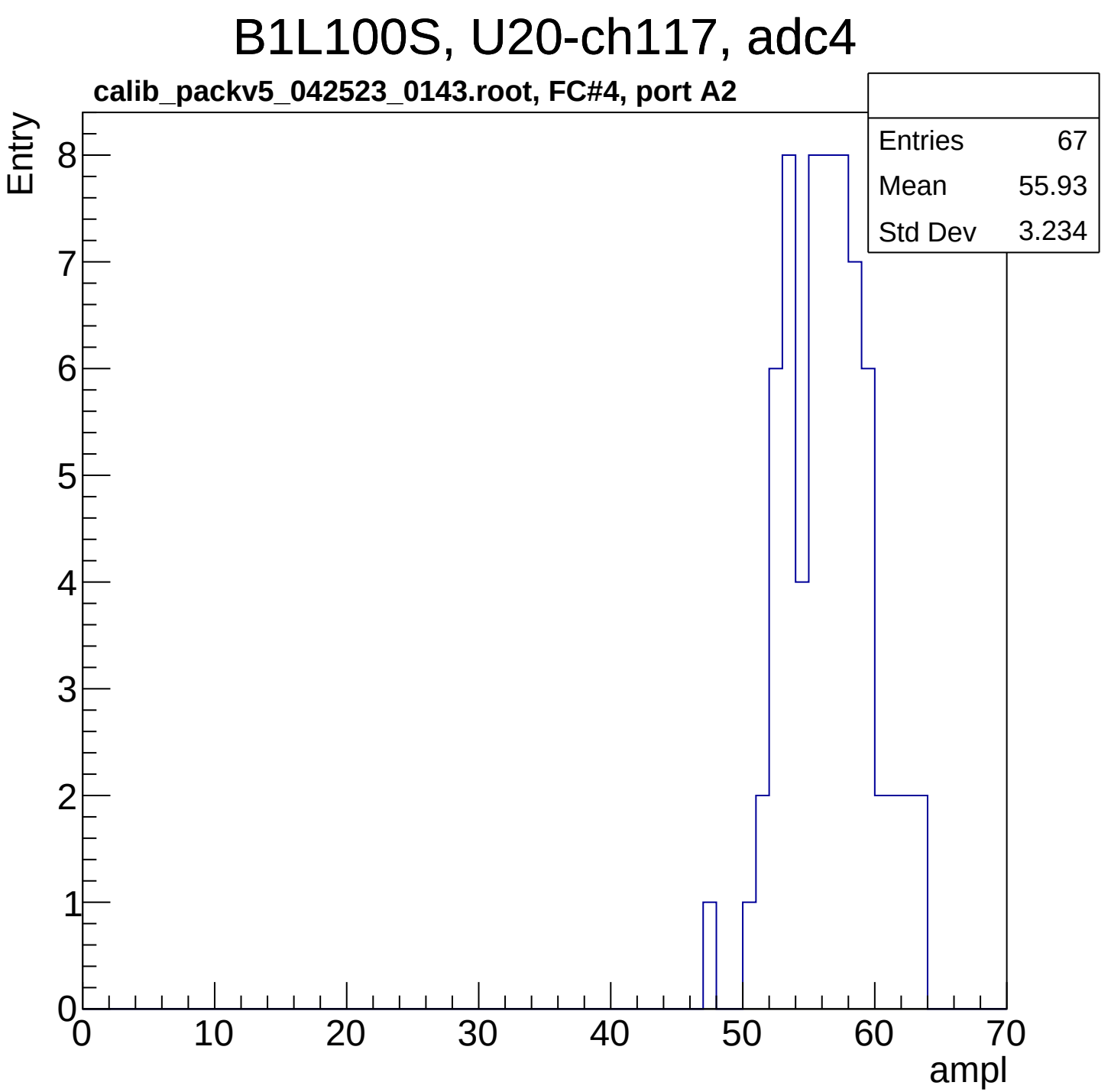
B1L100S, U20-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.93
Std Dev 3.234
Entry
ampl
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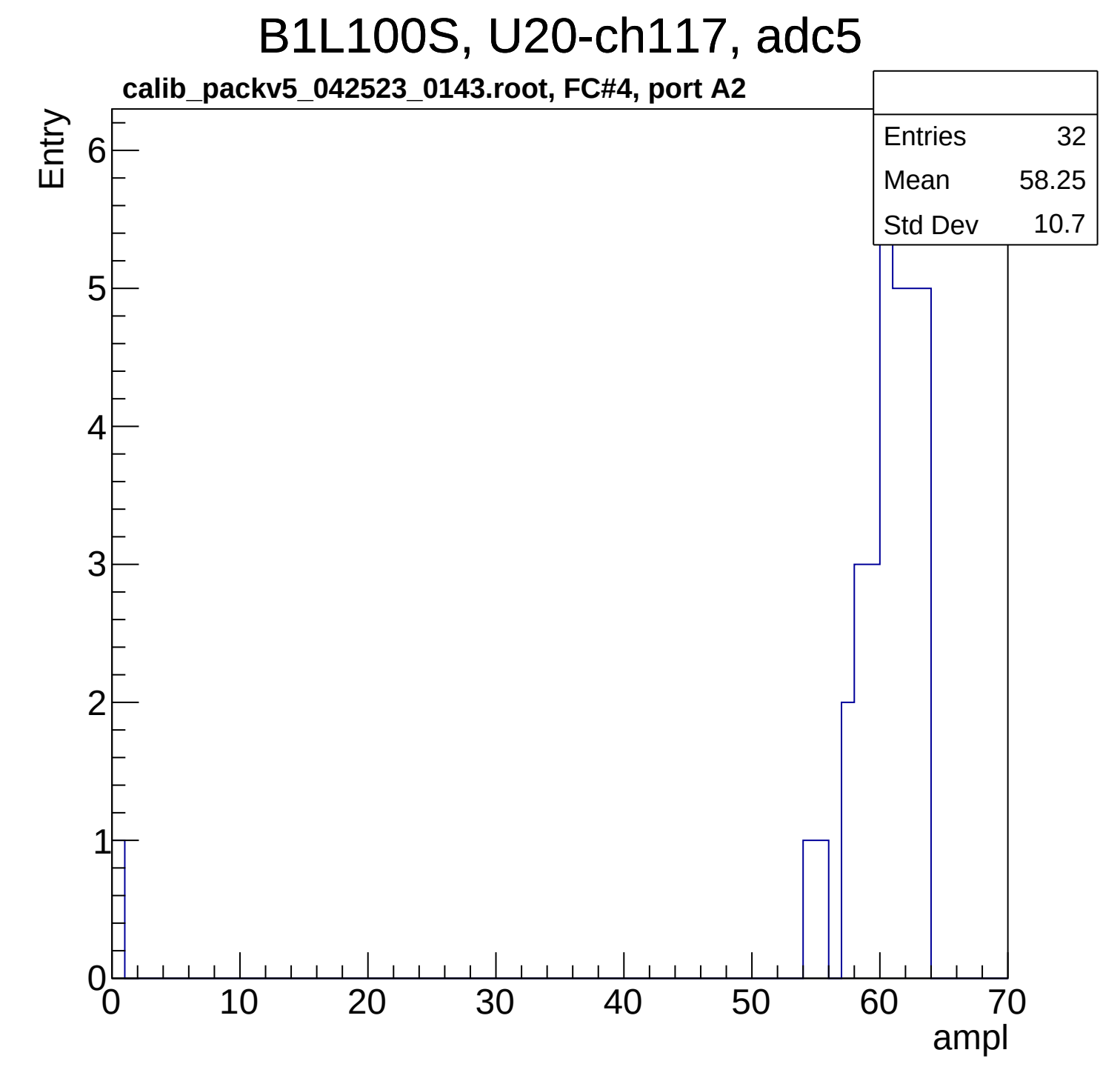
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch117, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.25
Std Dev 10.7
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
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60
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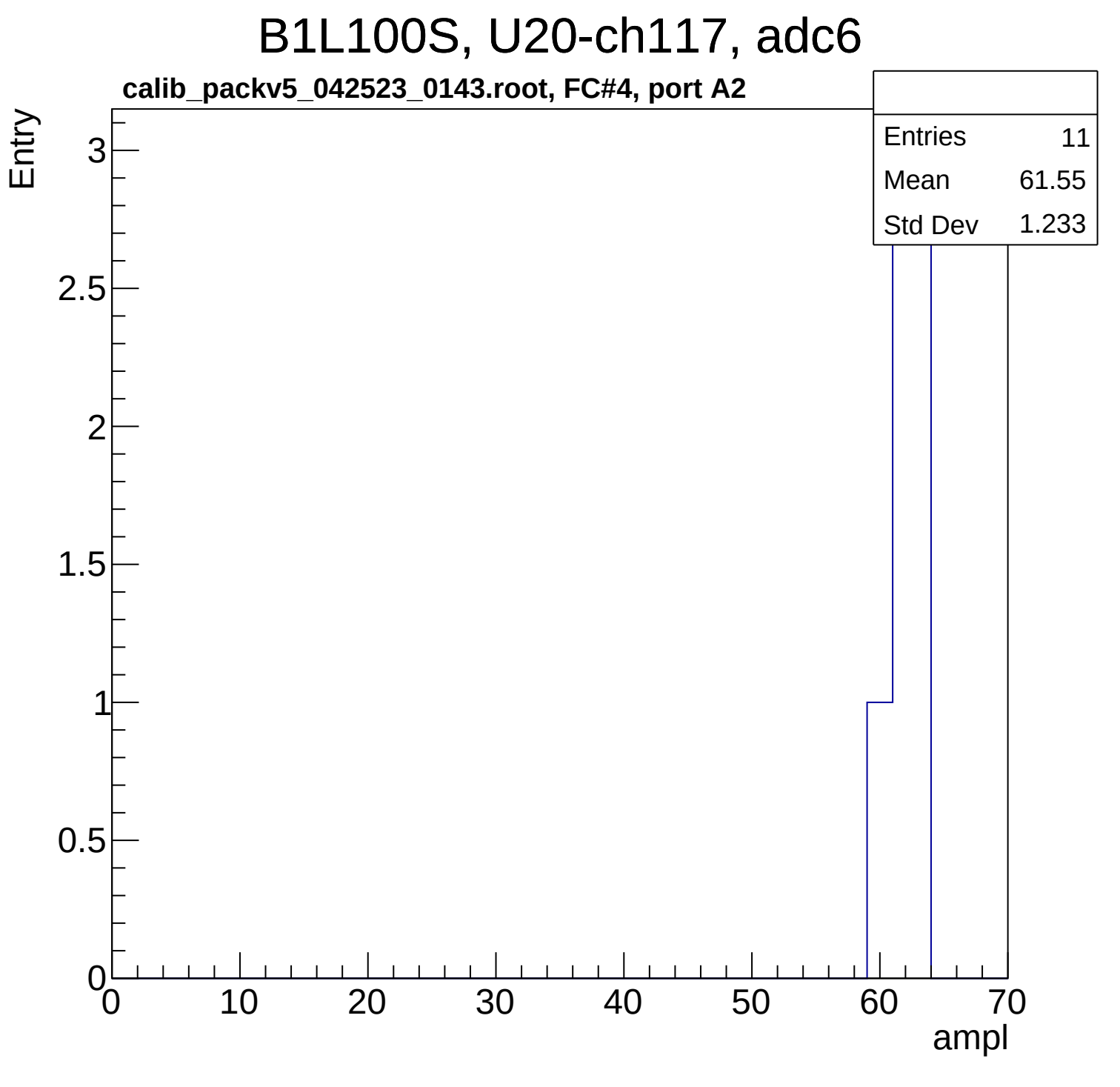
B1L100S, U20-ch117, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.55
Std Dev 1.233
Entry
ampl
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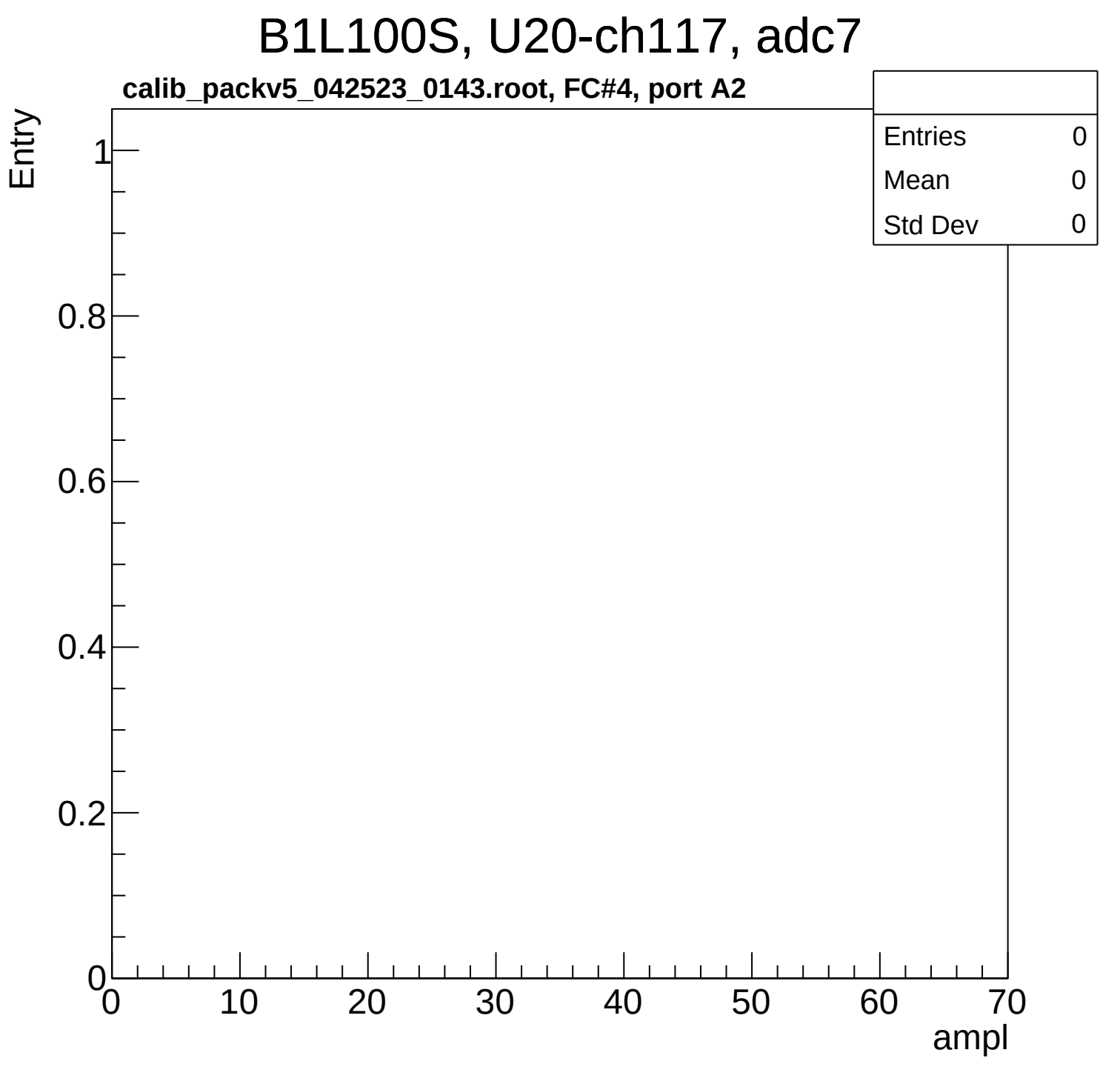
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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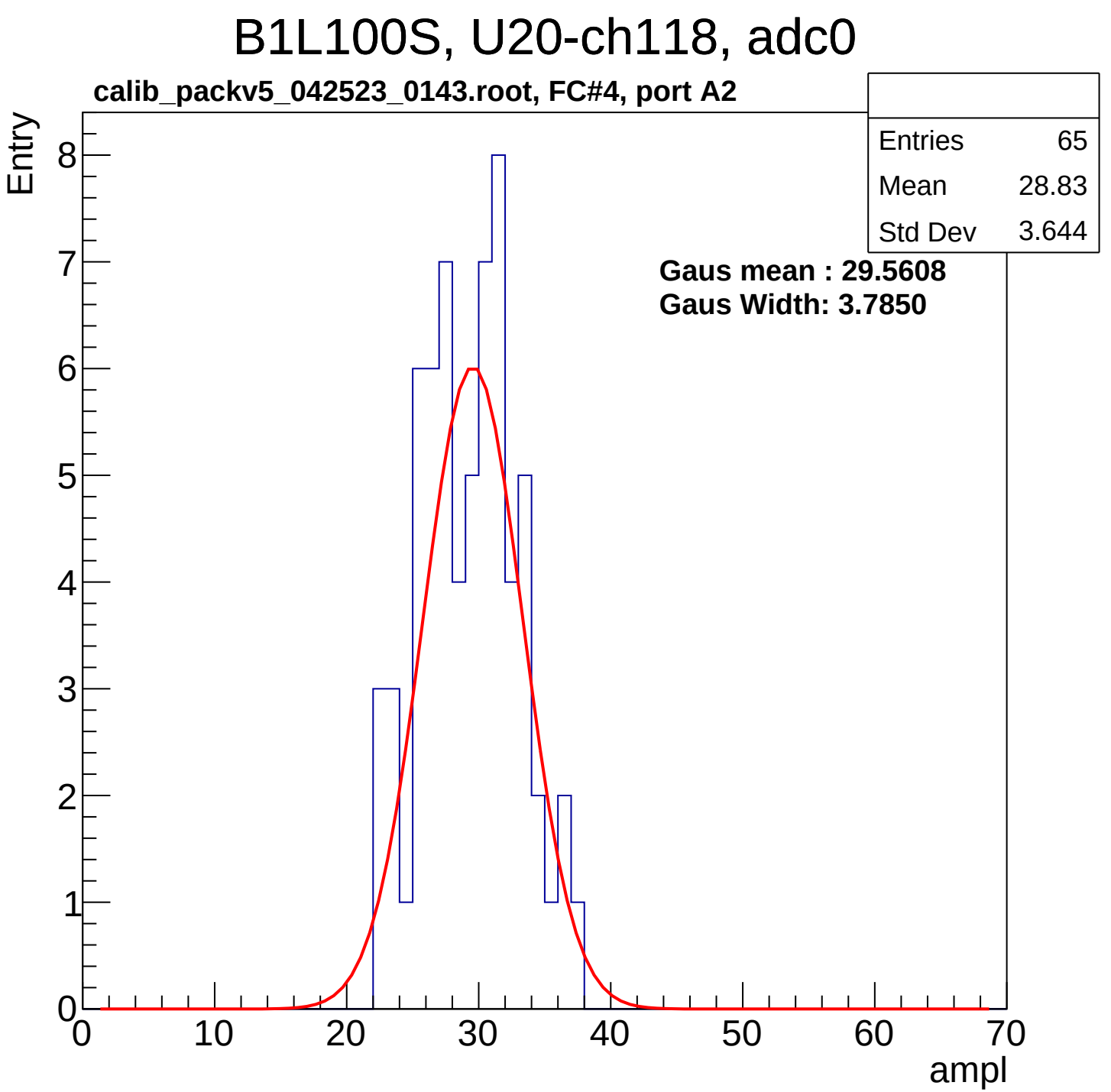
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 28.83
Std Dev 3.644
Gaus mean : 29.5608
Gaus Width: 3.7850
Entry
ampl
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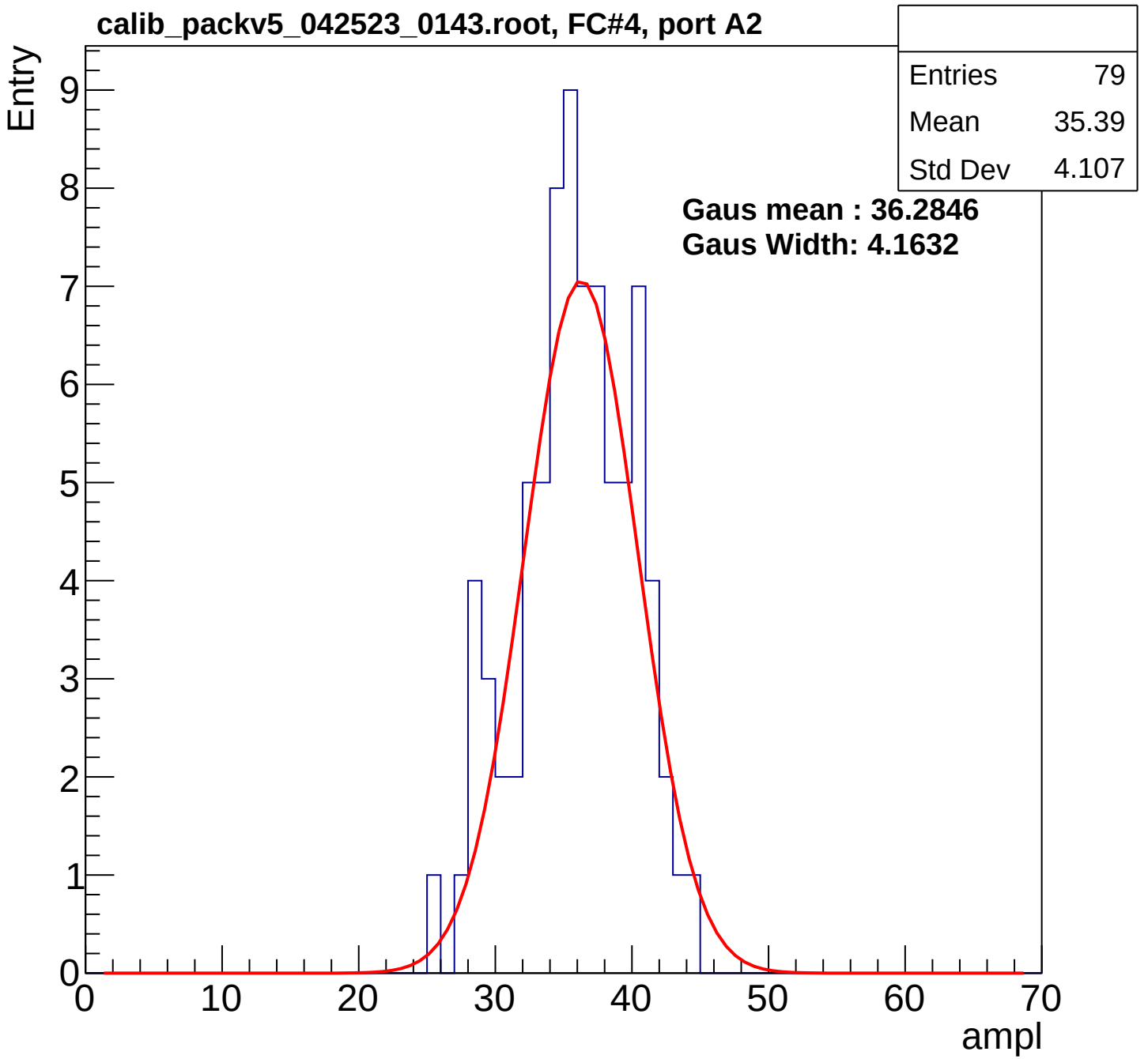
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.39
Std Dev 4.107
Gaus mean : 36.2846
Gaus Width: 4.1632
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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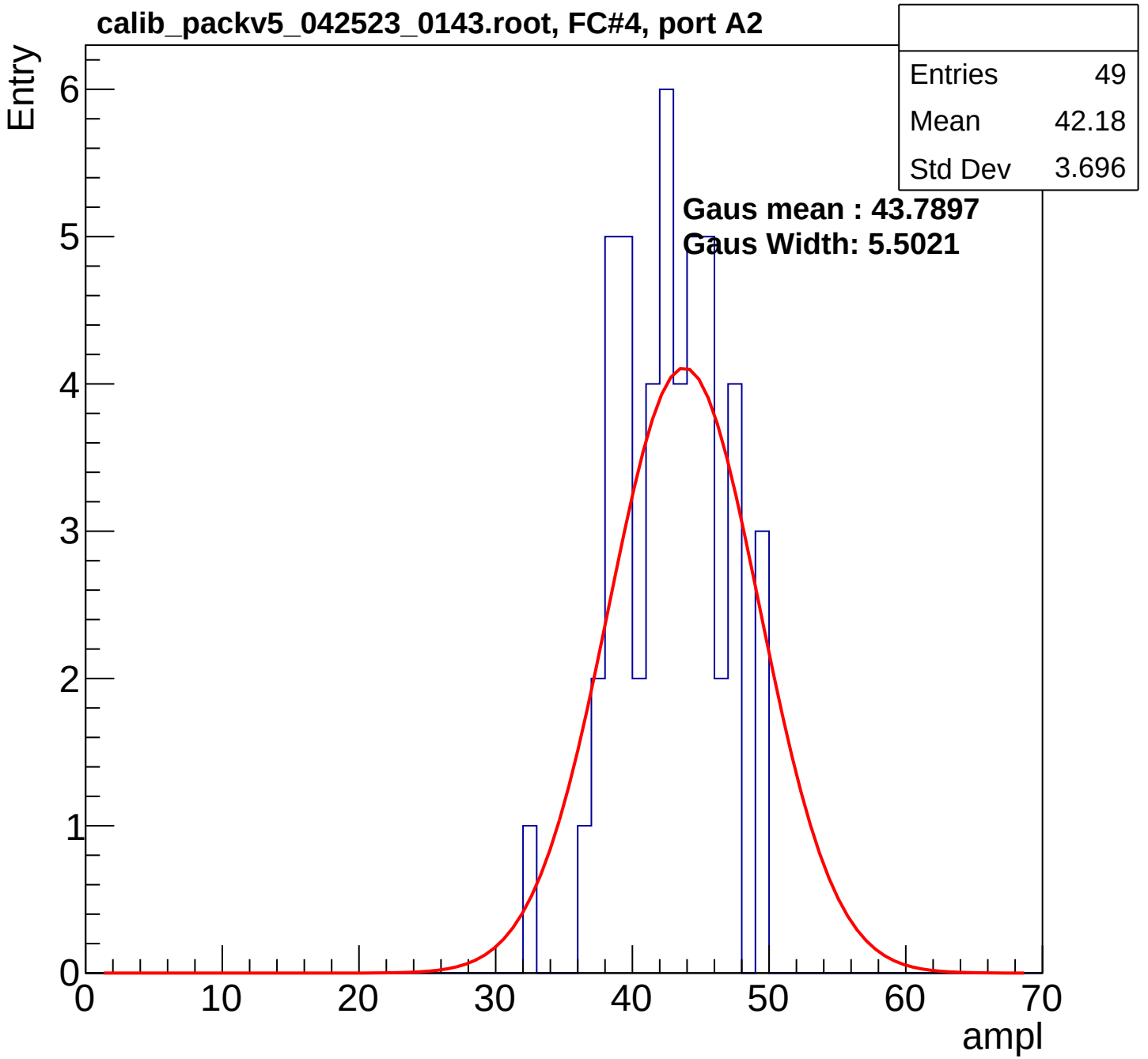
B1L100S, U20-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 42.18
Std Dev 3.696
Gaus mean : 43.7897
Gaus Width: 5.5021
Entry
ampl
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1
2
3
4
5
6
0
10
20
30
40
50
60
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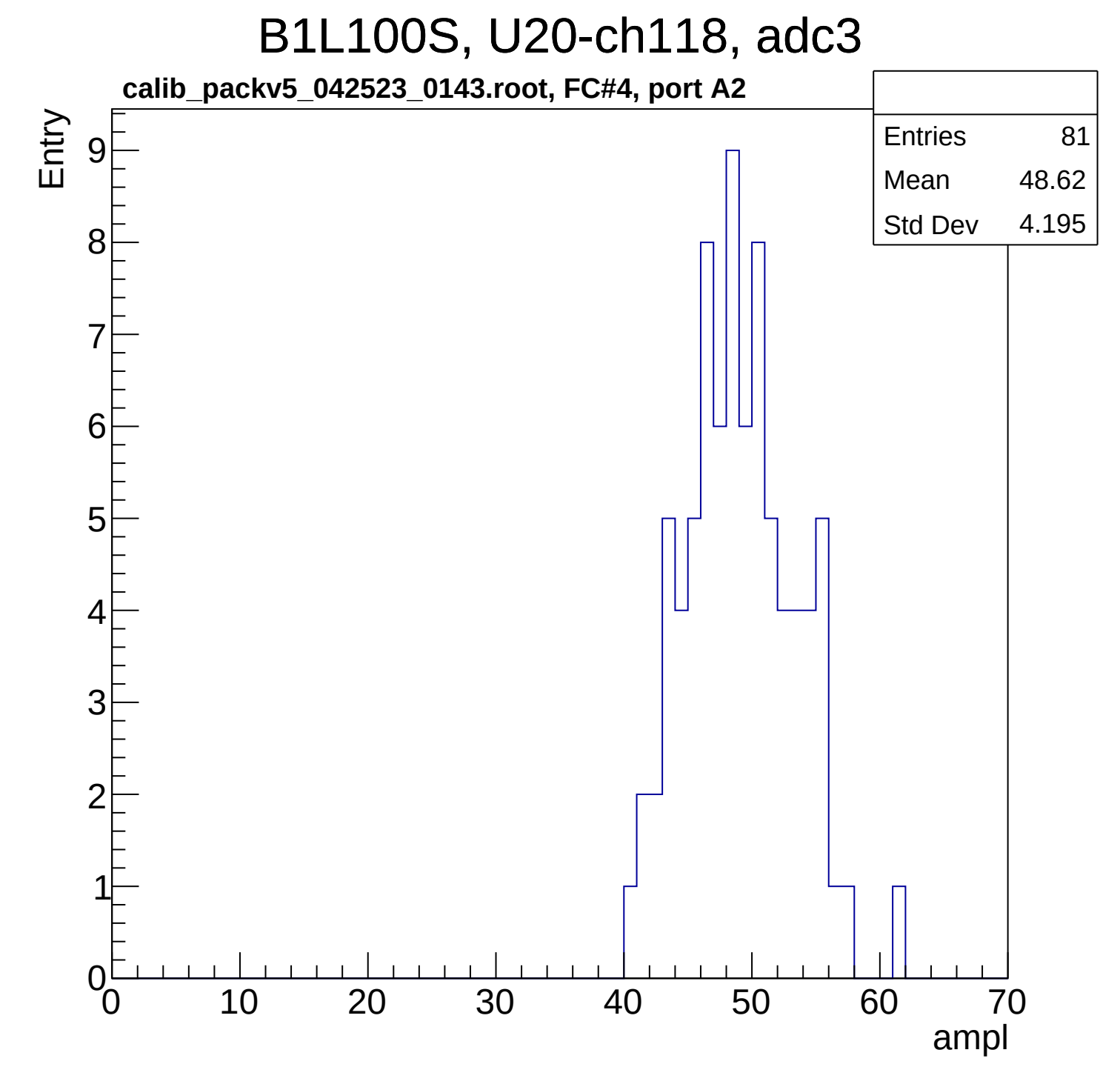
B1L100S, U20-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 48.62
Std Dev 4.195
Entry
ampl
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3
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6
7
8
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0
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40
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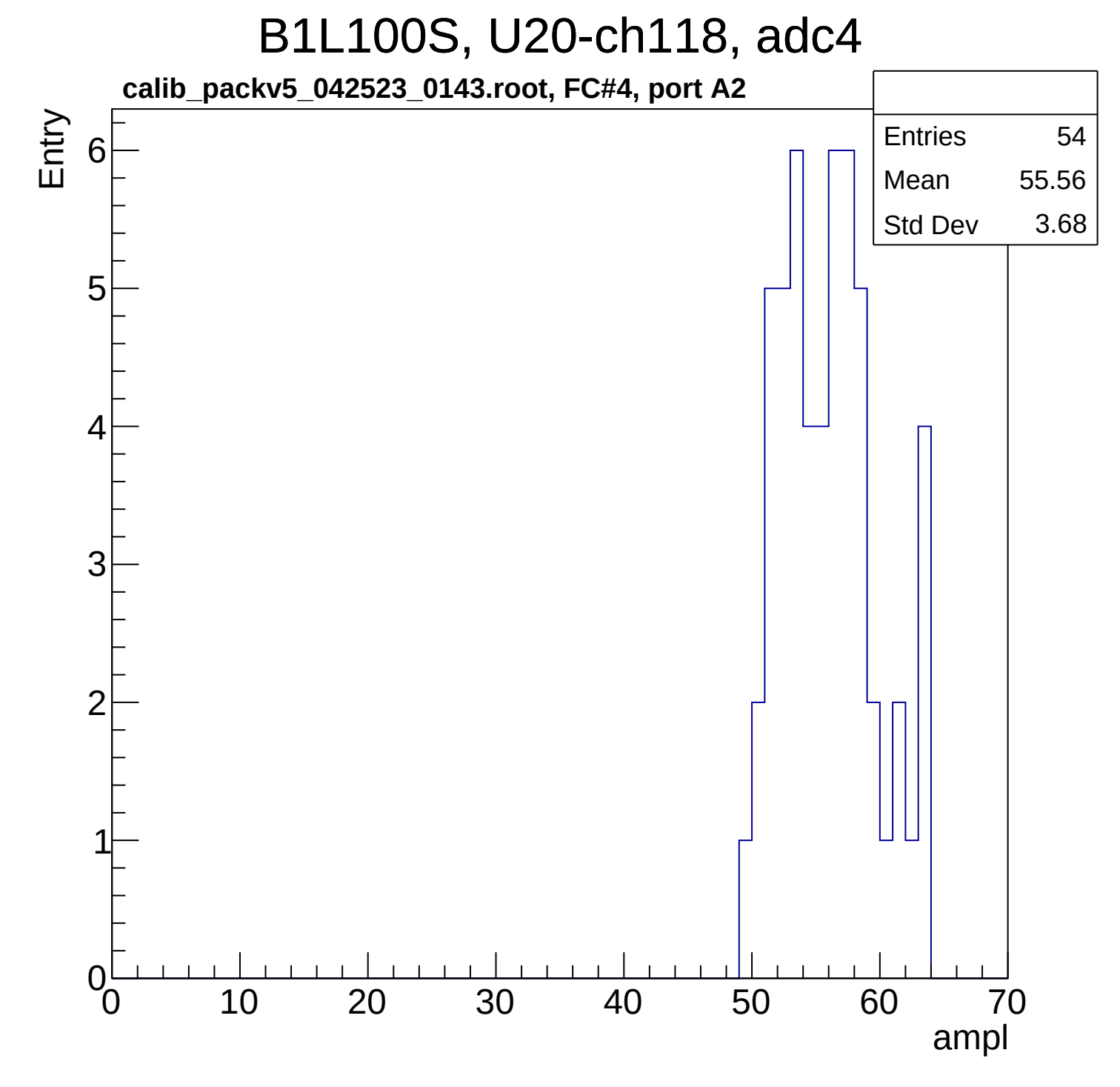
60
70


B1L100S, U20-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.56
Std Dev 3.68
Entry
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2
3
4
5
6
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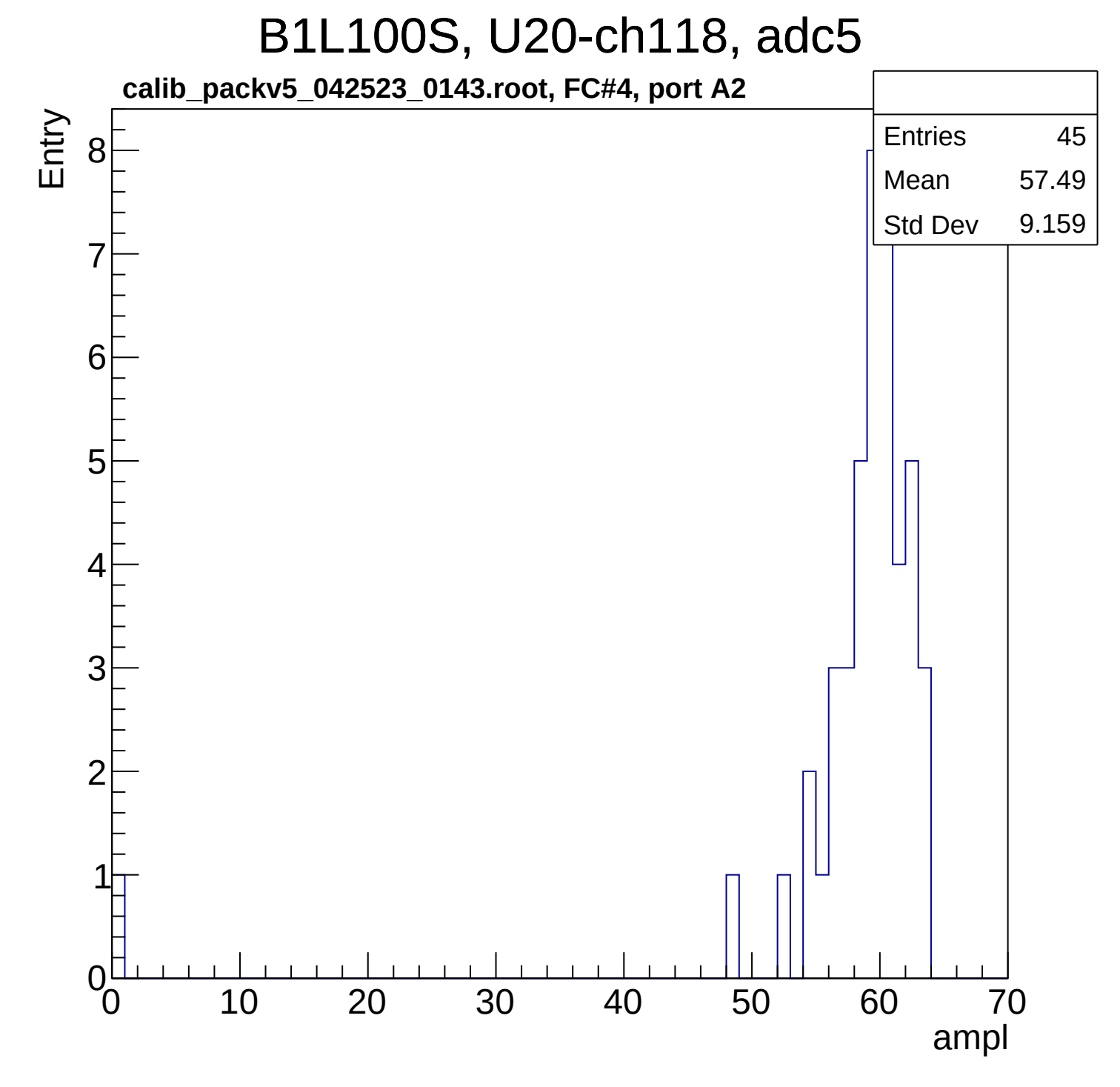
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 57.49
Std Dev 9.159
Entry
ampl
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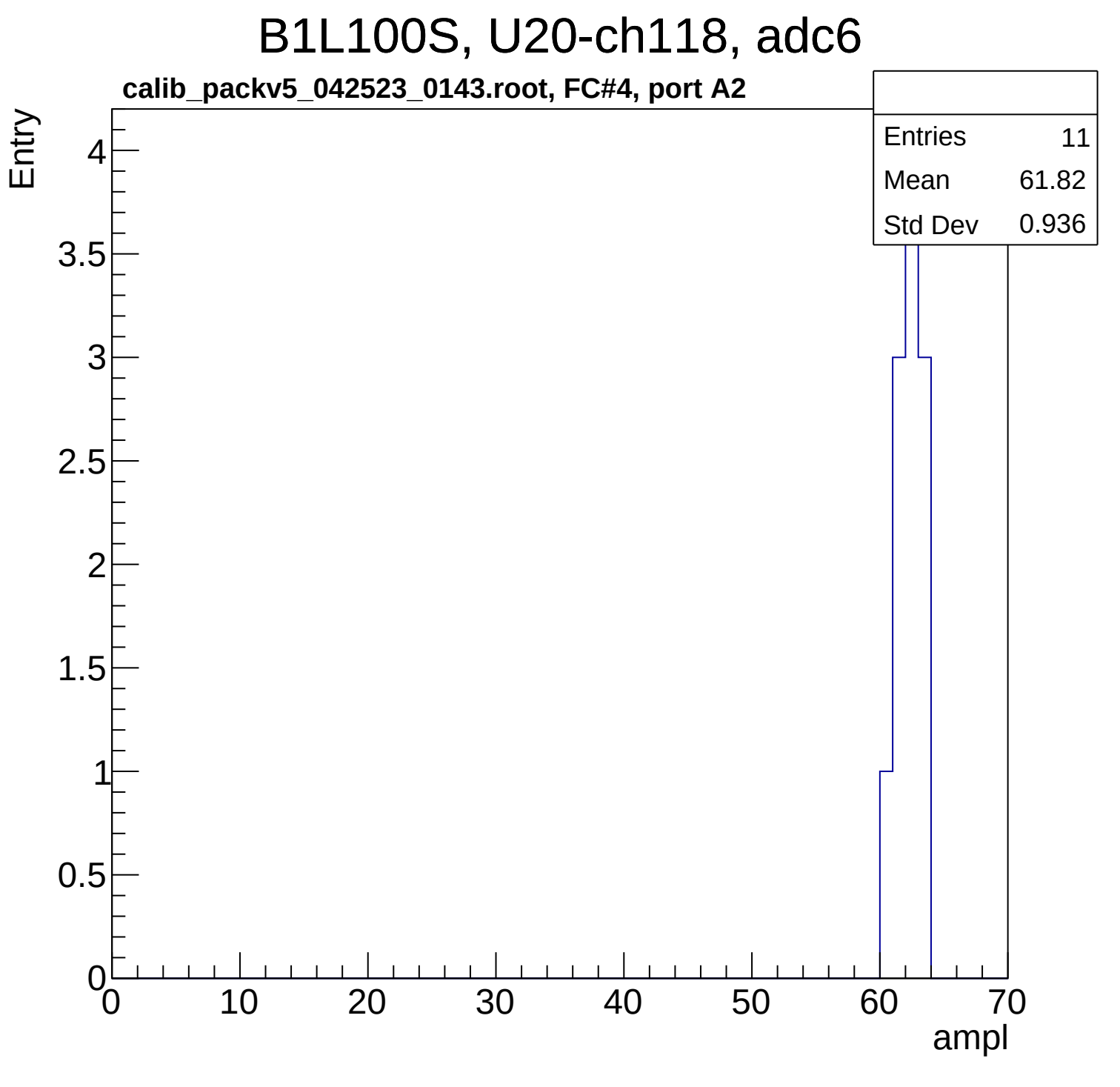
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch118, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.82
Std Dev 0.936
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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60
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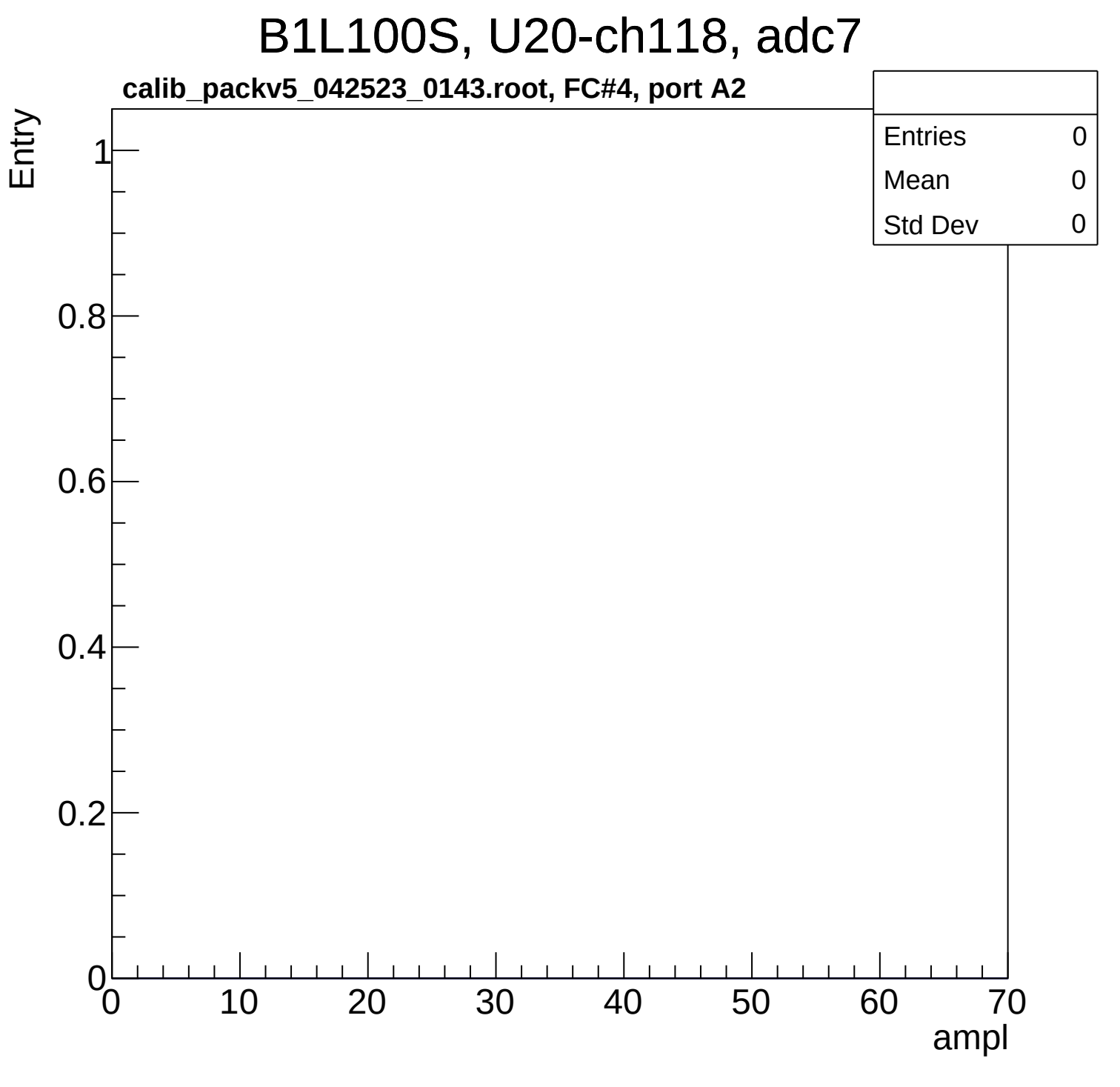
B1L100S, U20-ch118, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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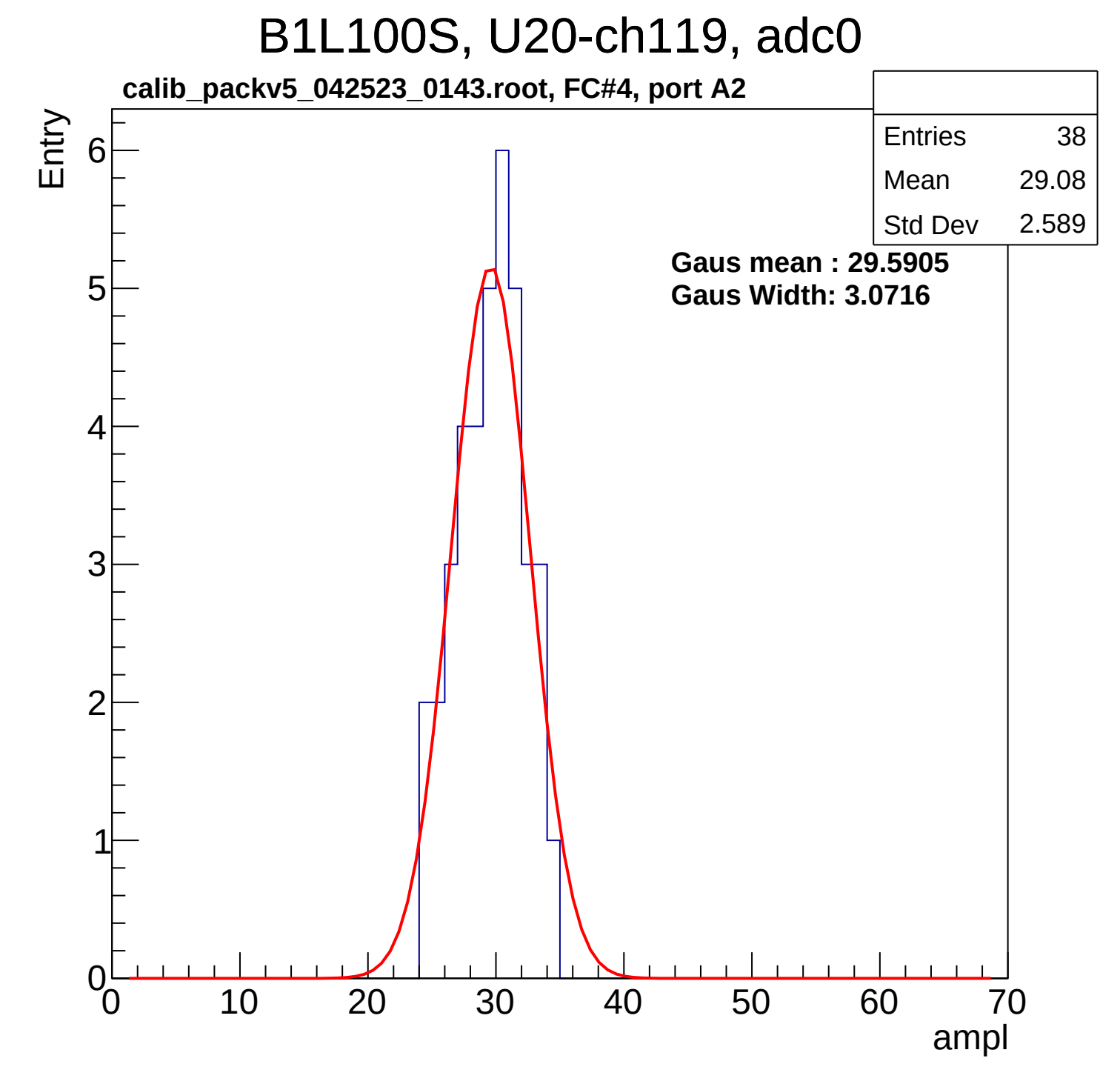
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 29.08
Std Dev 2.589
Gaus mean : 29.5905
Gaus Width: 3.0716
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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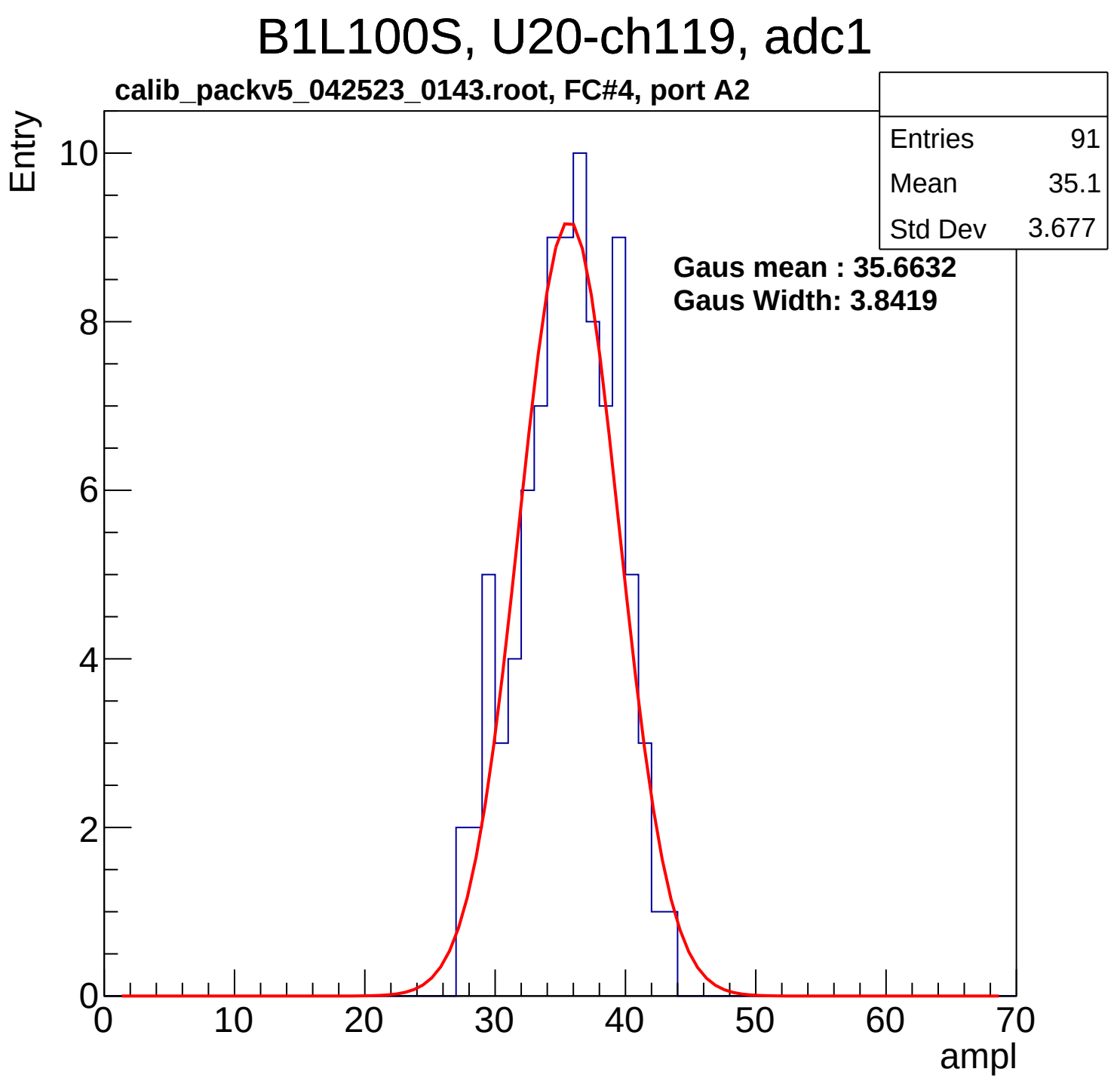
B1L100S, U20-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 35.1
Std Dev 3.677
Gaus mean : 35.6632
Gaus Width: 3.8419
Entry
ampl
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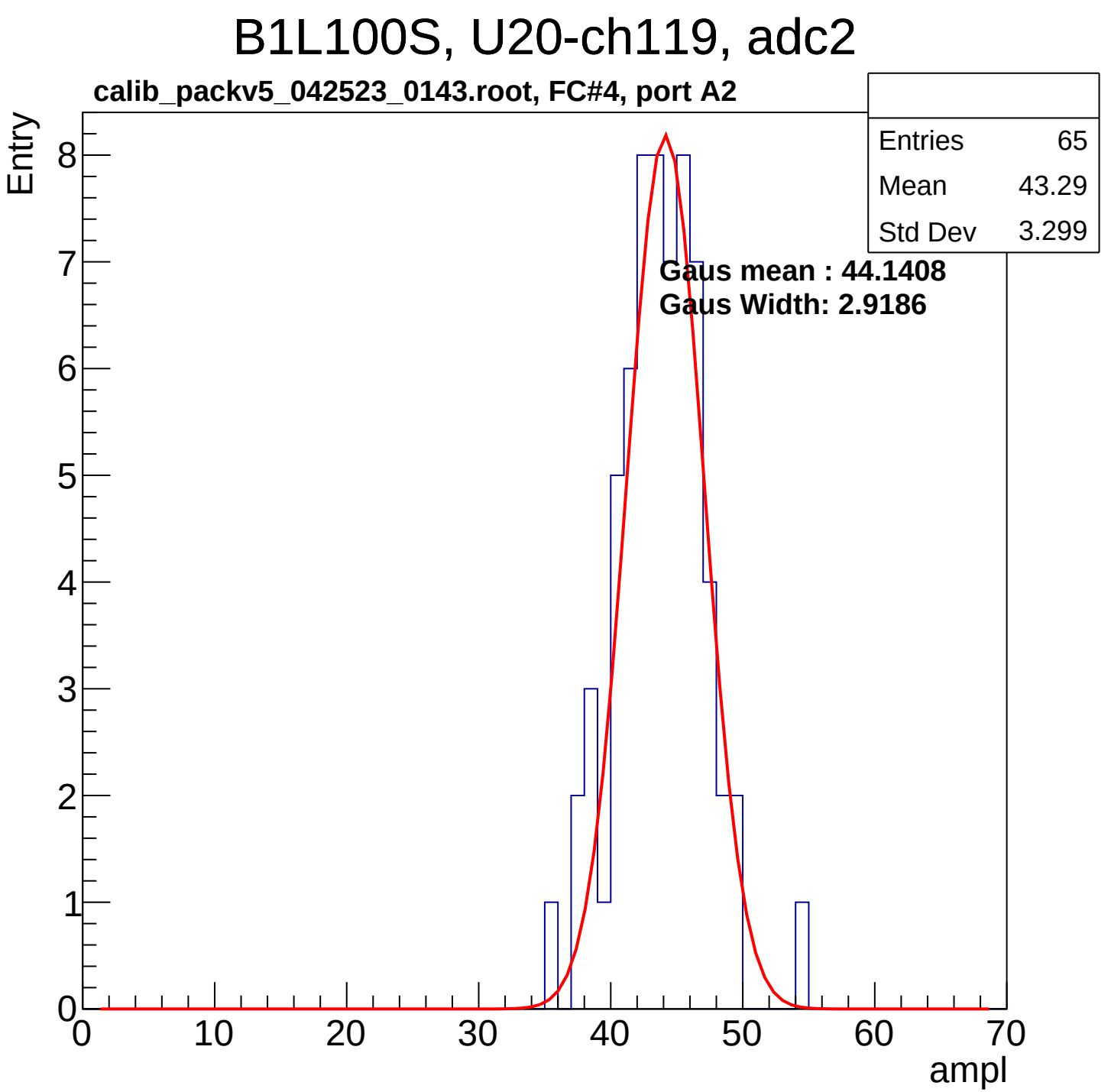
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.29
Std Dev 3.299
Gaus mean : 44.1408
Gaus Width: 2.9186
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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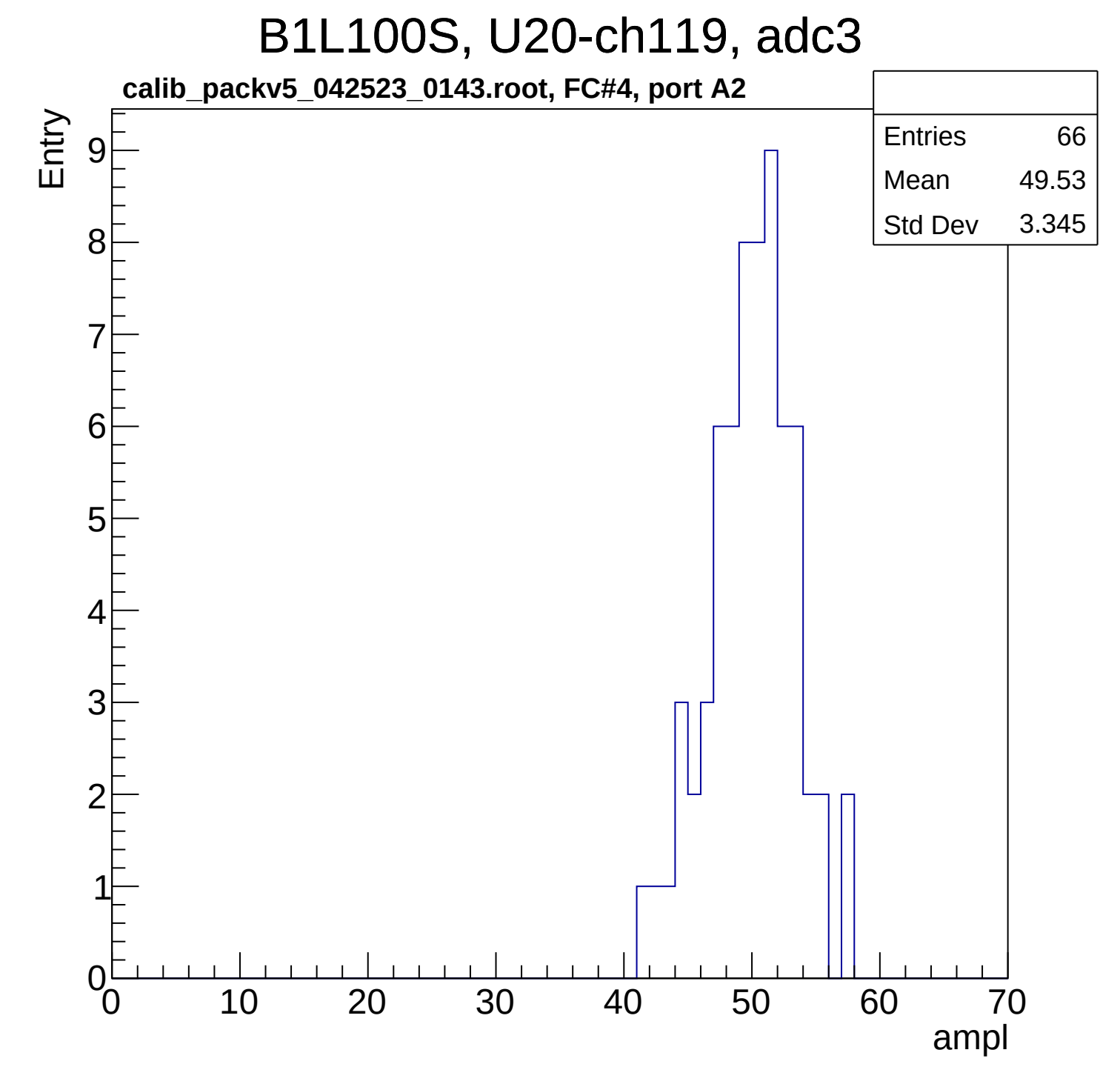
B1L100S, U20-ch119, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.53
Std Dev 3.345
Entry
ampl
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3
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5
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7
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9
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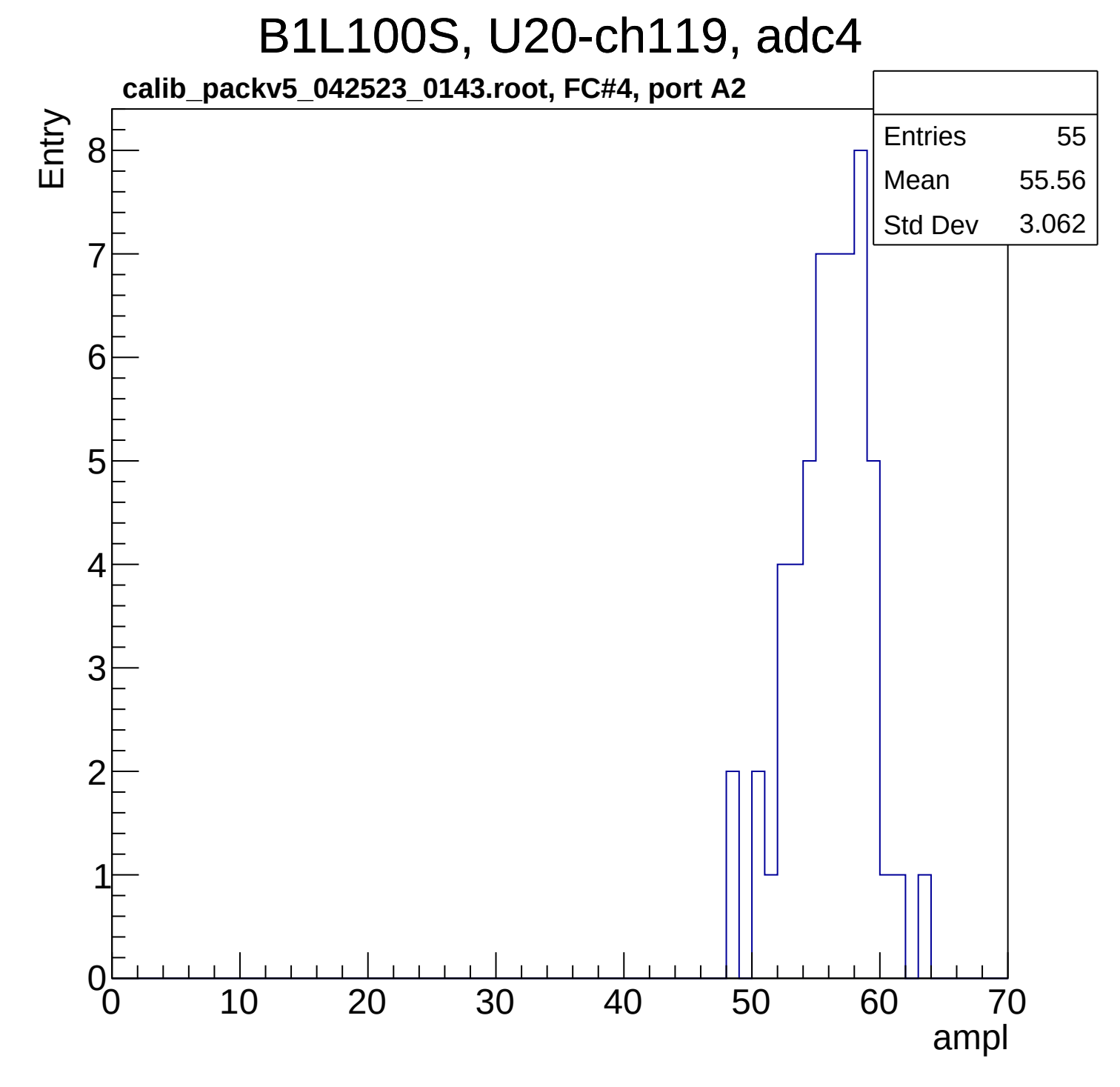
B1L100S, U20-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.56
Std Dev 3.062
Entry
ampl
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5
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60
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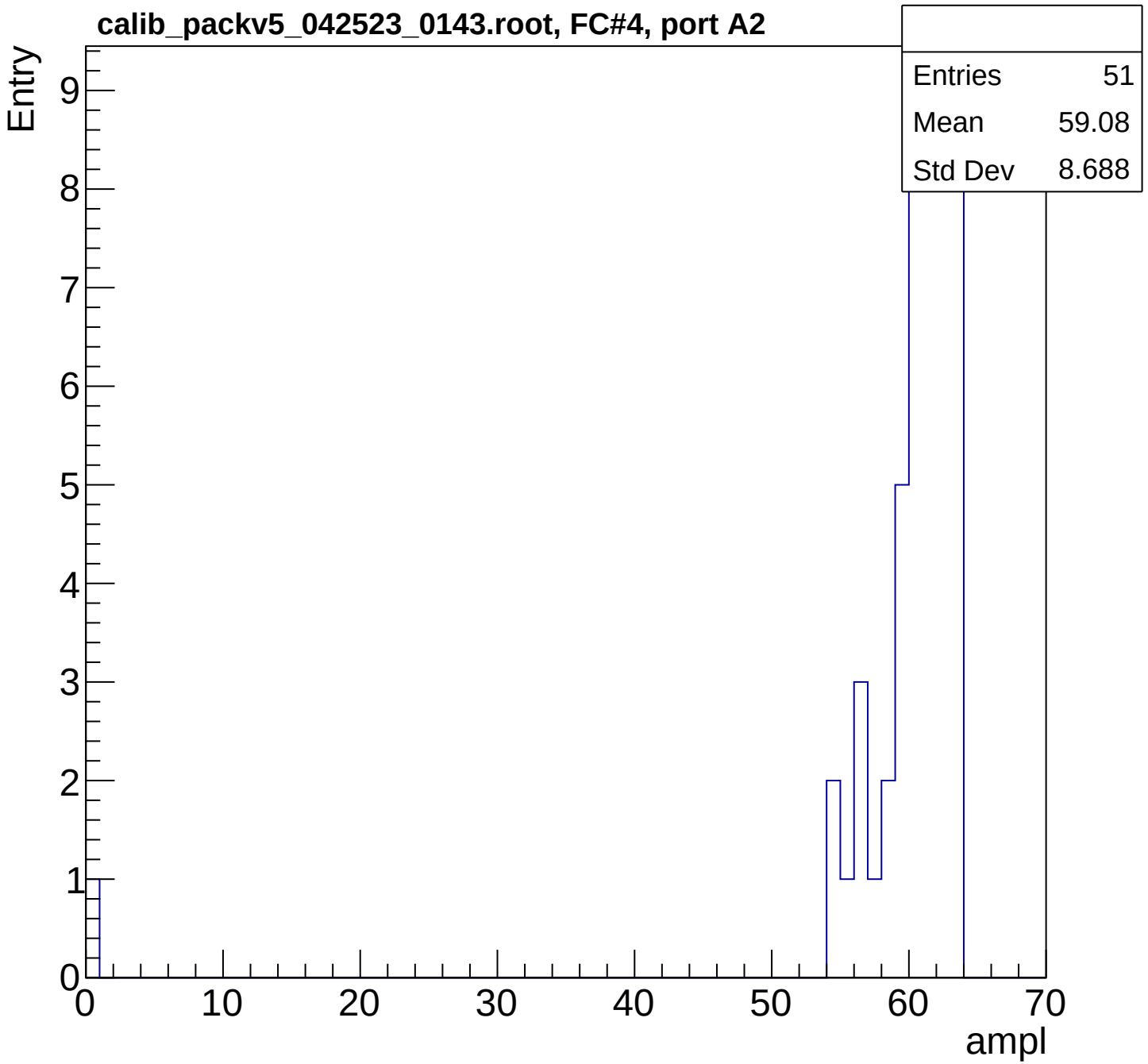
B1L100S, U20-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 59.08
Std Dev 8.688
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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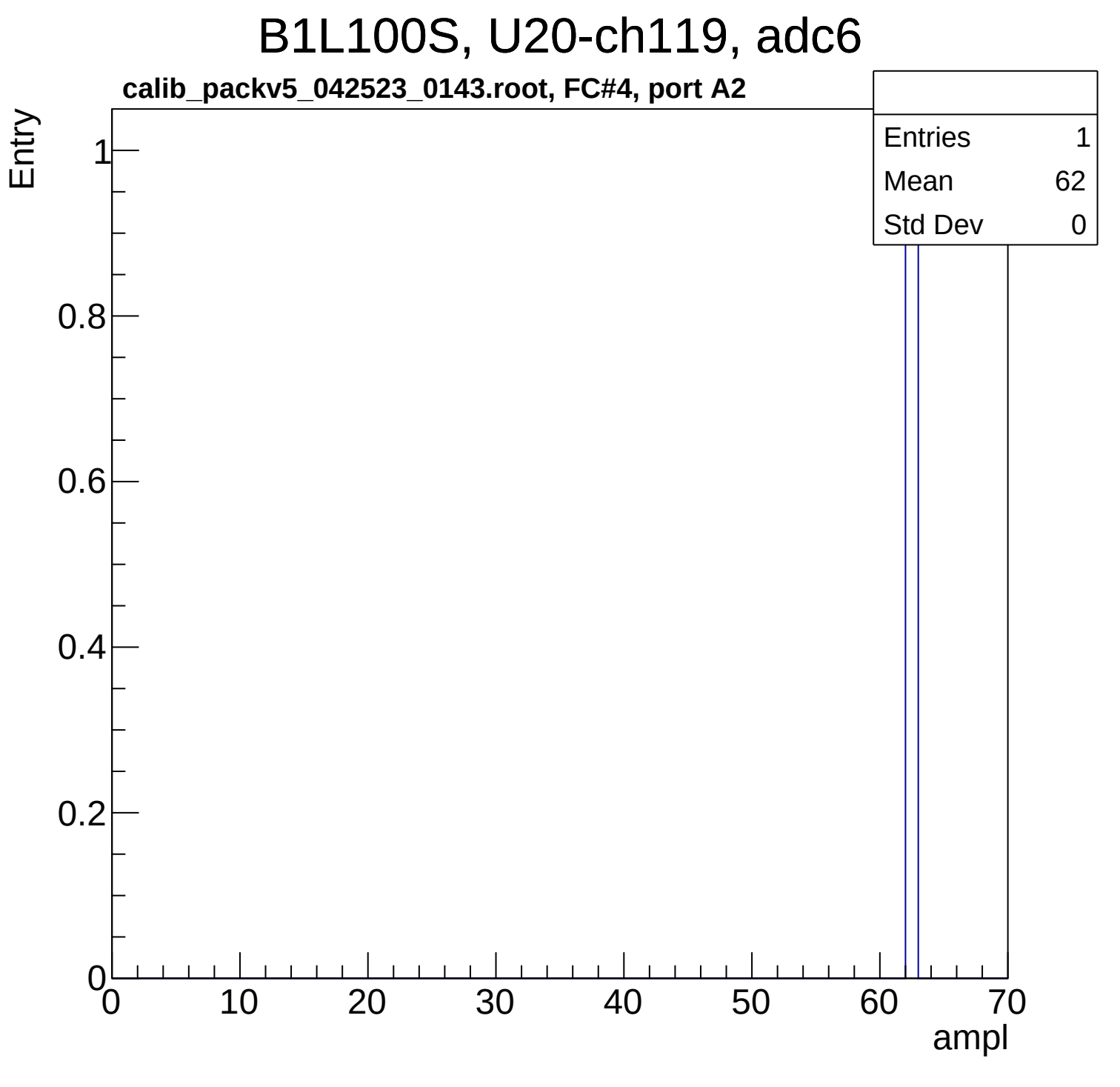
B1L100S, U20-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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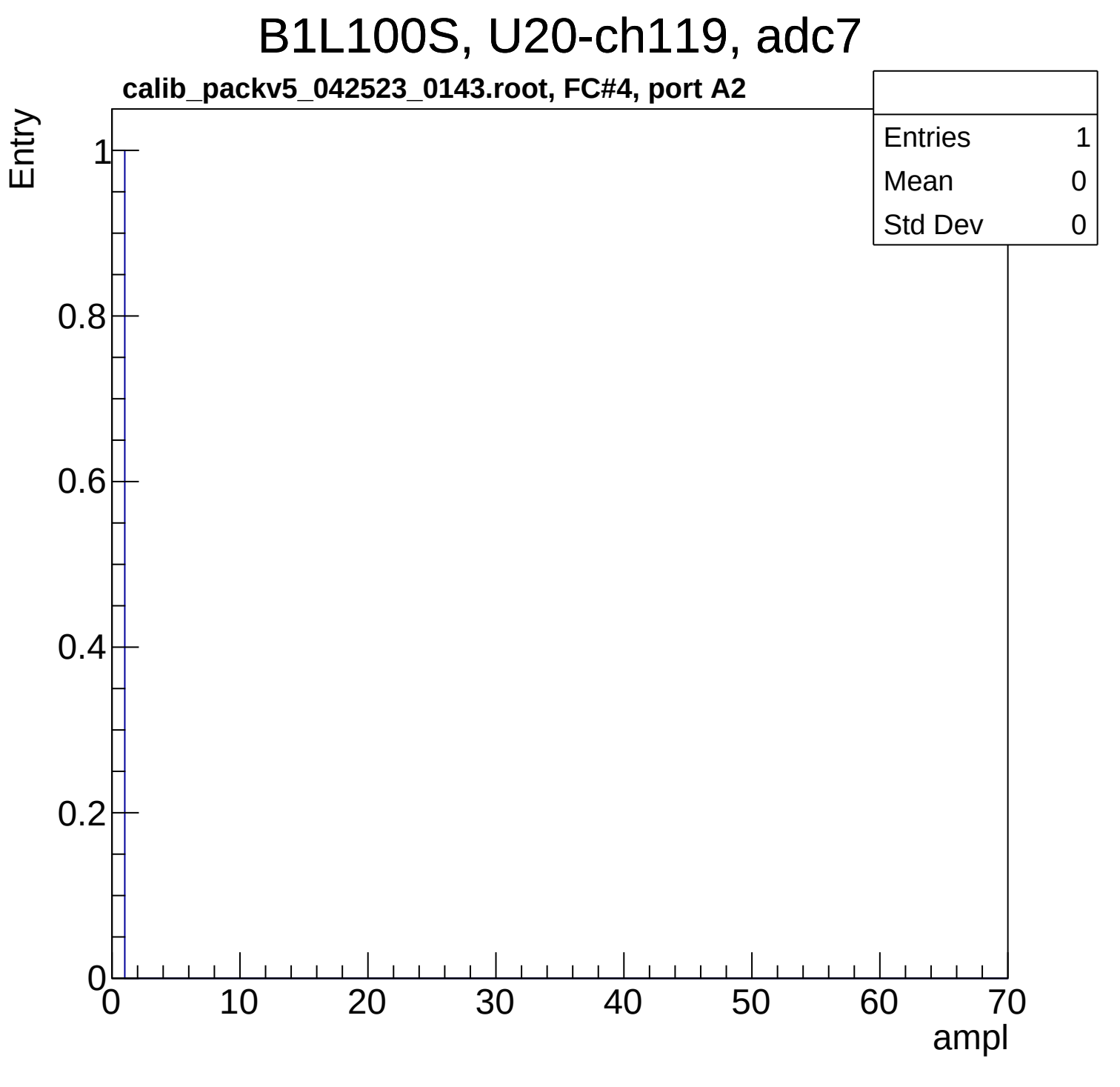
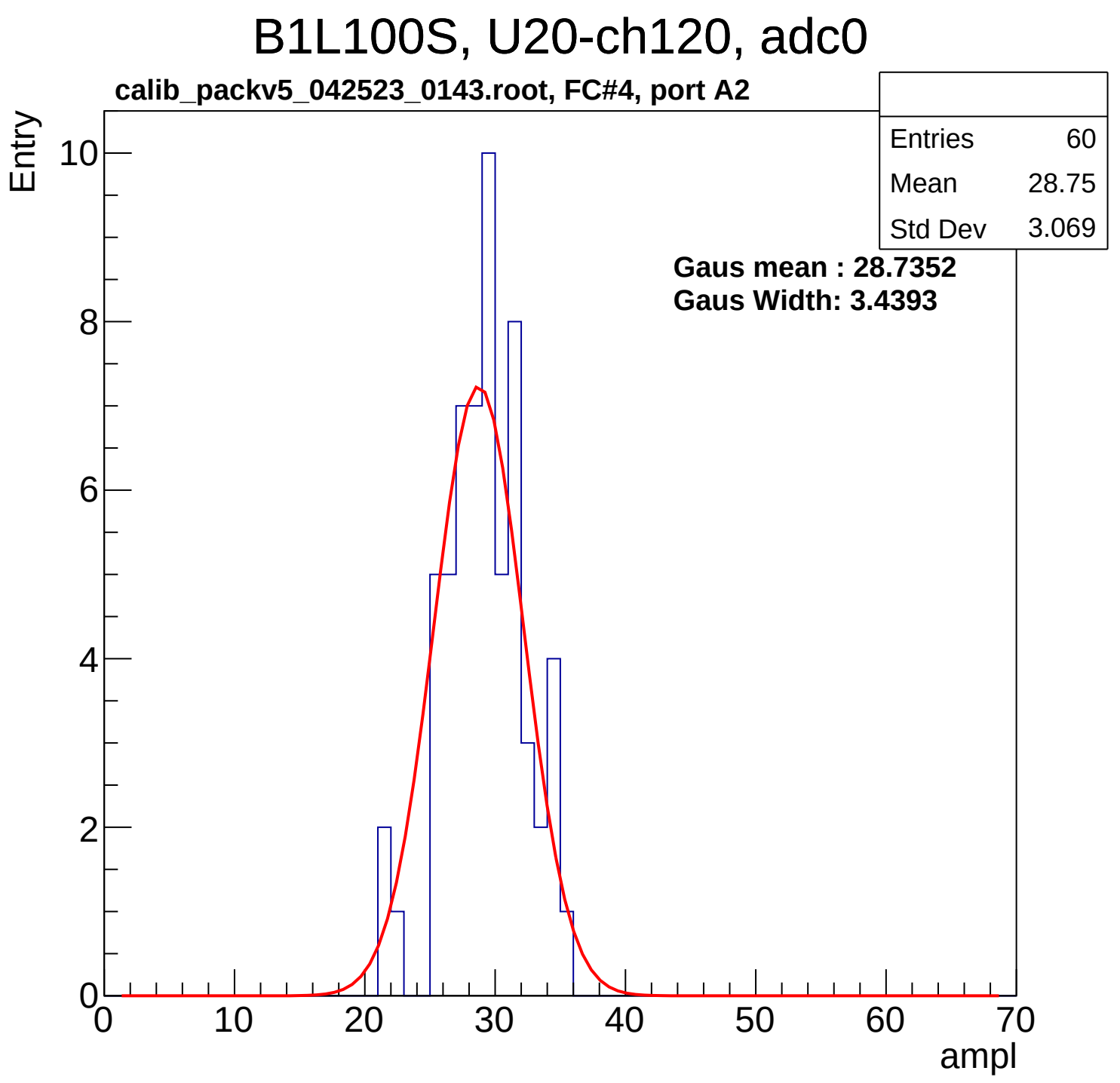
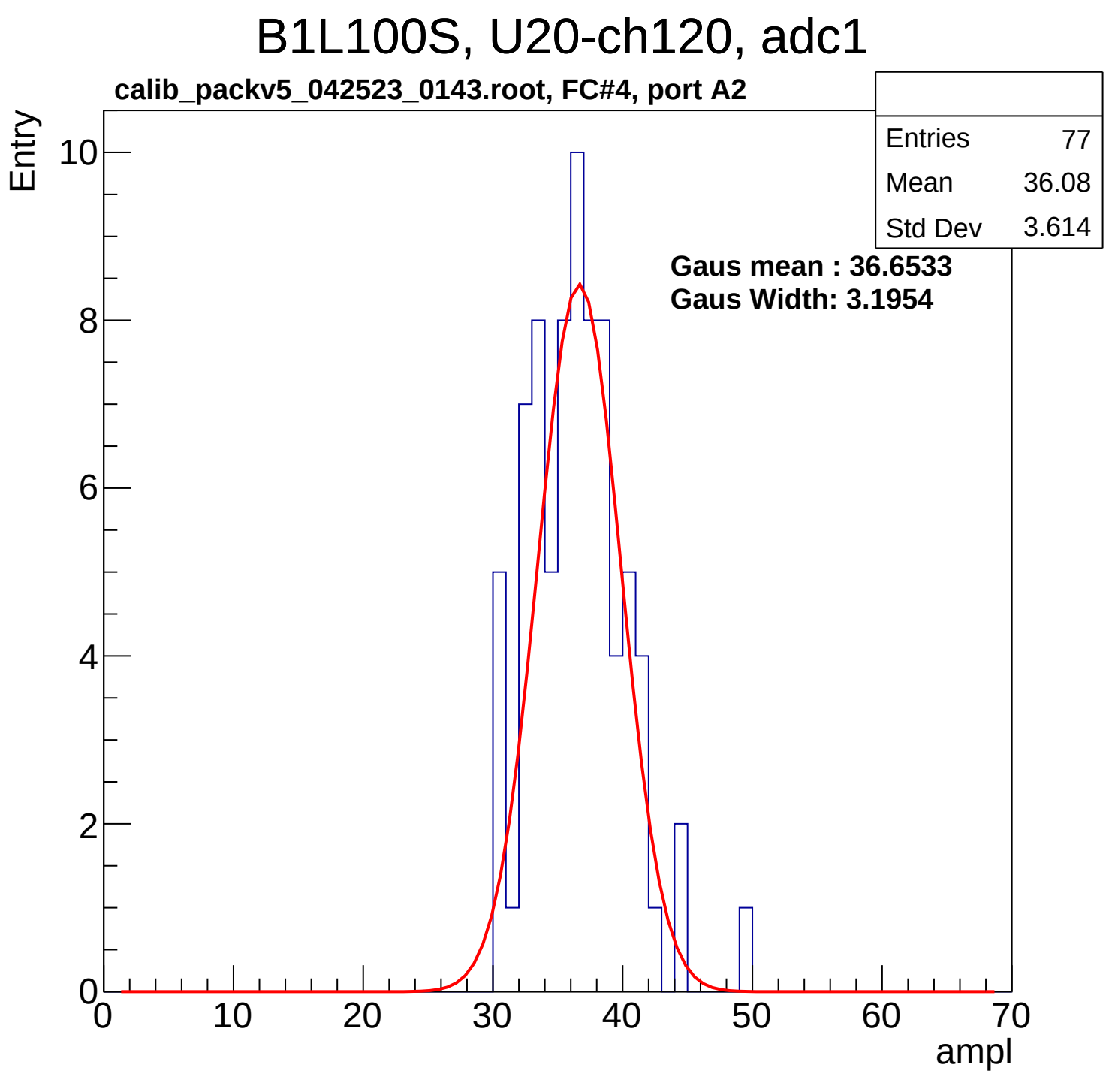
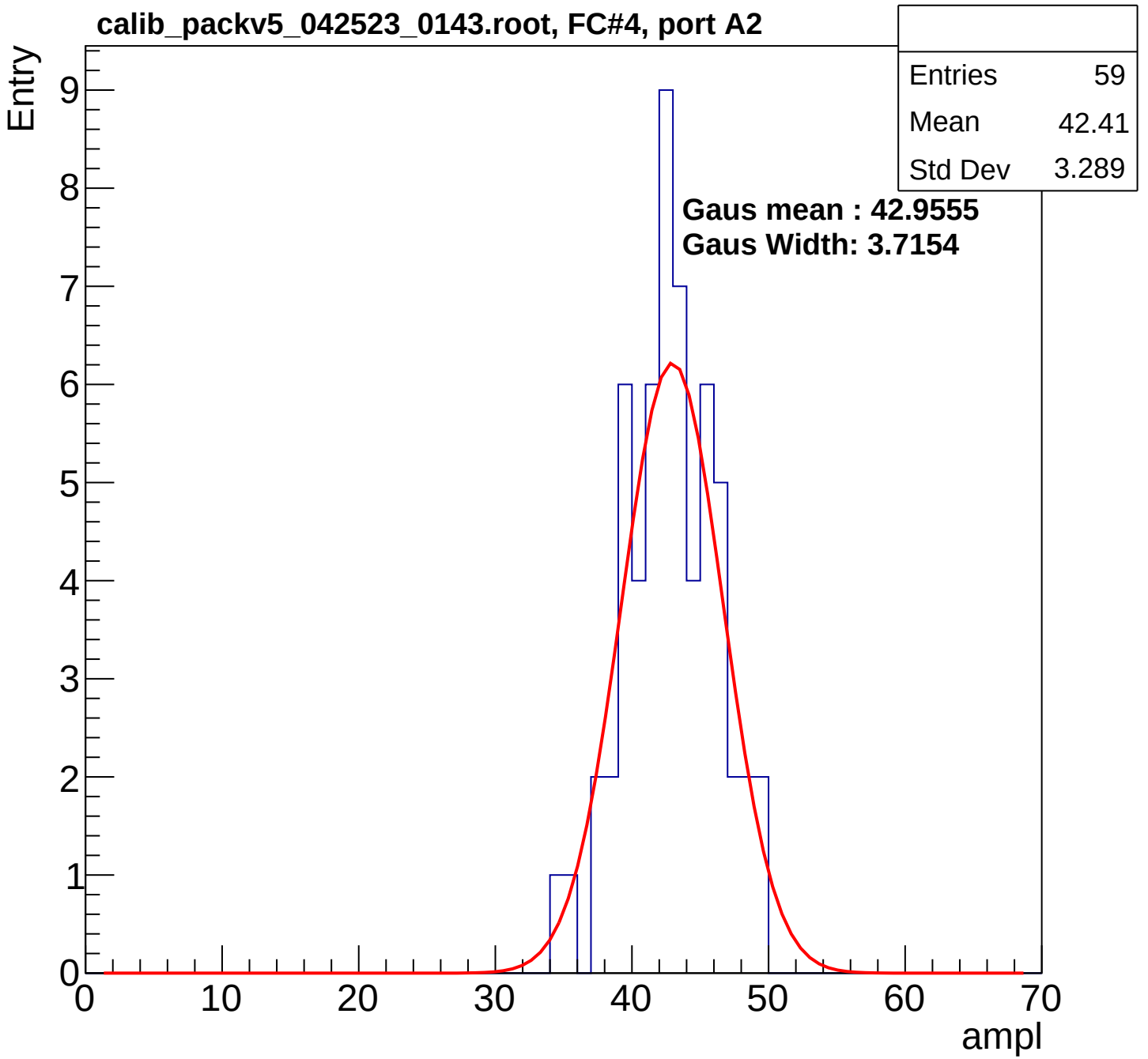
40
50
60
70
ampl


B1L100S, U20-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 28.75
Std Dev 3.069
Gaus mean : 28.7352
Gaus Width: 3.4393
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.08
Std Dev 3.614
Gaus mean : 36.6533
Gaus Width: 3.1954
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch120, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.41
Std Dev 3.289
Gaus mean : 42.9555
Gaus Width: 3.7154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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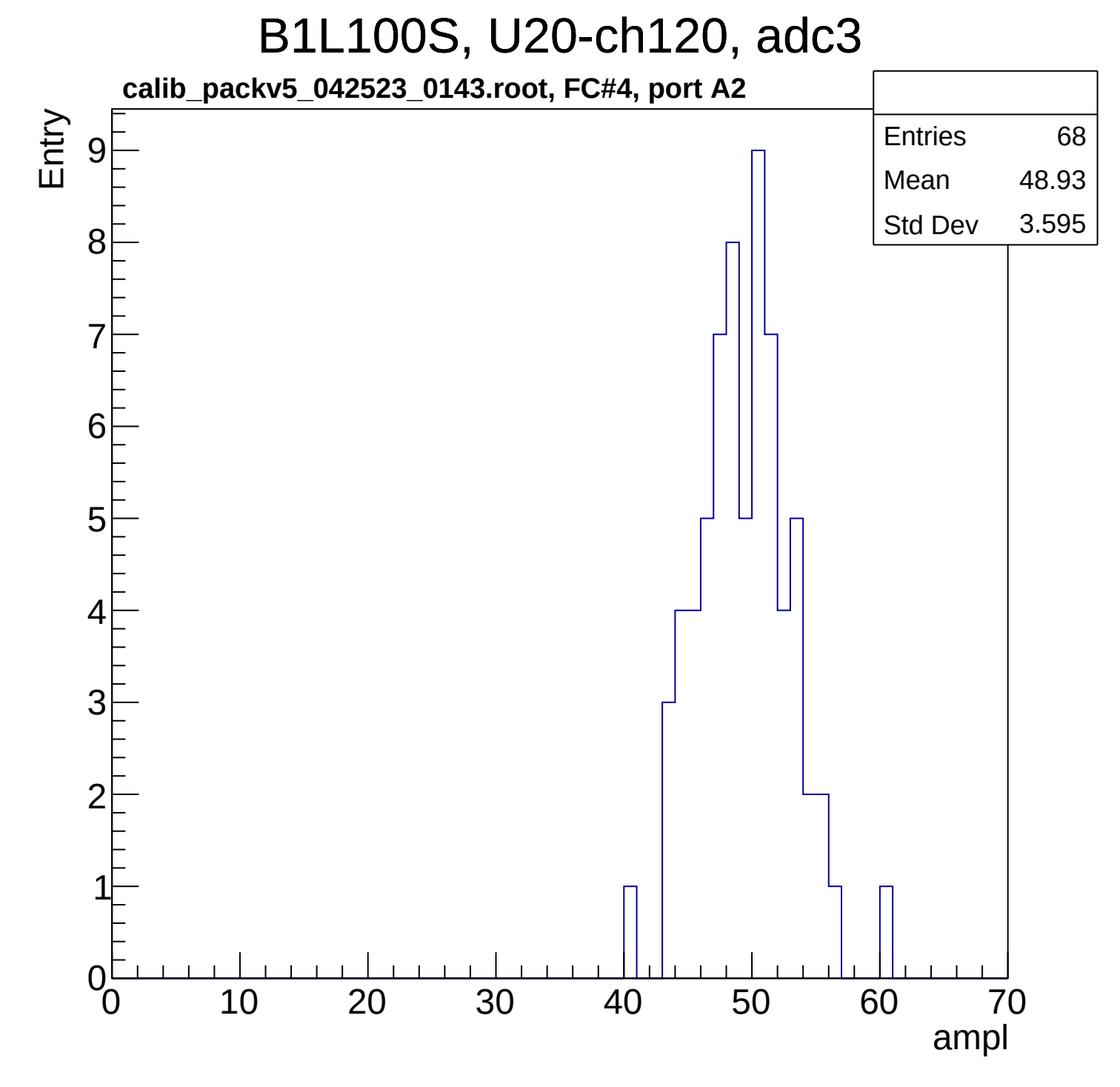
B1L100S, U20-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.93
Std Dev 3.595
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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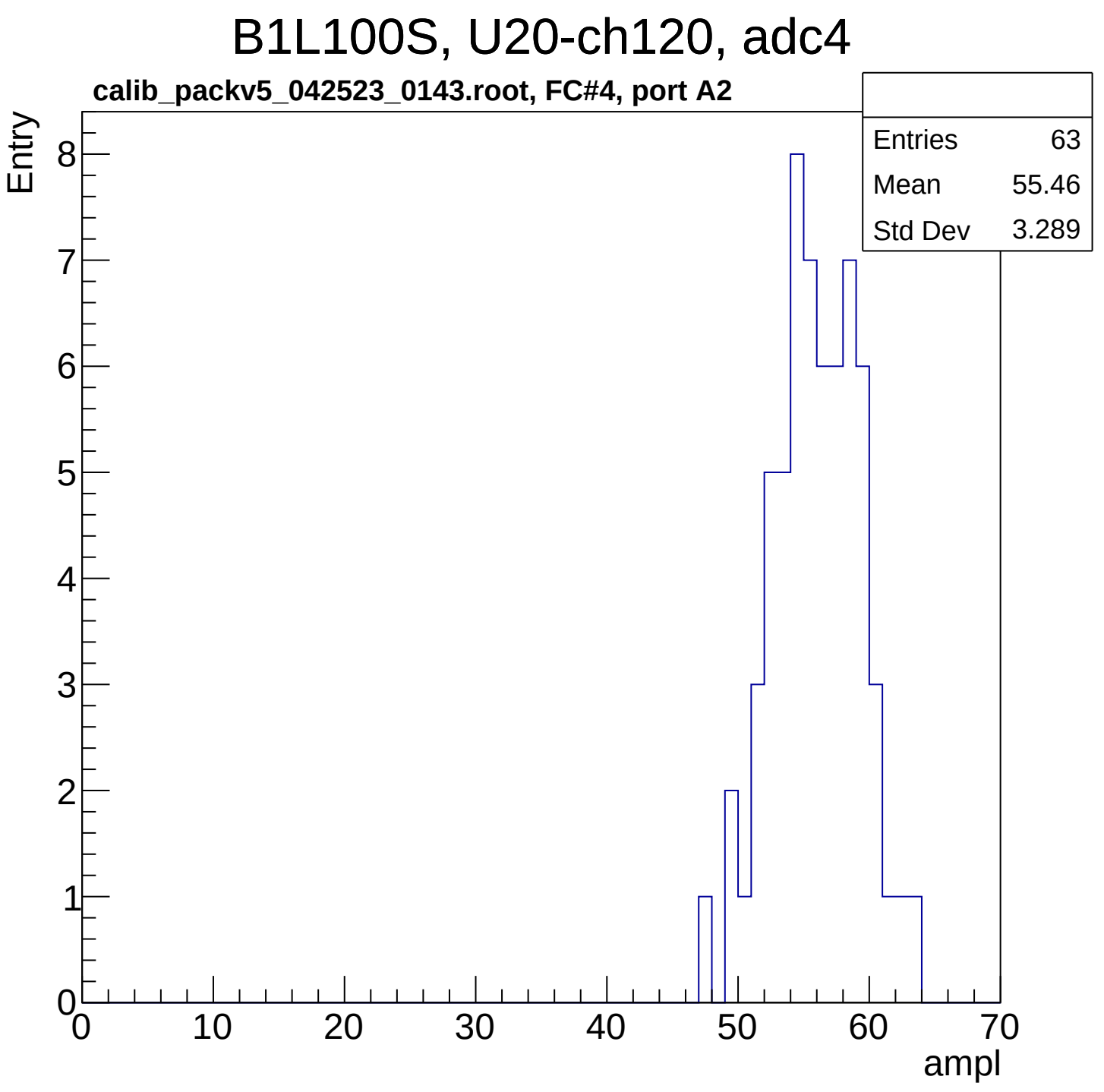
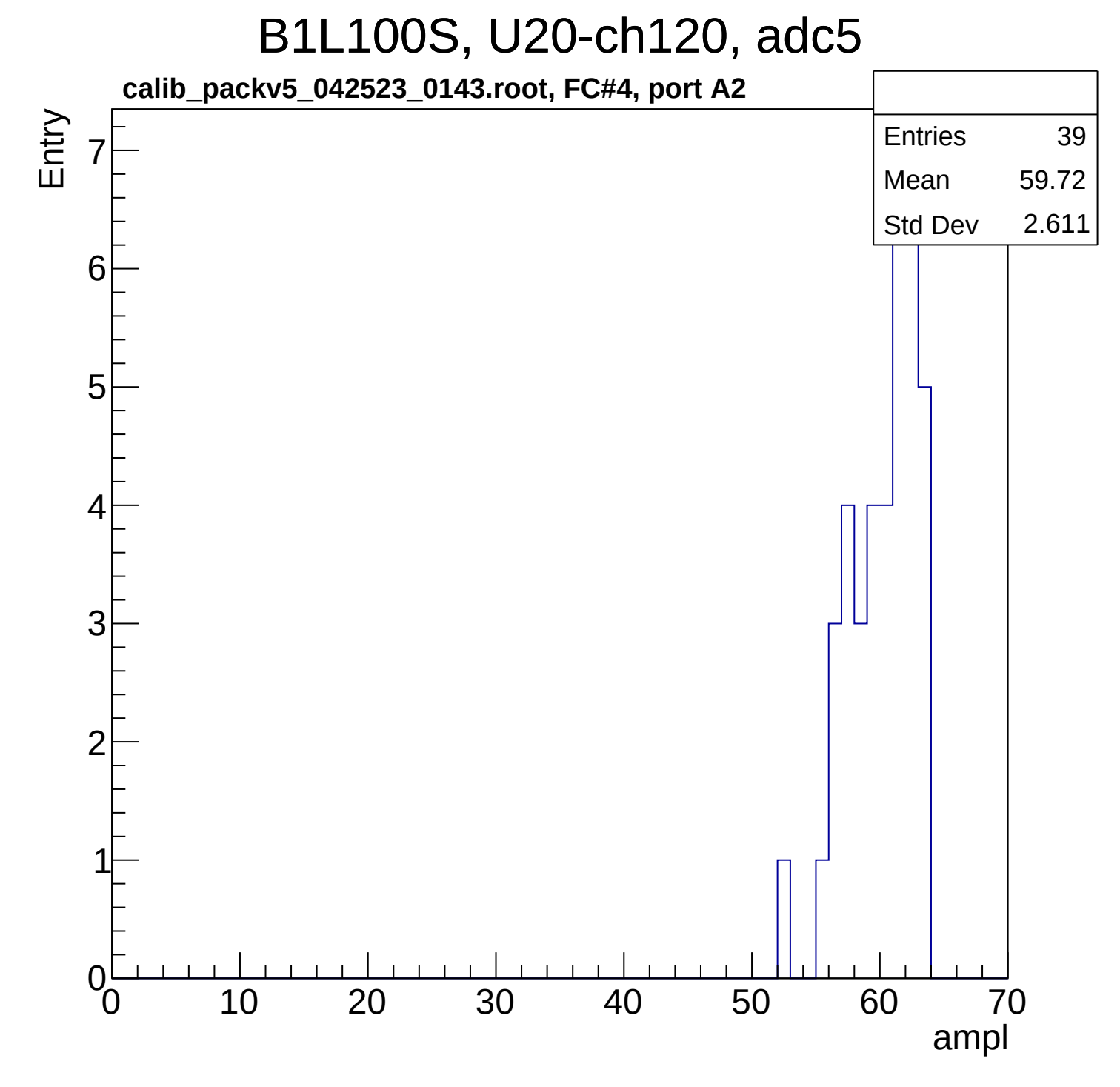
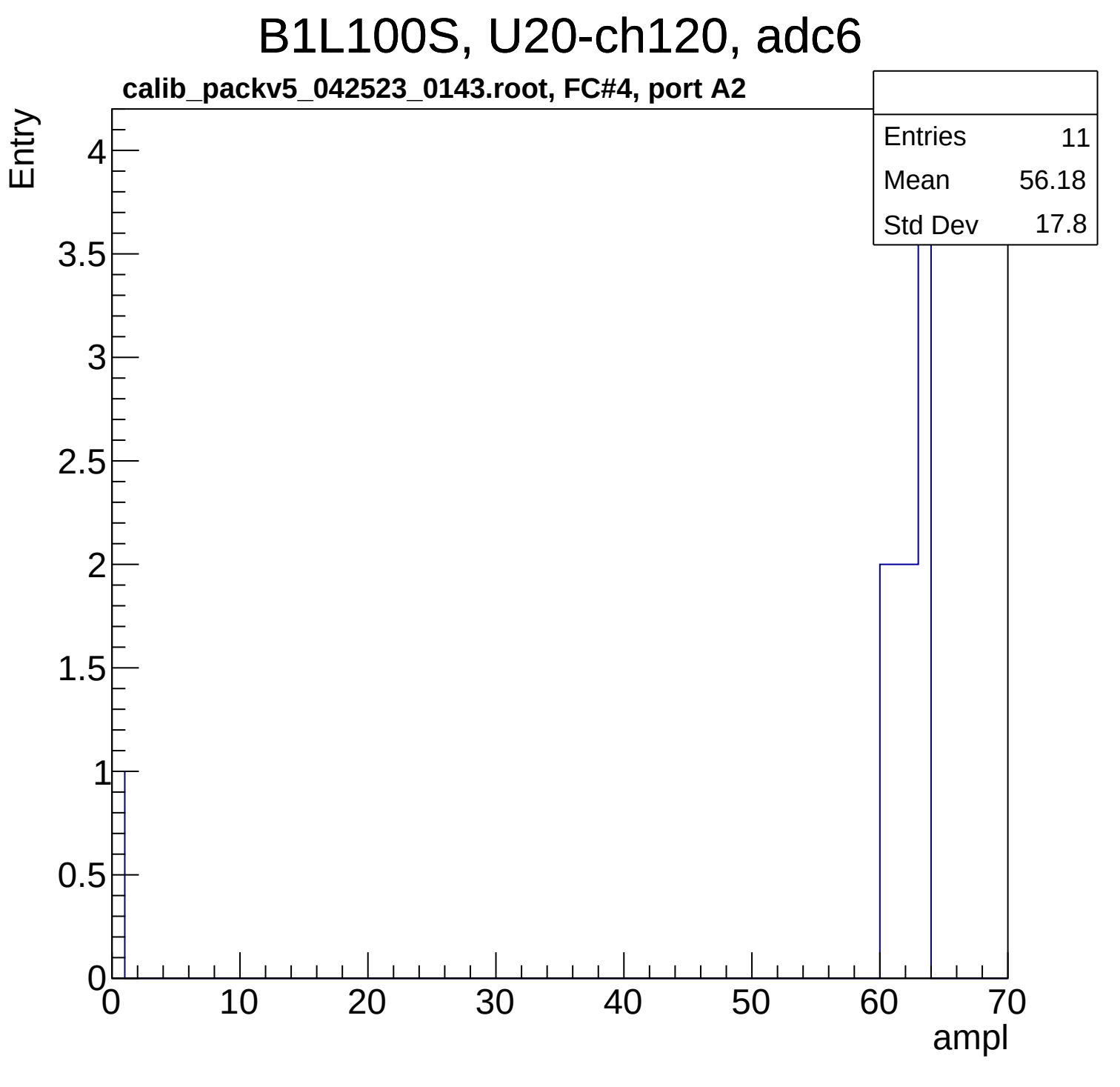

B1L100S, U20-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 55.46
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.72
Std Dev 2.611
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 56.18
Std Dev 17.8
Entry
ampl
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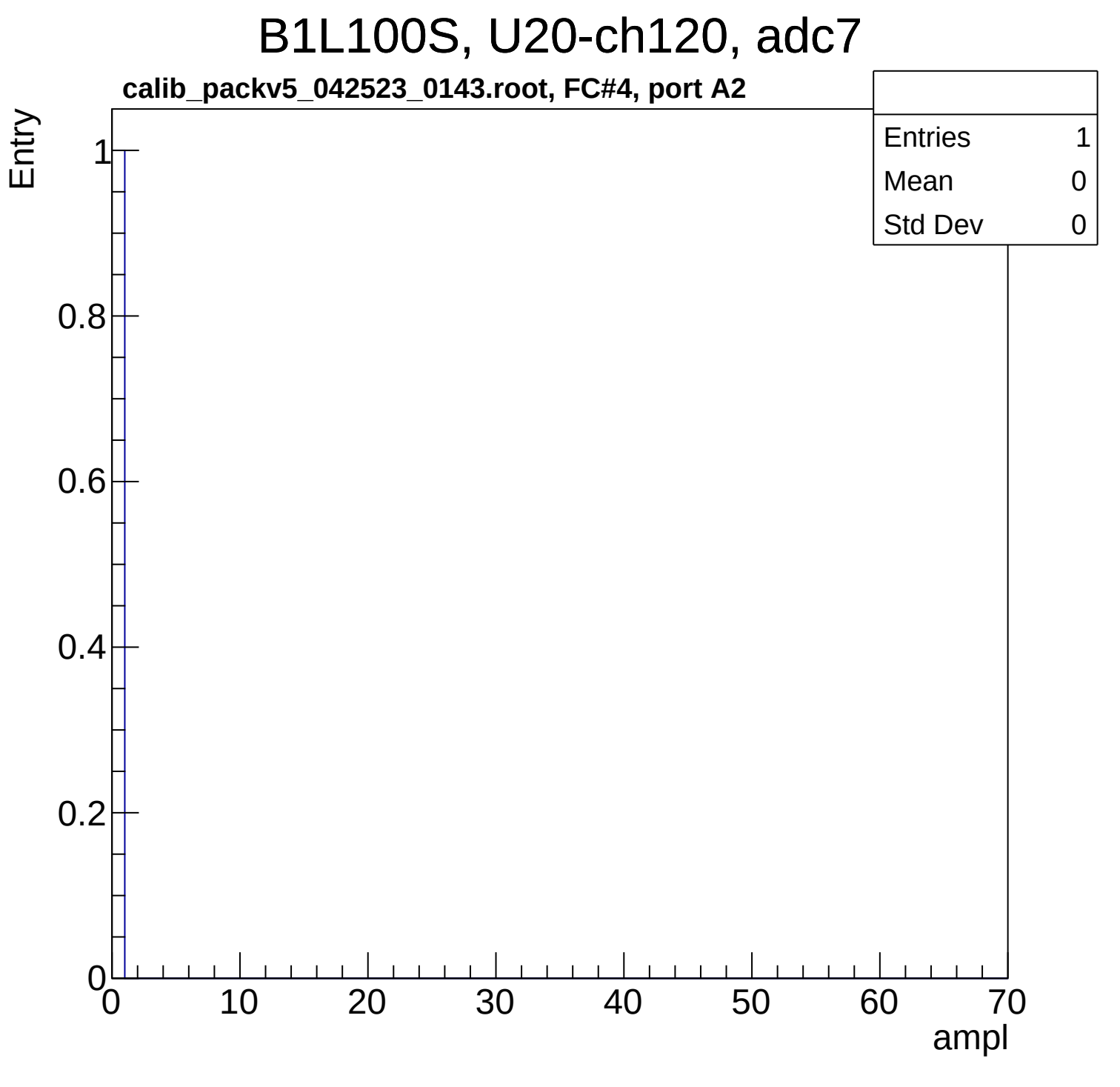
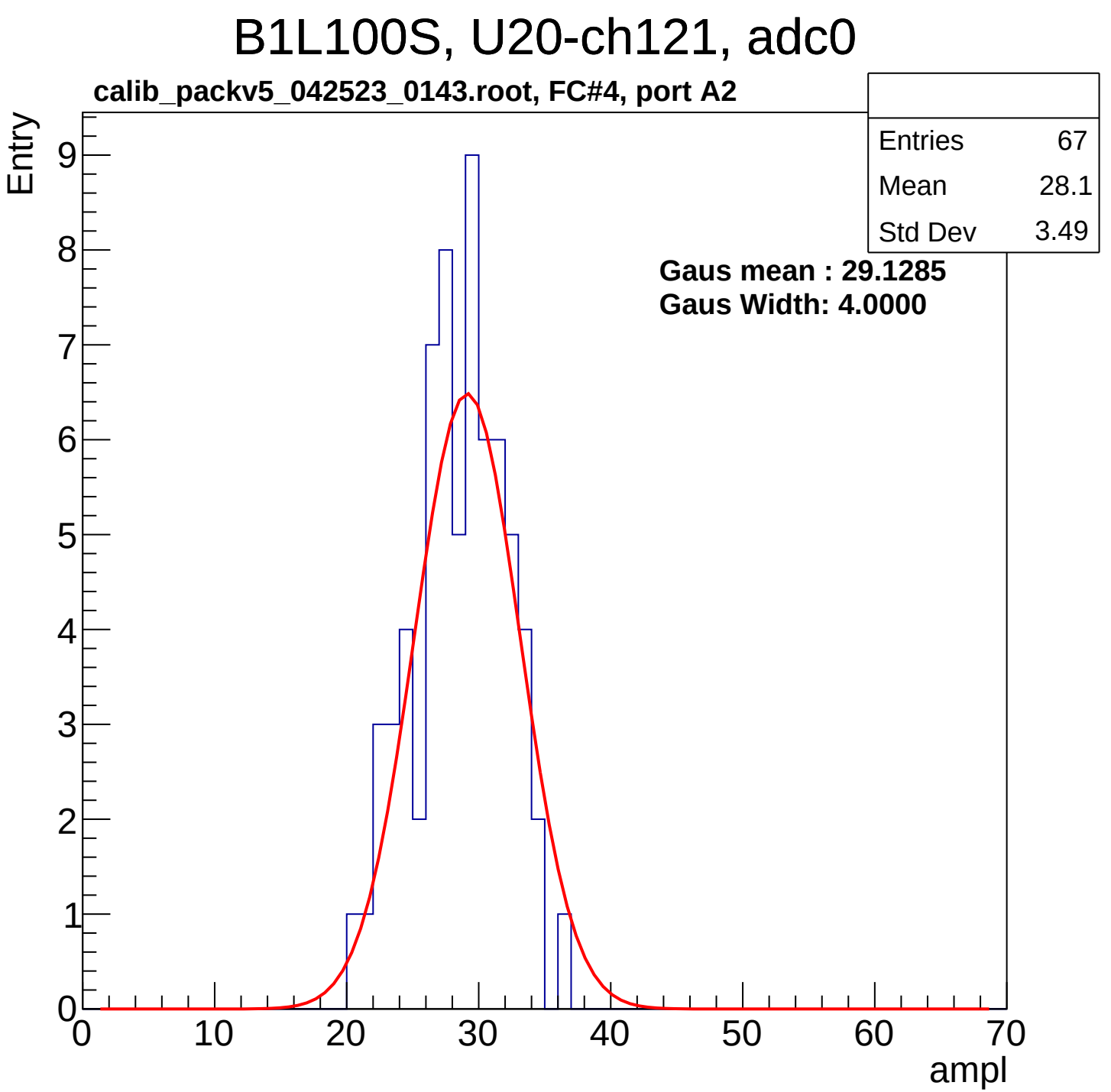
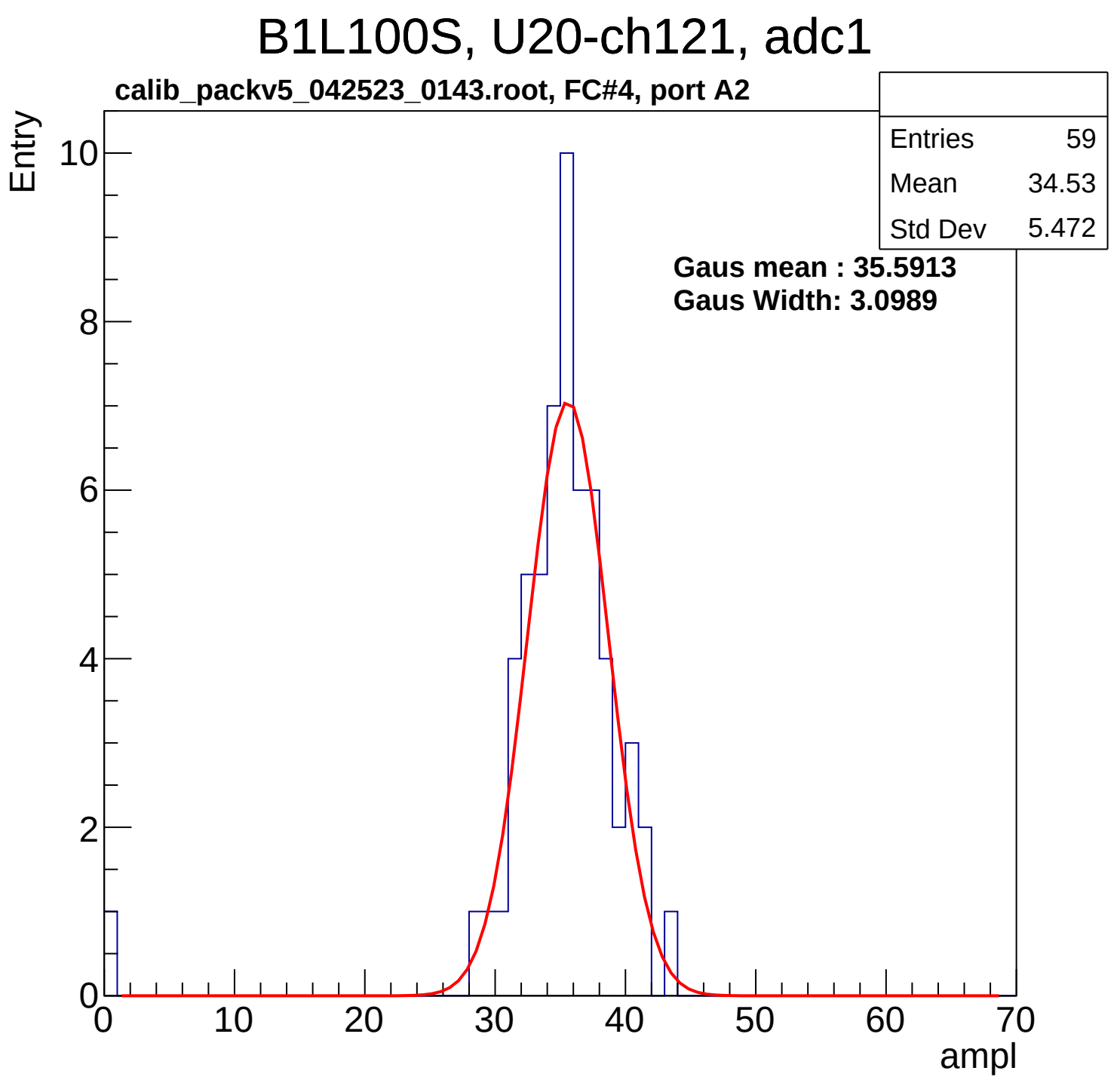
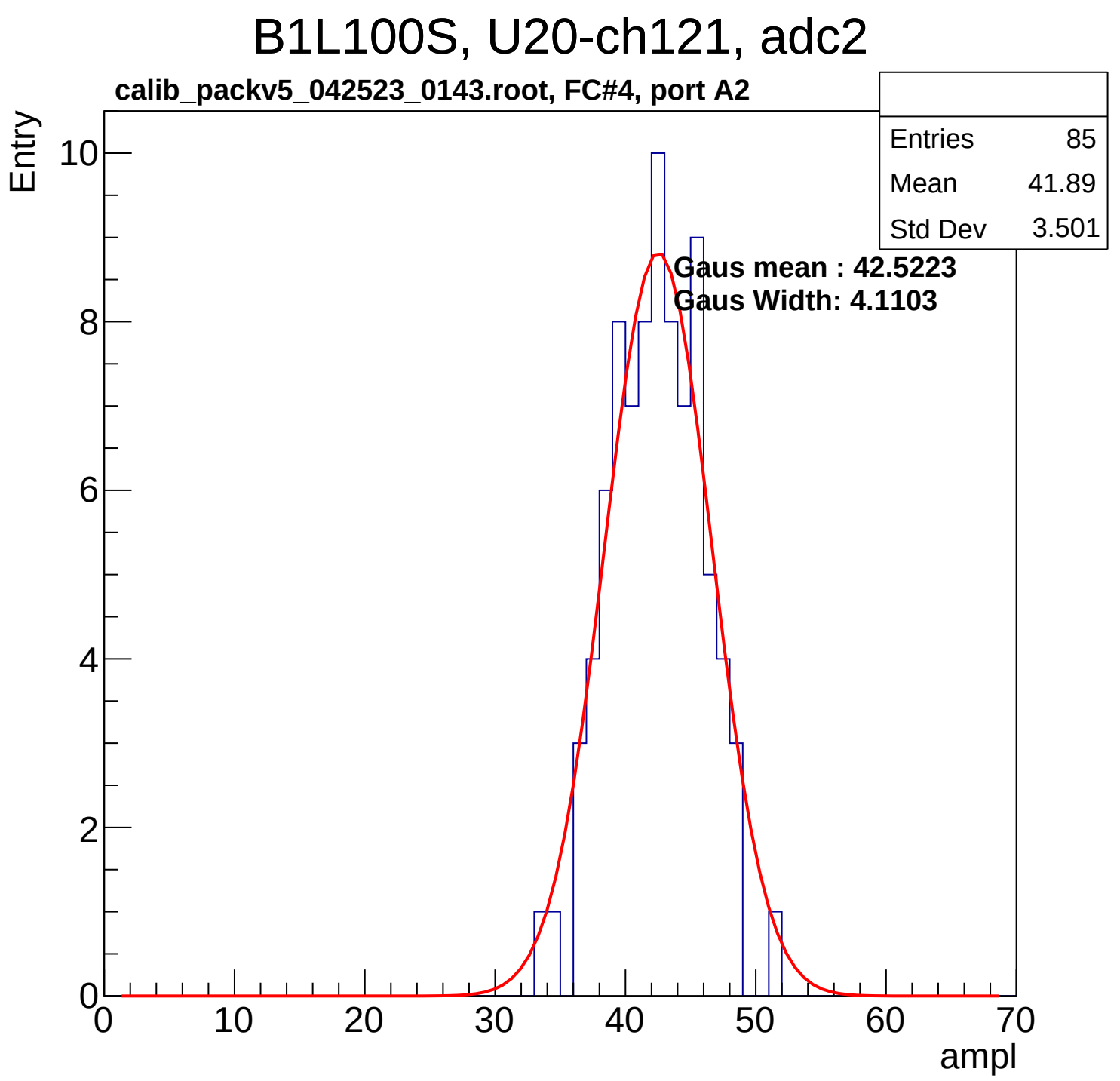
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U20-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 28.1
Std Dev 3.49
Gaus mean : 29.1285
Gaus Width: 4.0000
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 34.53
Std Dev 5.472
Gaus mean : 35.5913
Gaus Width: 3.0989
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch121, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 41.89
Std Dev 3.501
Gaus mean : 42.5223
Gaus Width: 4.1103
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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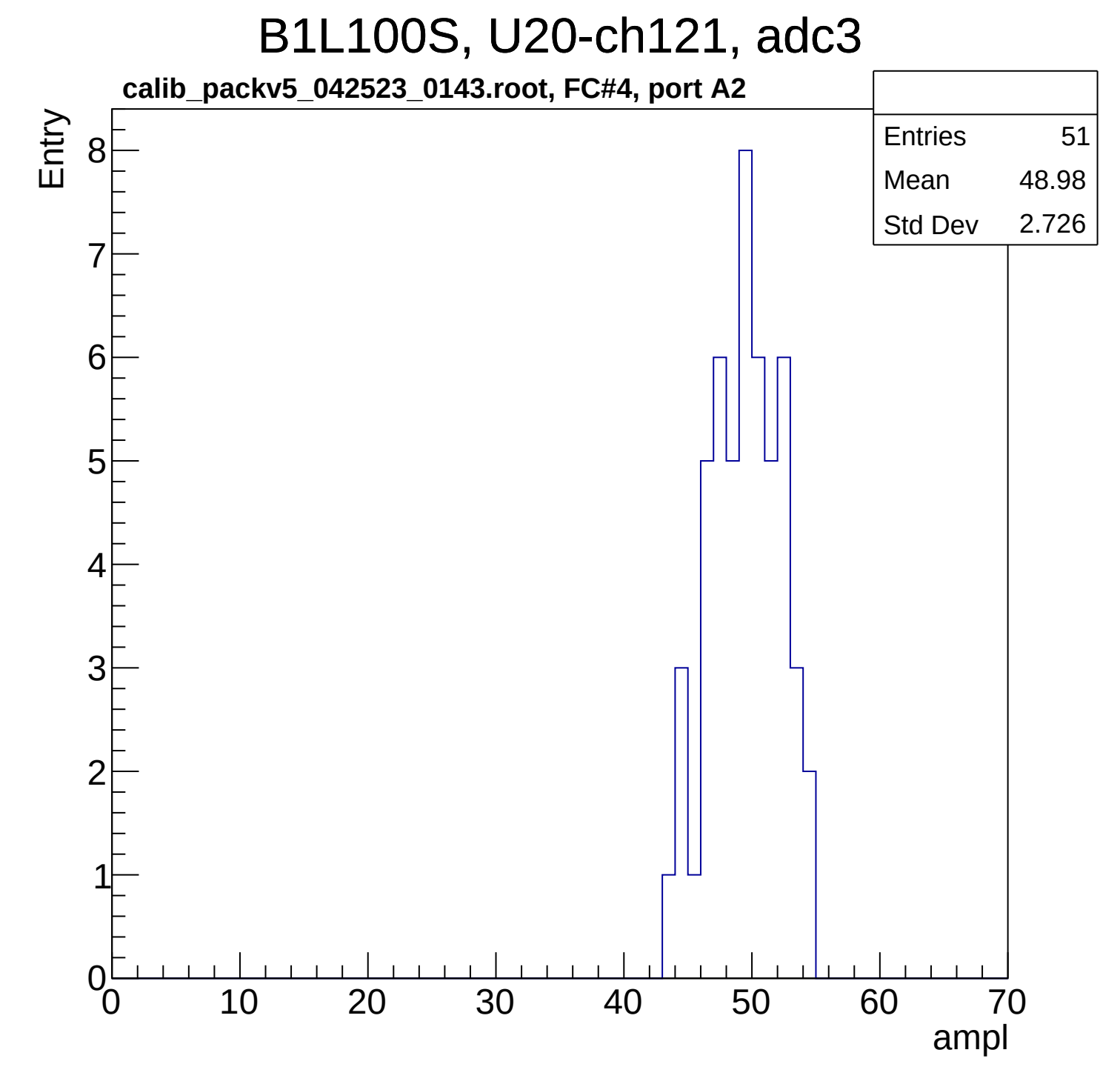
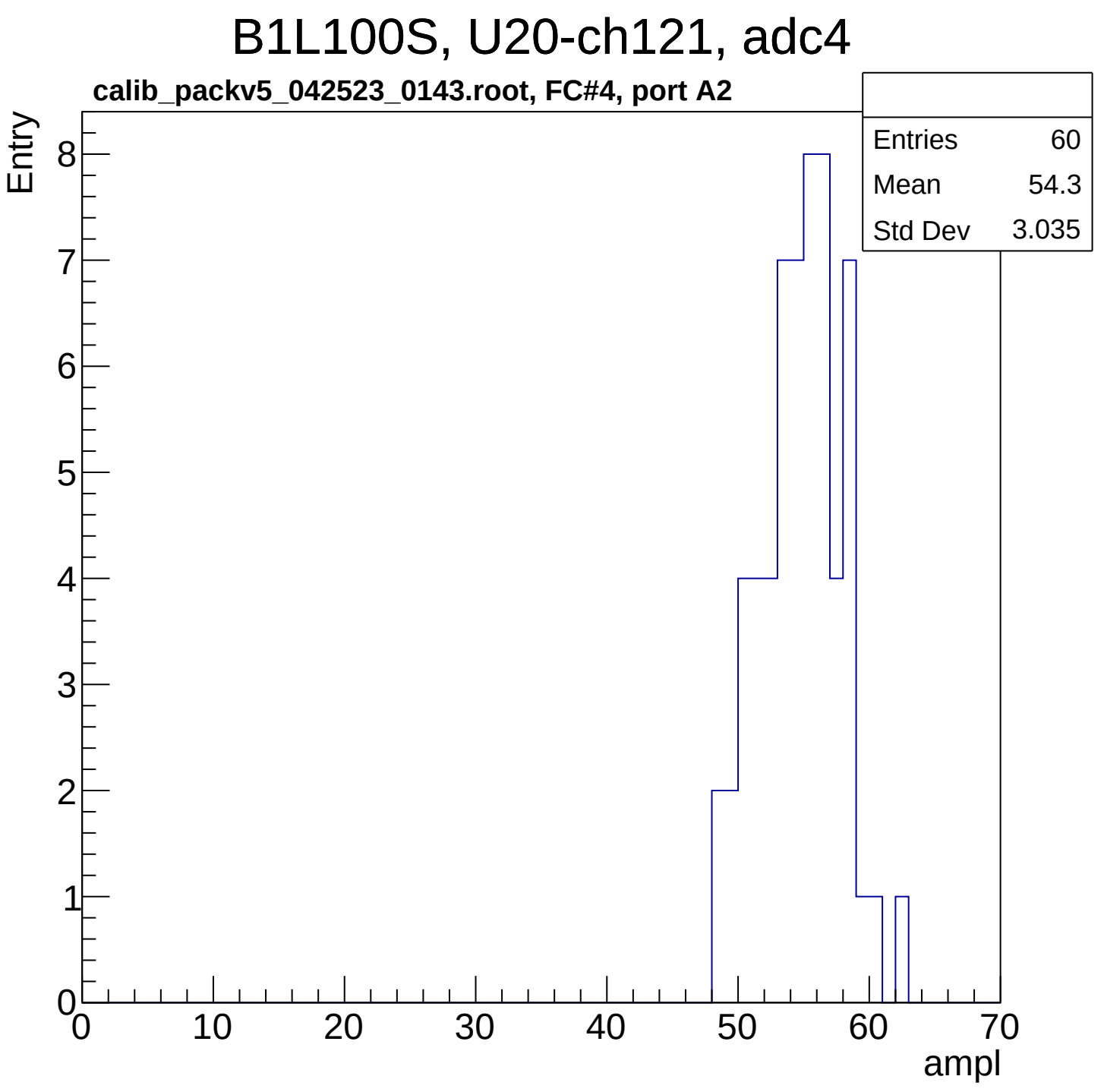
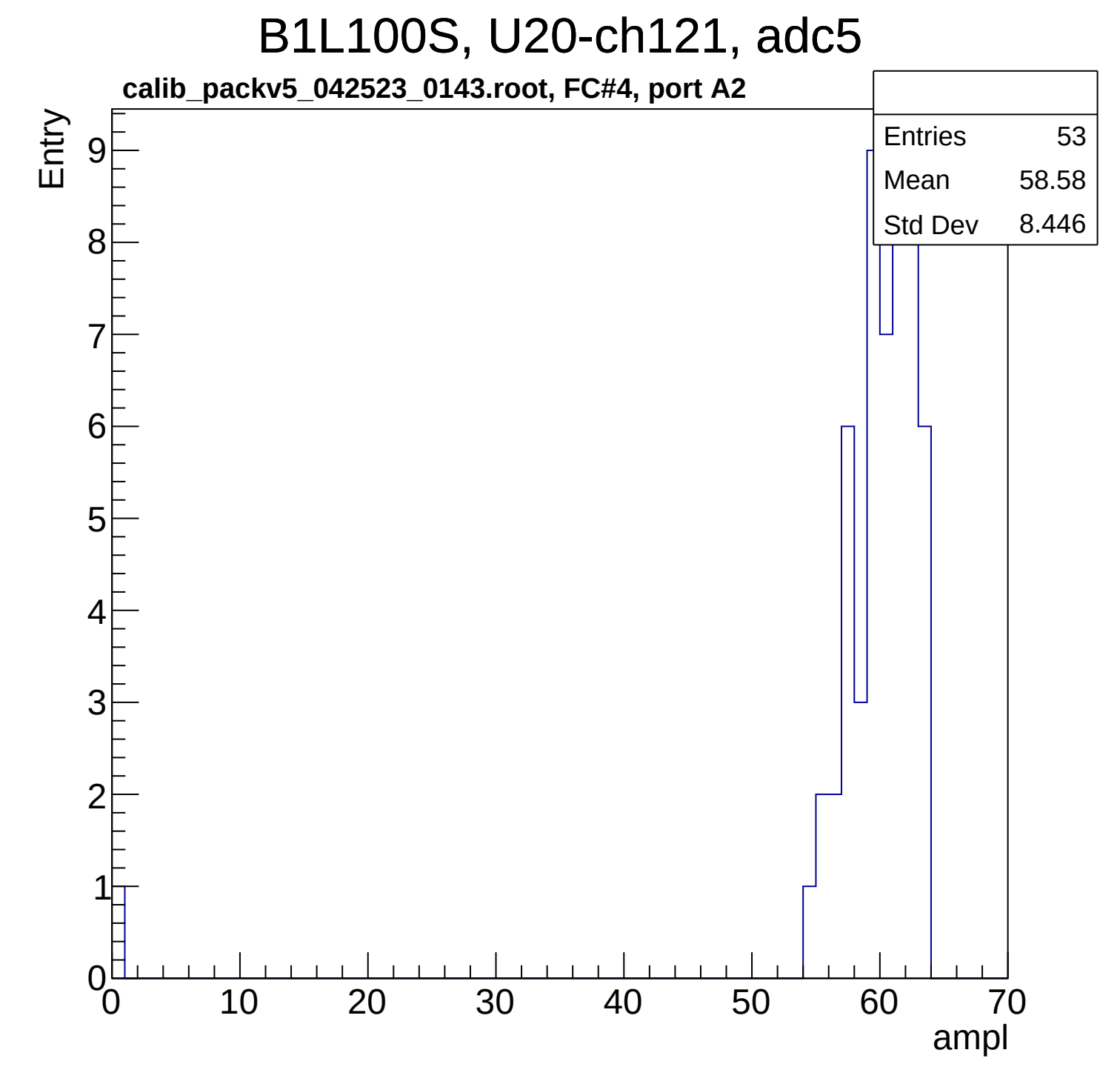

B1L100S, U20-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 48.98
Std Dev 2.726
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 54.3
Std Dev 3.035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch121, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.58
Std Dev 8.446
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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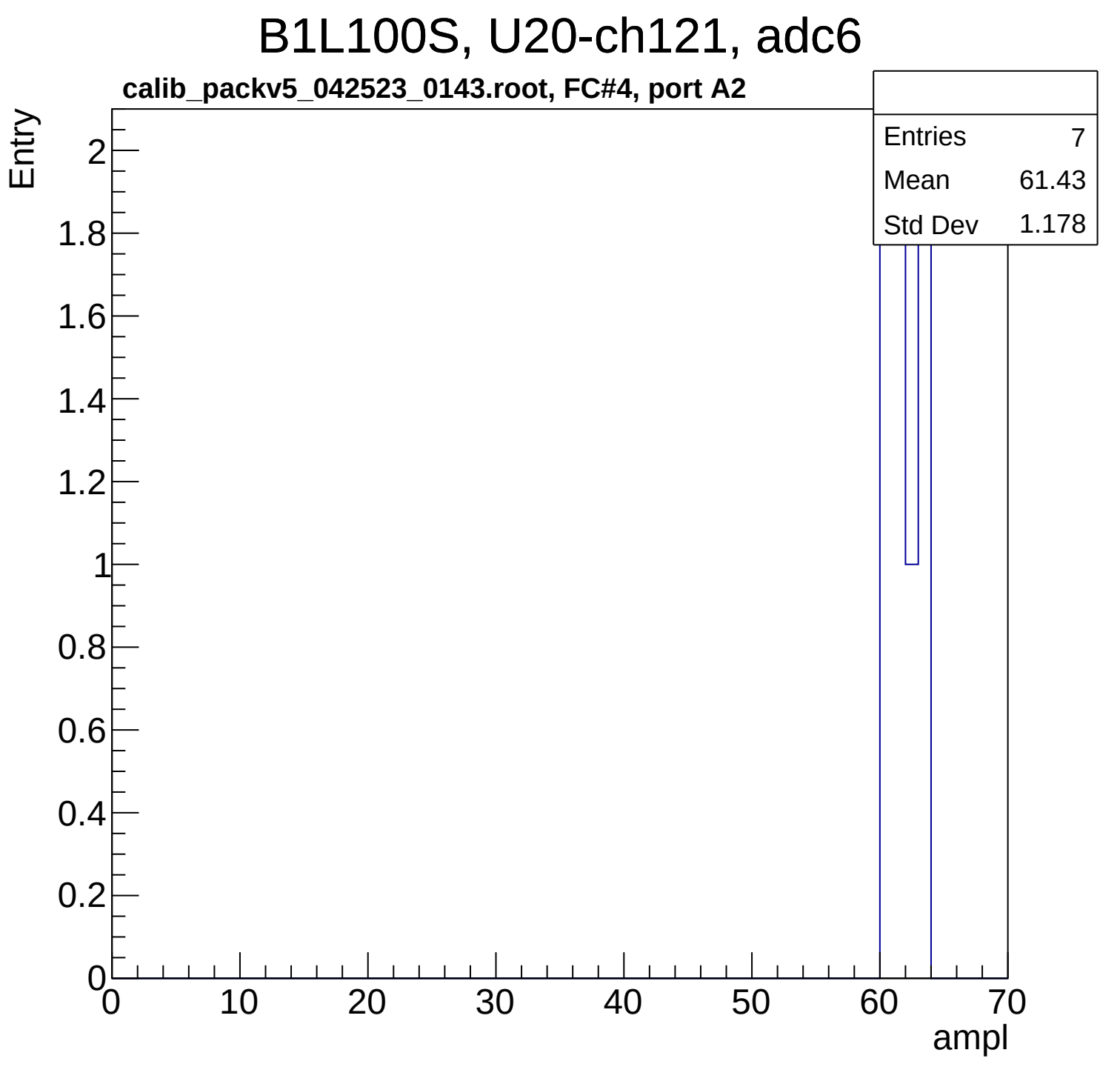
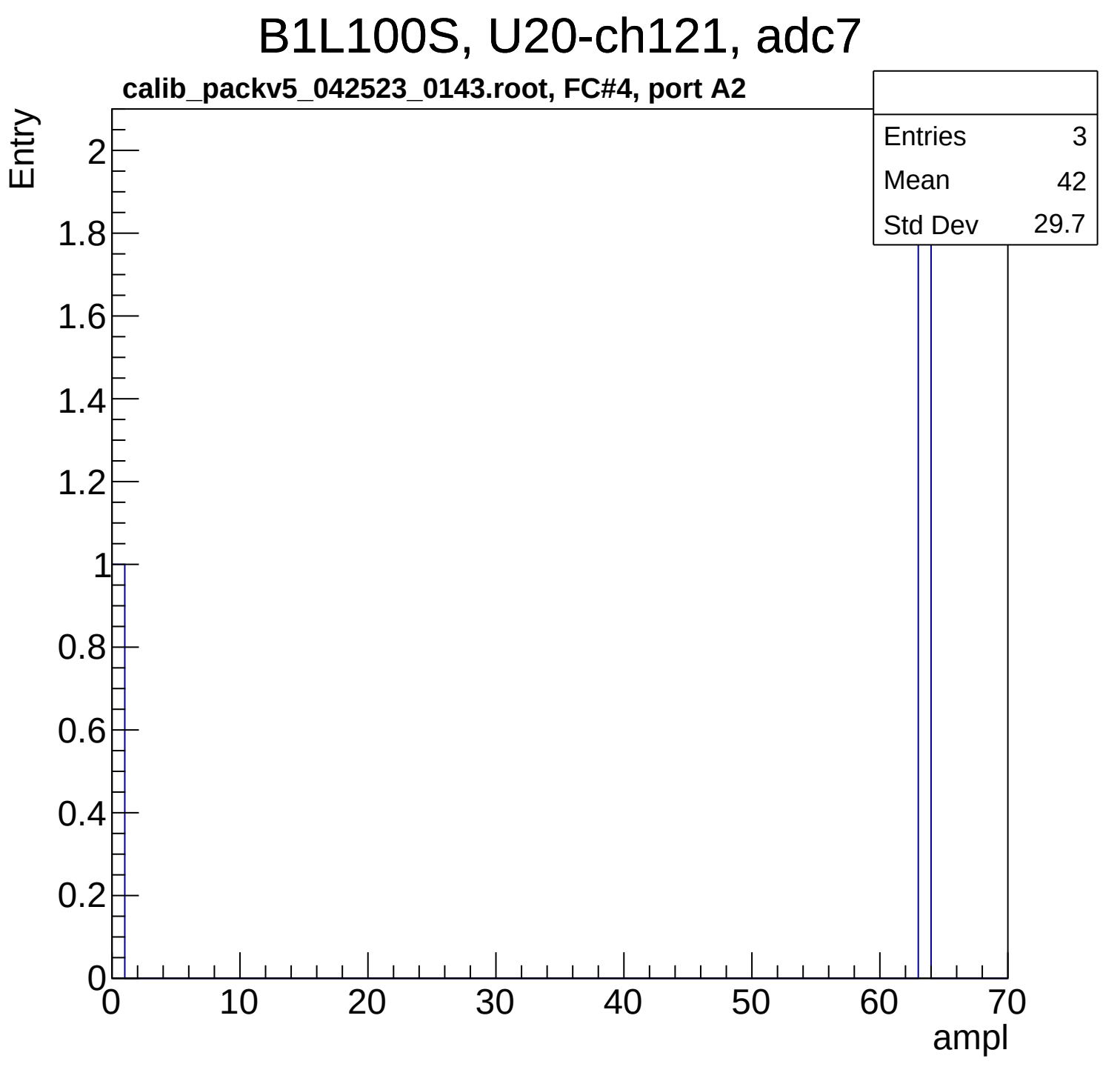
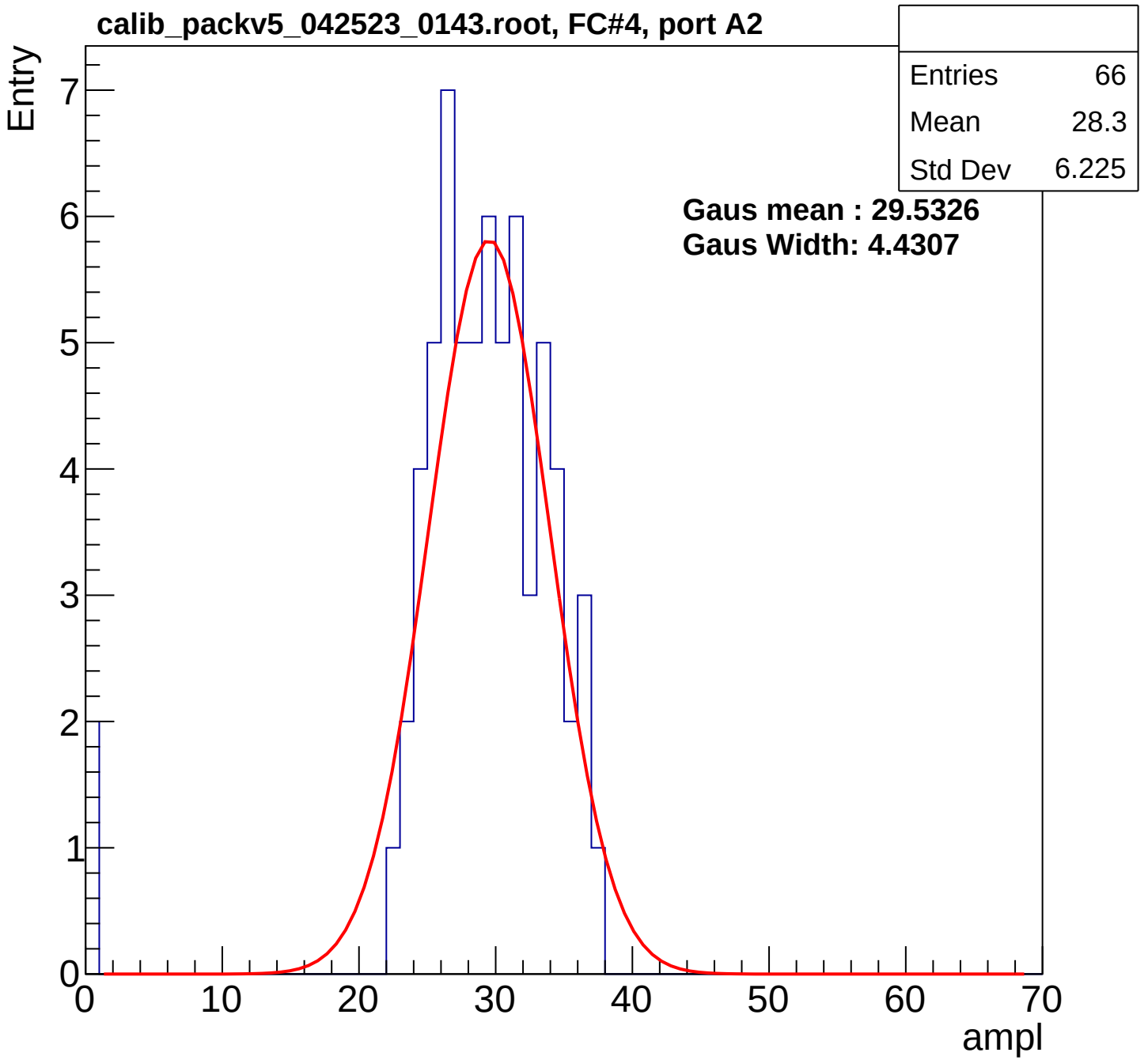

B1L100S, U20-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.43
Std Dev 1.178
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 28.3
Std Dev 6.225
Gaus mean : 29.5326
Gaus Width: 4.4307
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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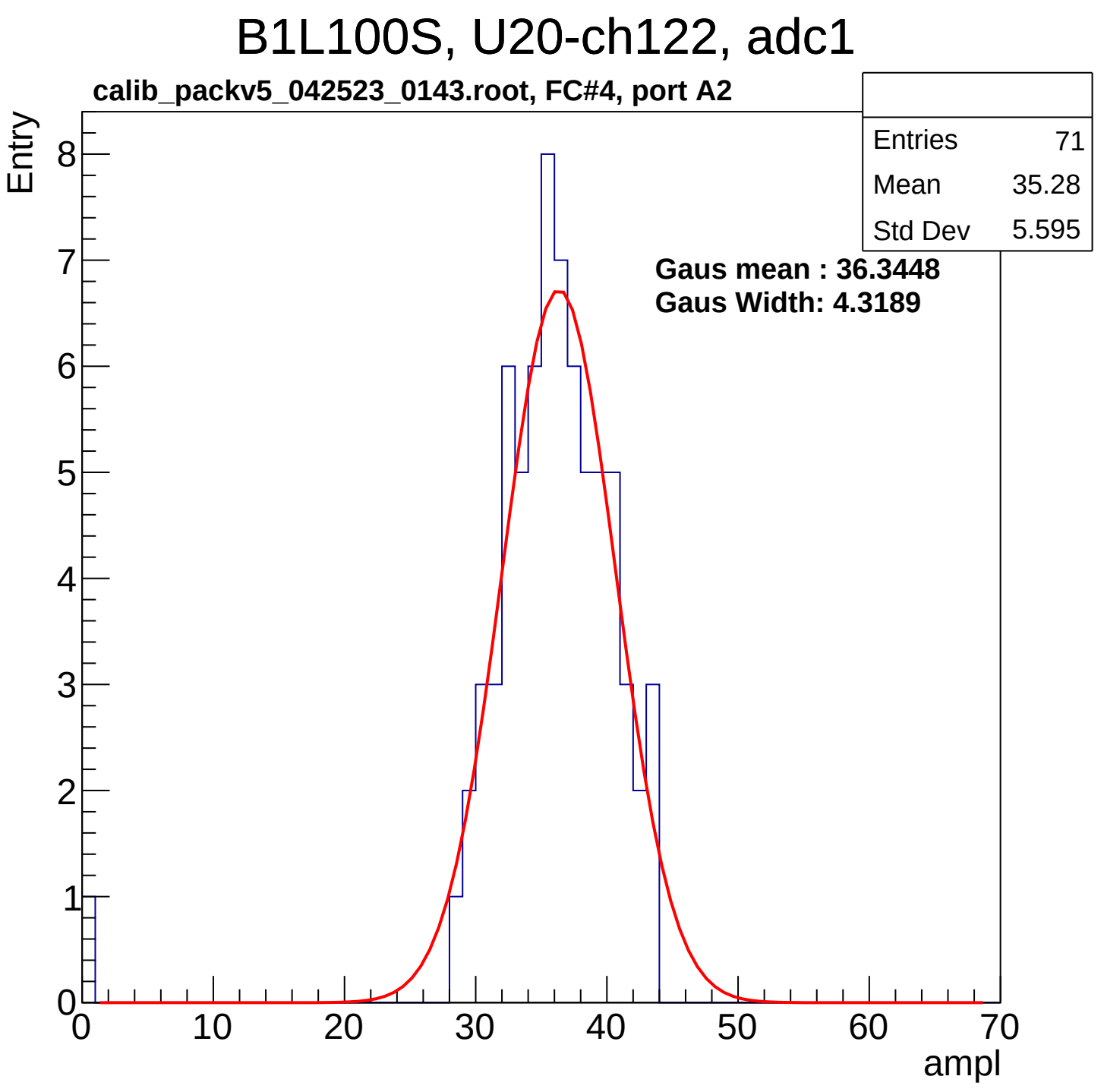
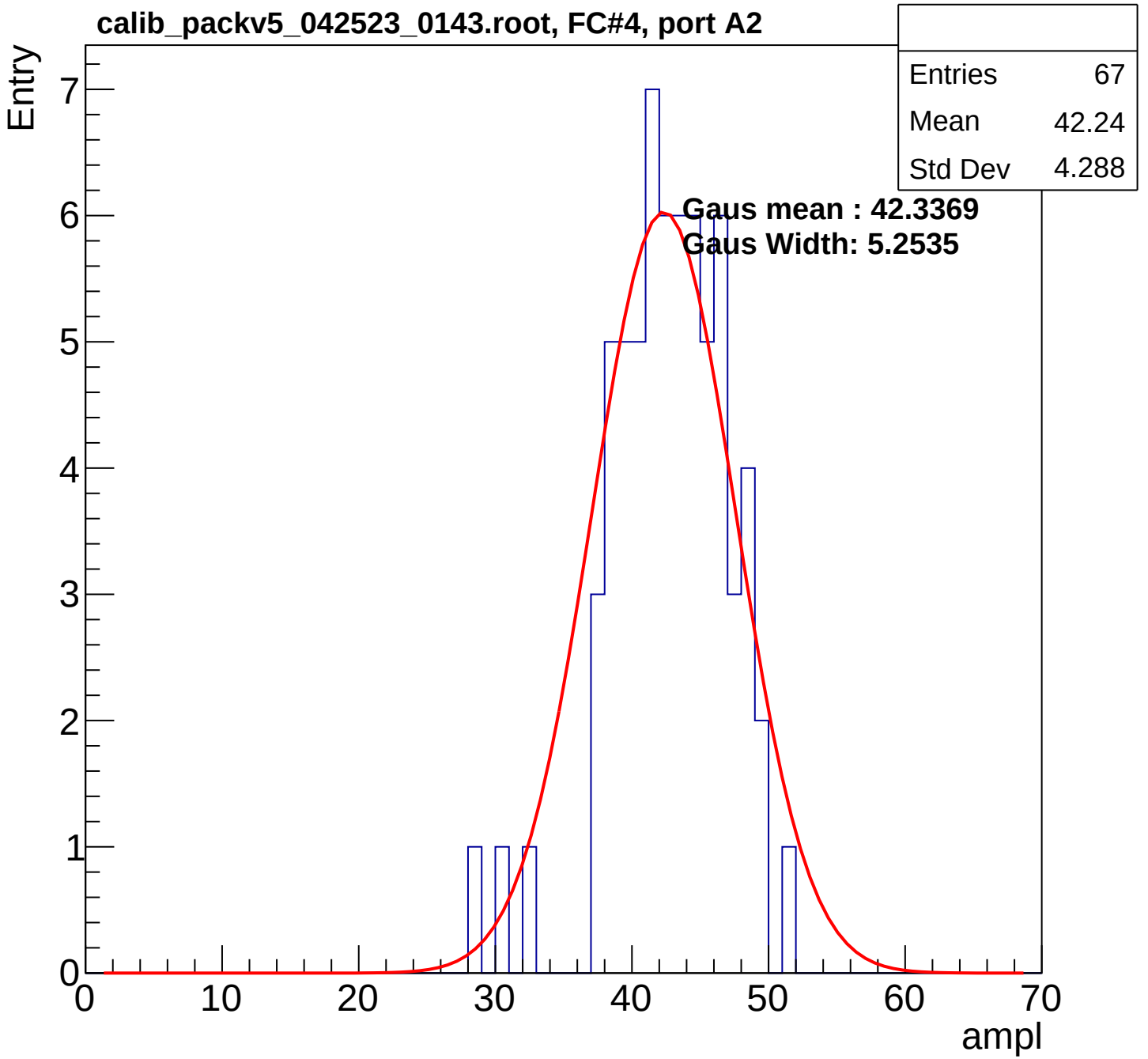
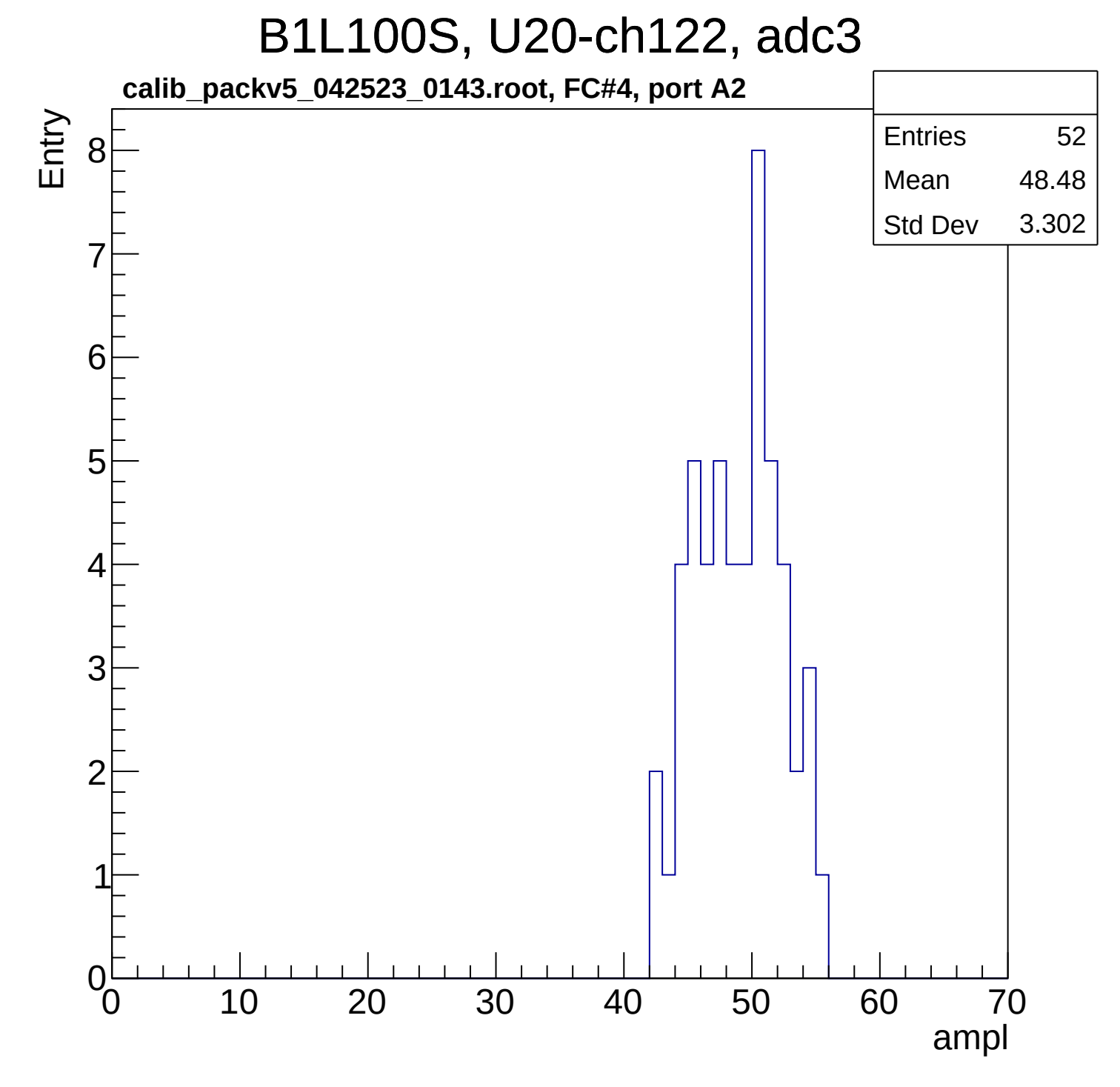
40
50
60
70


B1L100S, U20-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 35.28
Std Dev 5.595
Gaus mean : 36.3448
Gaus Width: 4.3189
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 42.24
Std Dev 4.288
Gaus mean : 42.3369
Gaus Width: 5.2535
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 48.48
Std Dev 3.302
Entry
ampl
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1
2
3
4
5
6
7
8
0
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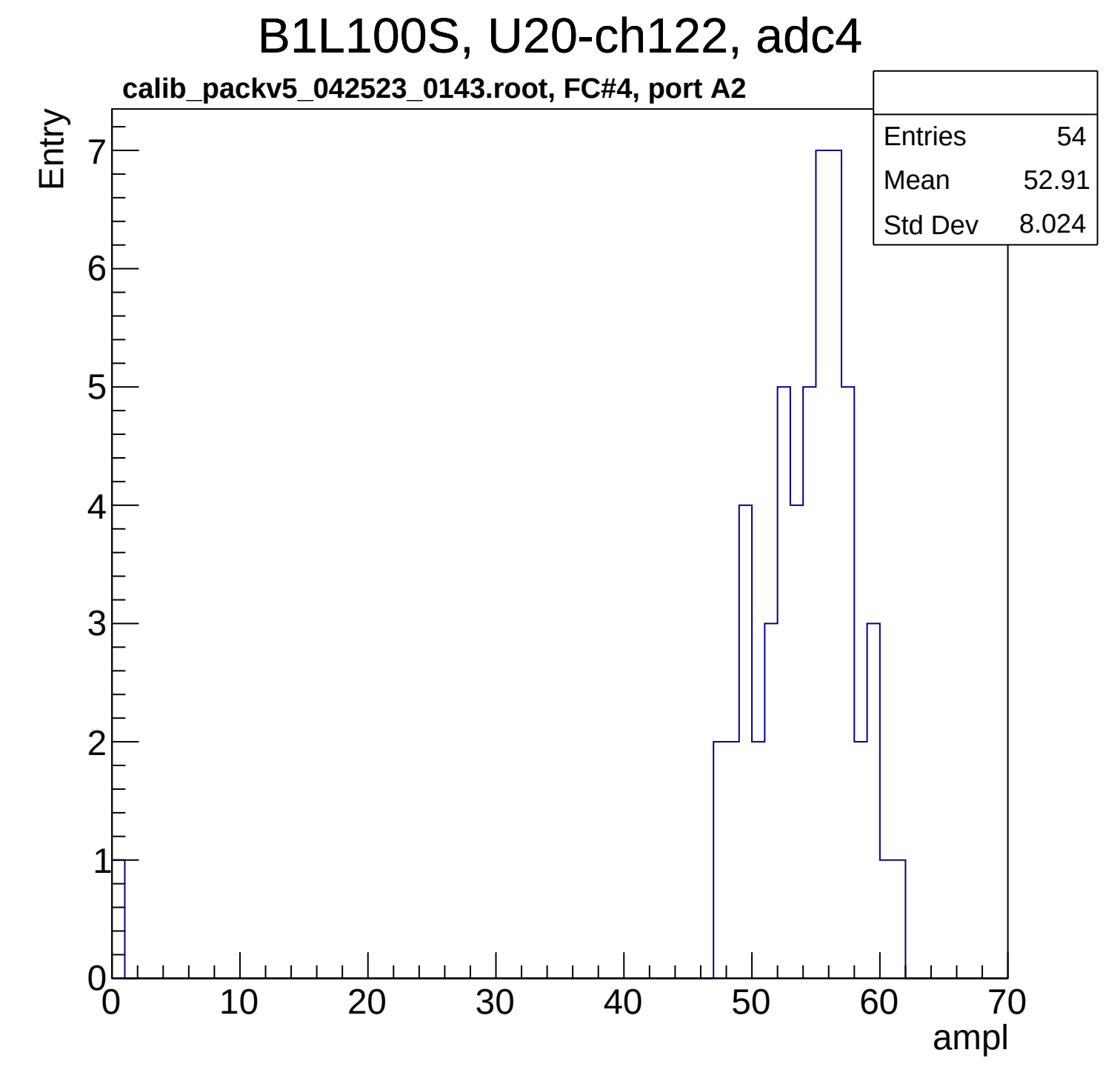
40
50
60
70


B1L100S, U20-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 52.91
Std Dev 8.024
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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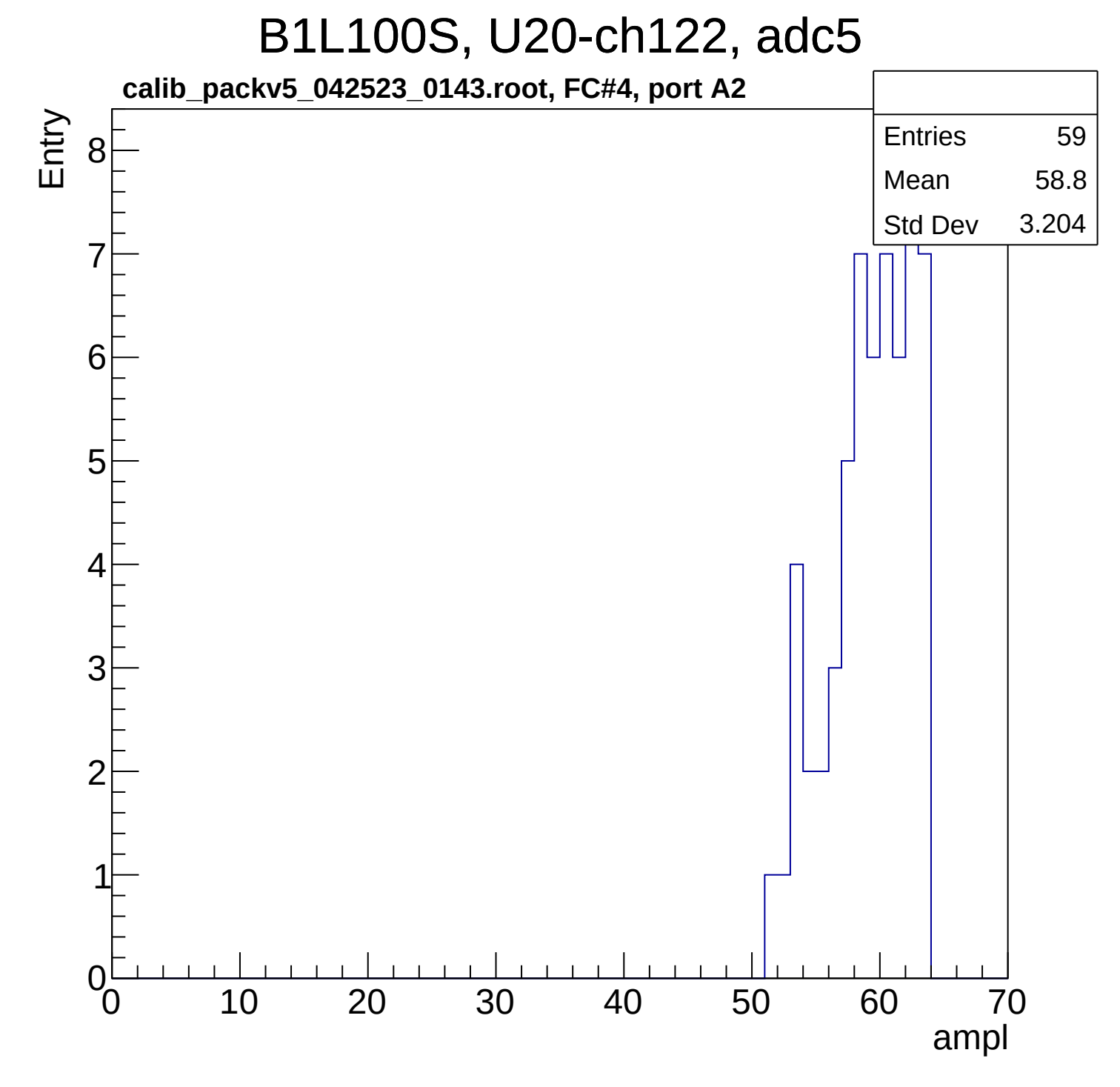
40
50
60
70


B1L100S, U20-ch122, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.8
Std Dev 3.204
Entry
ampl
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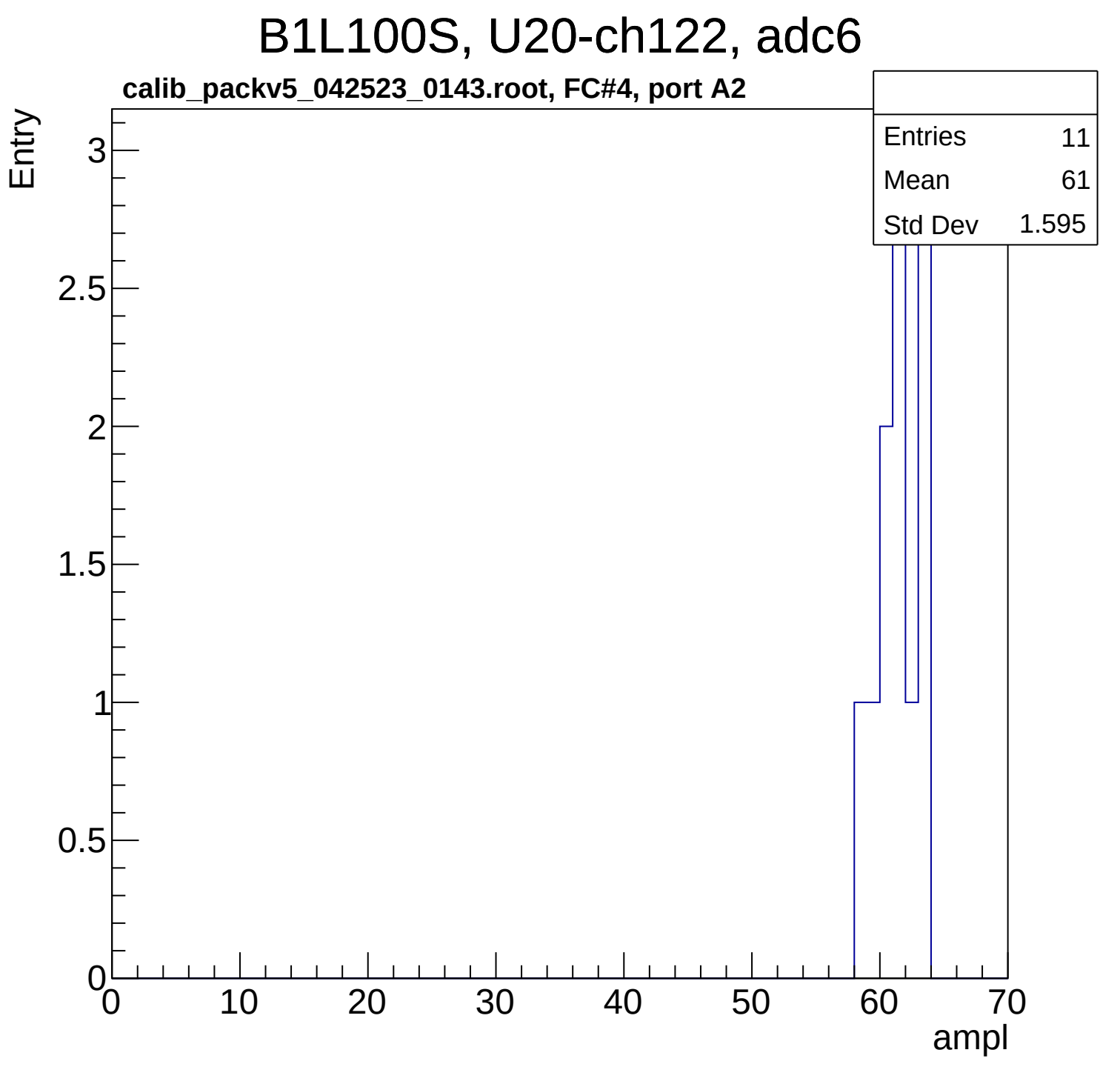
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch122, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61
Std Dev 1.595
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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40
50
60
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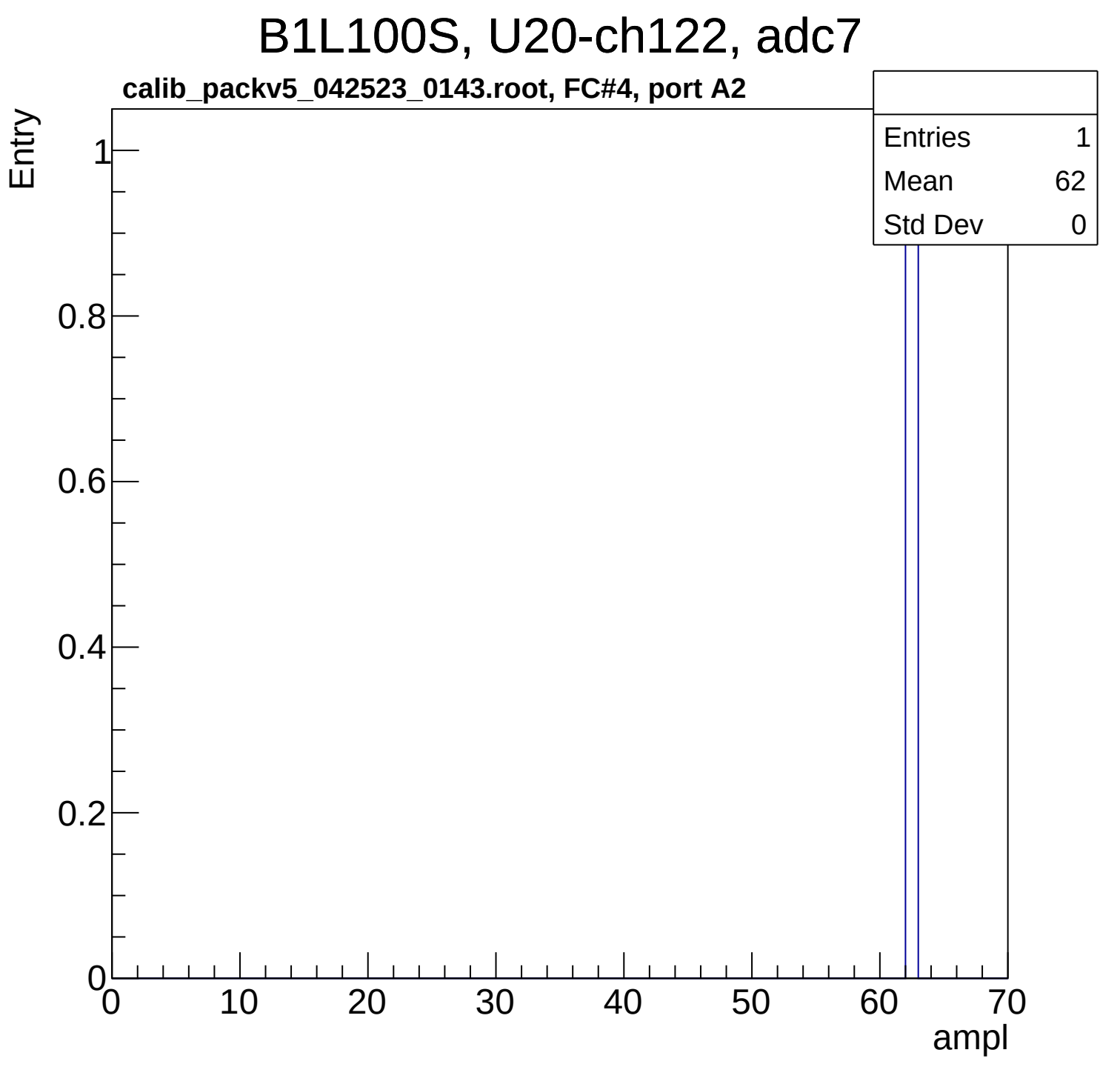
B1L100S, U20-ch122, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
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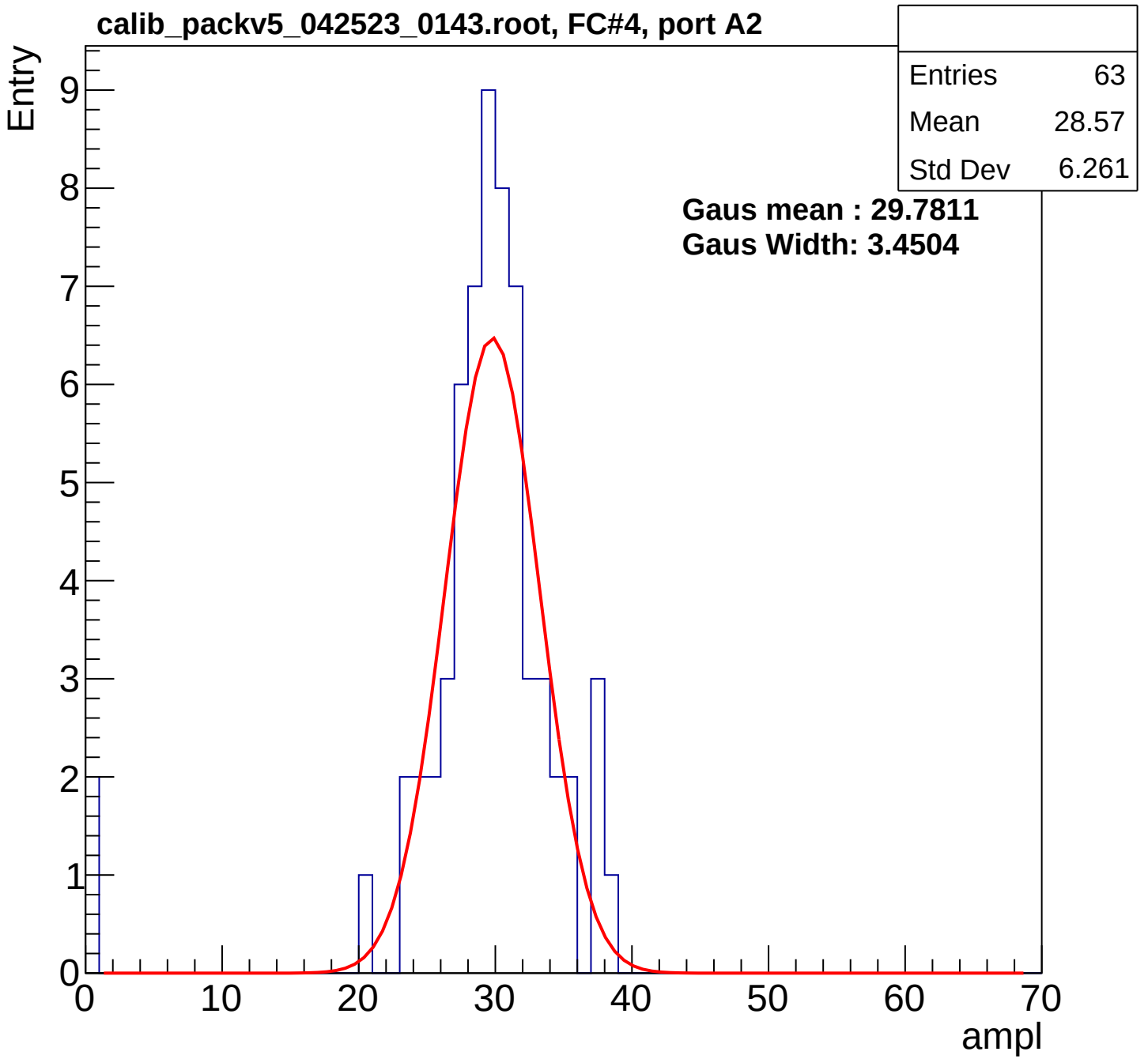
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.57
Std Dev 6.261
Gaus mean : 29.7811
Gaus Width: 3.4504
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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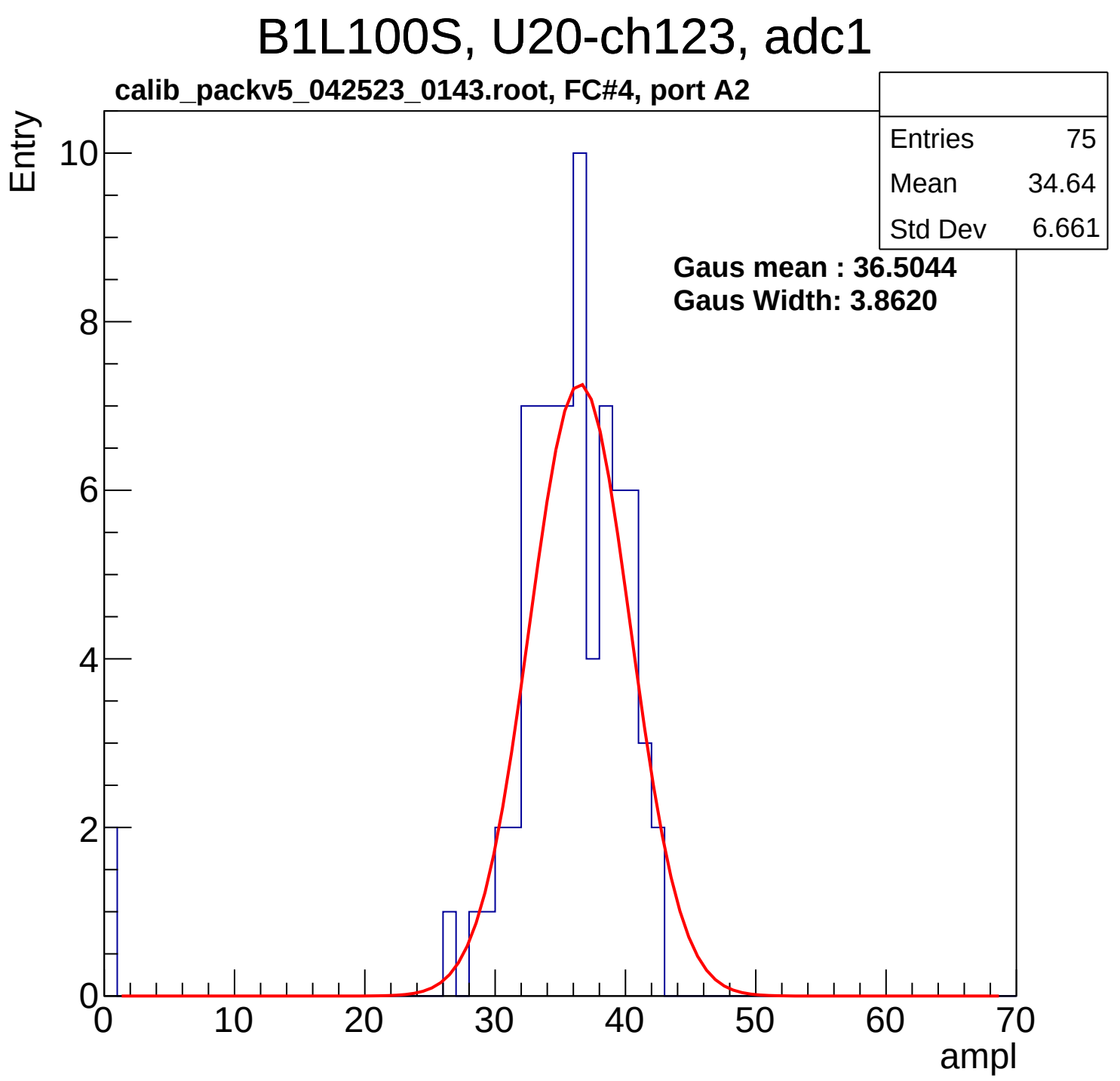
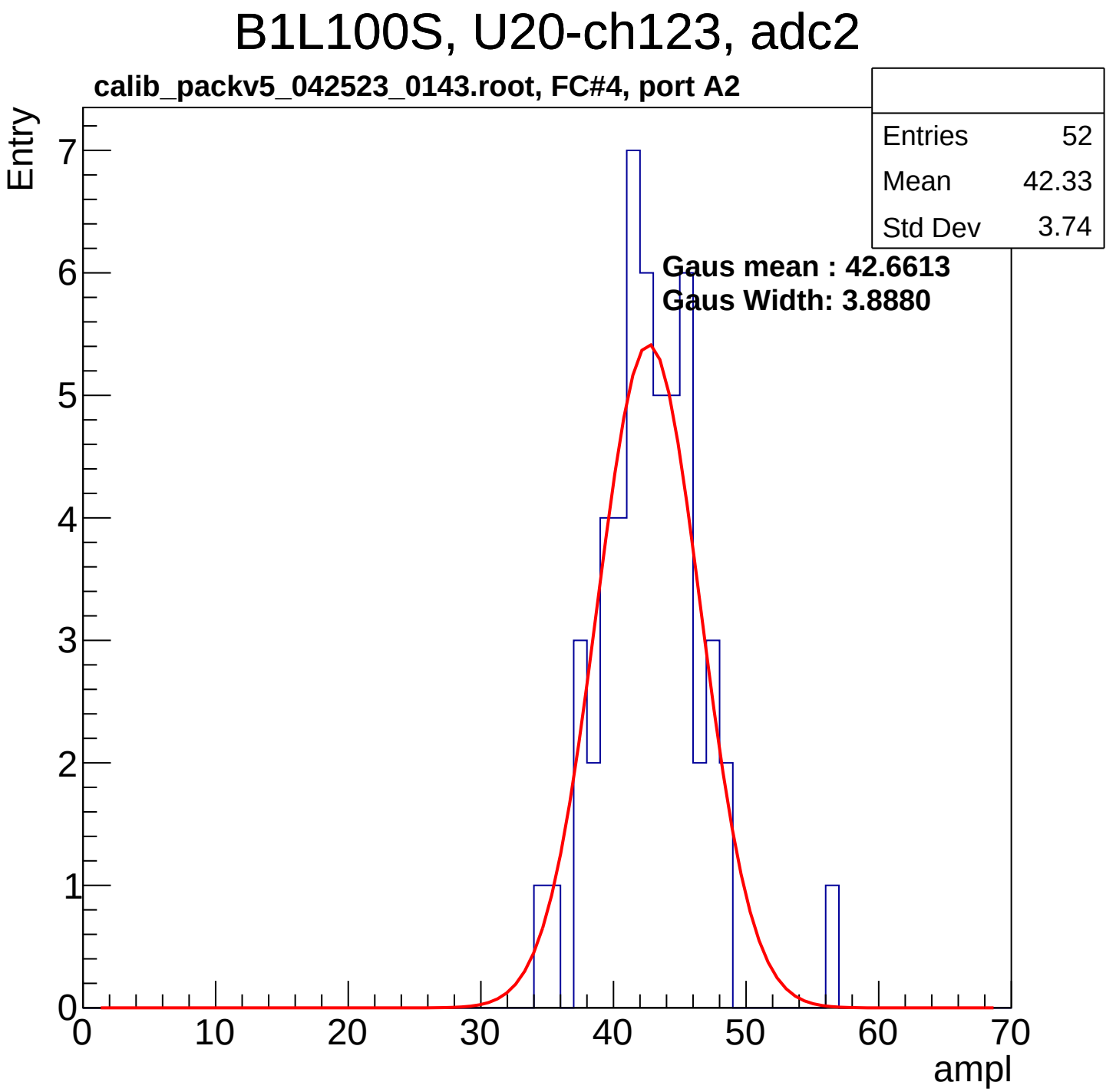

B1L100S, U20-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 34.64
Std Dev 6.661
Gaus mean : 36.5044
Gaus Width: 3.8620
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 42.33
Std Dev 3.74
Gaus mean : 42.6613
Gaus Width: 3.8880
Entry
ampl
0
1
2
3
4
5
6
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0
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60
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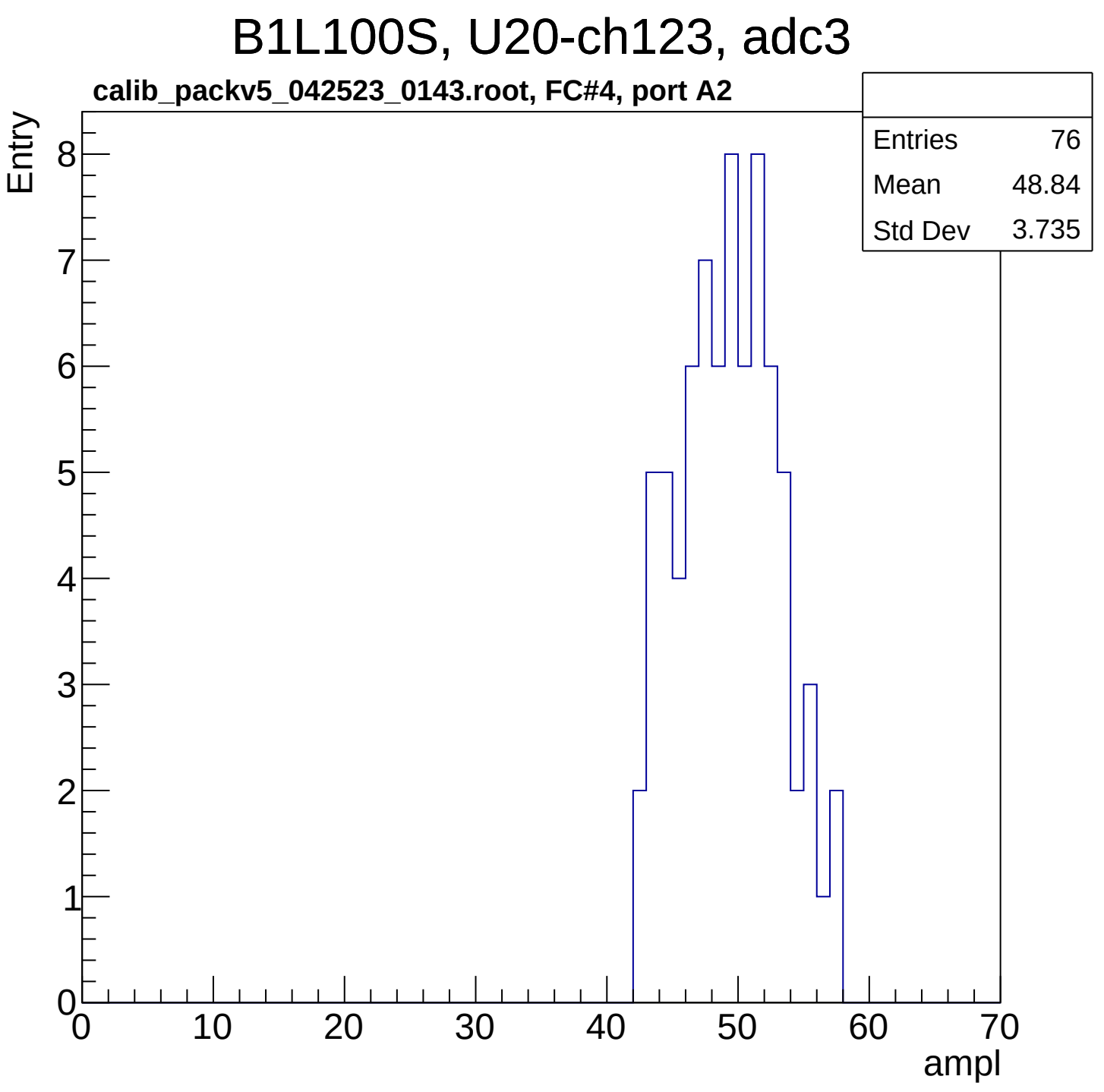
B1L100S, U20-ch123, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 48.84
Std Dev 3.735
Entry
ampl
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3
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5
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7
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0
10
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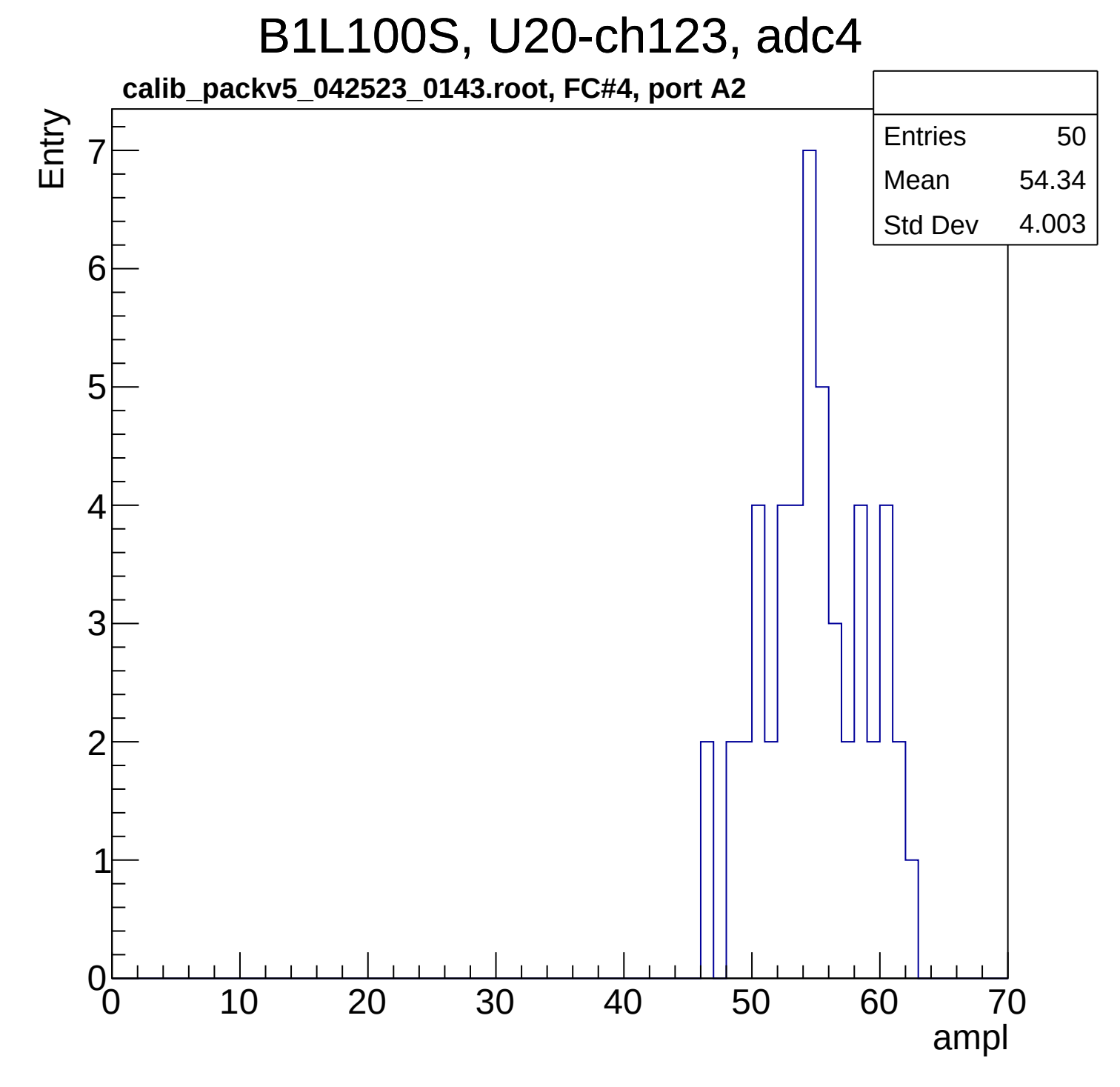
40
50
60
70


B1L100S, U20-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 54.34
Std Dev 4.003
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
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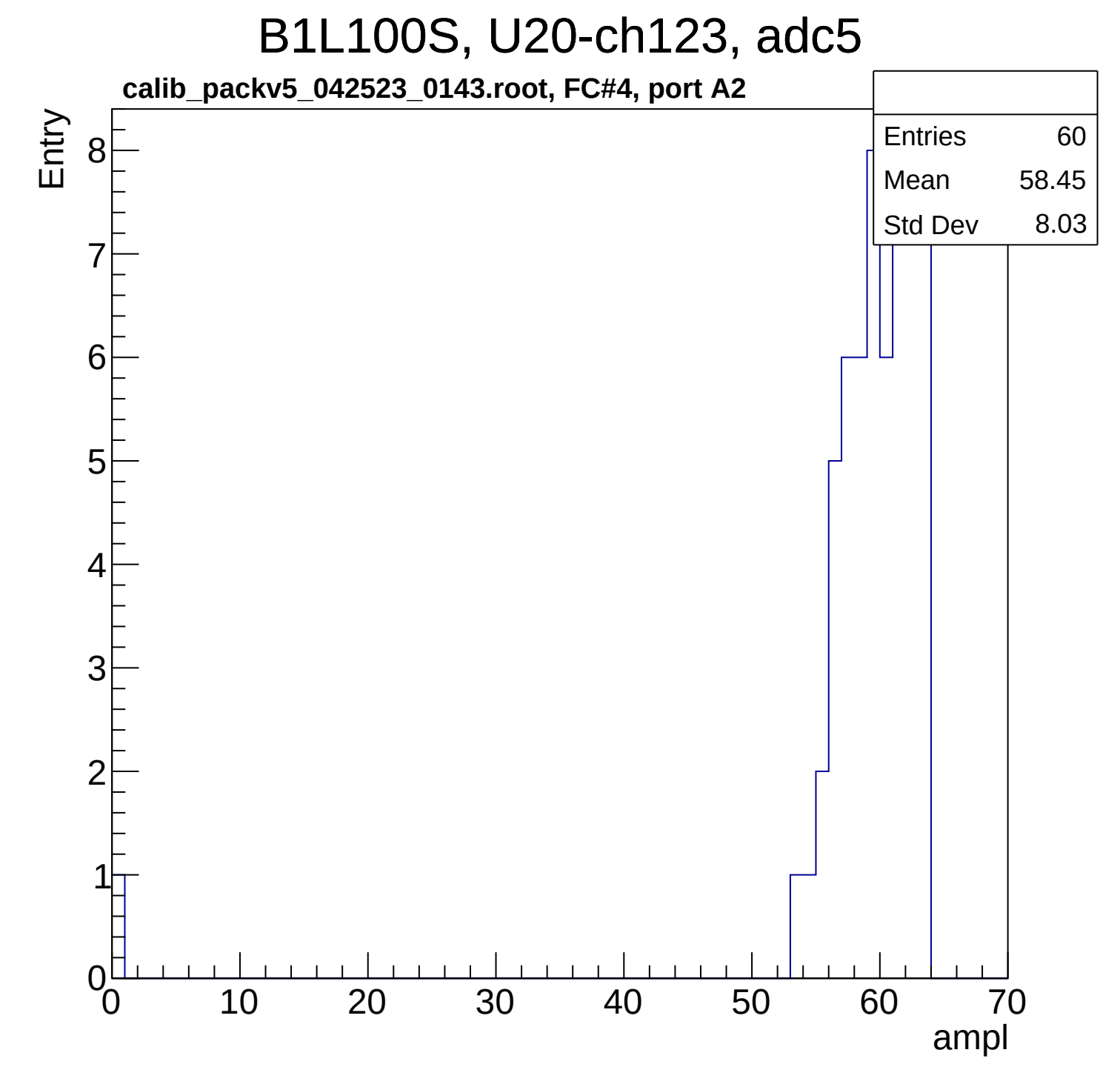
B1L100S, U20-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 58.45
Std Dev 8.03
Entry
ampl
0
1
2
3
4
5
6
7
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0
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20
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40
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60
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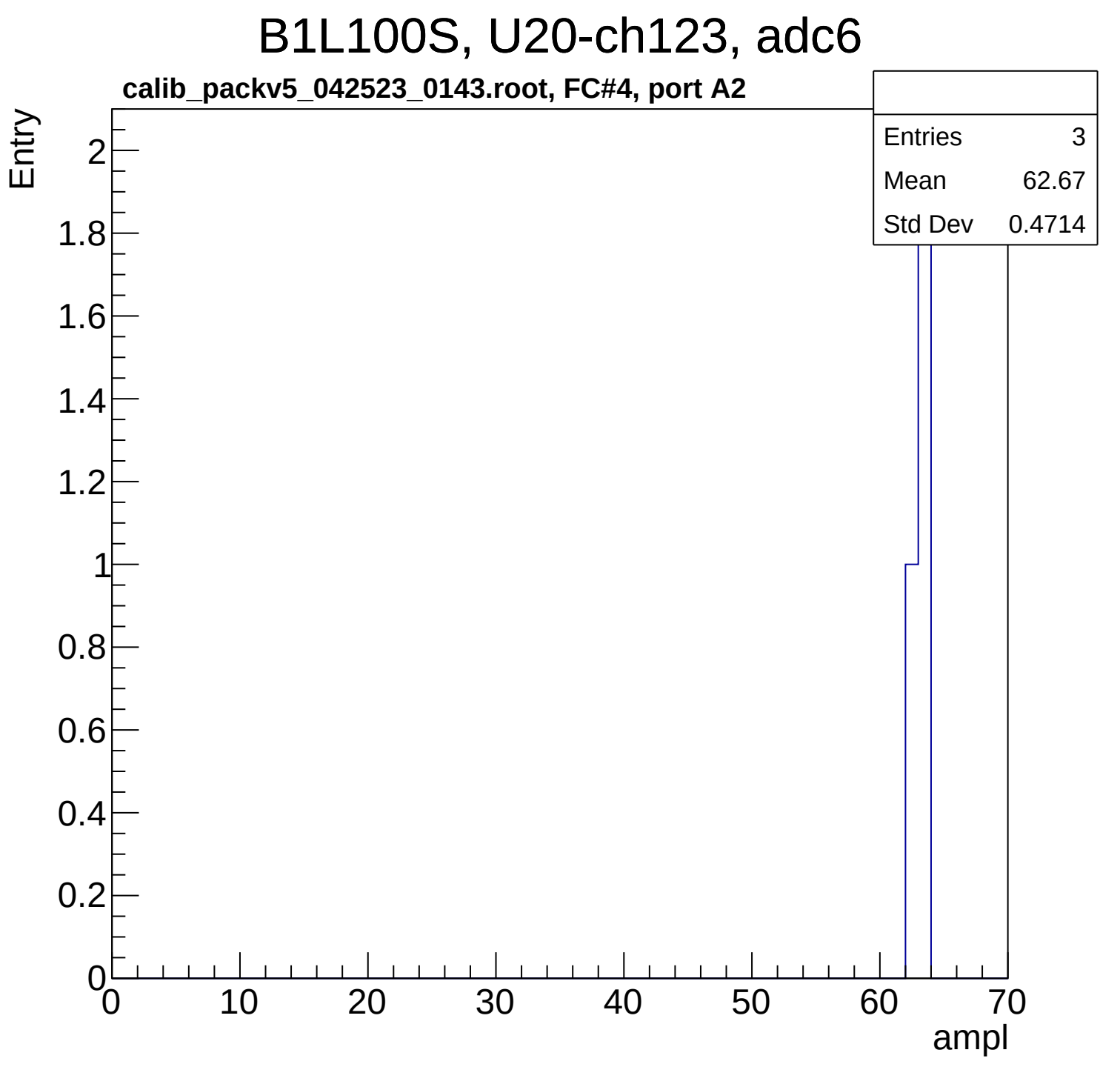
B1L100S, U20-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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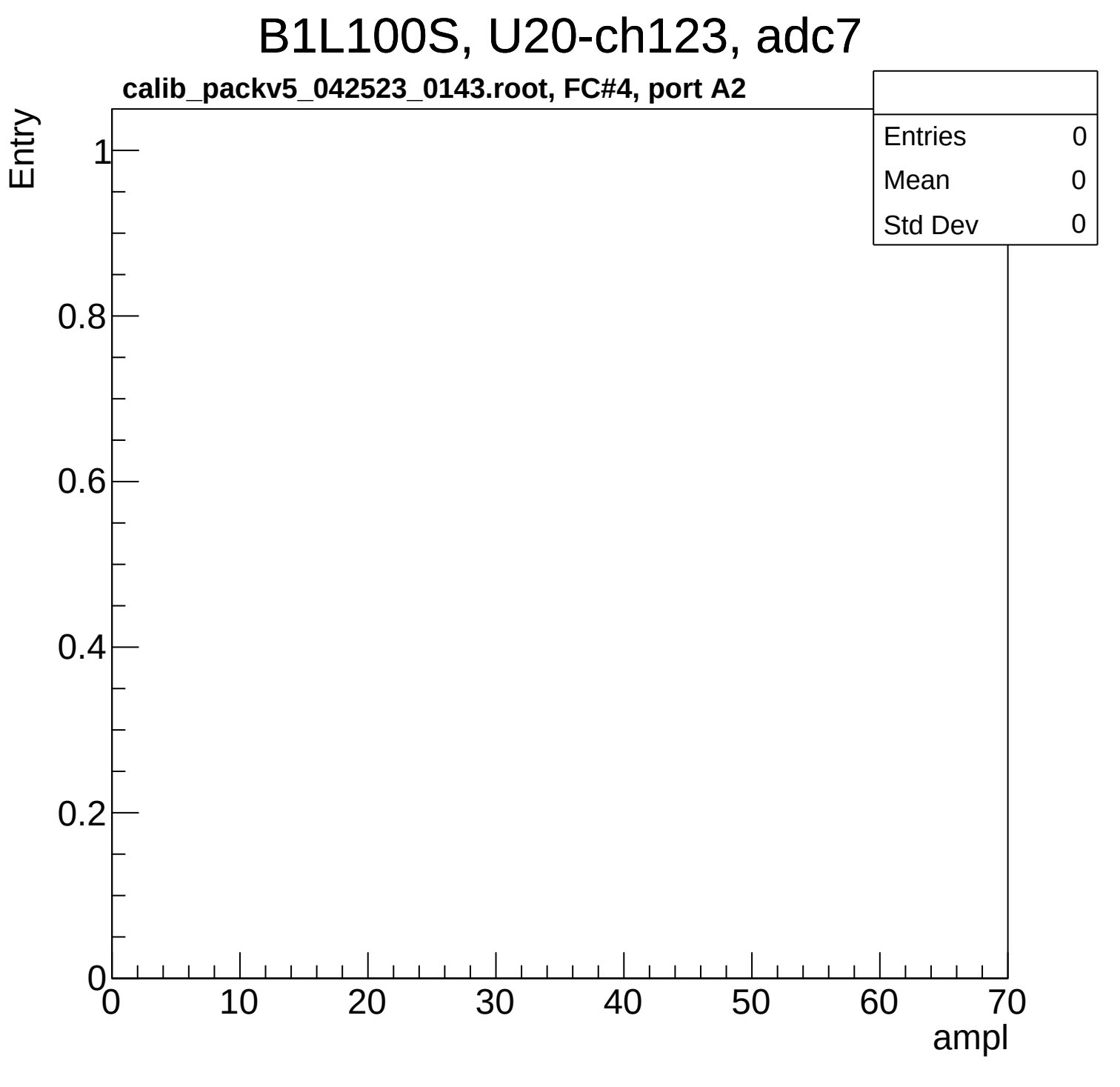
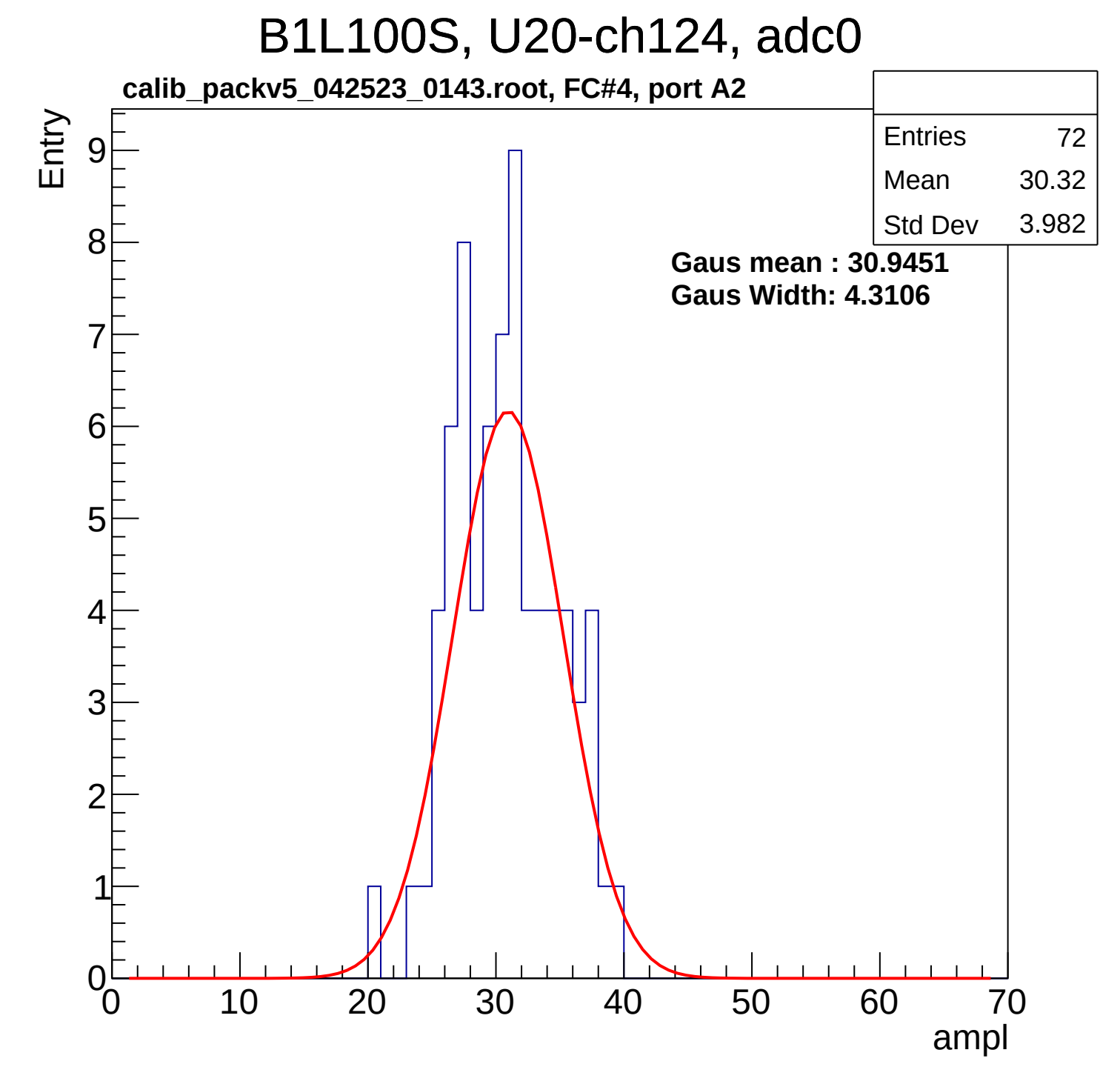

B1L100S, U20-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.32
Std Dev 3.982
Gaus mean : 30.9451
Gaus Width: 4.3106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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30
40
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60
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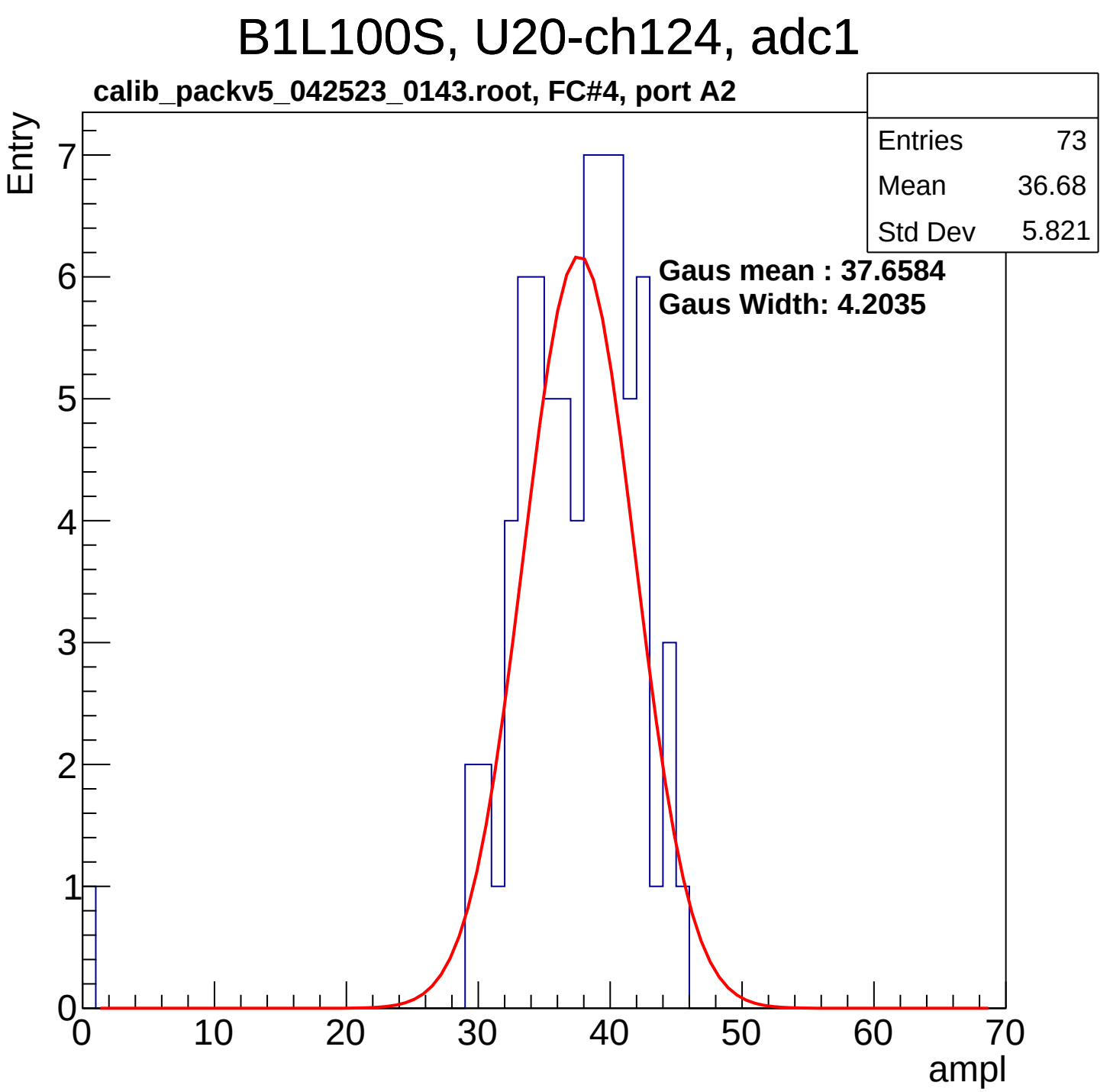
B1L100S, U20-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.68
Std Dev 5.821
Gaus mean : 37.6584
Gaus Width: 4.2035
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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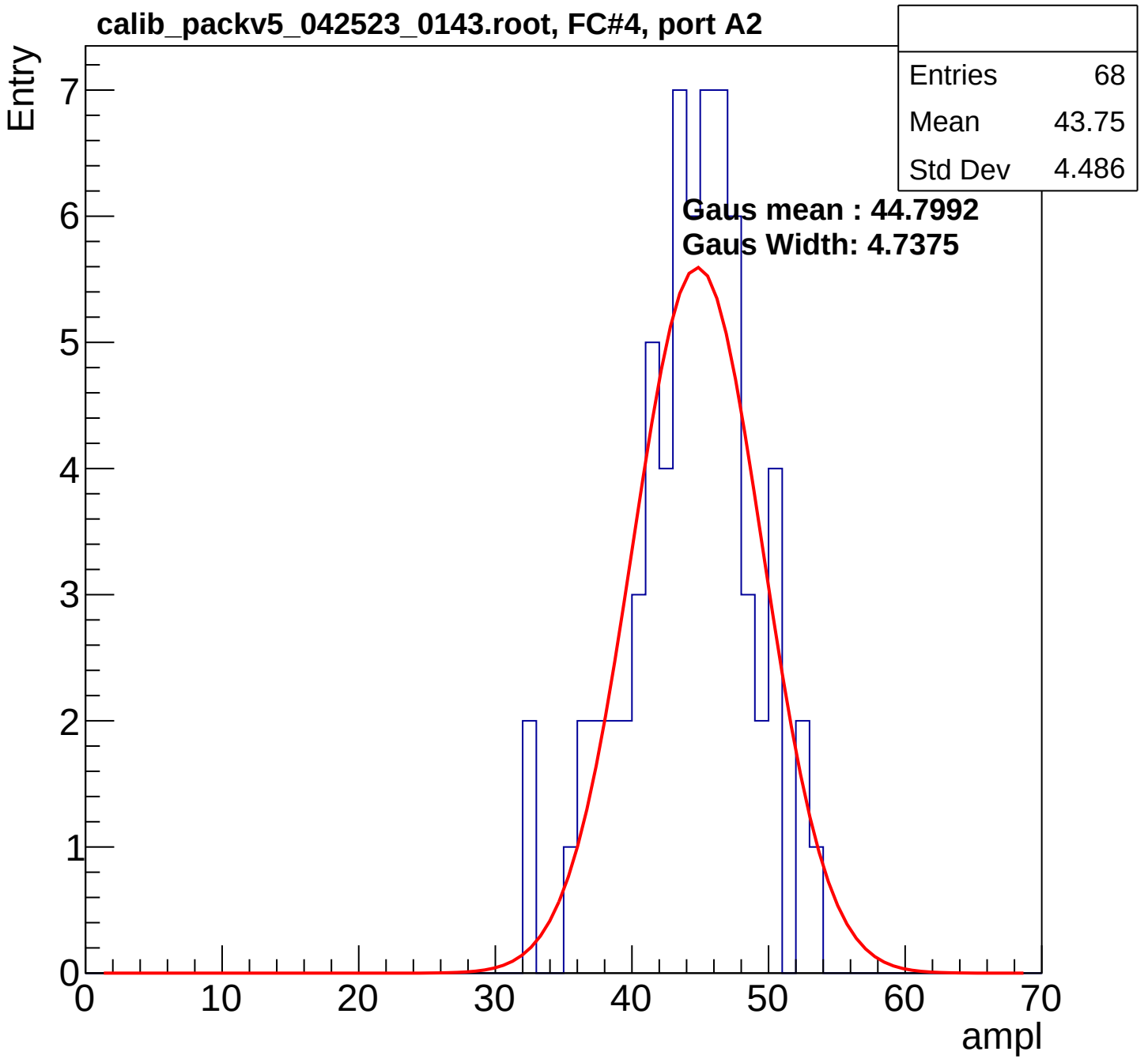
B1L100S, U20-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.75
Std Dev 4.486
Gaus mean : 44.7992
Gaus Width: 4.7375
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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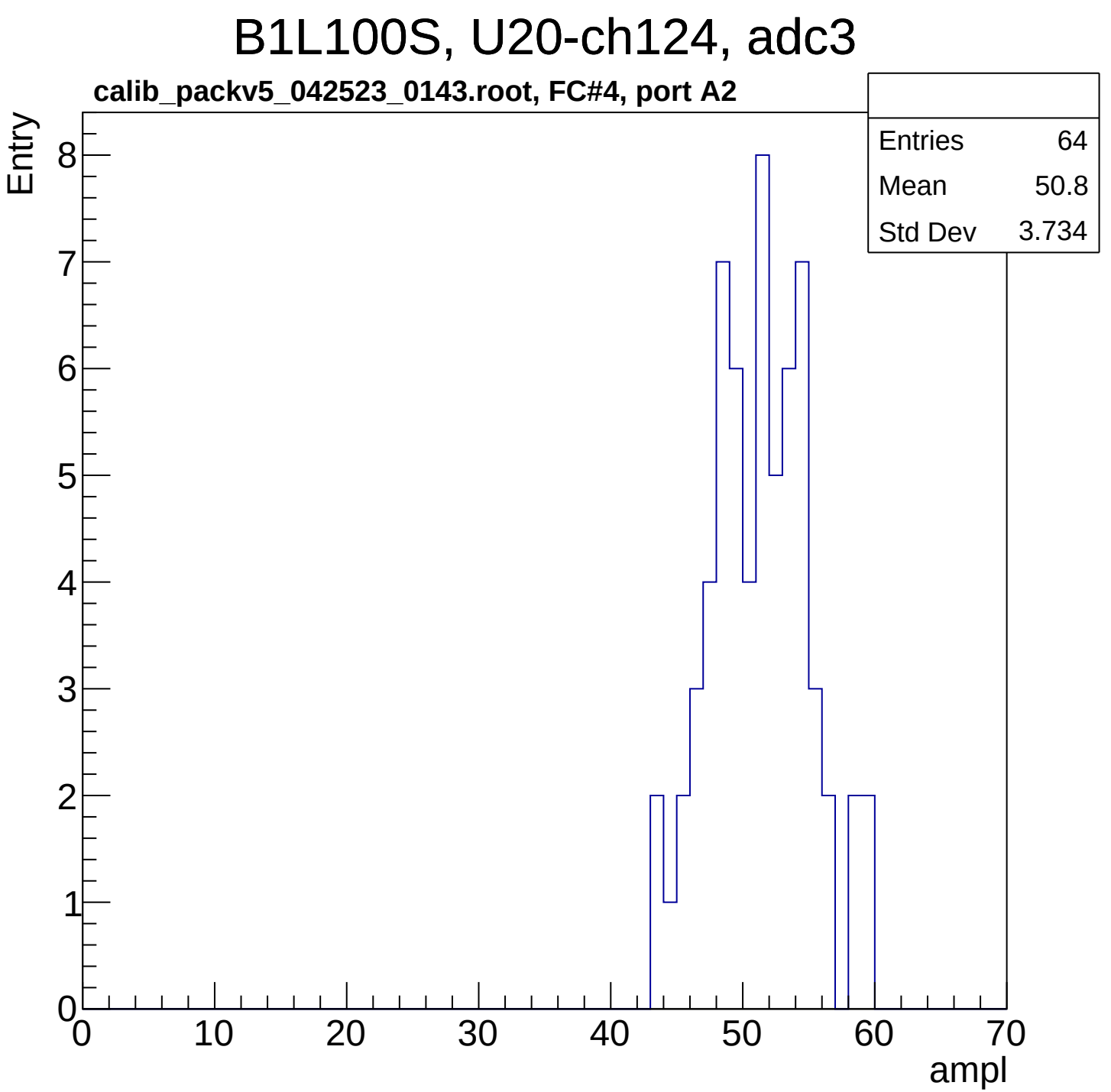
B1L100S, U20-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.8
Std Dev 3.734
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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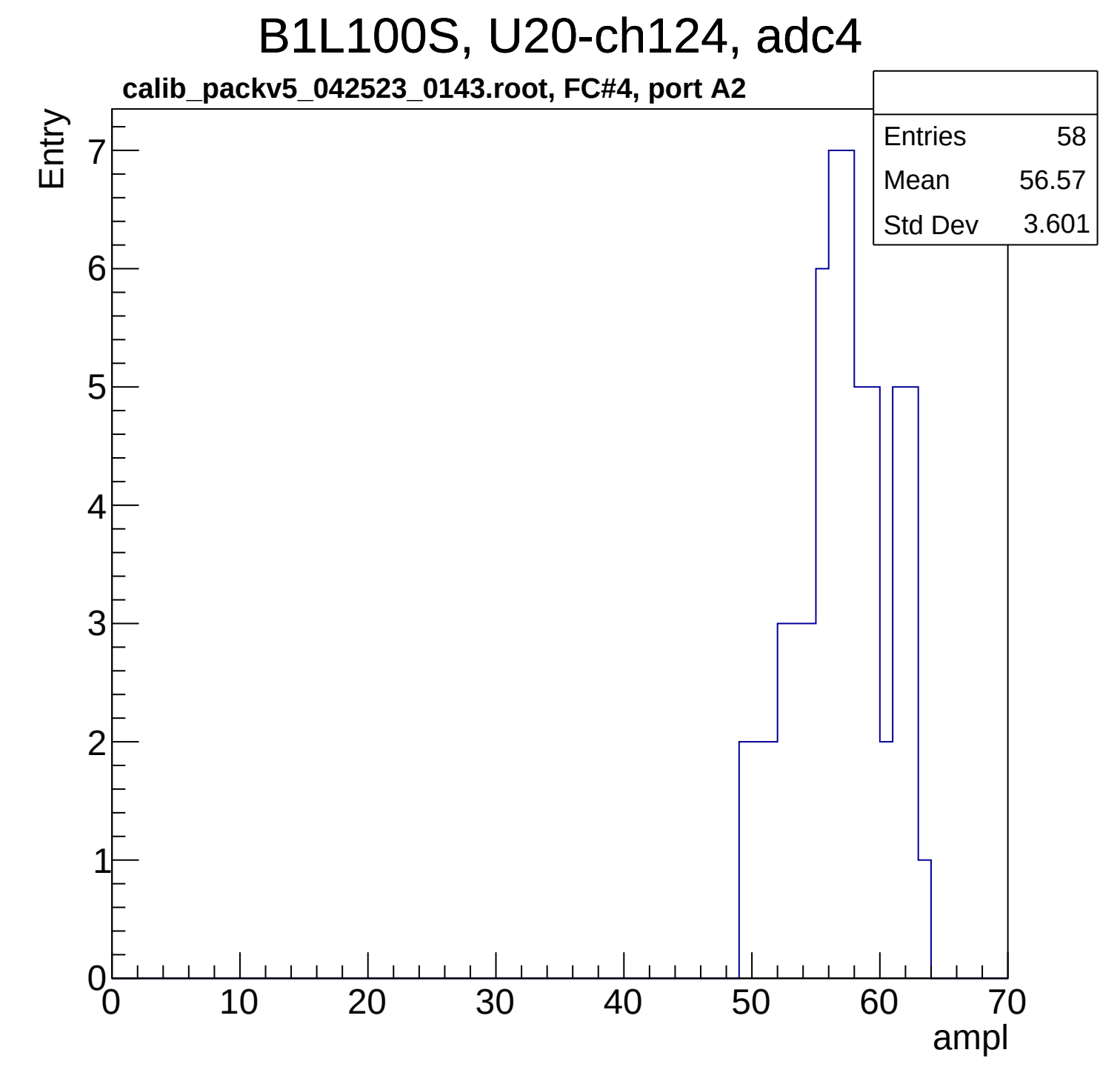
B1L100S, U20-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.57
Std Dev 3.601
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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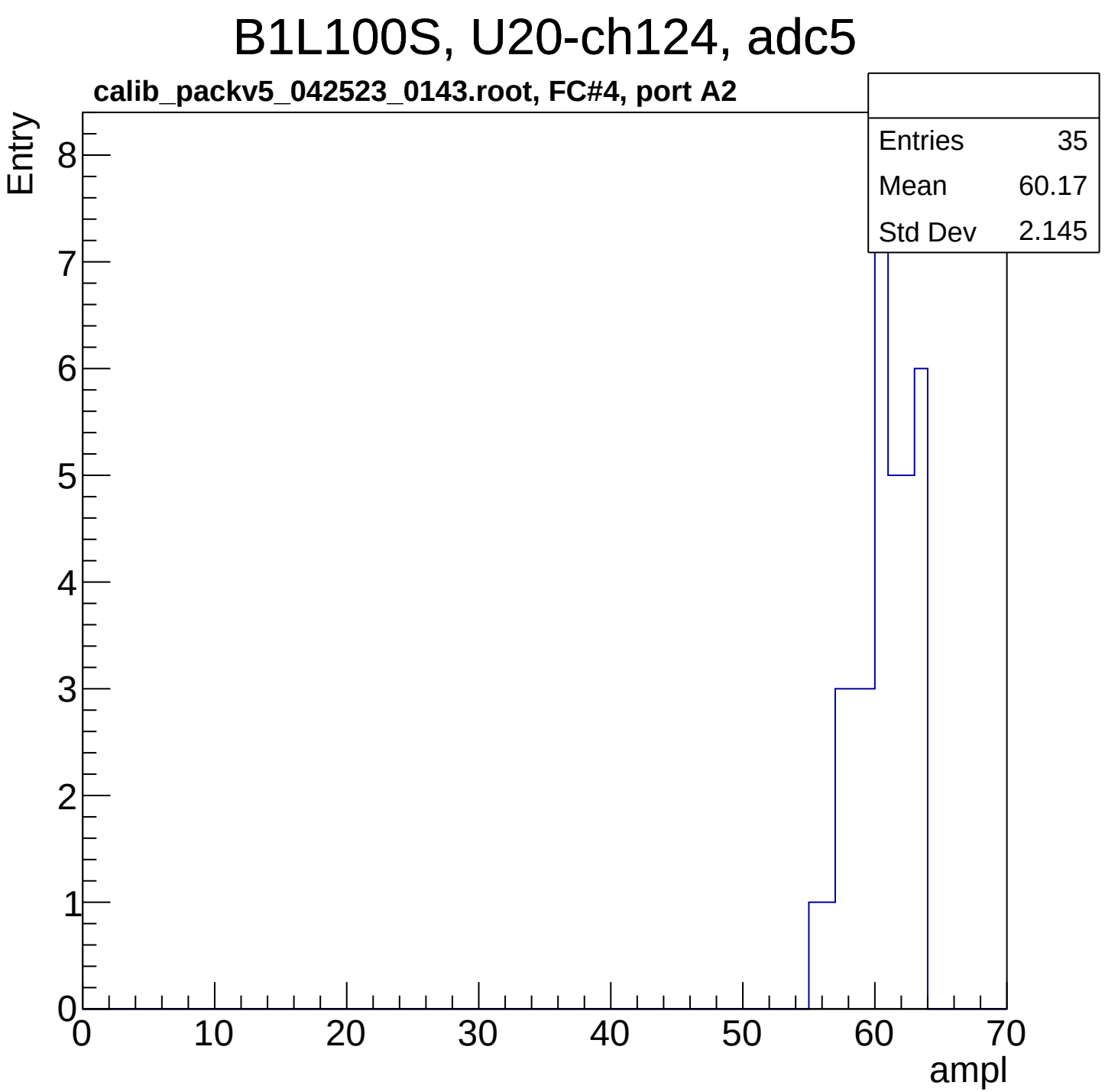
B1L100S, U20-ch124, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 60.17
Std Dev 2.145
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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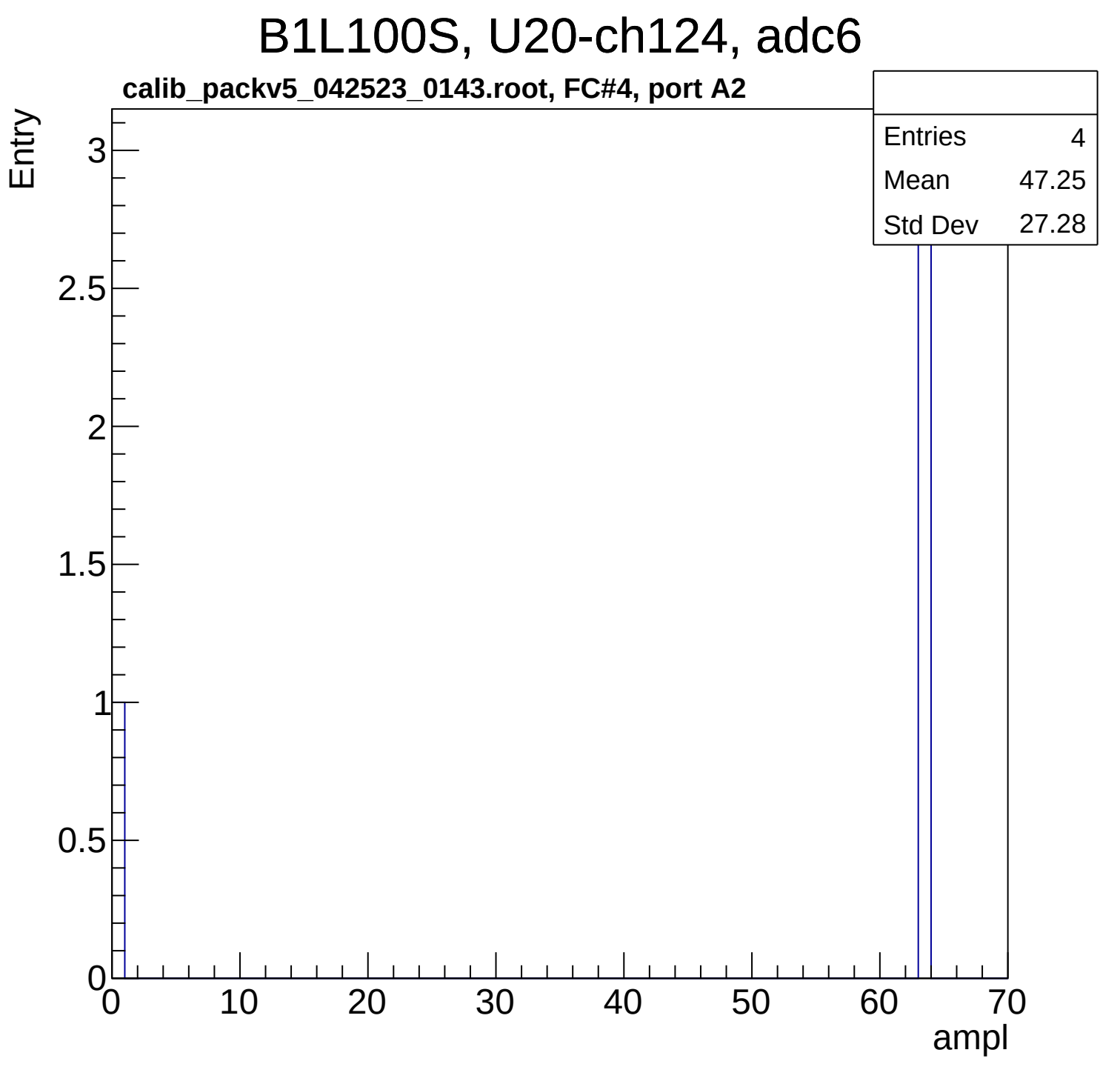
B1L100S, U20-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 47.25
Std Dev 27.28
Entry
ampl
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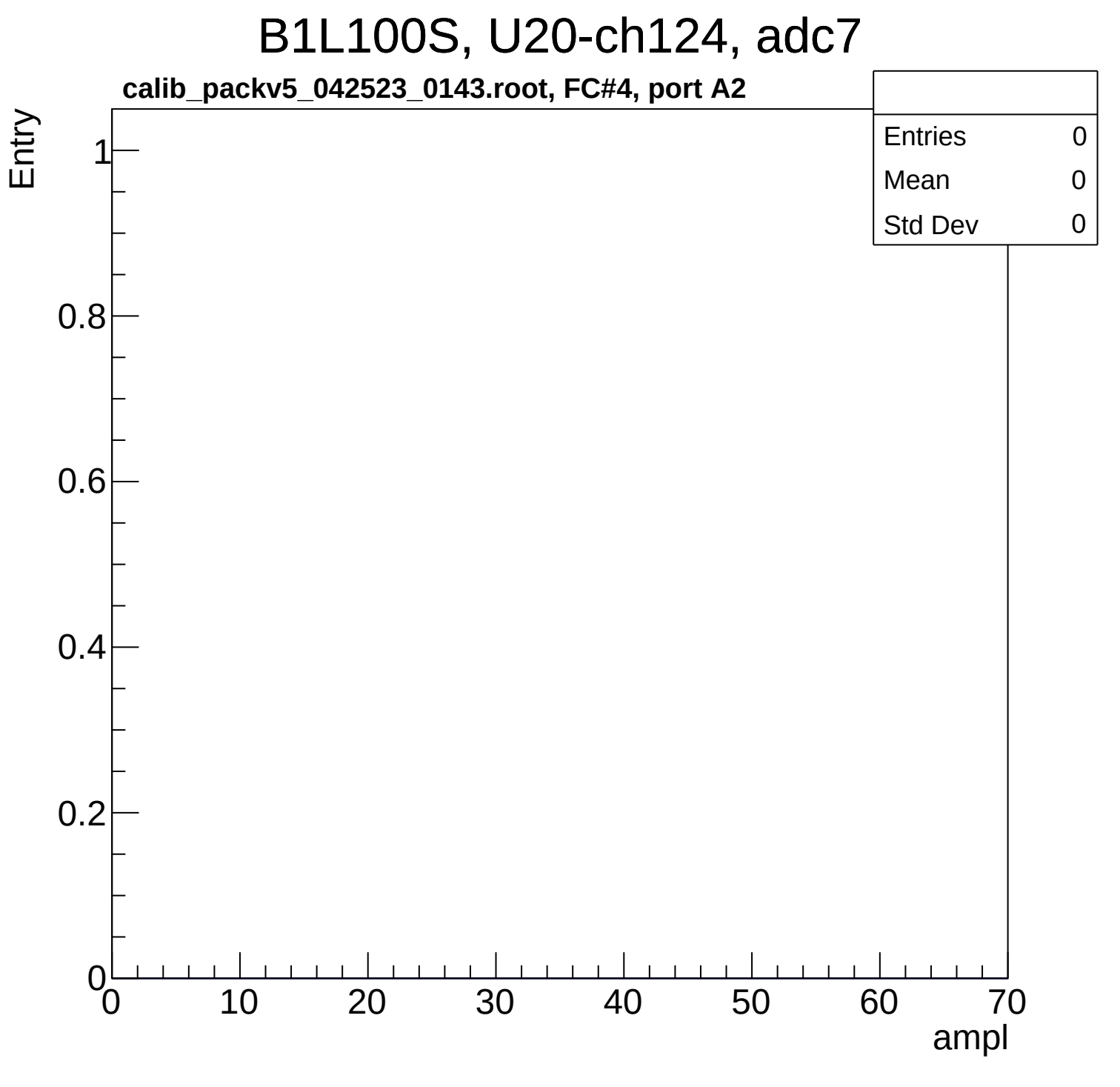
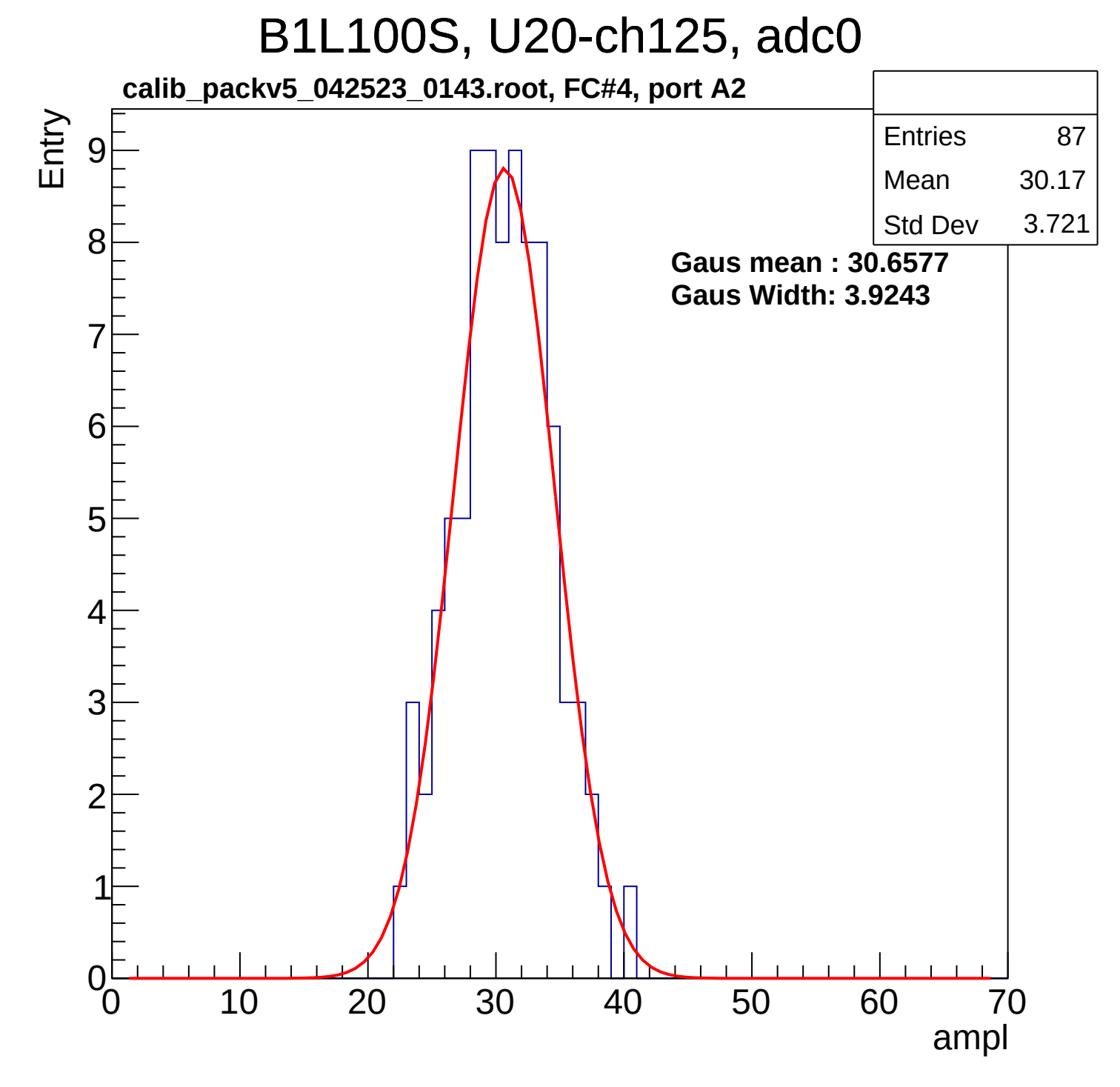
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 30.17
Std Dev 3.721
Gaus mean : 30.6577
Gaus Width: 3.9243
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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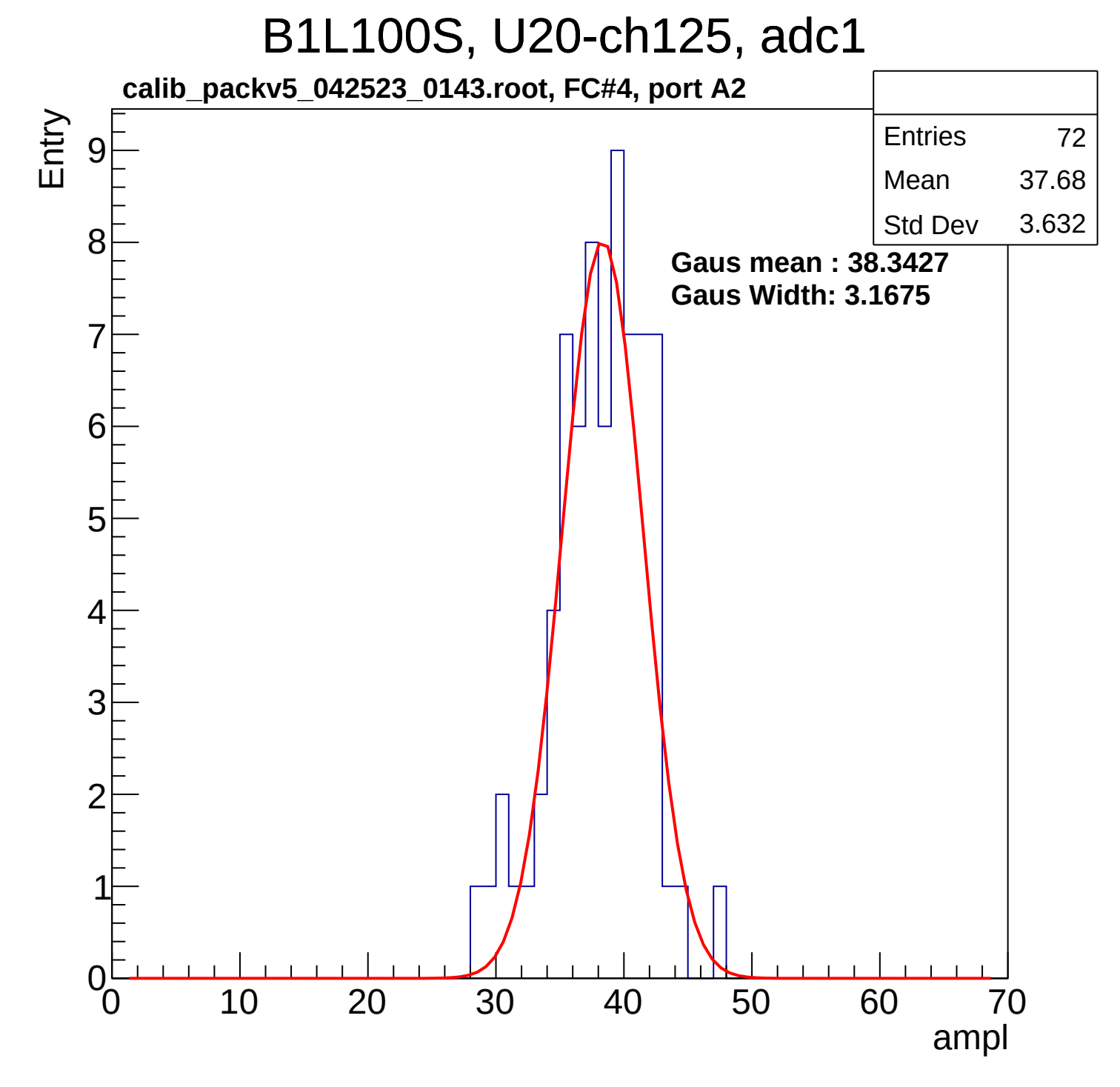
B1L100S, U20-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.68
Std Dev 3.632
Gaus mean : 38.3427
Gaus Width: 3.1675
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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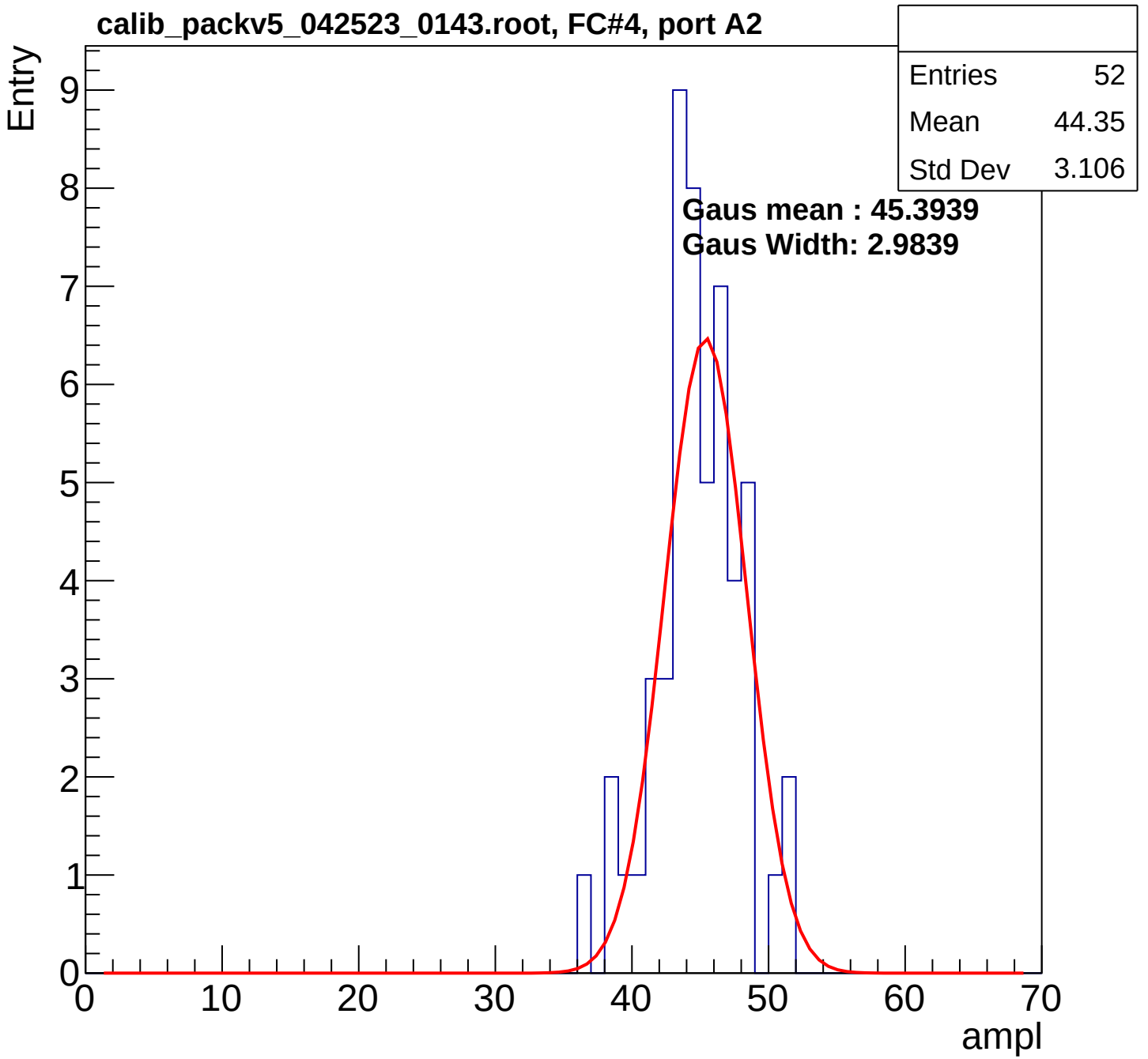
B1L100S, U20-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 44.35
Std Dev 3.106
Gaus mean : 45.3939
Gaus Width: 2.9839
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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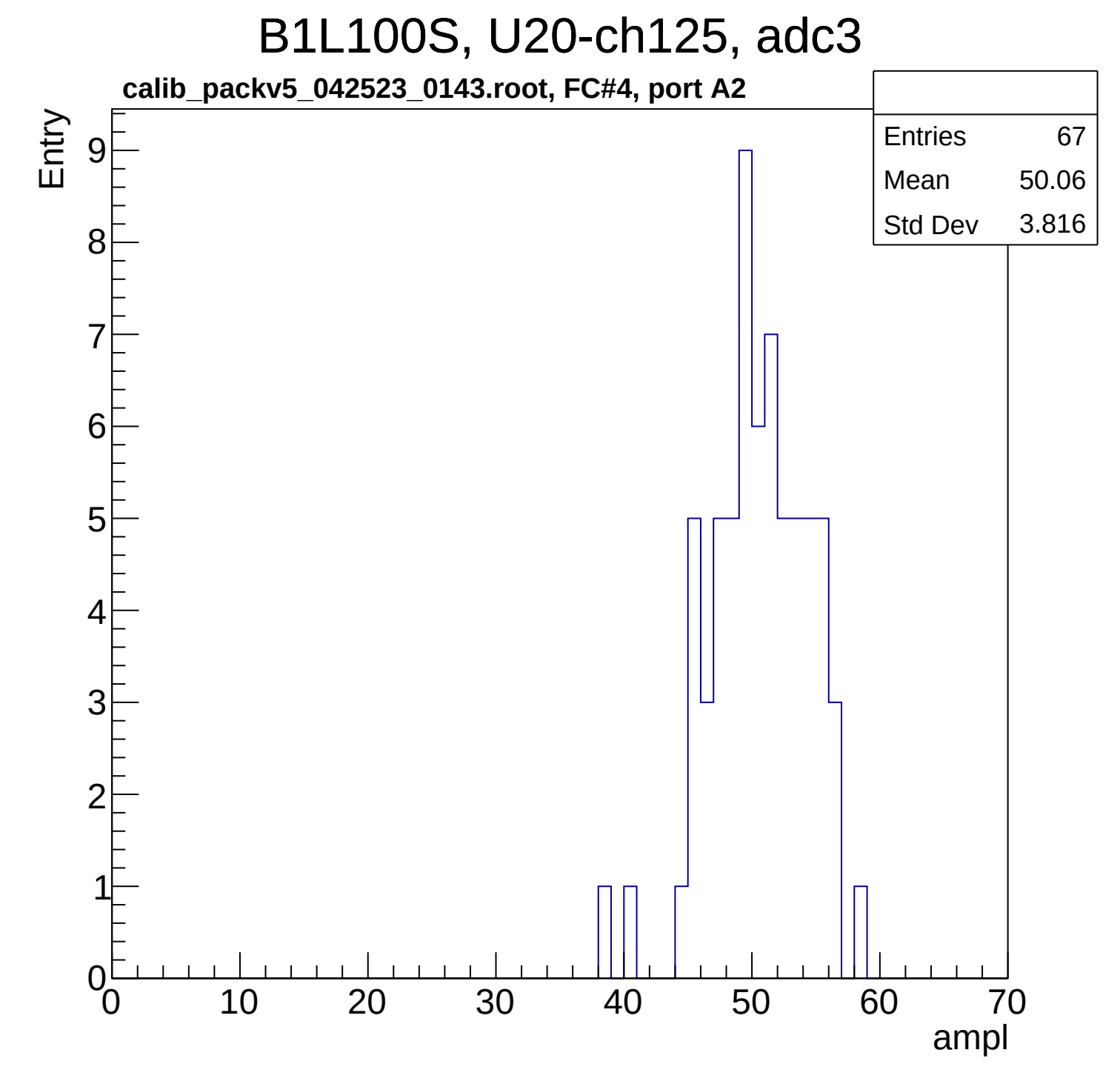
B1L100S, U20-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.06
Std Dev 3.816
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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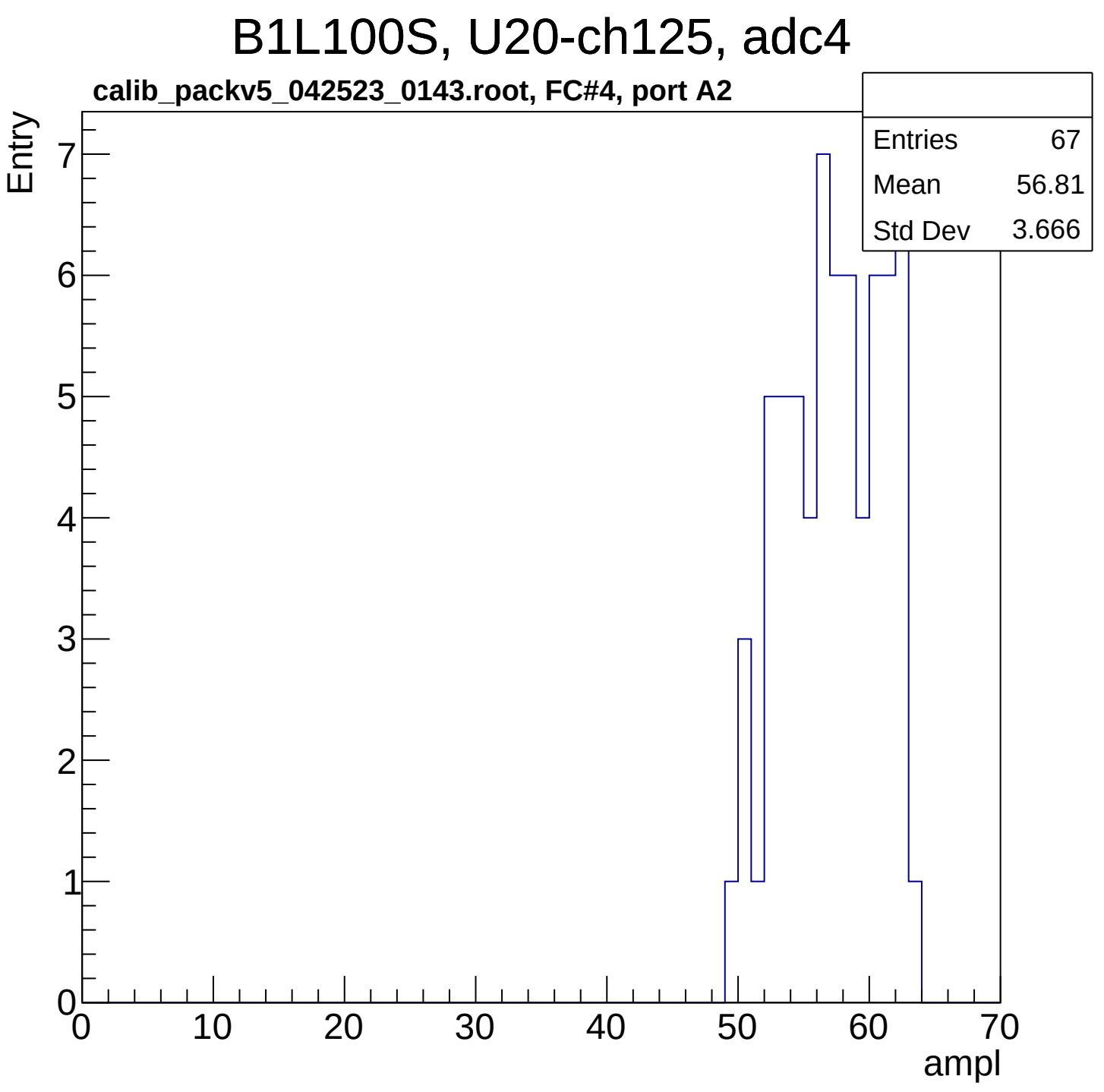
B1L100S, U20-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.81
Std Dev 3.666
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
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60
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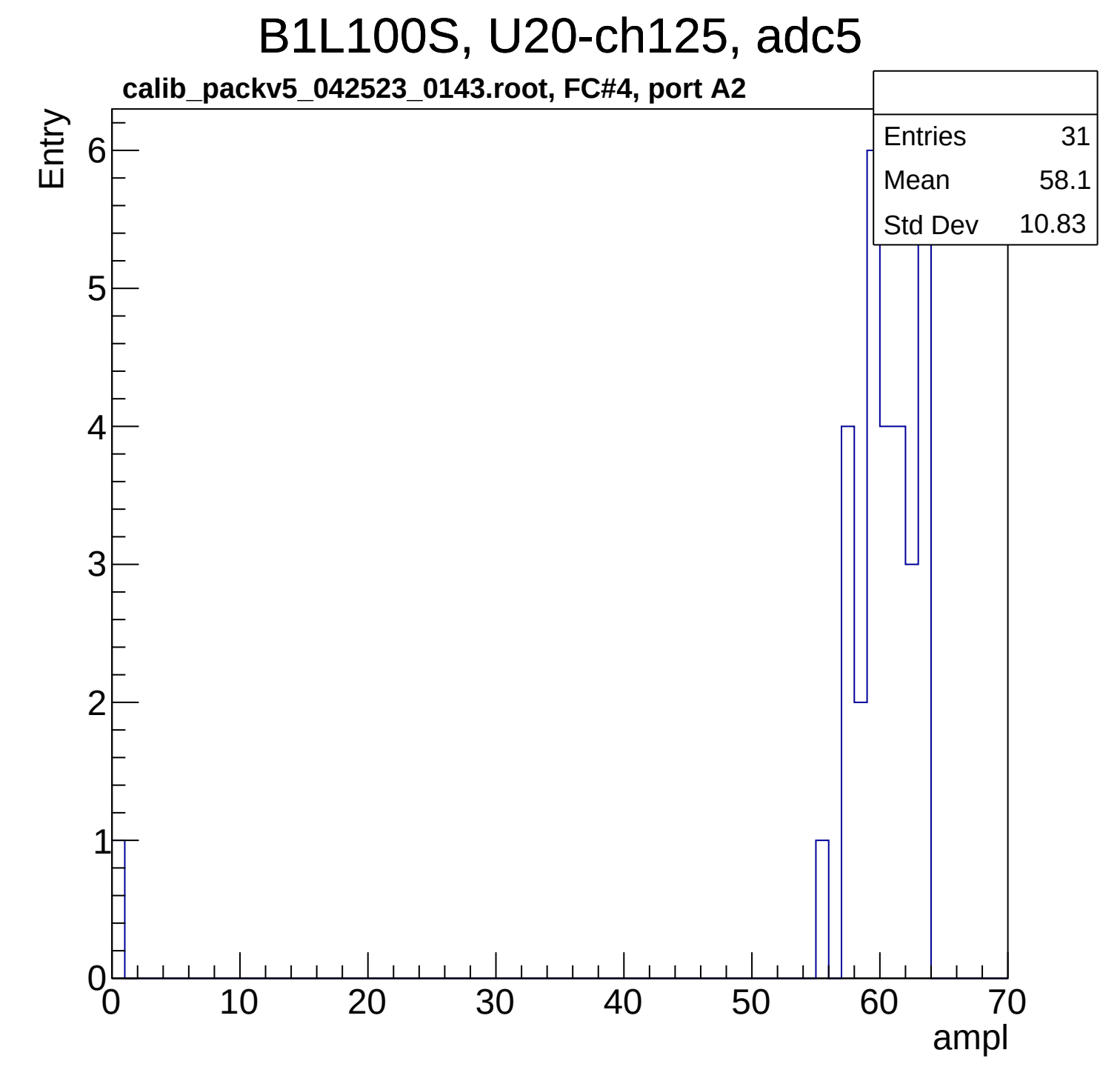
B1L100S, U20-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 58.1
Std Dev 10.83
Entry
ampl
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1
2
3
4
5
6
0
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20
30
40
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60
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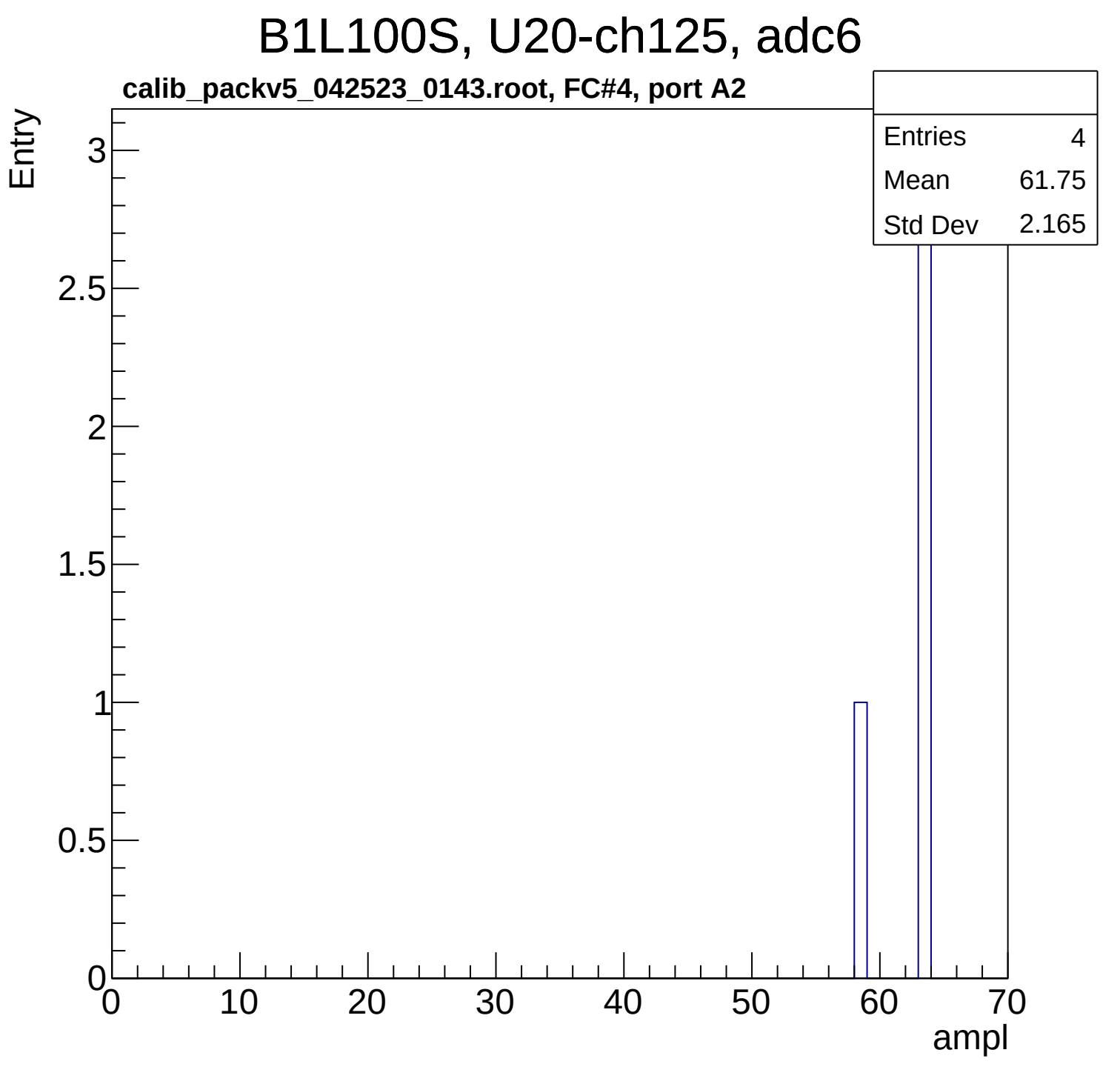
B1L100S, U20-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 61.75
Std Dev 2.165
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
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20
30
40
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60
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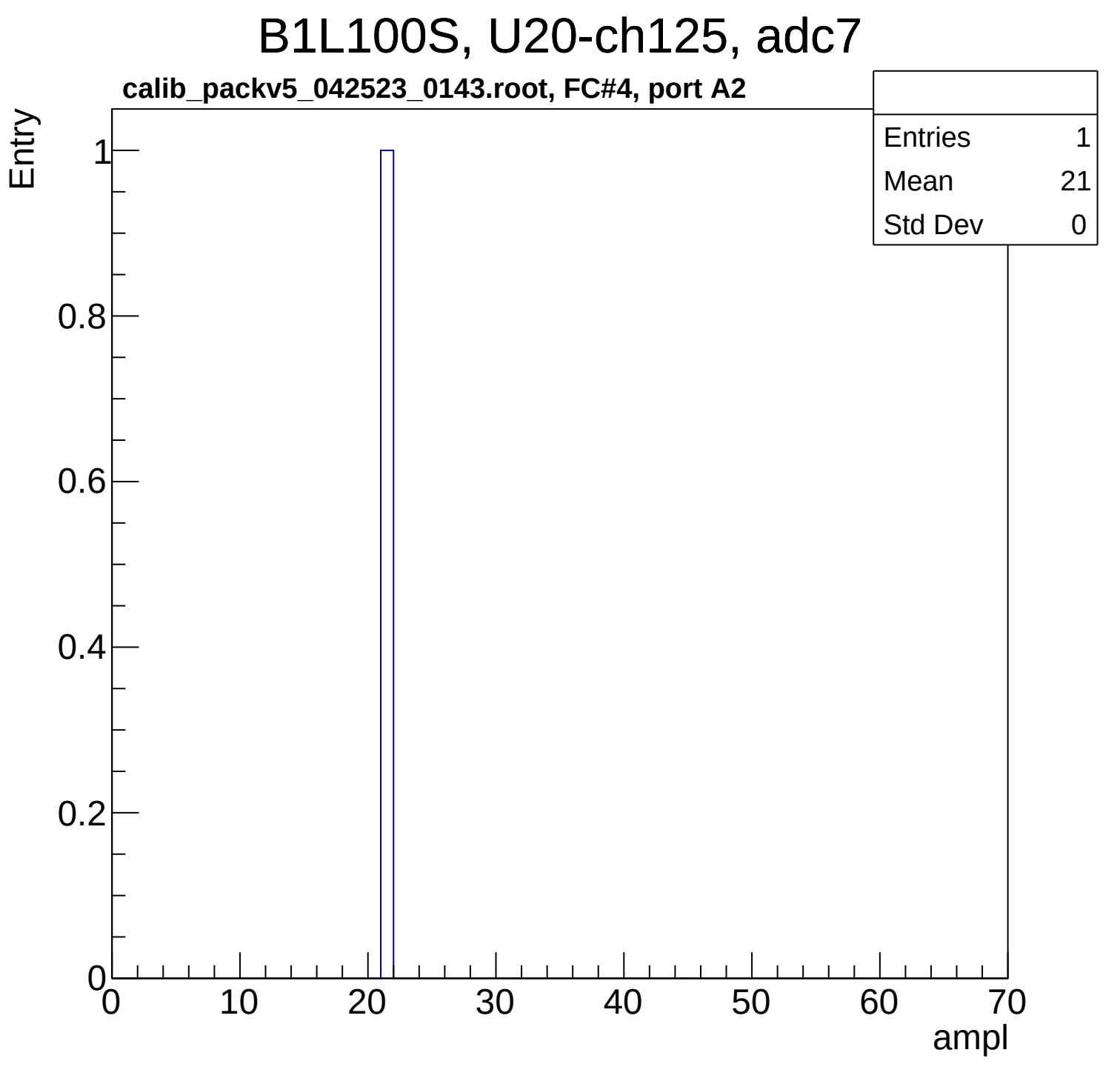
B1L100S, U20-ch125, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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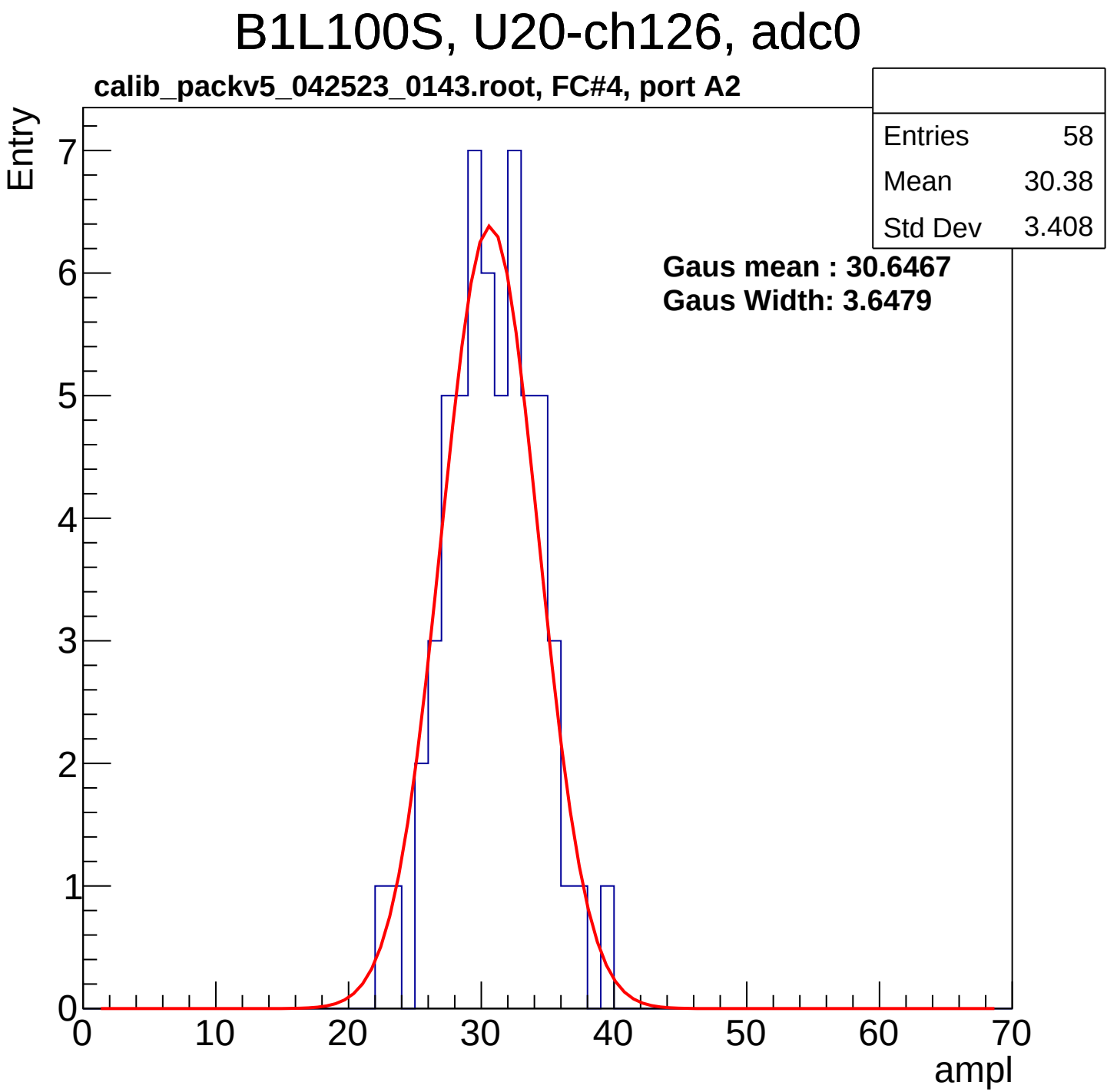
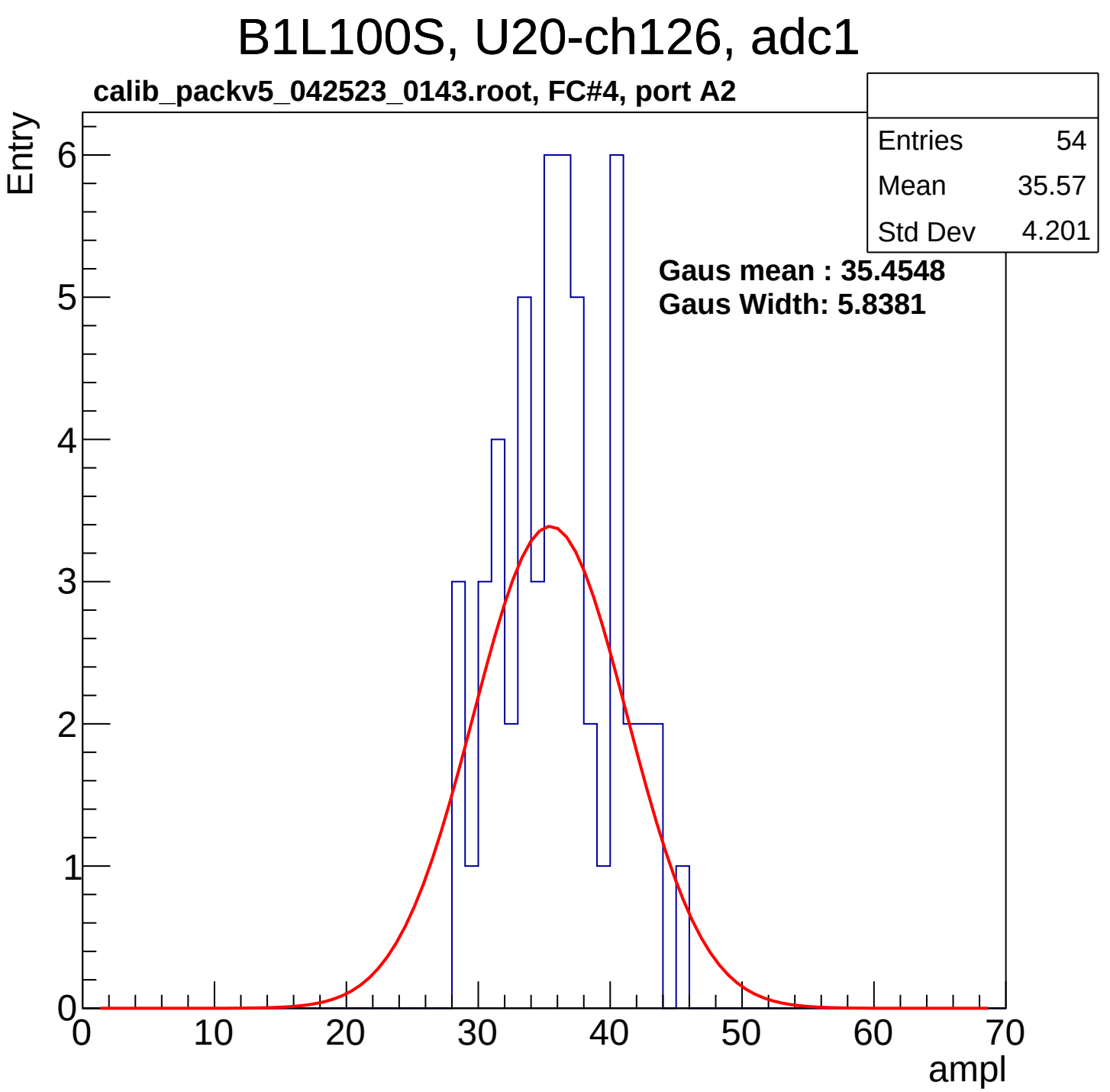
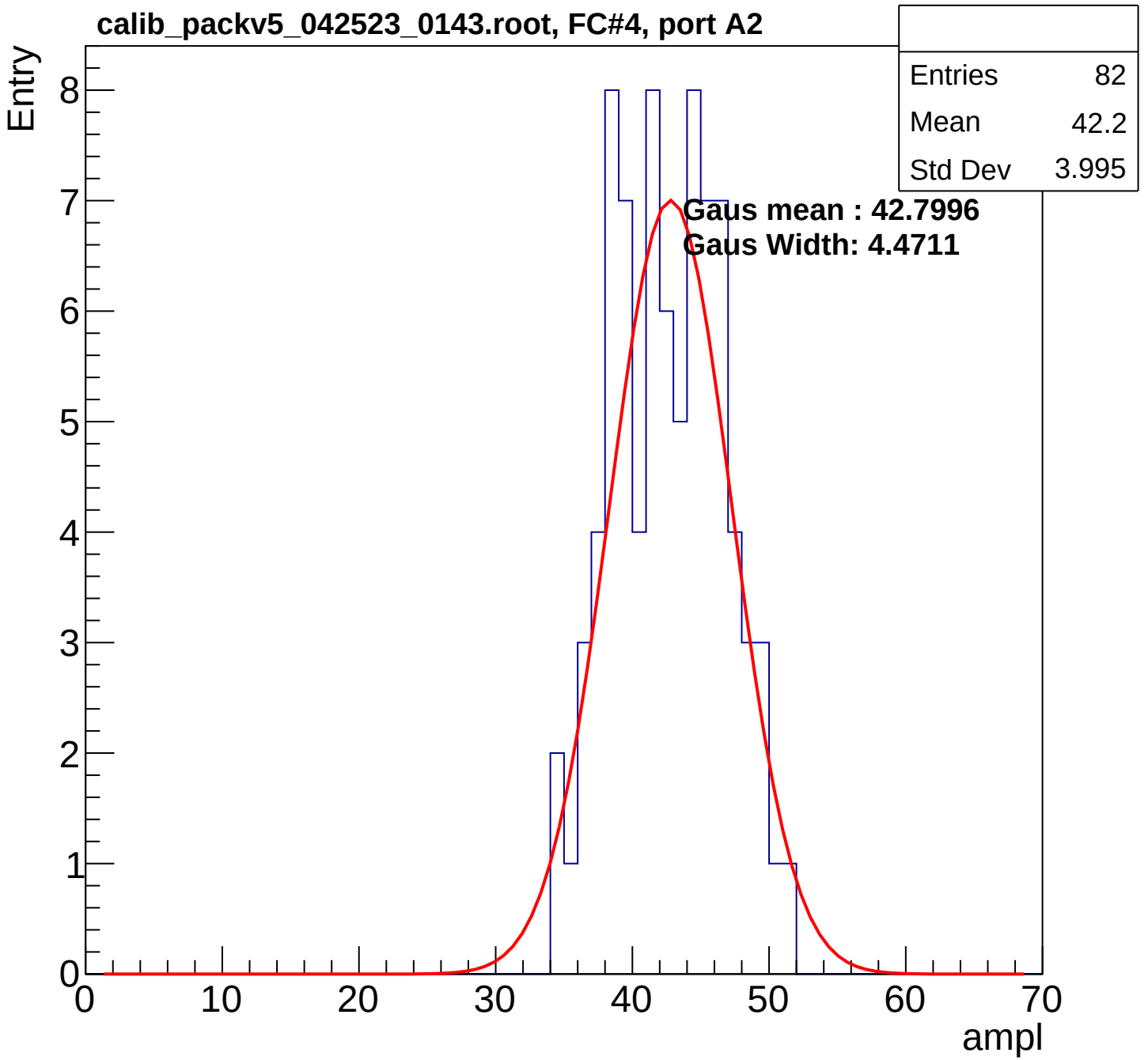
50
60
70
ampl


B1L100S, U20-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 30.38
Std Dev 3.408
Gaus mean : 30.6467
Gaus Width: 3.6479
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch126, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 35.57
Std Dev 4.201
Gaus mean : 35.4548
Gaus Width: 5.8381
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U20-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 42.2
Std Dev 3.995
Gaus mean : 42.7996
Gaus Width: 4.4711
Entry
ampl
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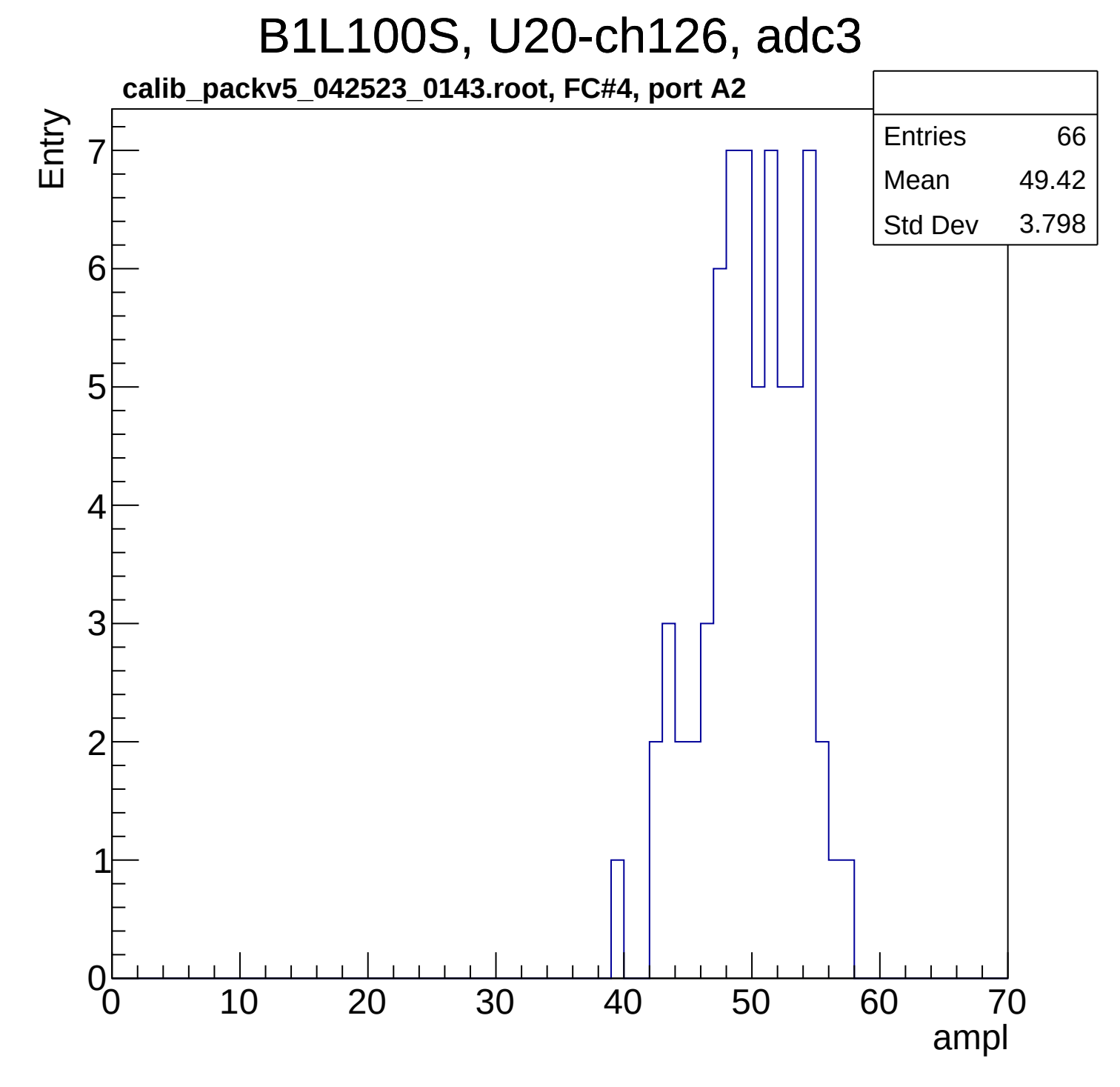
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch126, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.42
Std Dev 3.798
Entry
ampl
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3
4
5
6
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0
10
20
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60
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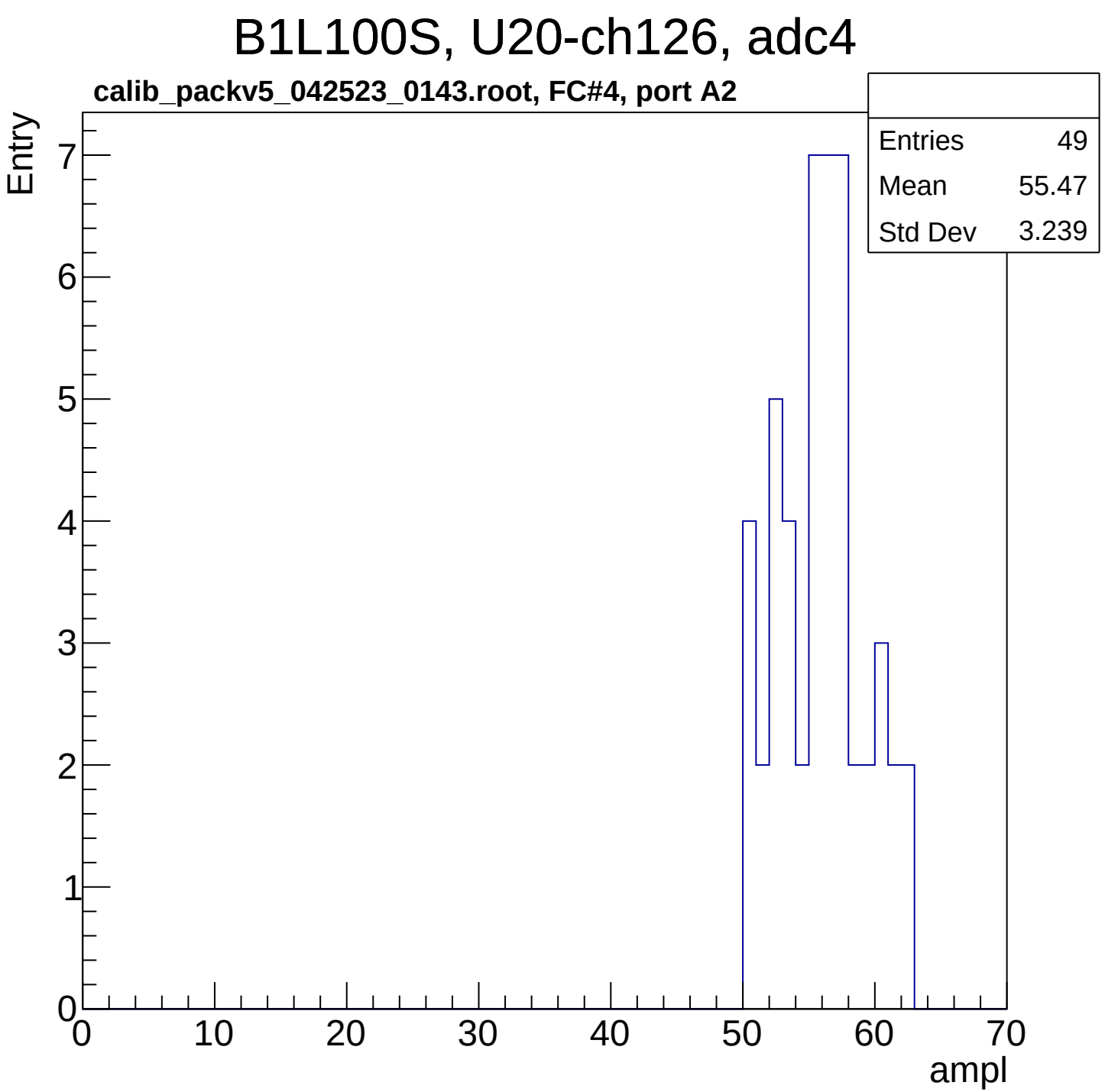
B1L100S, U20-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.47
Std Dev 3.239
Entry
ampl
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3
4
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0
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20
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40
50
60
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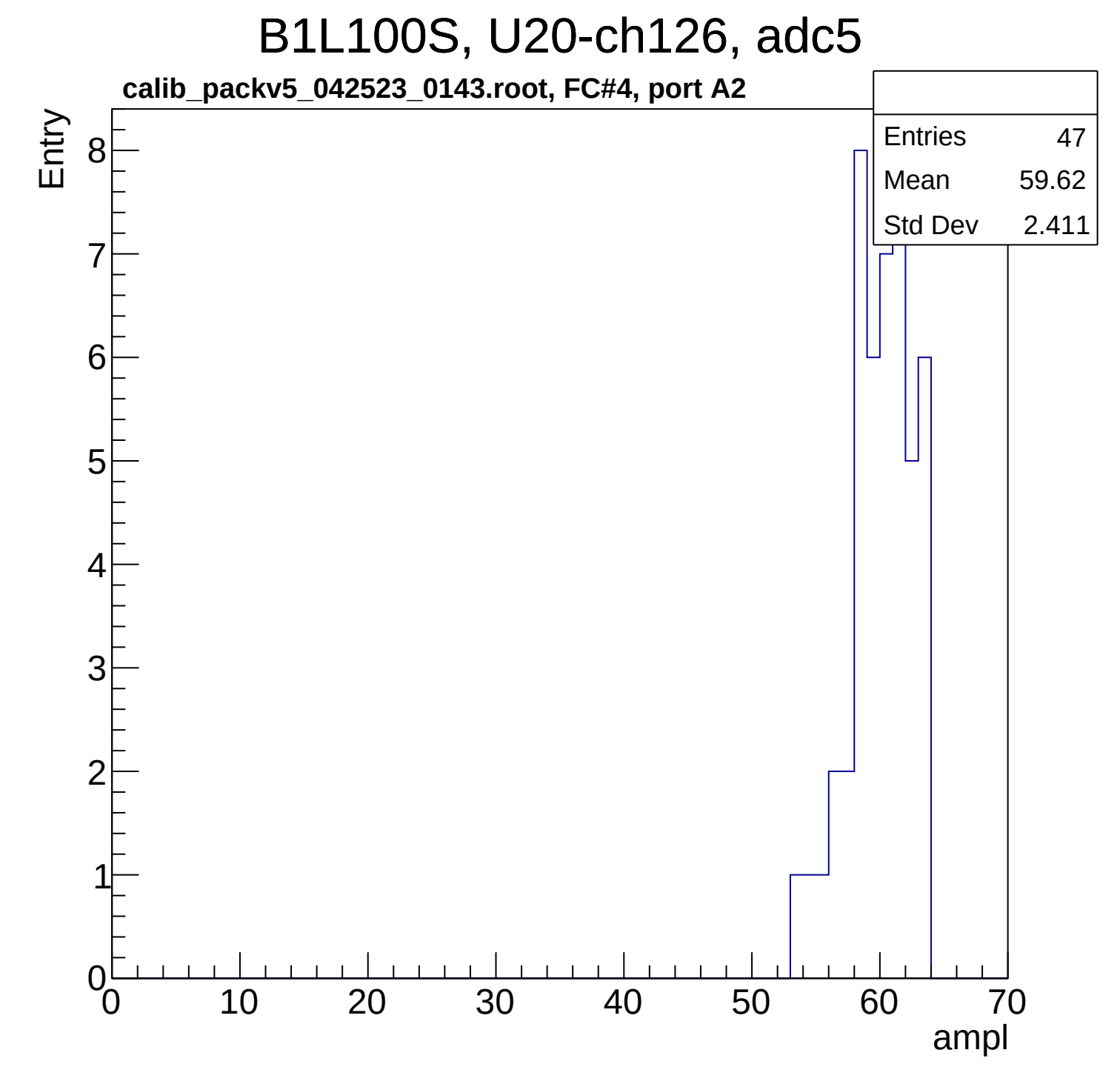
B1L100S, U20-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.62
Std Dev 2.411
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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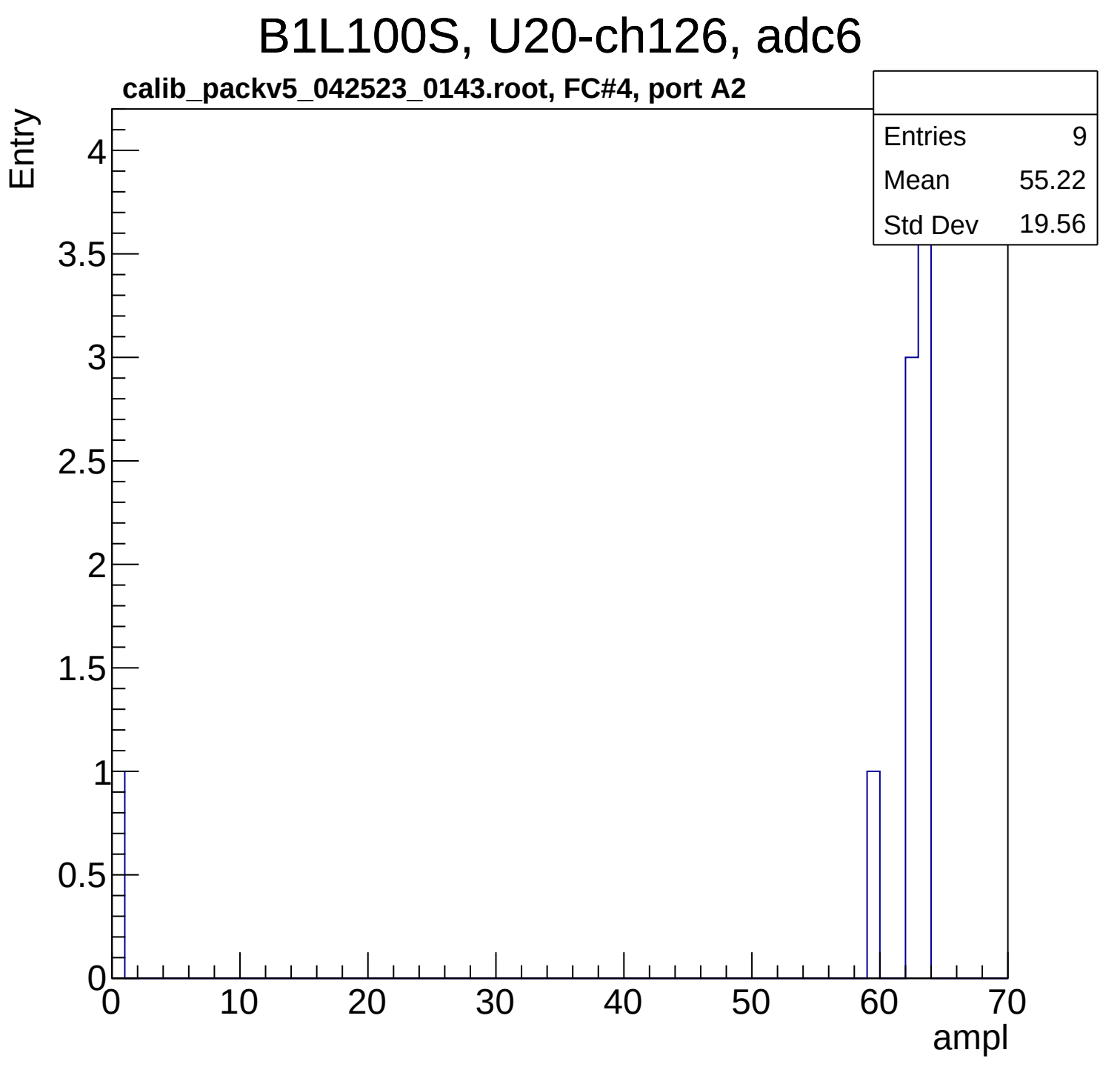
B1L100S, U20-ch126, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 55.22
Std Dev 19.56
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
10
20
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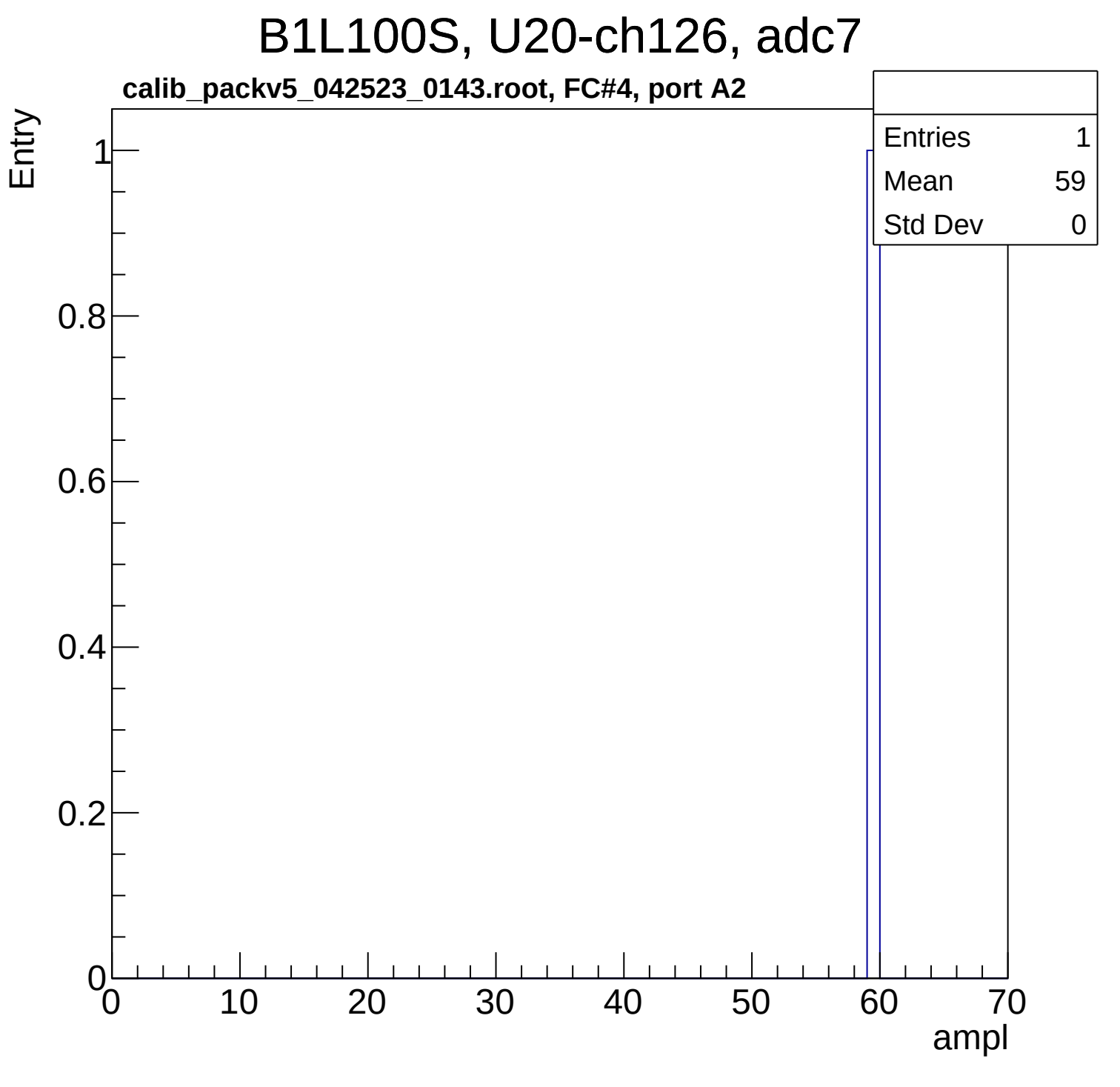
40
50
60
70


B1L100S, U20-ch126, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 59
Std Dev 0
Entry
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0.6
0.4
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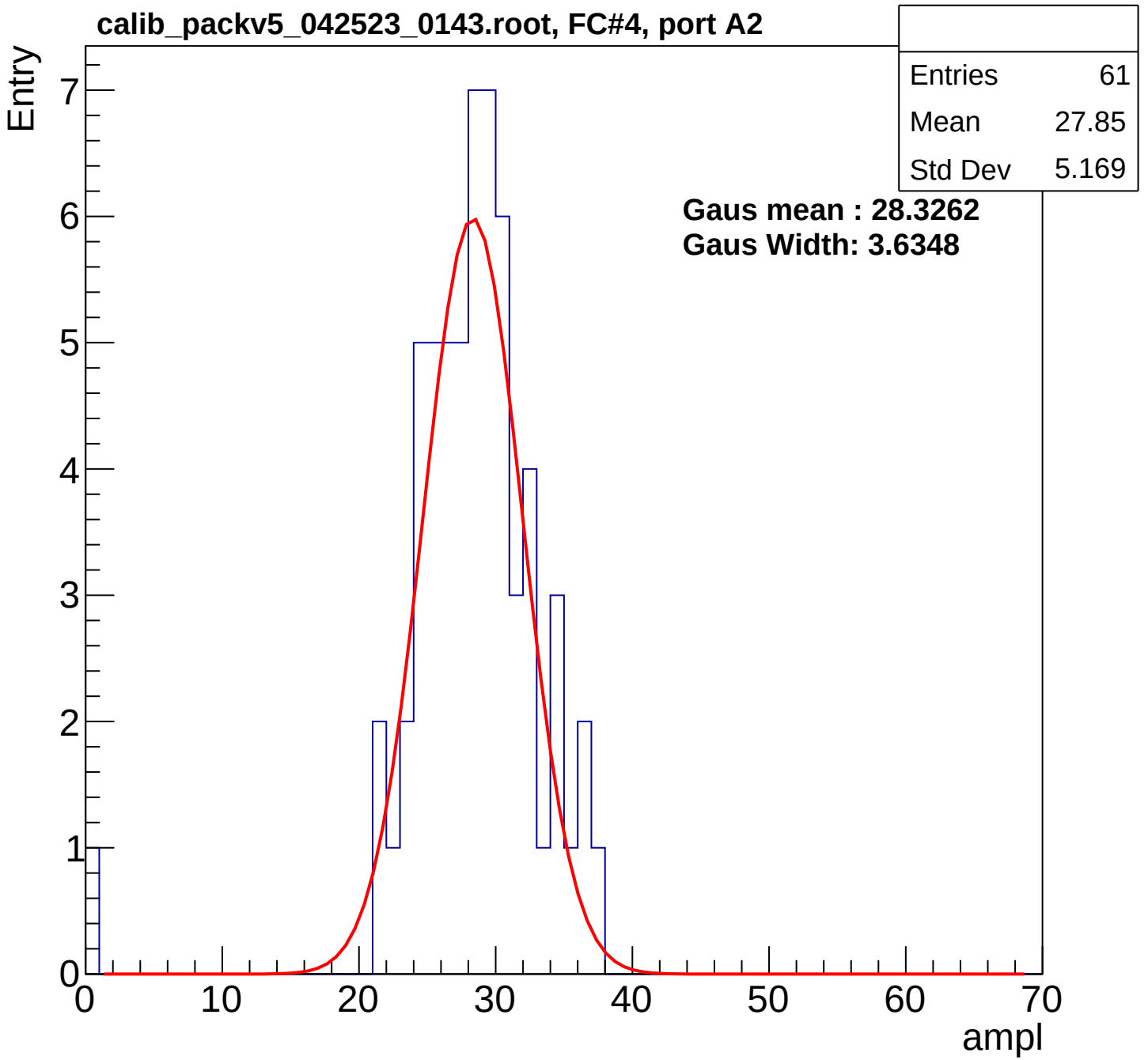
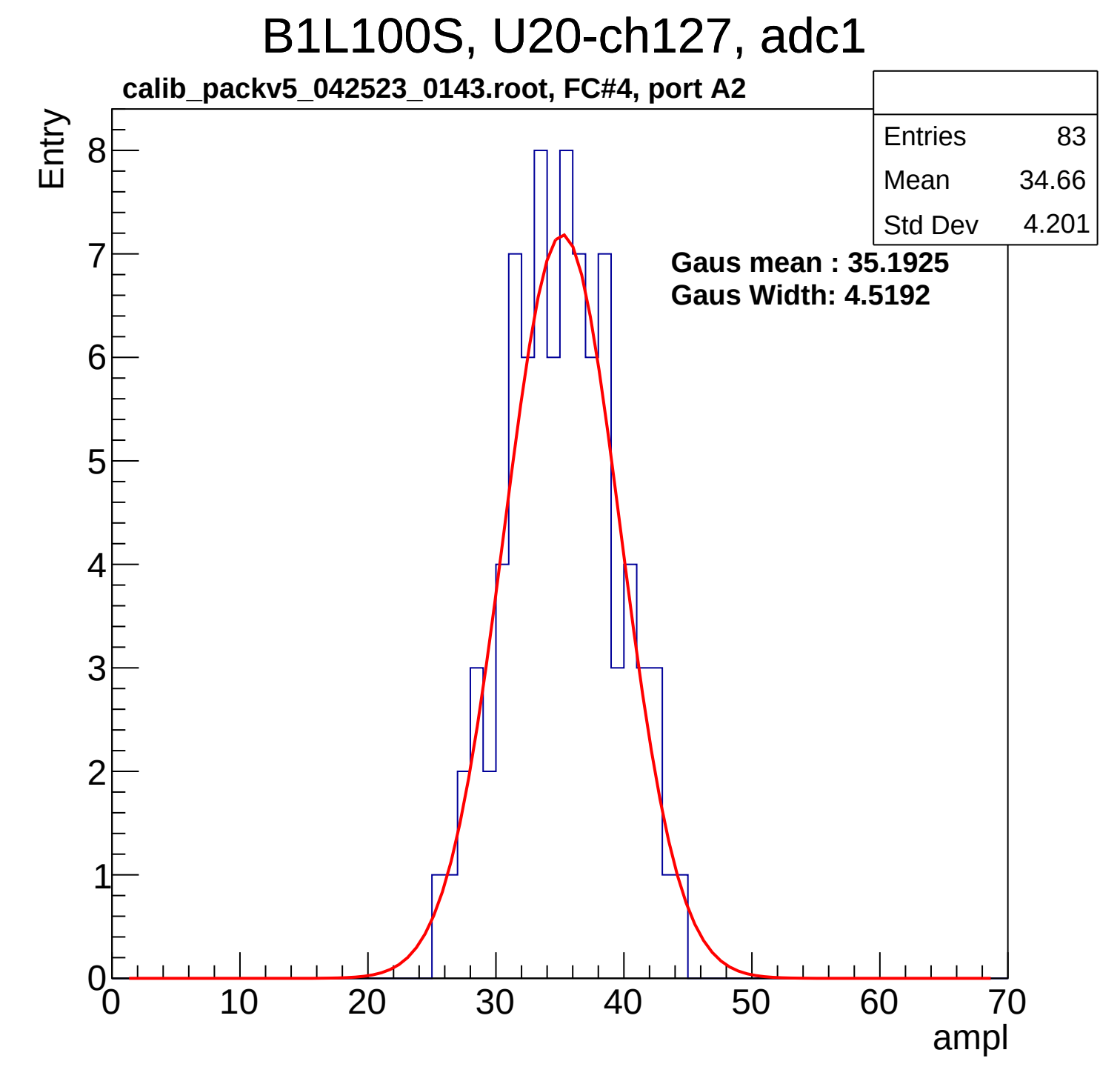
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U20-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 27.85
Std Dev 5.169
Gaus mean : 28.3262
Gaus Width: 3.6348
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 34.66
Std Dev 4.201
Gaus mean : 35.1925
Gaus Width: 4.5192
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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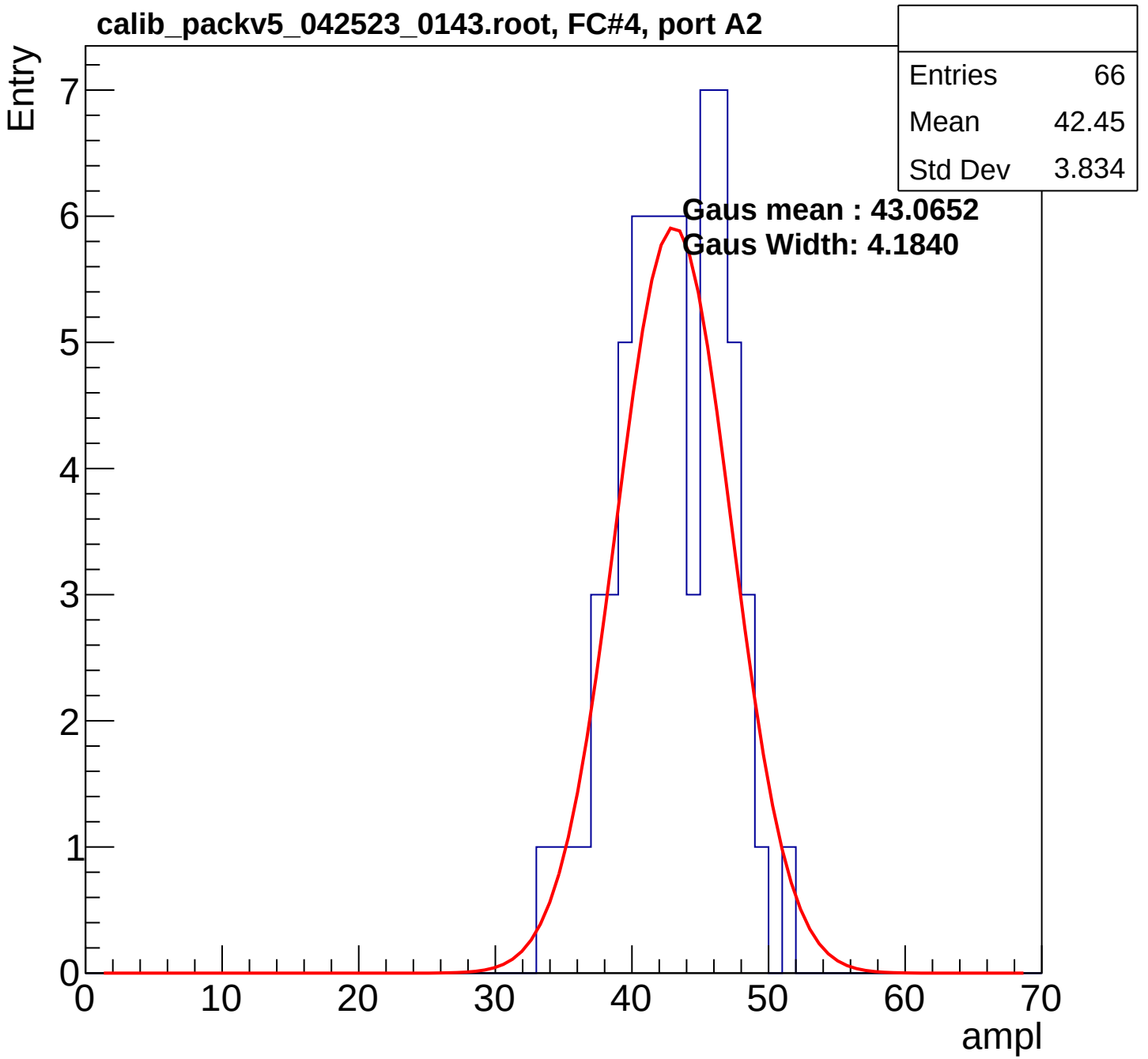
B1L100S, U20-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.45
Std Dev 3.834
Gaus mean : 43.0652
Gaus Width: 4.1840
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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60
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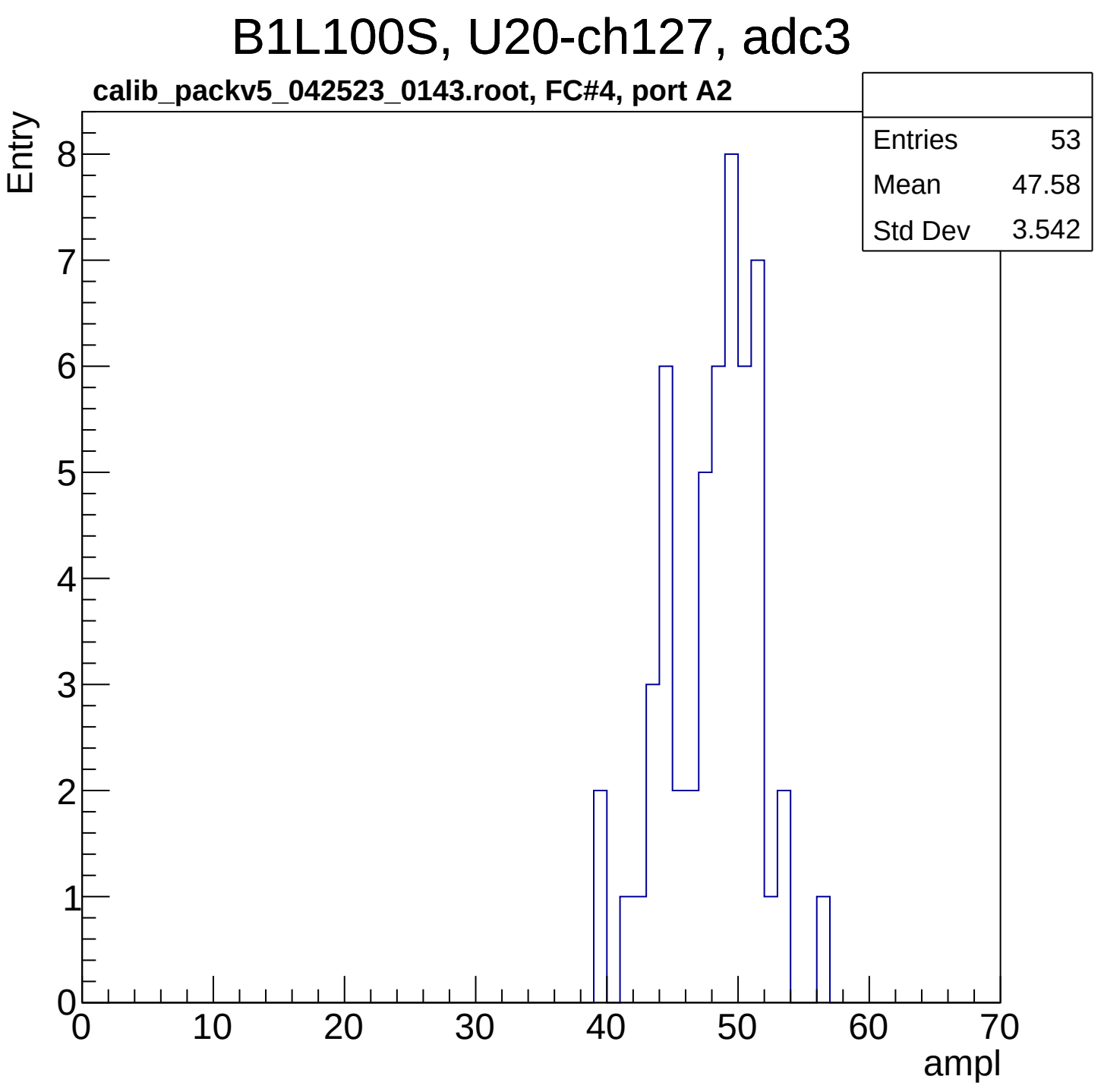
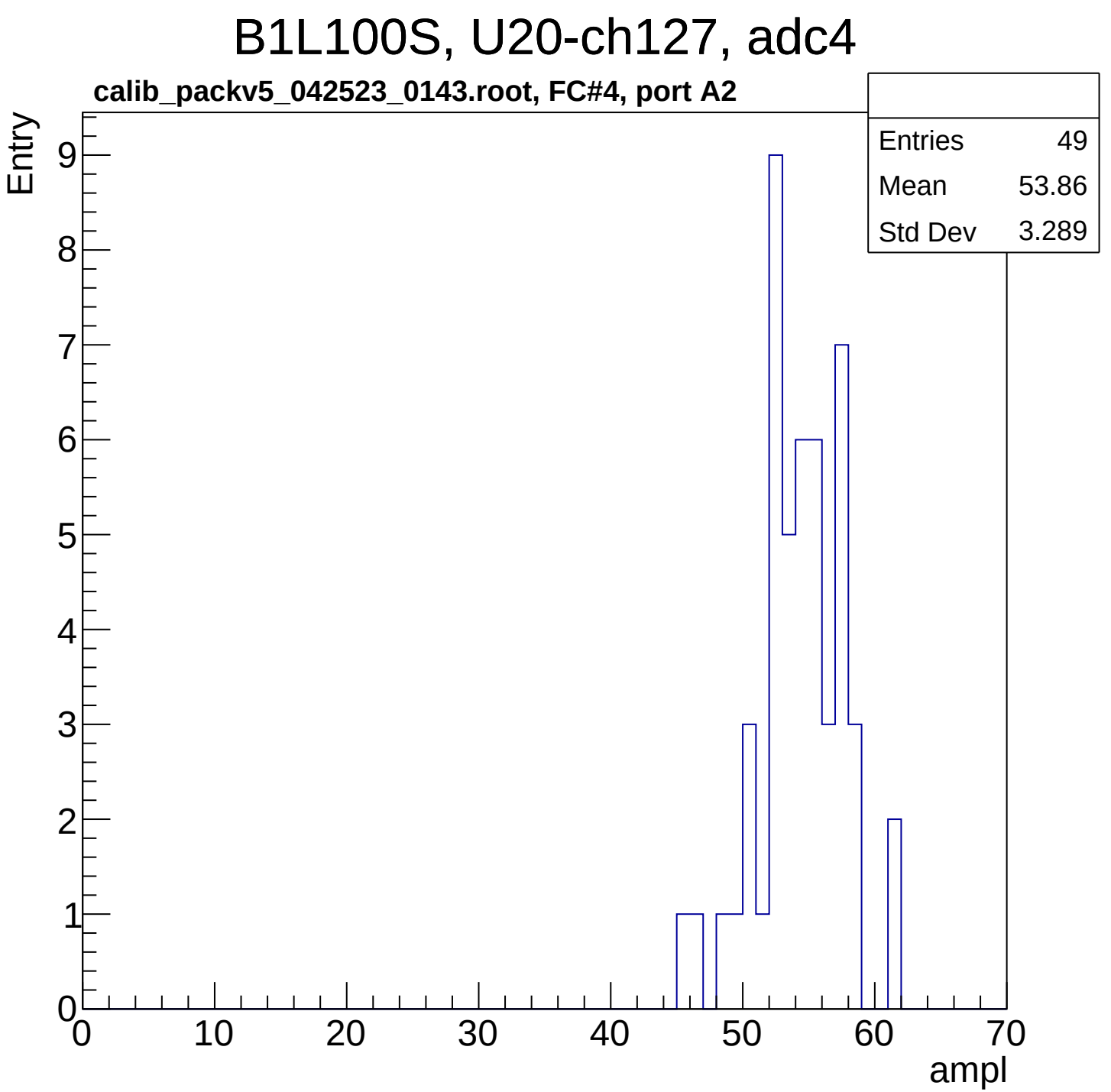
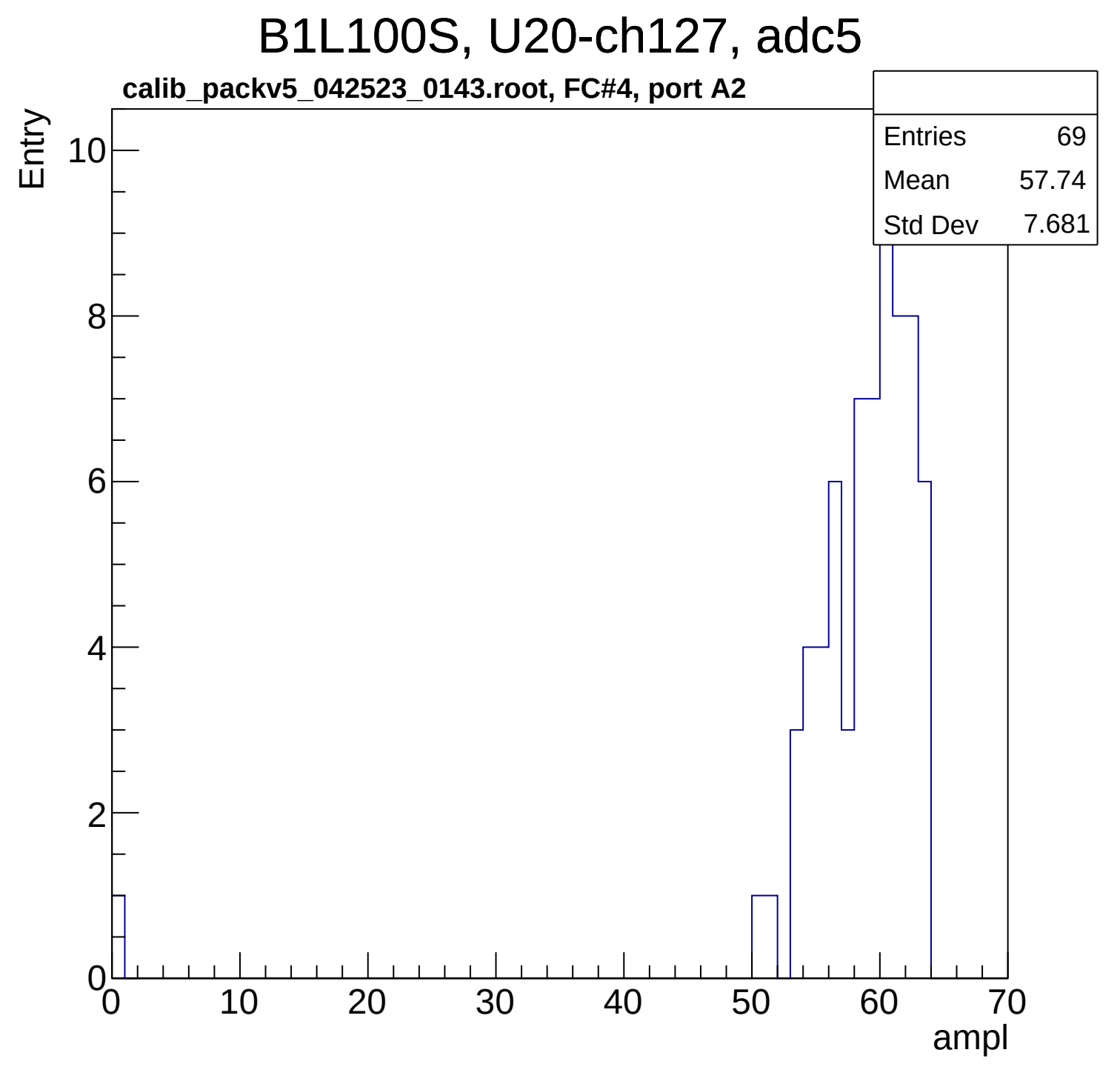
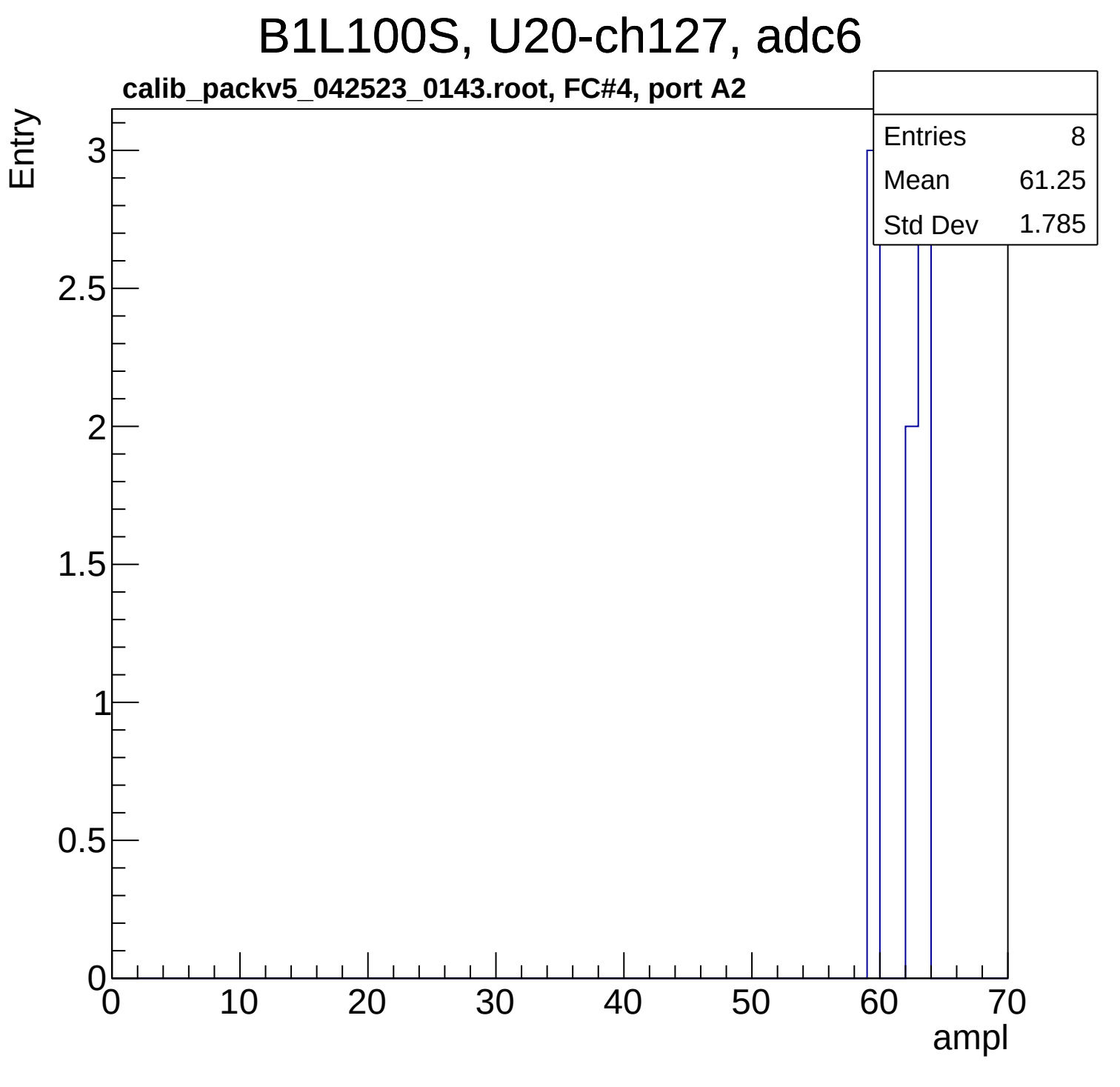
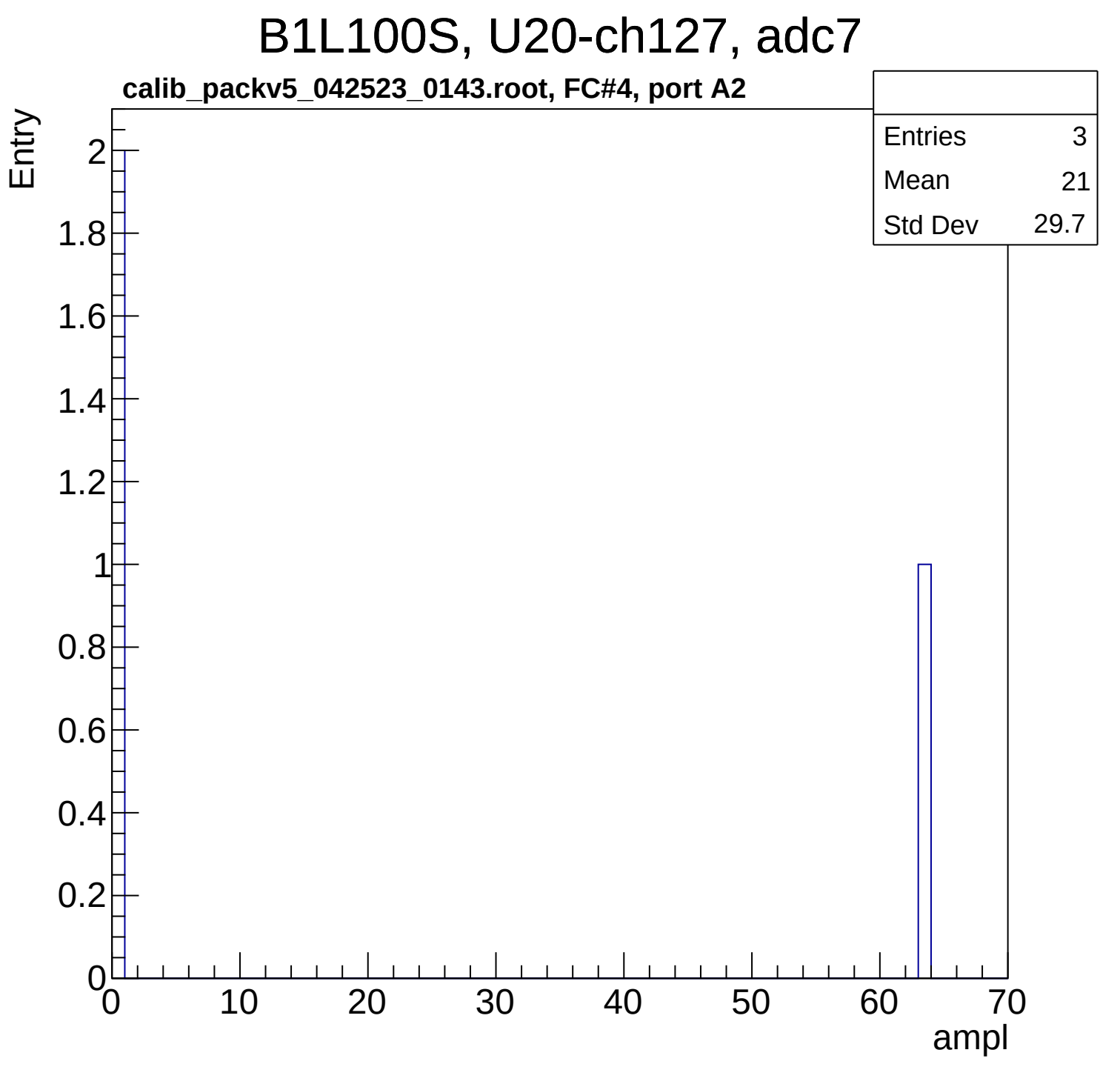
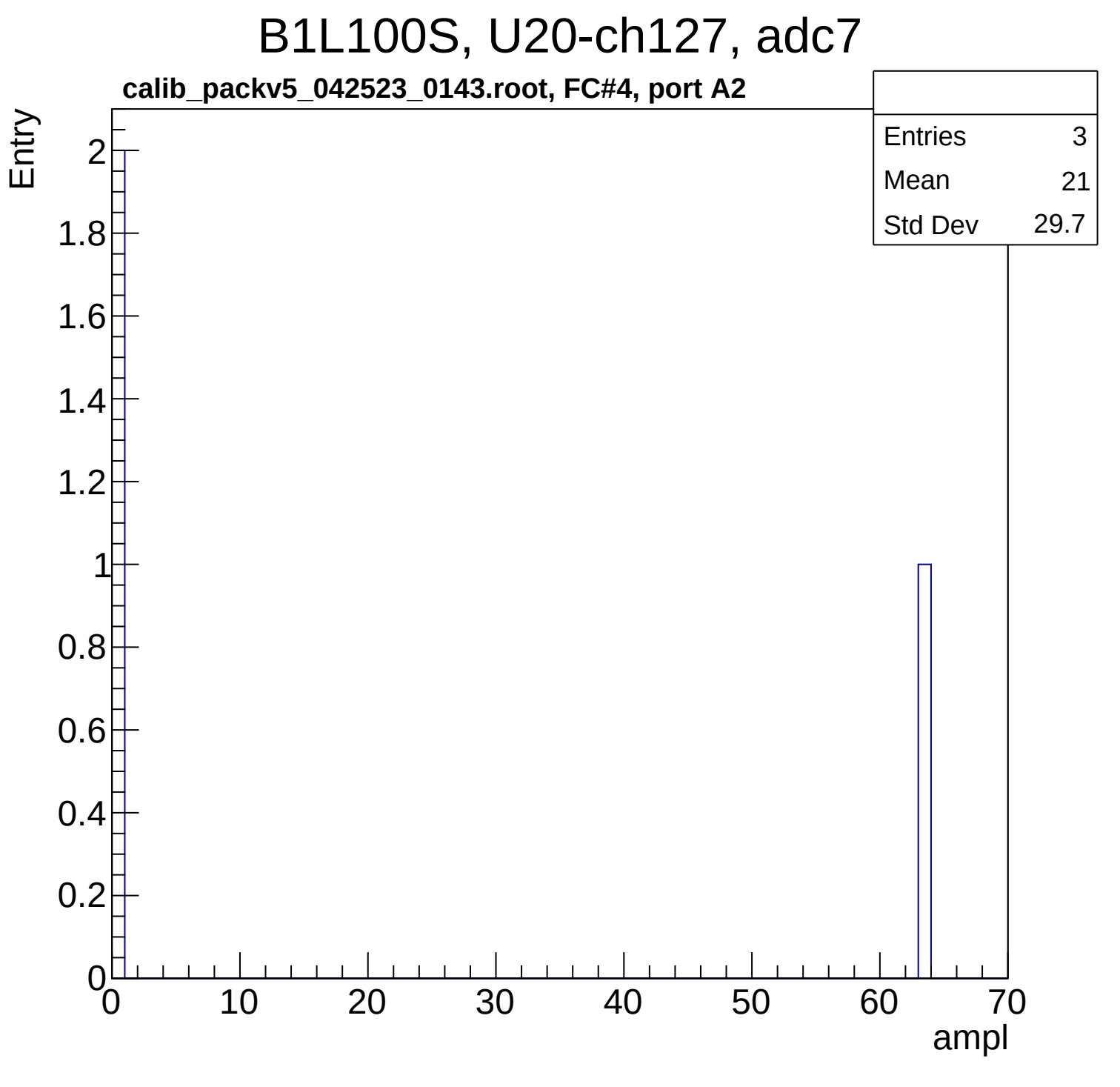

B1L100S, U20-ch127, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 47.58
Std Dev 3.542
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 53.86
Std Dev 3.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 57.74
Std Dev 7.681
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.25
Std Dev 1.785
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U20-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70