



# B0L103S, U19-ch0

calib\_packv5\_040323\_1717.root, FC#2, port C3

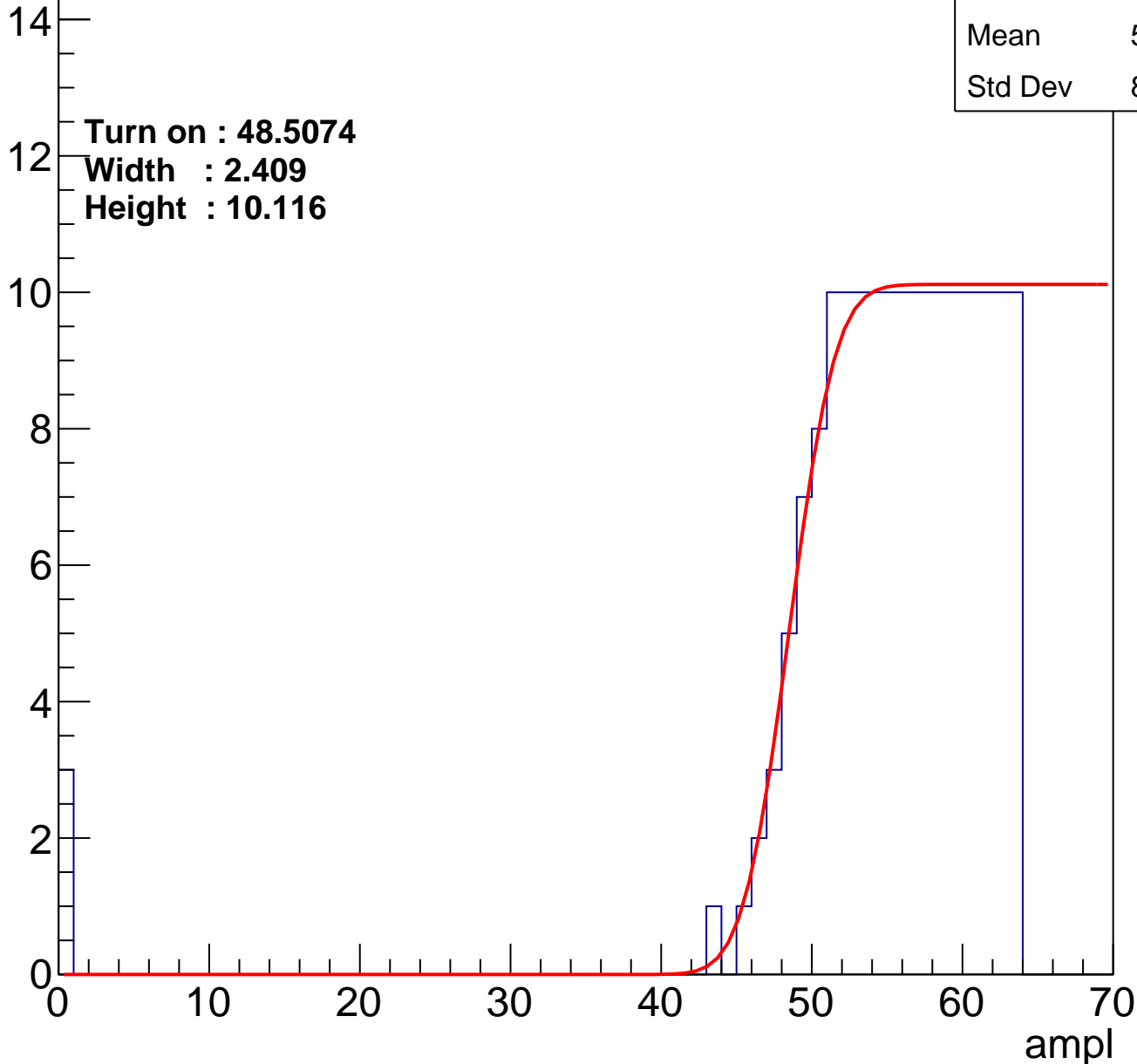
Entry

Entries	160
Mean	54.46
Std Dev	8.897

Turn on : 48.5074

Width : 2.409

Height : 10.116



# B0L103S, U19-ch1

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.79
Std Dev	6.418

Turn on : 51.7499

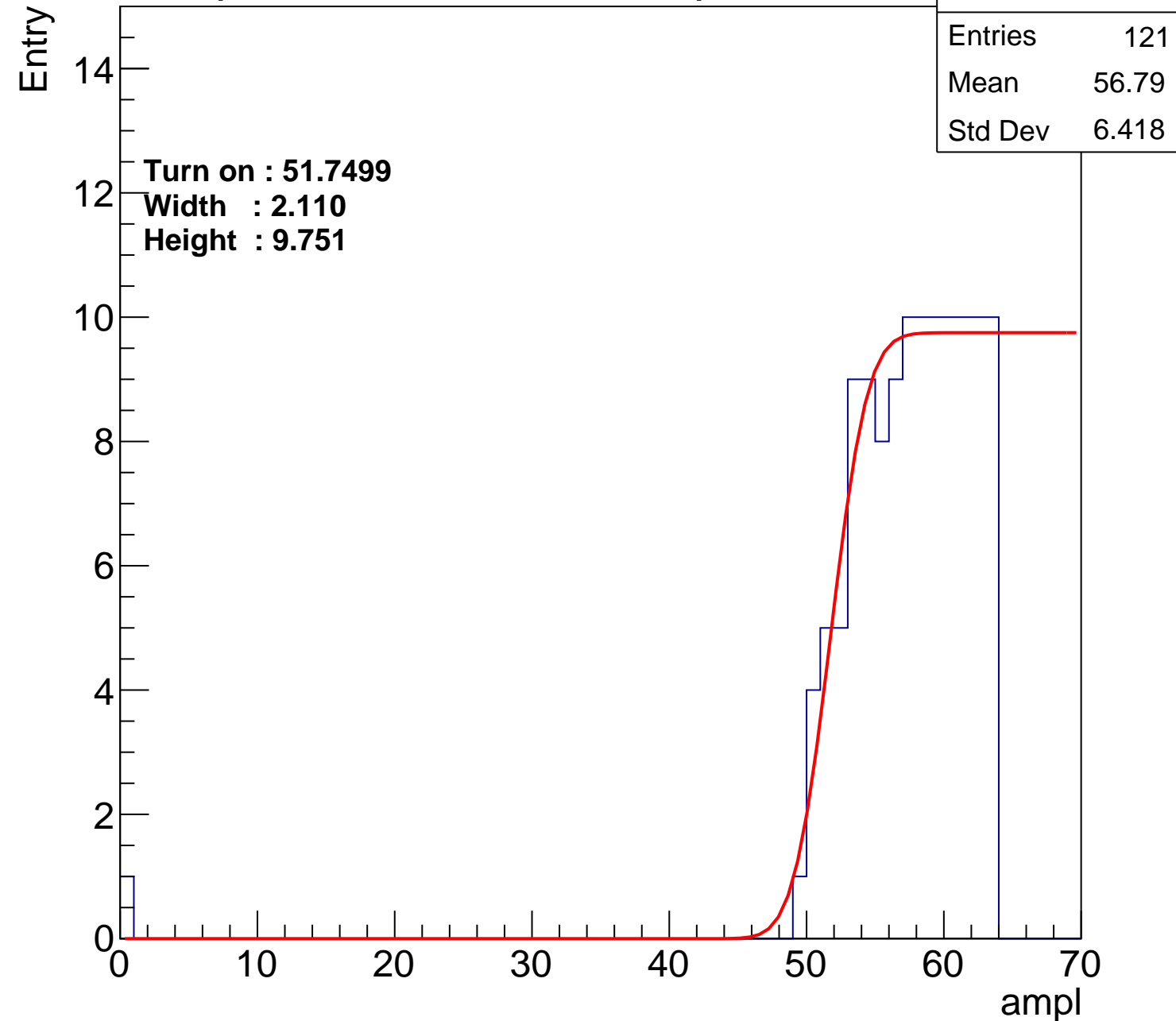
Width : 2.110

Height : 9.751

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch2

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	56.48
Std Dev	6.361

Turn on : 51.1119

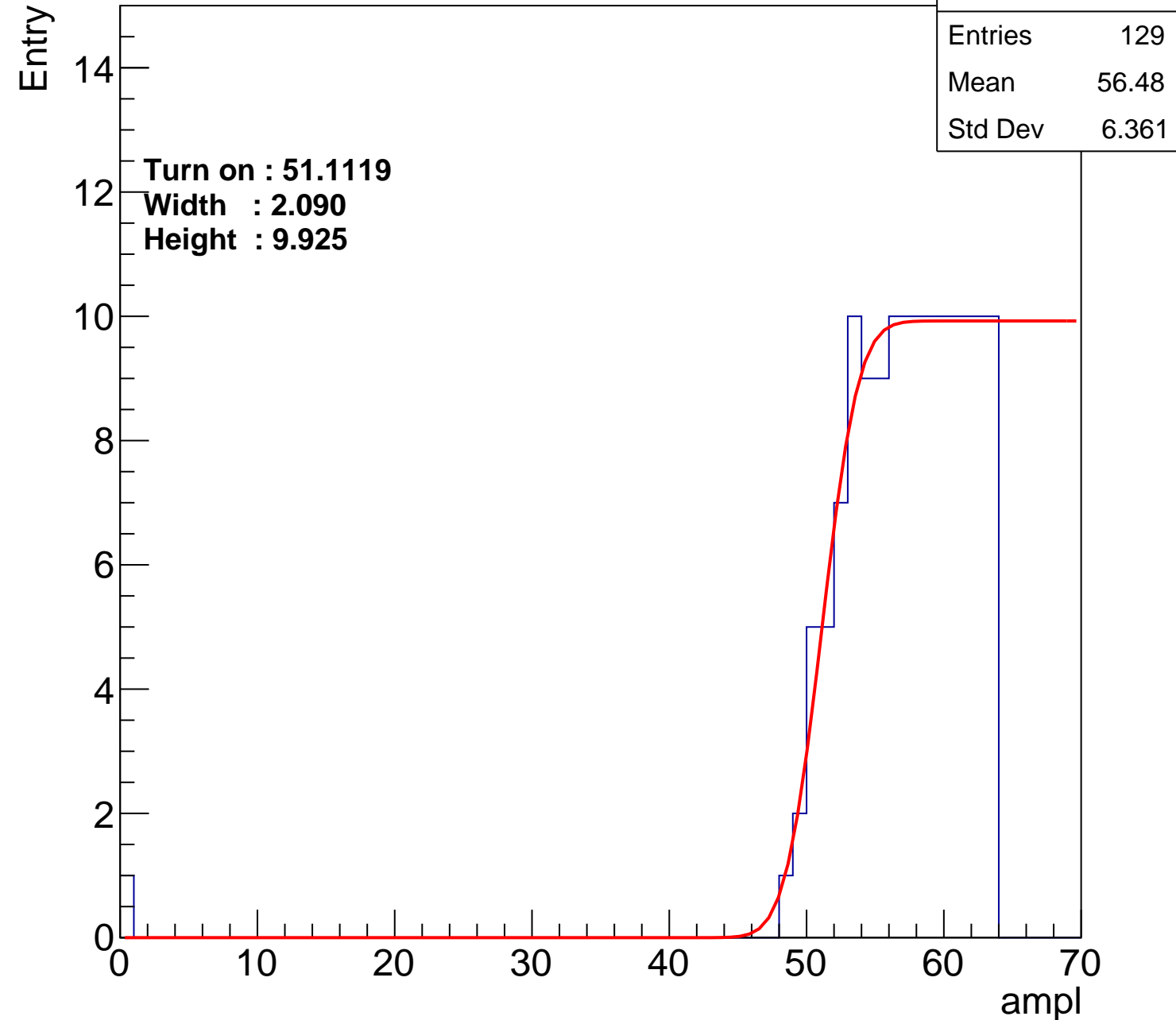
Width : 2.090

Height : 9.925

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch3

calib\_packv5\_040323\_1717.root, FC#2, port C3

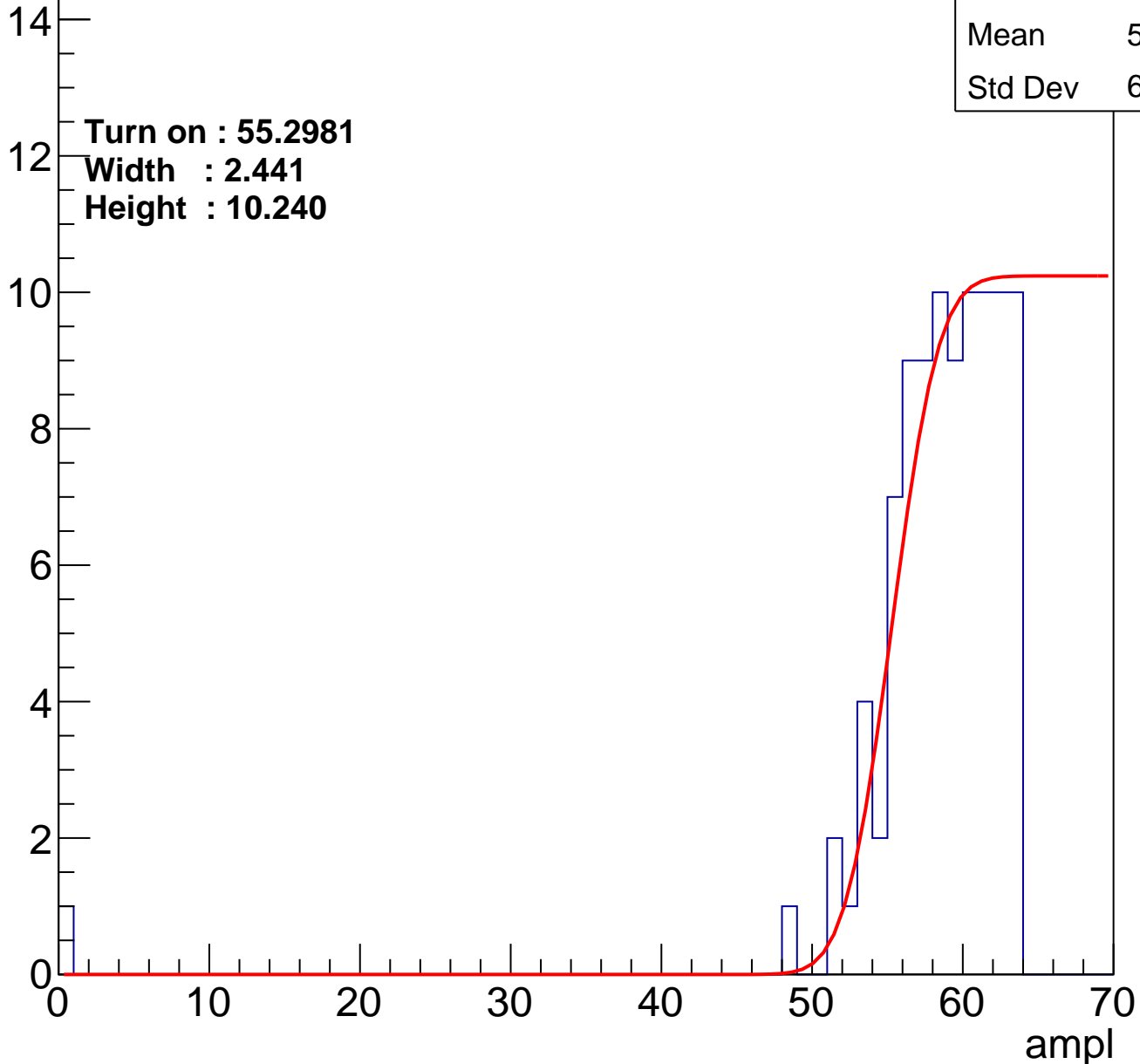
Entries	95
Mean	57.84
Std Dev	6.794

Turn on : 55.2981

Width : 2.441

Height : 10.240

Entry



# B0L103S, U19-ch4

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.56
Std Dev	10.99

Turn on : 52.8218

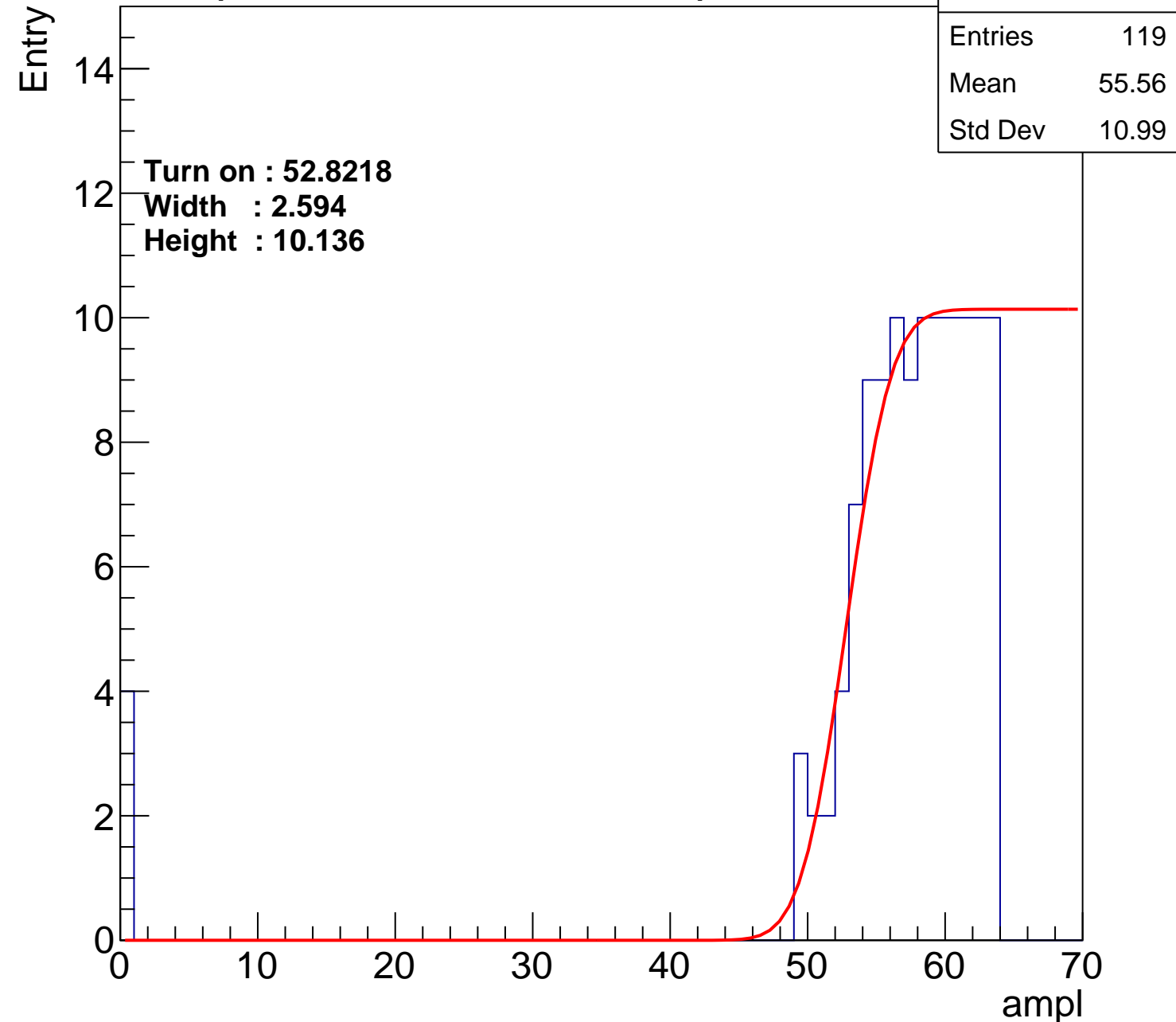
Width : 2.594

Height : 10.136

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch5

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.3
Std Dev	10.81

Turn on : 52.2571

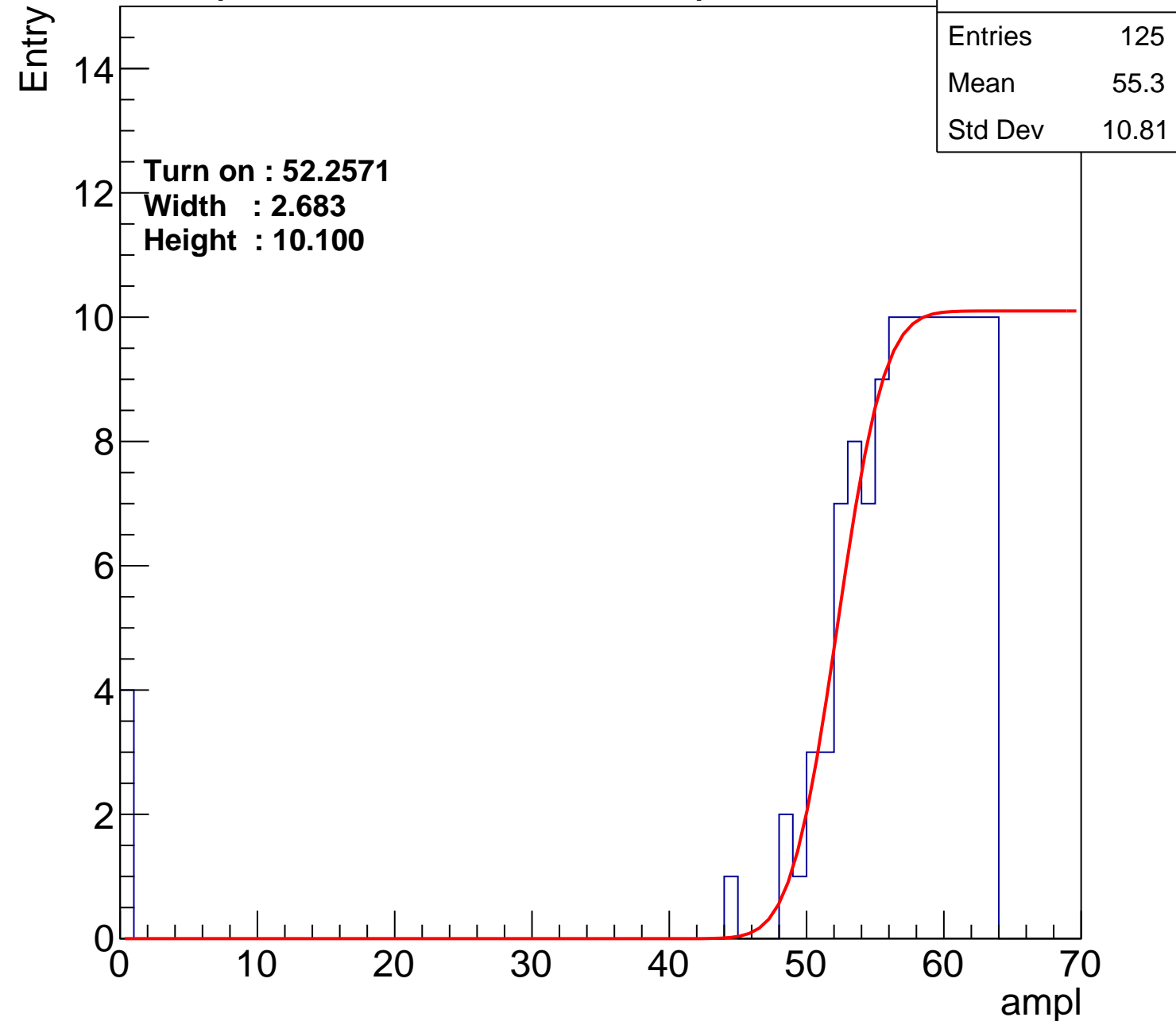
Width : 2.683

Height : 10.100

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch6

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	163
Mean	53.98
Std Dev	9.86

Turn on : 49.9681

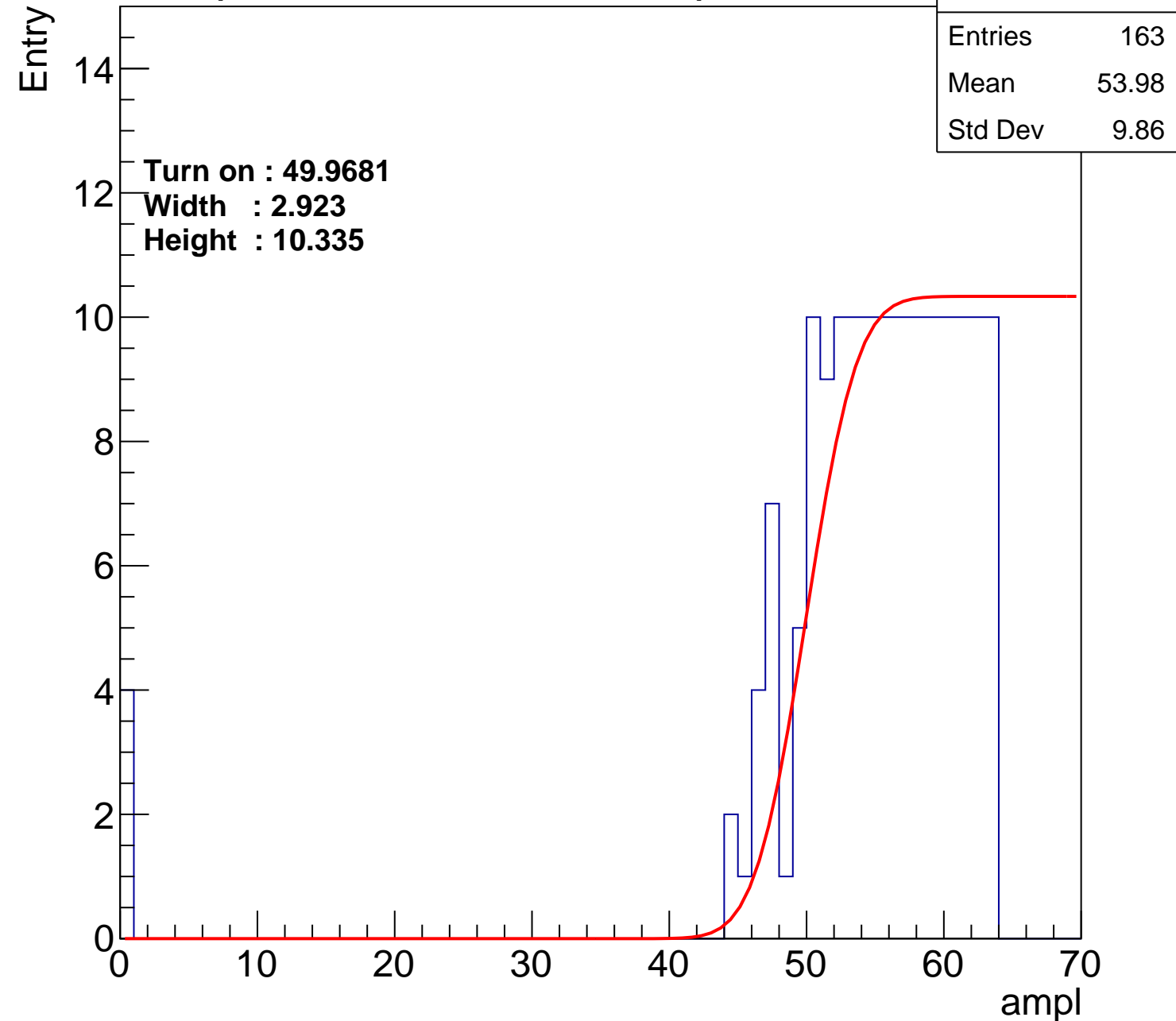
Width : 2.923

Height : 10.335

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch7

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	55.47
Std Dev	9.319

Turn on : 50.7432

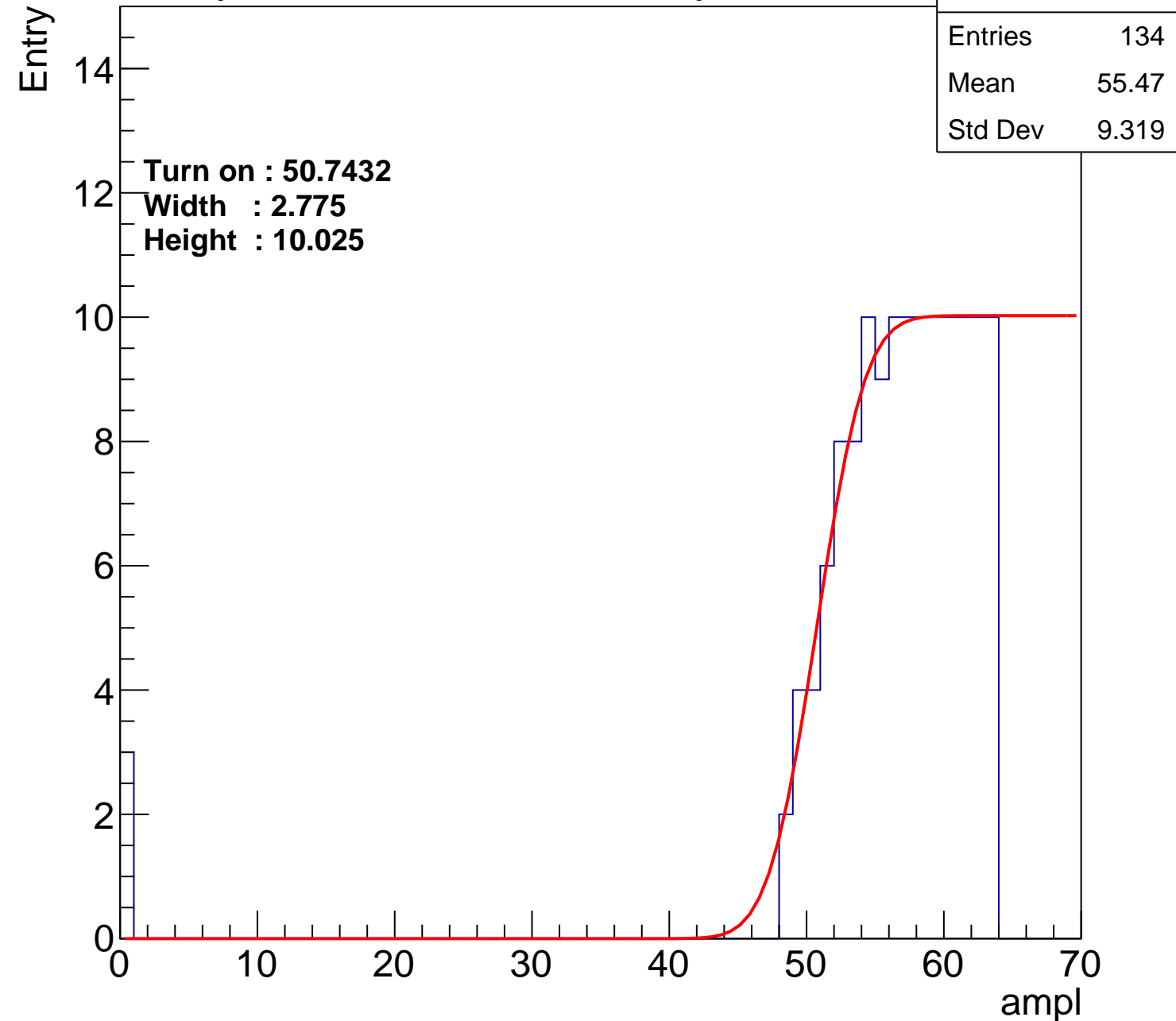
Width : 2.775

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch8

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	125
Mean	55.33
Std Dev	10.8

Turn on : 51.7748

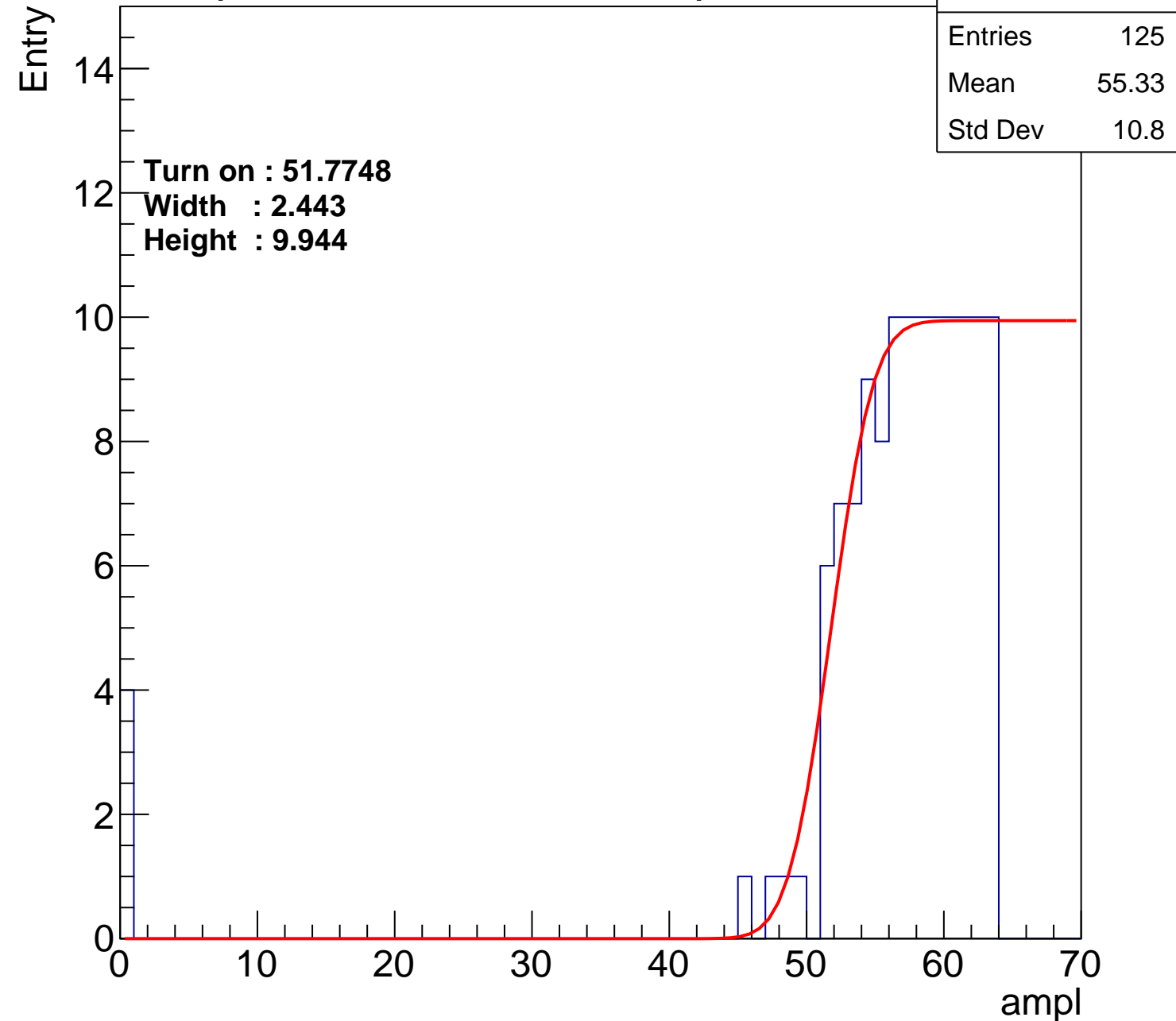
Width : 2.443

Height : 9.944

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch9

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entry

14

12

10

8

6

4

2

0

Turn on : 51.7327

Width : 2.924

Height : 10.184

Entries	126
Mean	56.17
Std Dev	8.132

ampl

0

10

20

30

40

50

60

70

# B0L103S, U19-ch10

calib\_packv5\_040323\_1717.root, FC#2, port C3

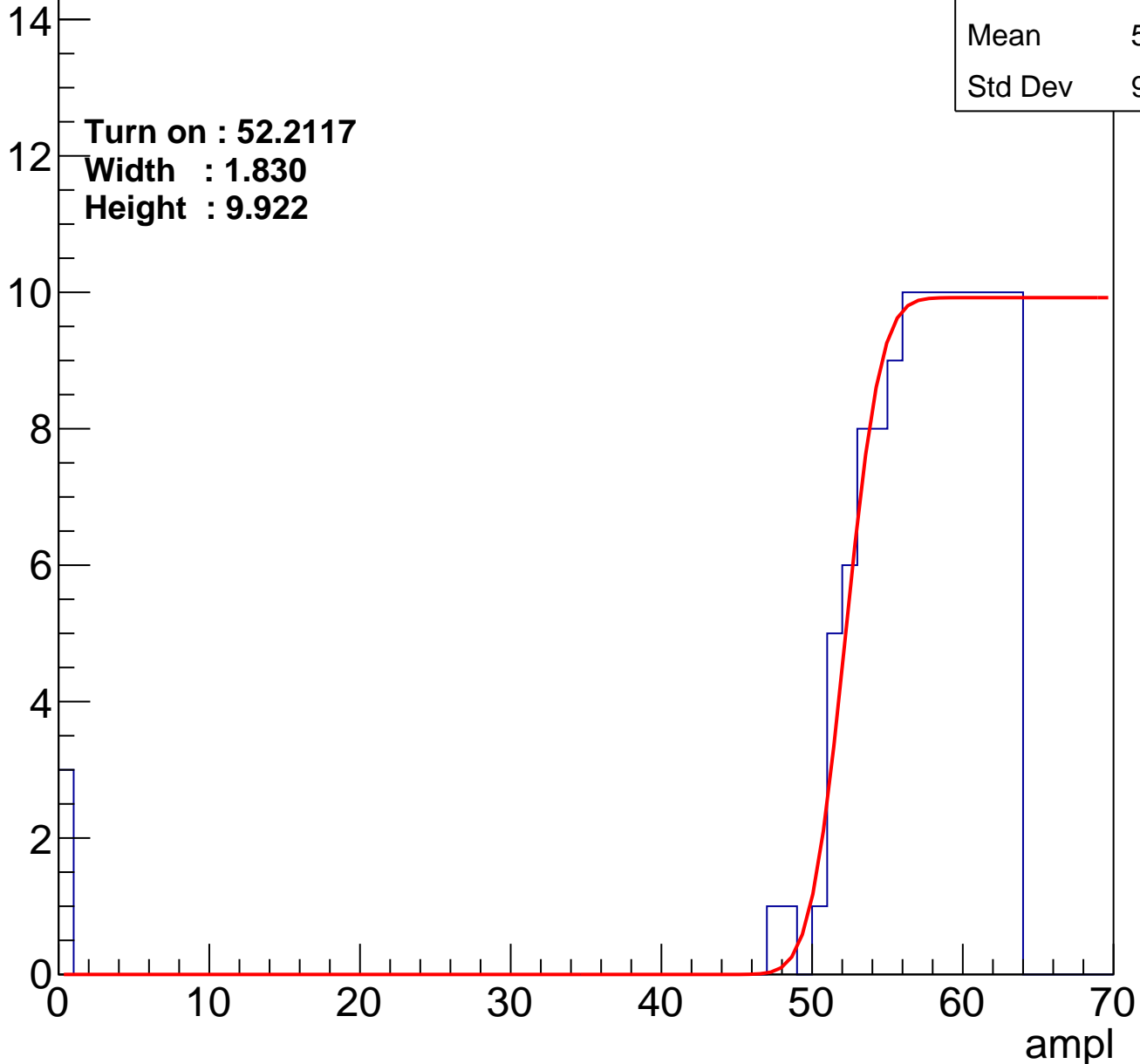
Entries	122
Mean	55.93
Std Dev	9.634

Turn on : 52.2117

Width : 1.830

Height : 9.922

Entry



# B0L103S, U19-ch11

calib\_packv5\_040323\_1717.root, FC#2, port C3

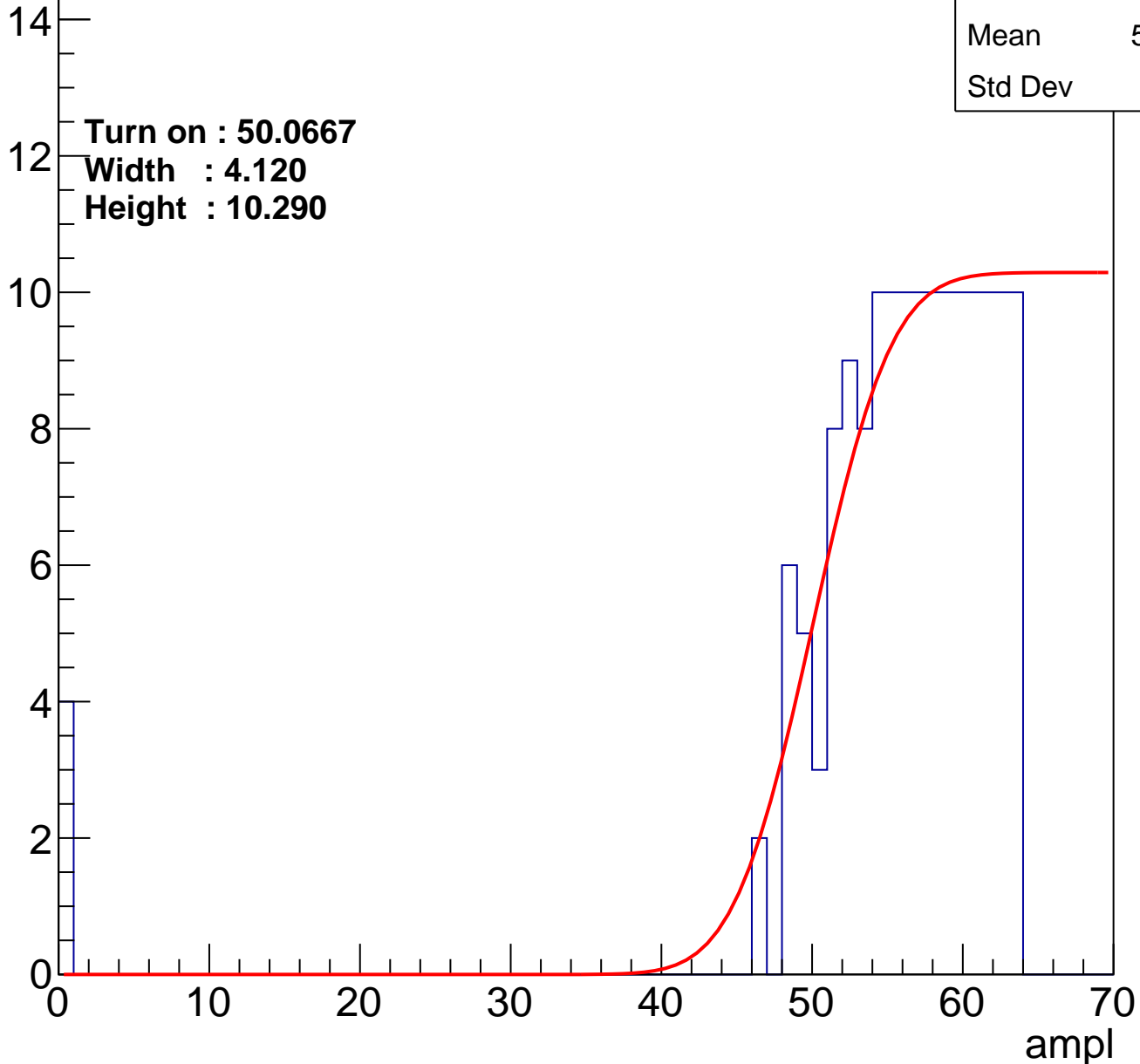
Entries	145
Mean	54.66
Std Dev	10.2

Turn on : 50.0667

Width : 4.120

Height : 10.290

Entry



# B0L103S, U19-ch12

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	56.73
Std Dev	8.54

Turn on : 53.4181

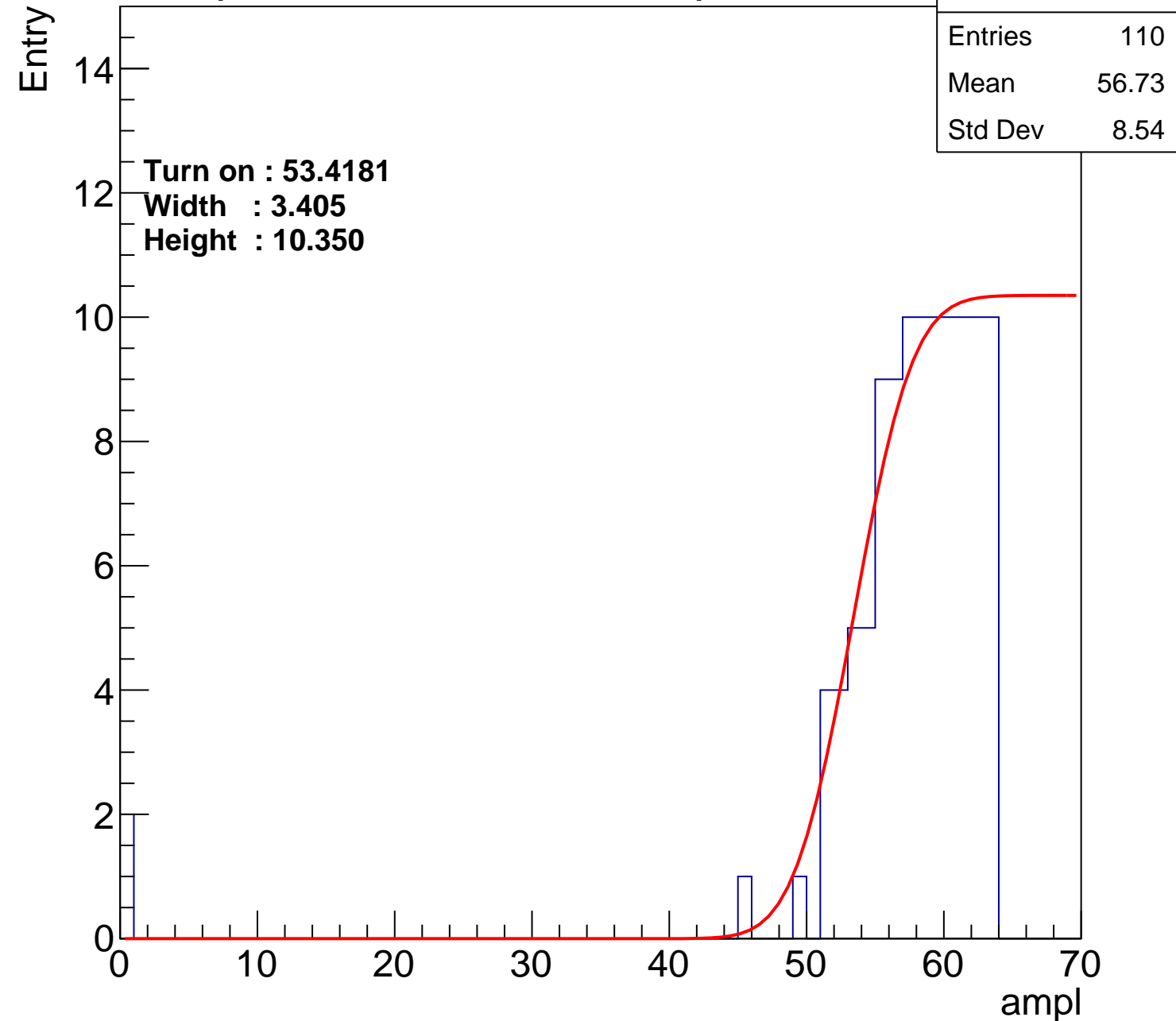
Width : 3.405

Height : 10.350

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch13

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.91
Std Dev	9.686

Turn on : 52.4656

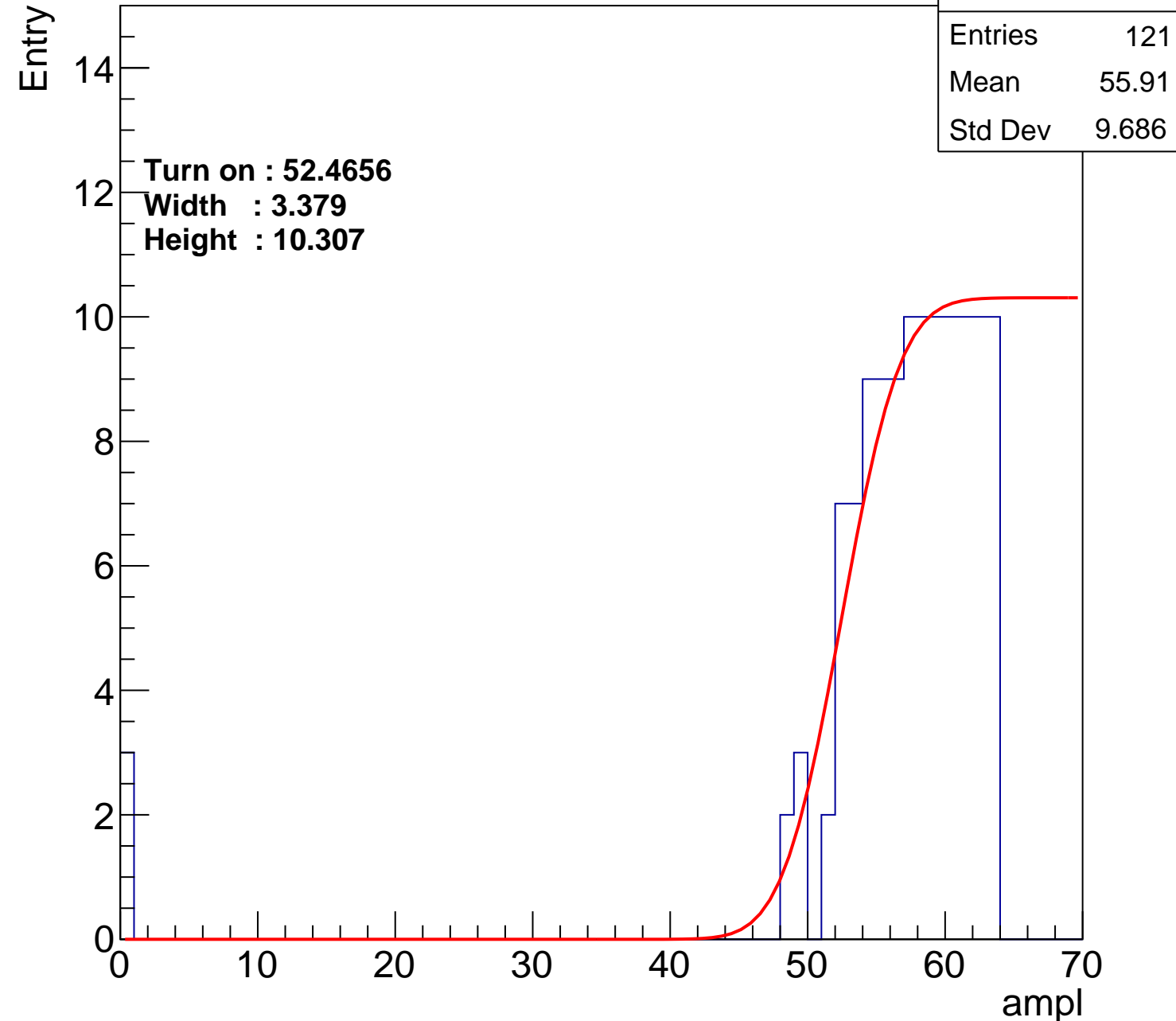
Width : 3.379

Height : 10.307

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch14

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.76
Std Dev	9.67

Turn on : 52.6587

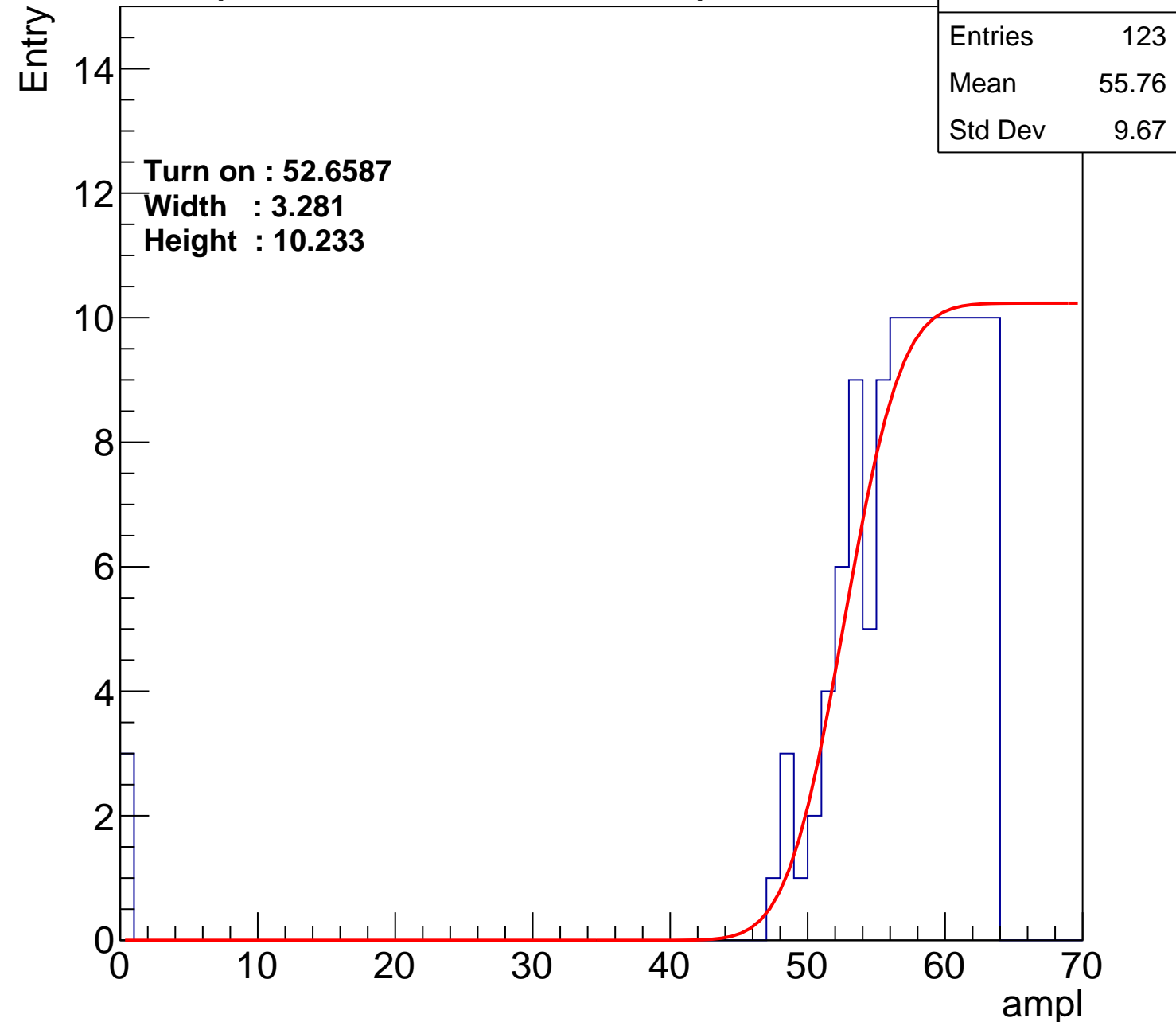
Width : 3.281

Height : 10.233

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





**calib\_packv5\_040323\_1717.root, FC#2, port C3**

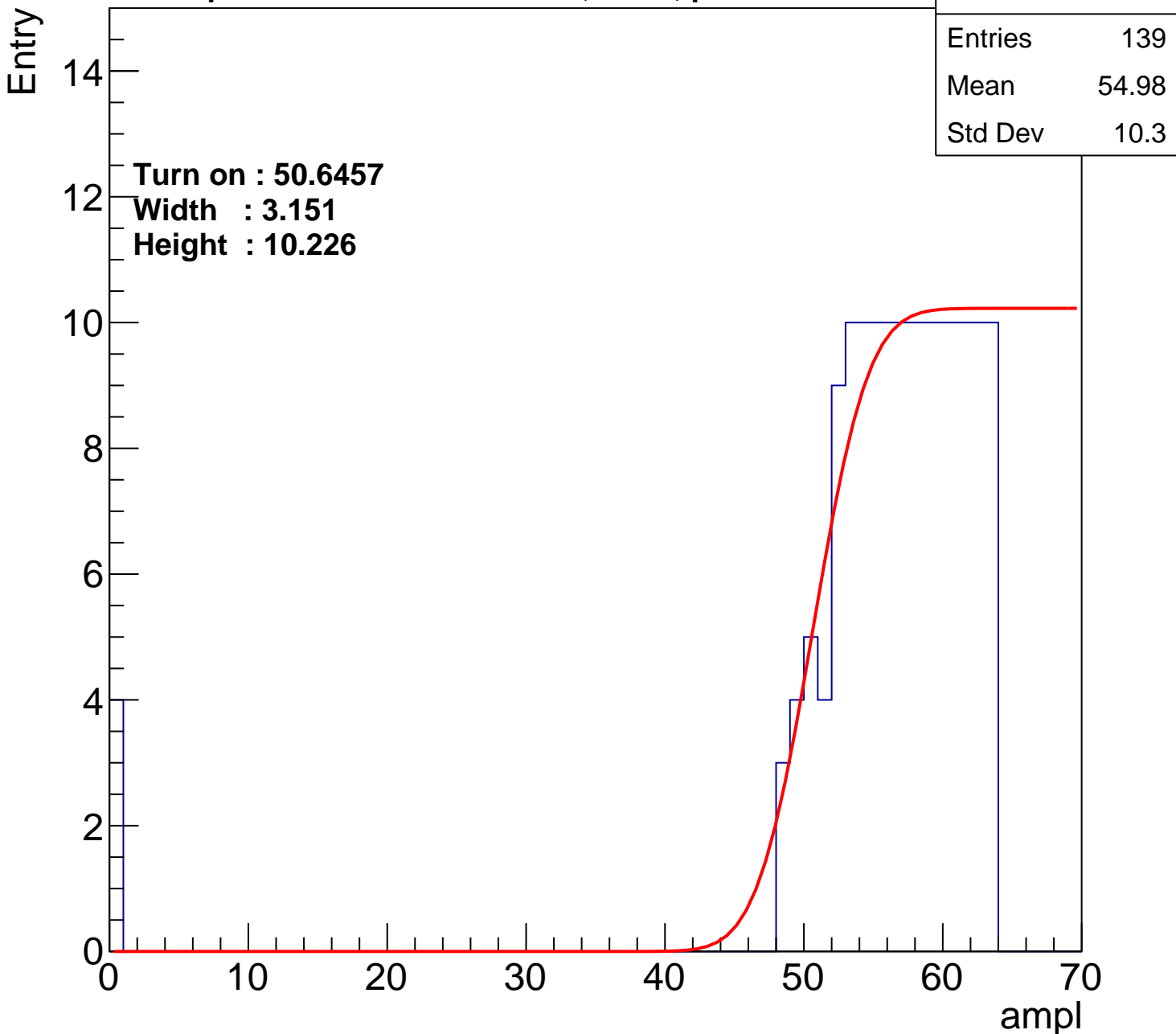
Entries	139
Mean	54.98
Std Dev	10.3

Mean	54.98
------	-------

Std Dev	10.3
---------	------

**Width : 3.151**

**Height : 10.226**



# B0L103S, U19-ch16

calib\_packv5\_040323\_1717.root, FC#2, port C3

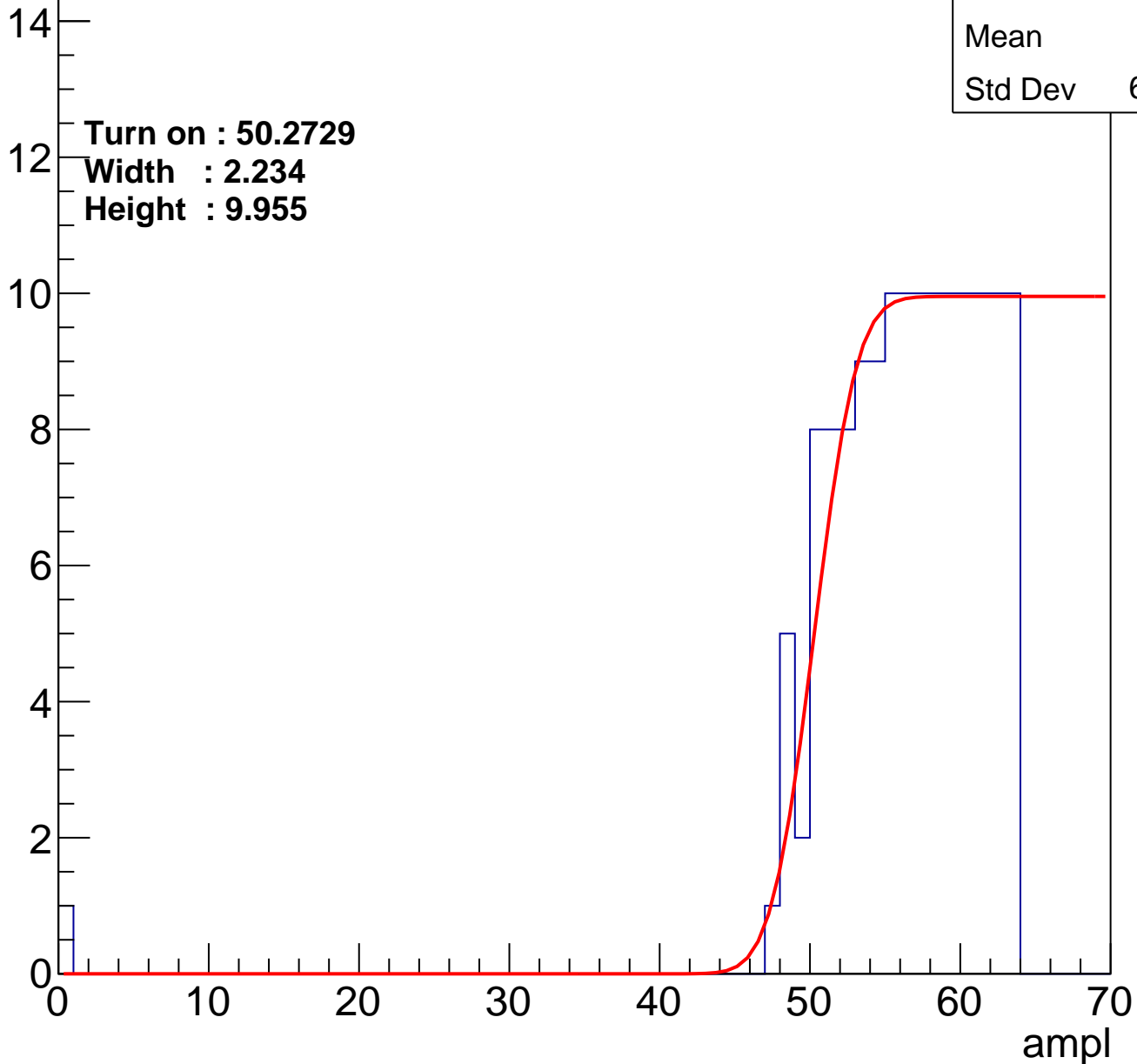
Entries	141
Mean	55.9
Std Dev	6.401

Turn on : 50.2729

Width : 2.234

Height : 9.955

Entry



# B0L103S, U19-ch17

calib\_packv5\_040323\_1717.root, FC#2, port C3

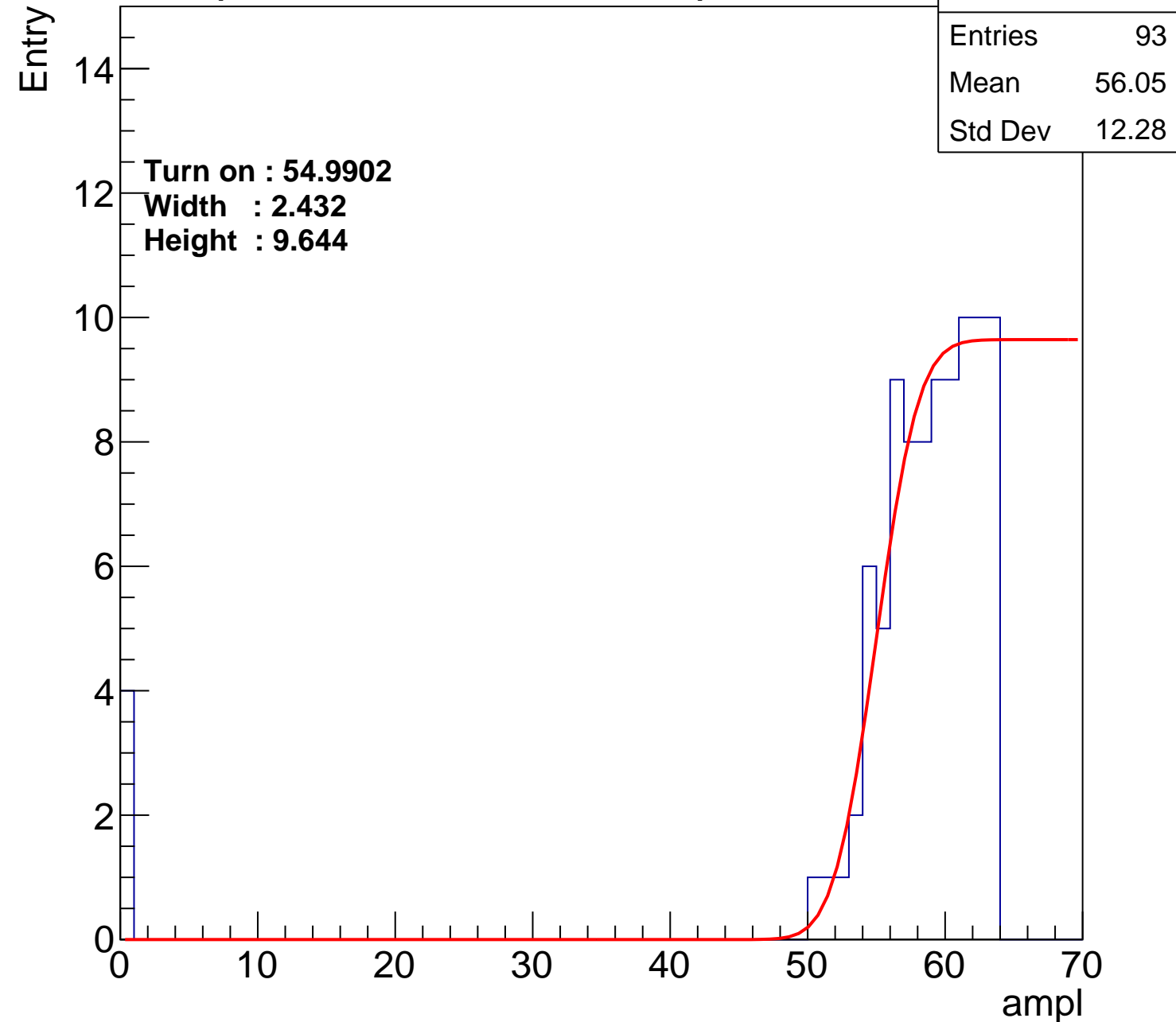
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 54.9902  
Width : 2.432  
Height : 9.644

Entries	93
Mean	56.05
Std Dev	12.28

ampl



# B0L103S, U19-ch18

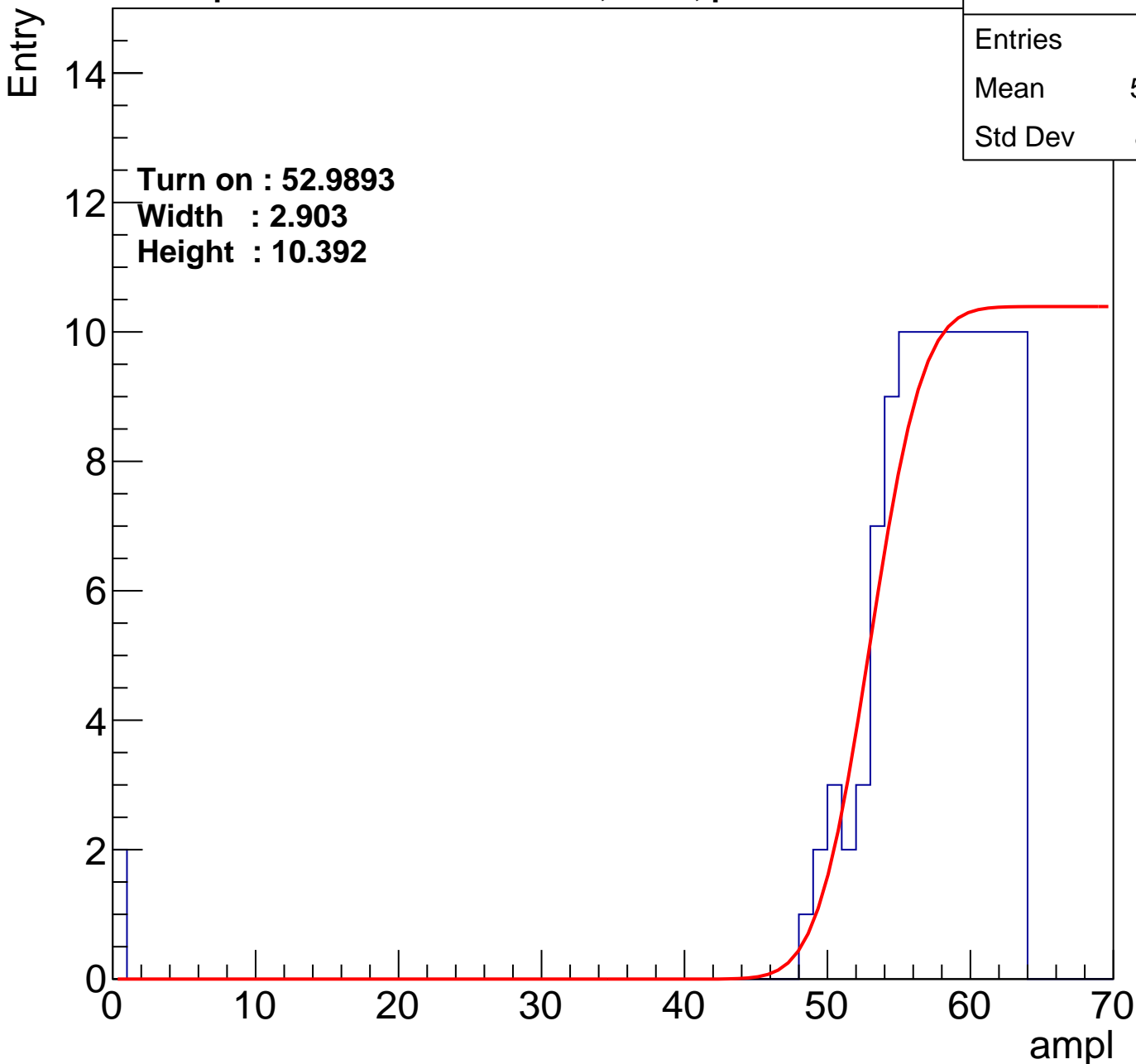
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	119
Mean	56.48
Std Dev	8.261

**Turn on : 52.9893**

**Width : 2.903**

**Height : 10.392**



# B0L103S, U19-ch19

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	54.42
Std Dev	11.18

Turn on : 50.1863

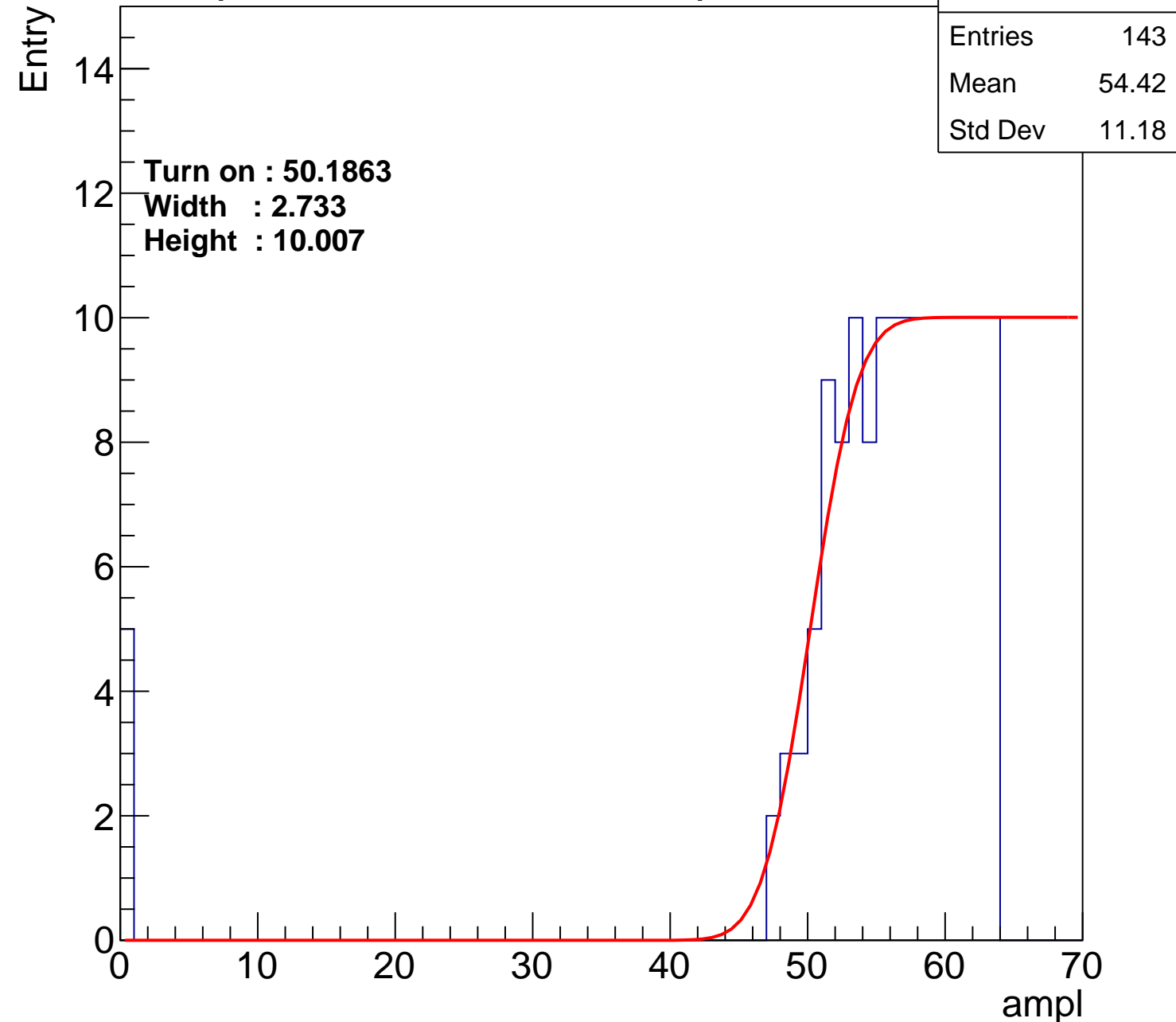
Width : 2.733

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch20

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	56.38
Std Dev	6.469

Turn on : 51.0297

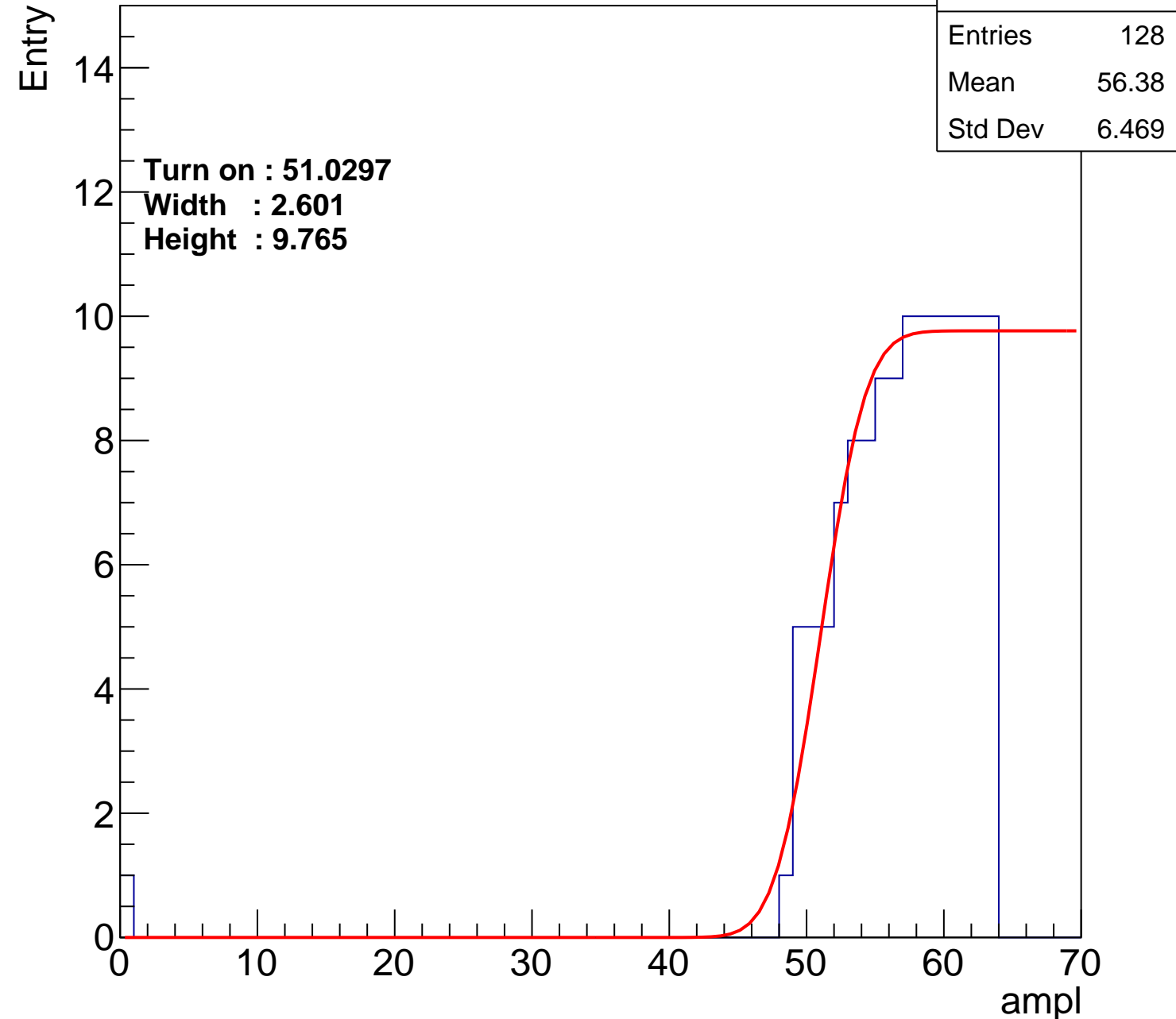
Width : 2.601

Height : 9.765

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch21

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	54.61
Std Dev	11.49

Turn on : 51.9551

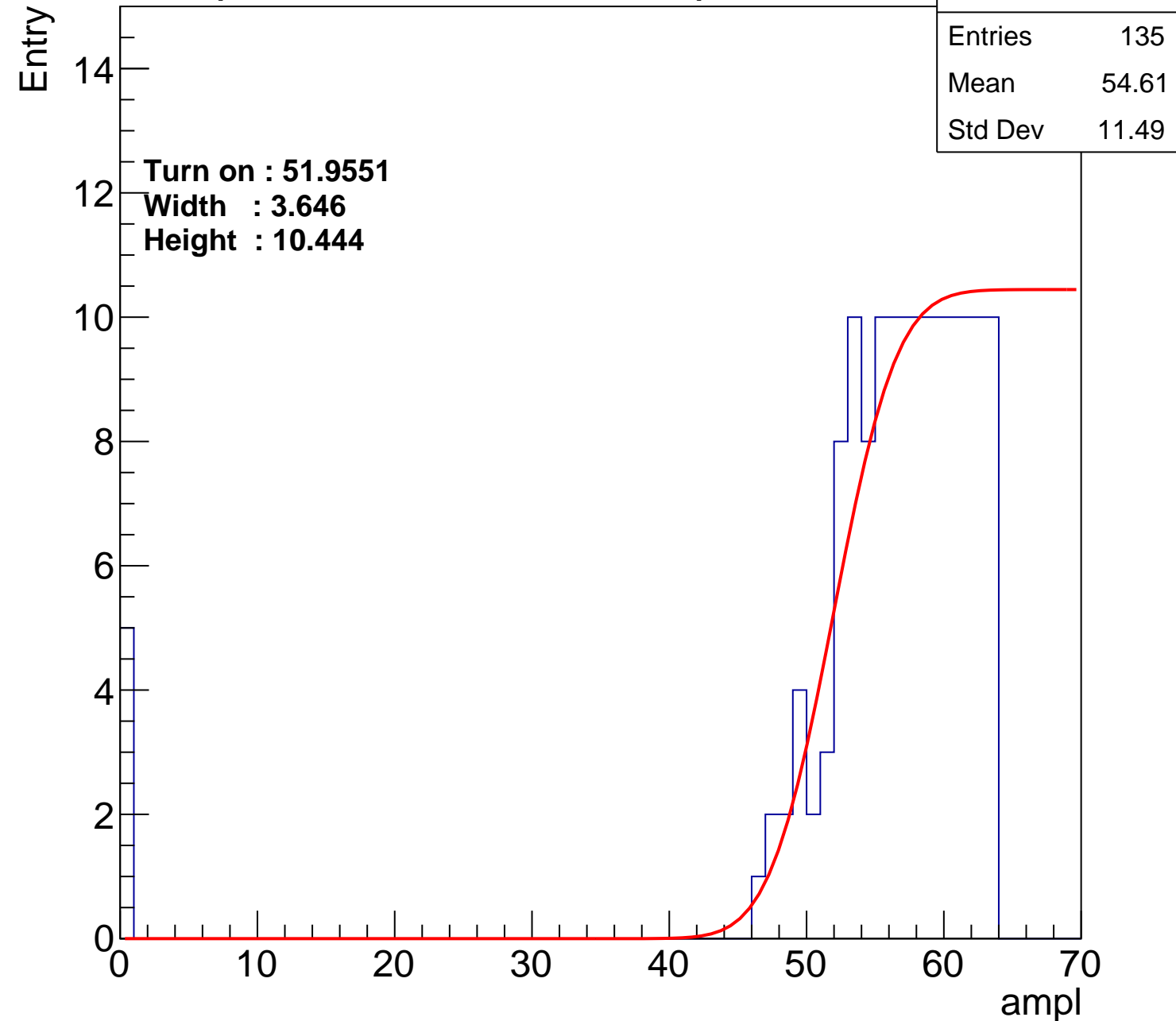
Width : 3.646

Height : 10.444

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch22

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	55.2
Std Dev	9.271

Turn on : 51.7247

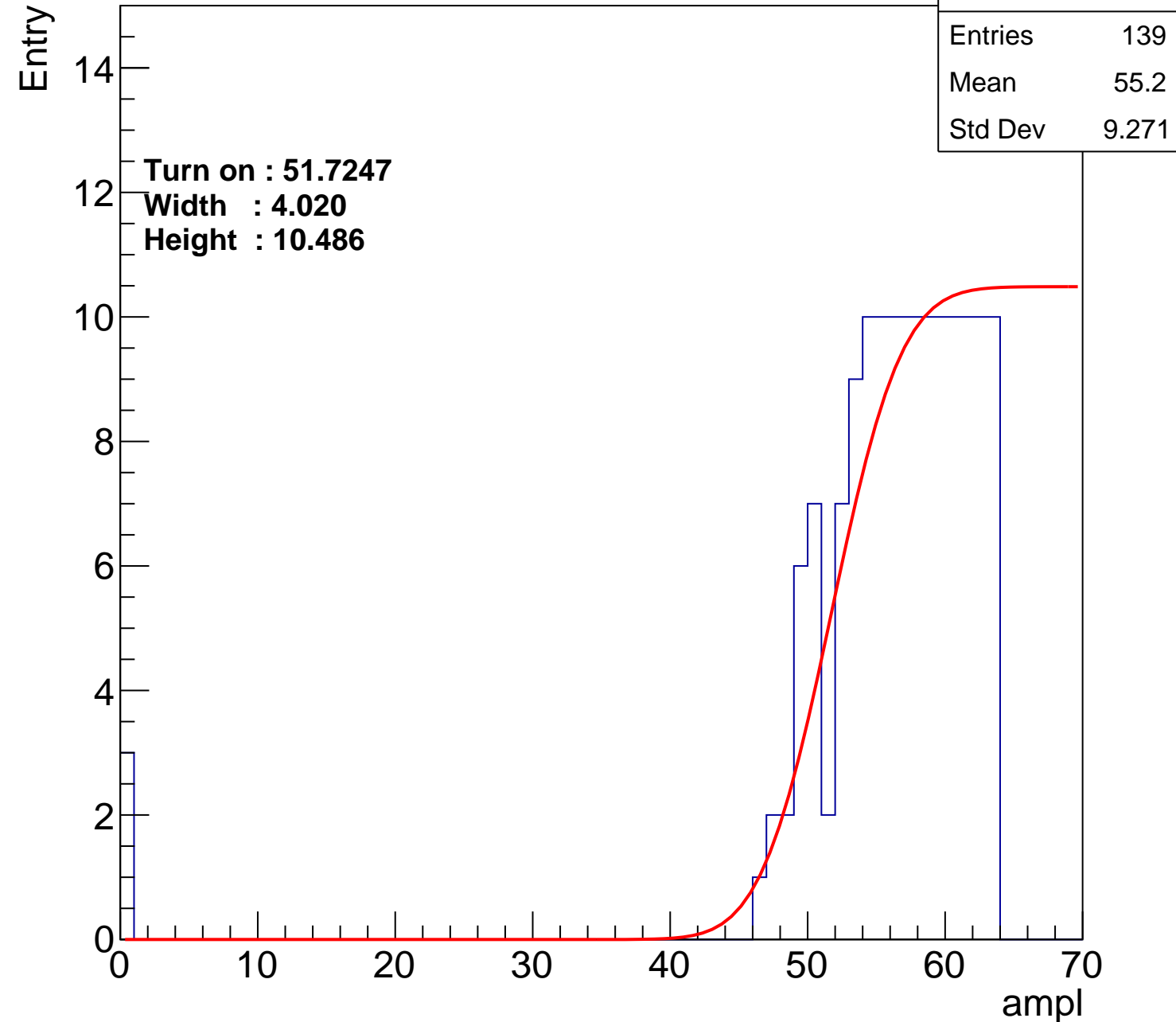
Width : 4.020

Height : 10.486

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch23

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	56.45
Std Dev	6.488

Turn on : 51.0983

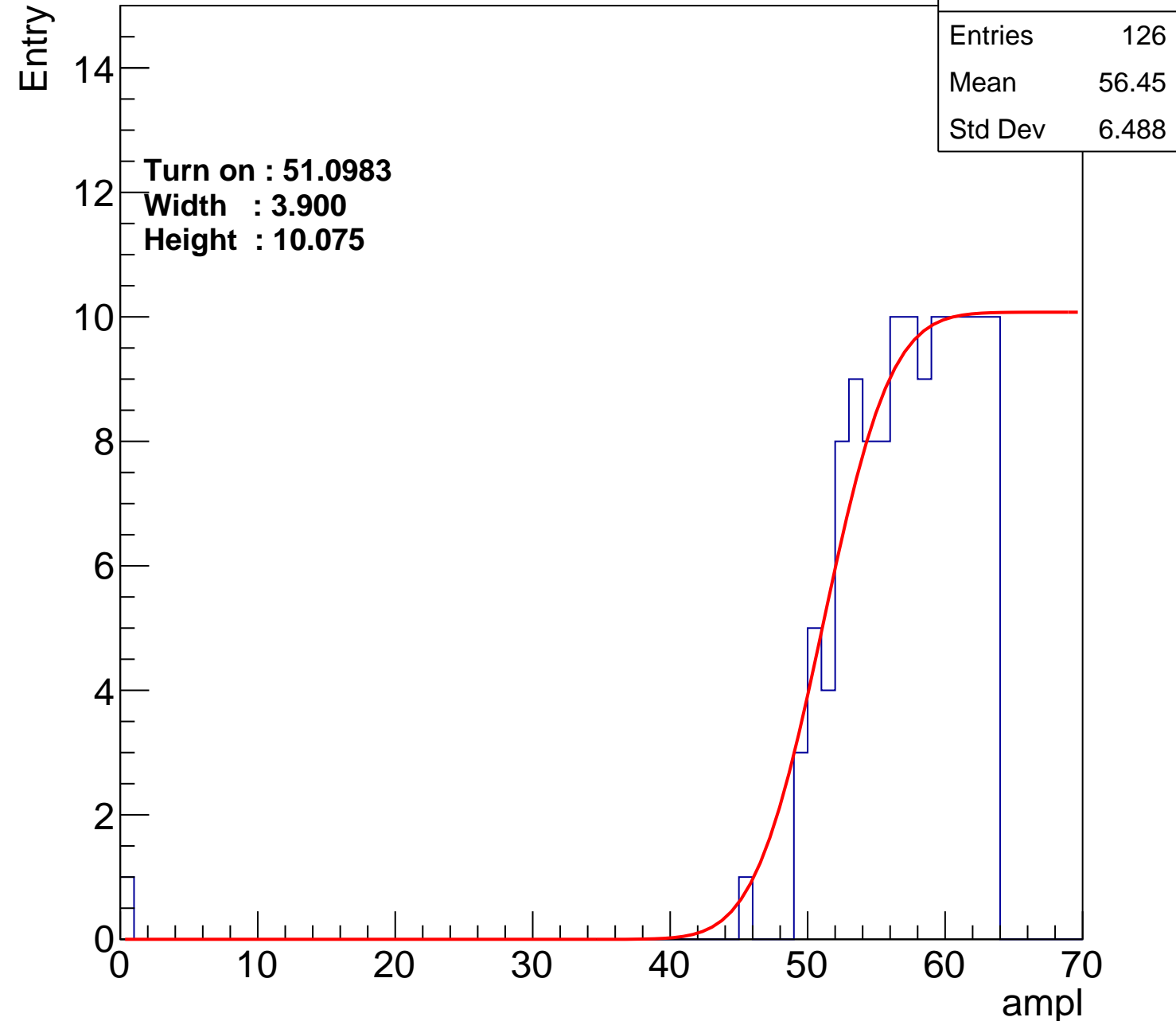
Width : 3.900

Height : 10.075

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch24

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	56.22
Std Dev	8.184

Turn on : 51.9828

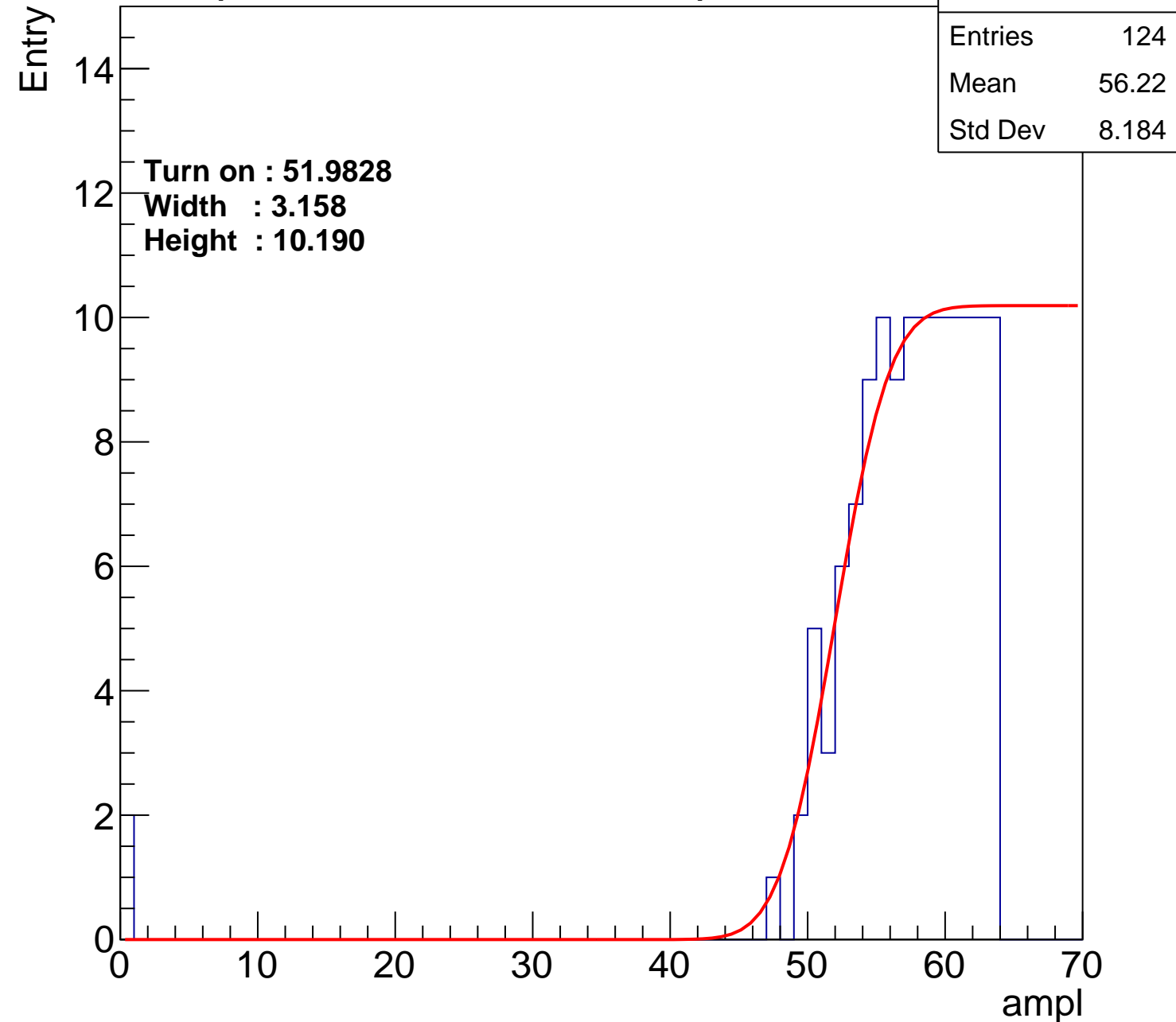
Width : 3.158

Height : 10.190

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch25

calib\_packv5\_040323\_1717.root, FC#2, port C3

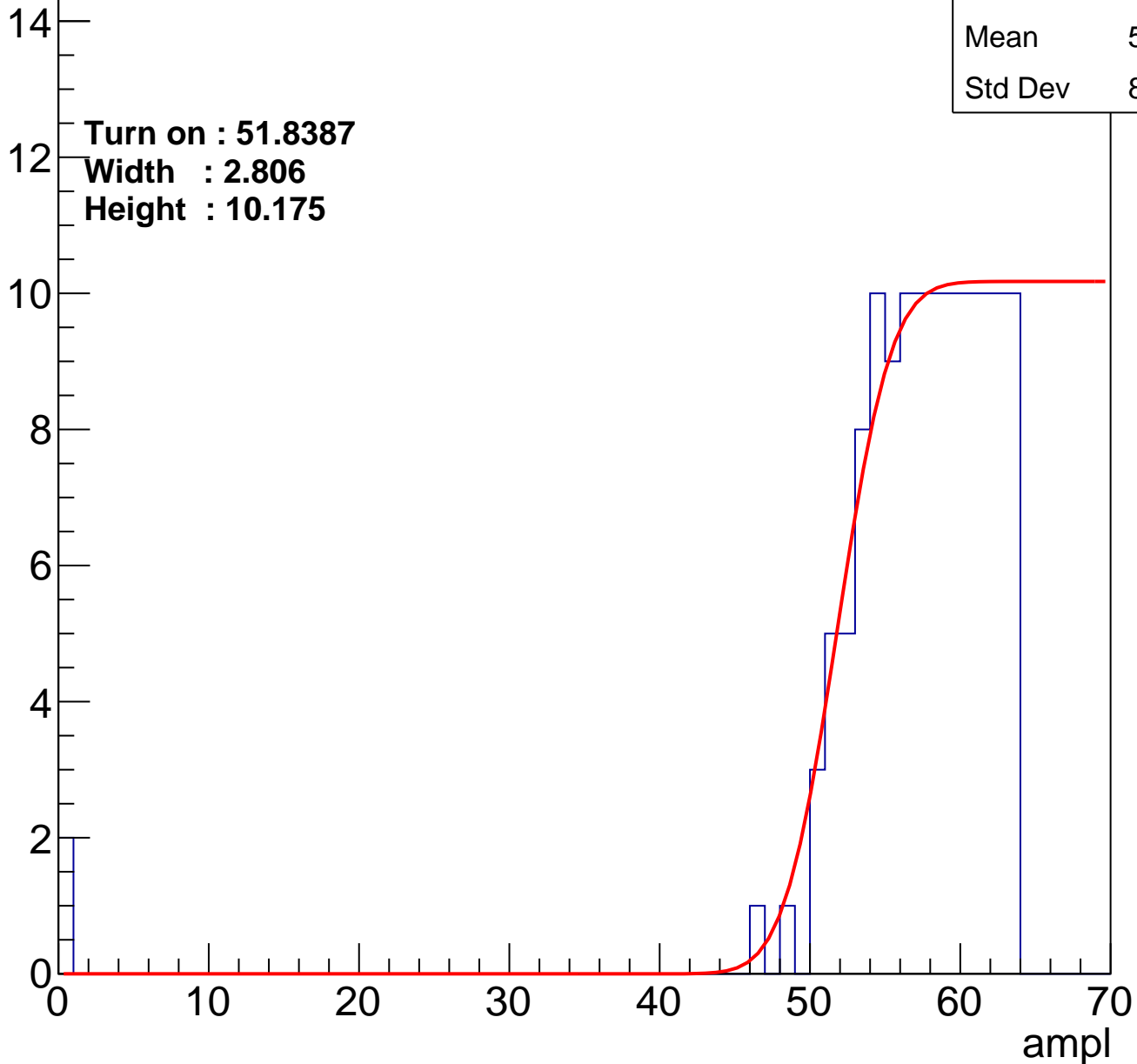
Entries	124
Mean	56.27
Std Dev	8.162

Turn on : 51.8387

Width : 2.806

Height : 10.175

Entry



# B0L103S, U19-ch26

calib\_packv5\_040323\_1717.root, FC#2, port C3

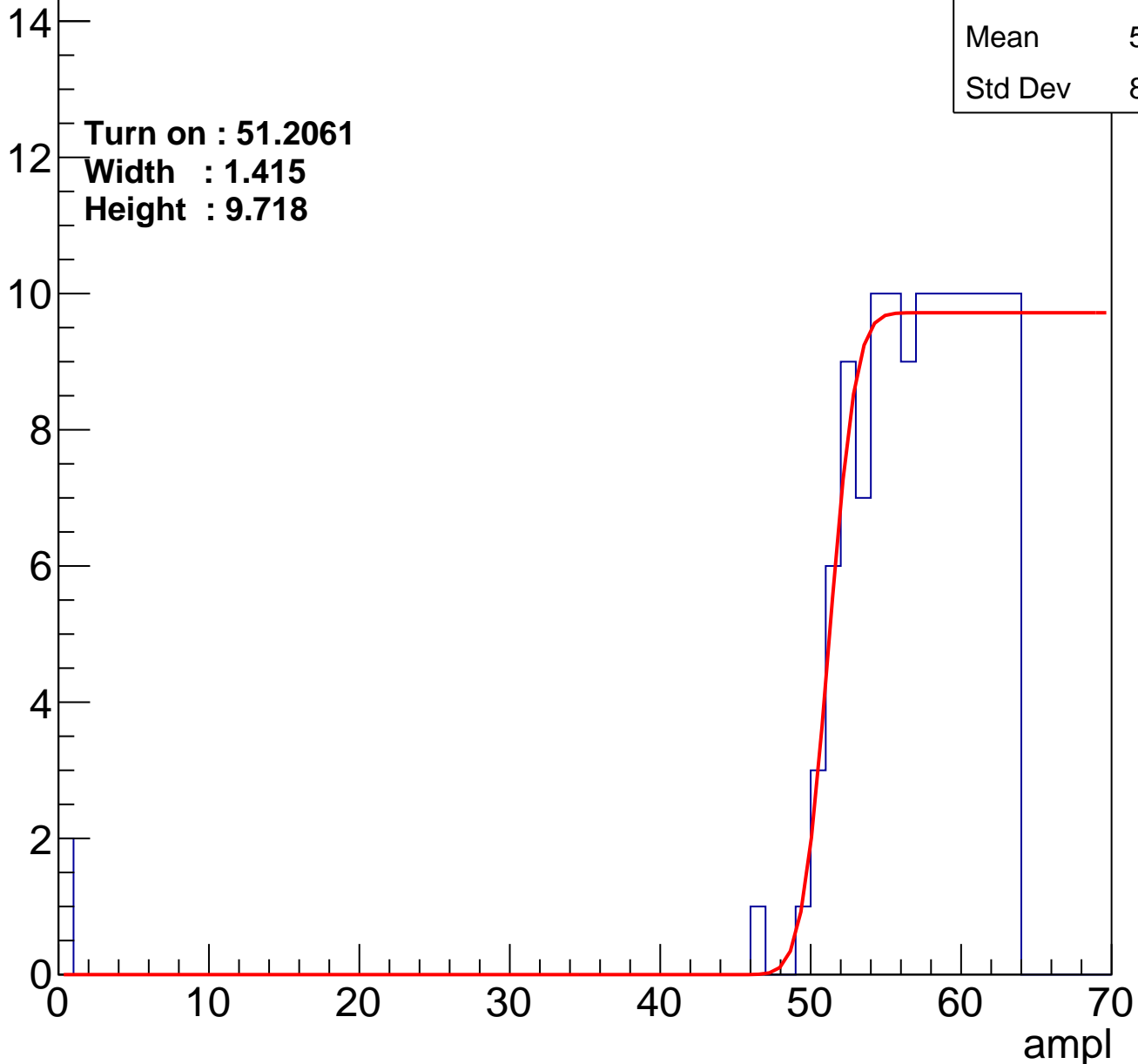
Entries	128
Mean	56.12
Std Dev	8.069

Turn on : 51.2061

Width : 1.415

Height : 9.718

Entry



# B0L103S, U19-ch27

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	56.18
Std Dev	6.496

Turn on : 50.6359

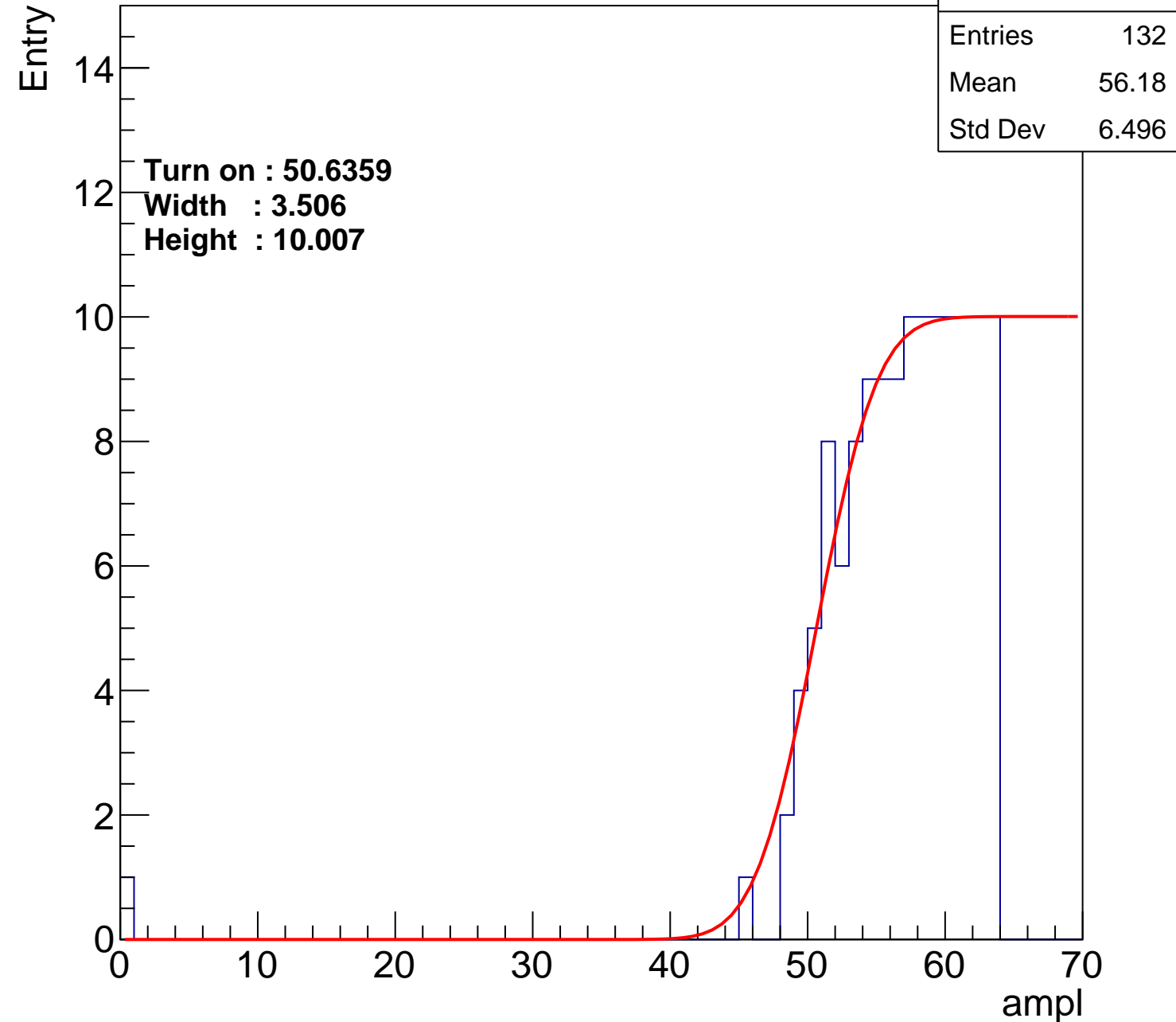
Width : 3.506

Height : 10.007

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch28

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	135
Mean	55.46
Std Dev	9.31

Turn on : 51.6489

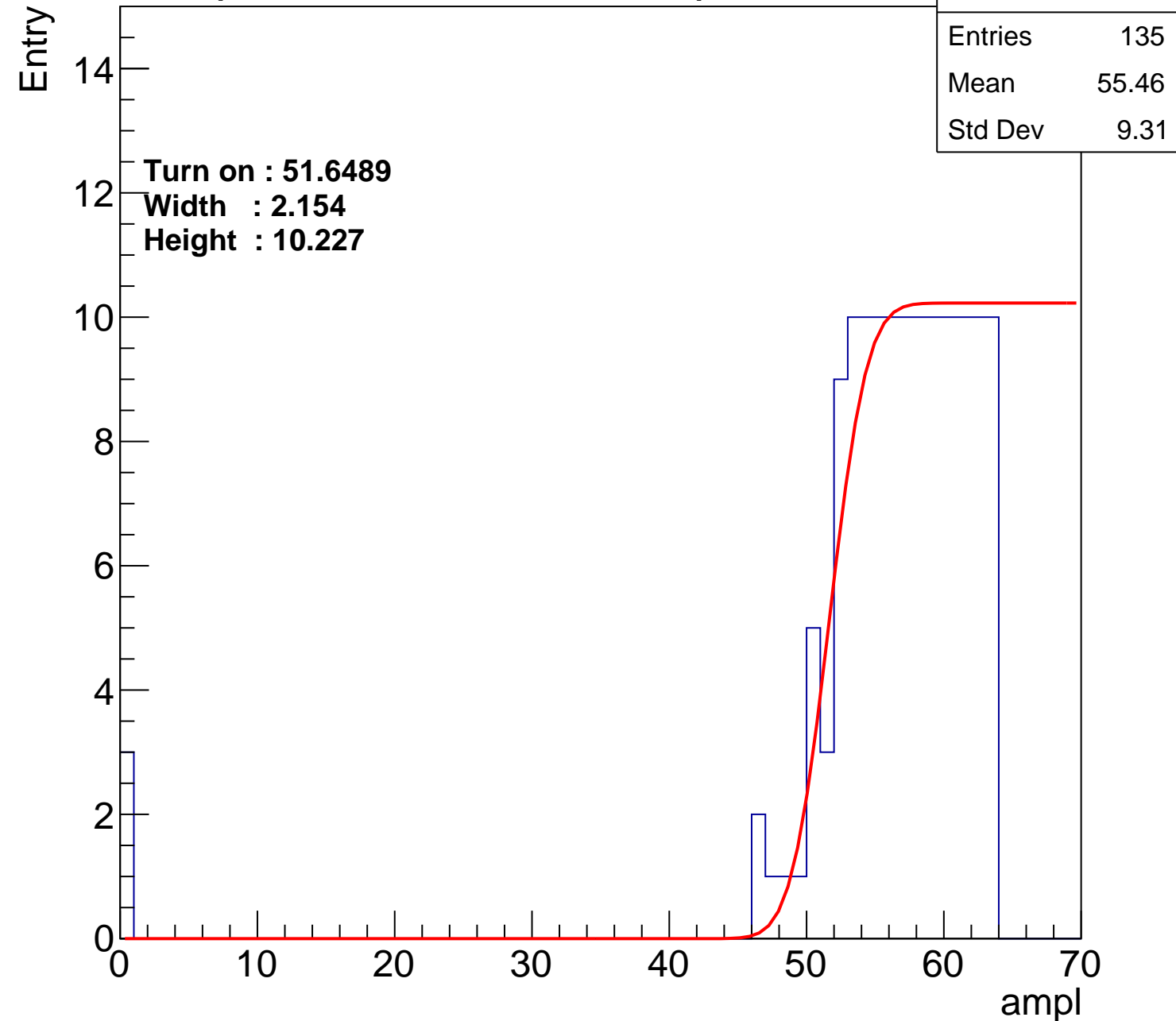
Width : 2.154

Height : 10.227

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch29

calib\_packv5\_040323\_1717.root, FC#2, port C3

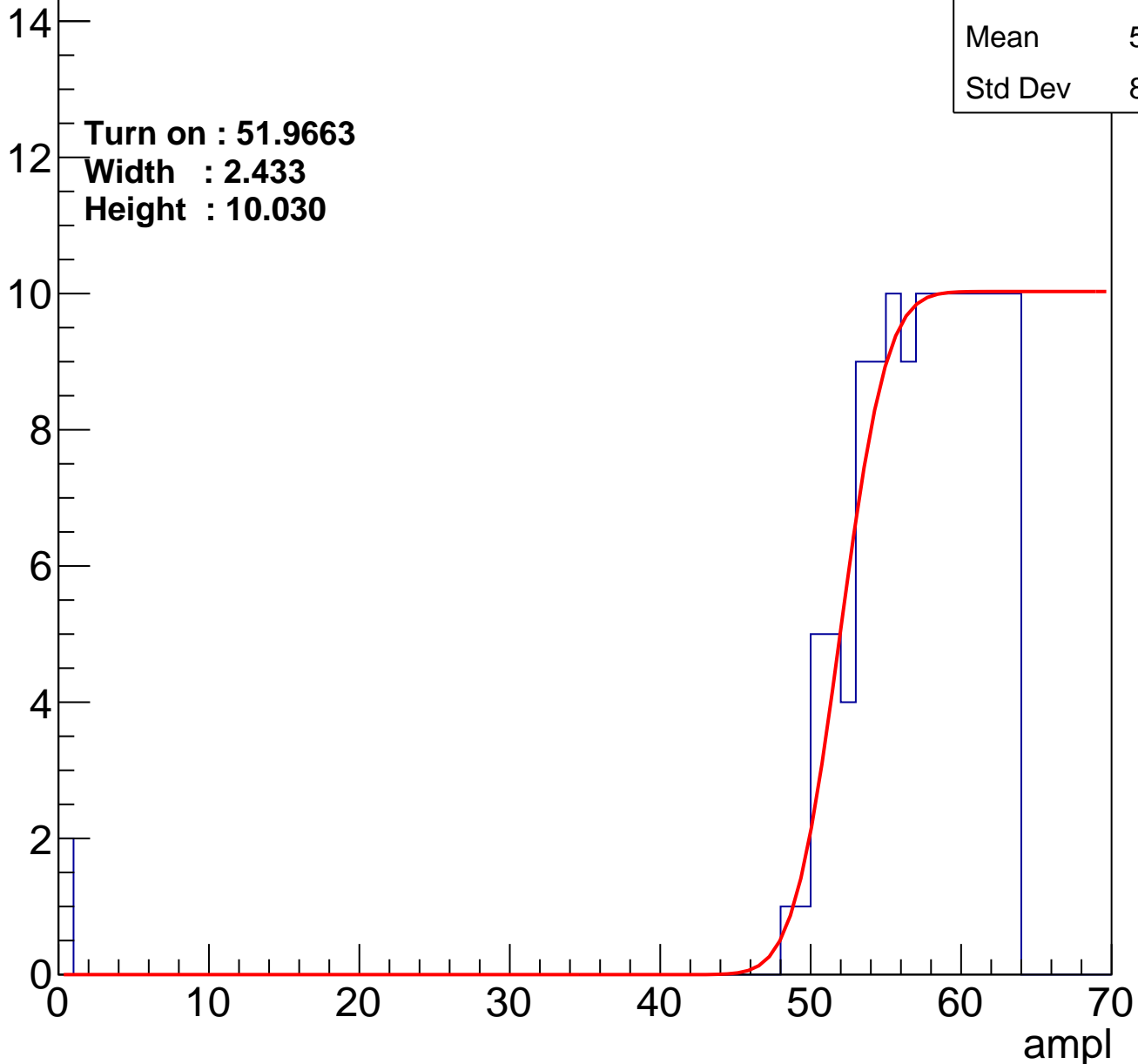
Entries	125
Mean	56.22
Std Dev	8.136

Turn on : 51.9663

Width : 2.433

Height : 10.030

Entry



# B0L103S, U19-ch30

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.63
Std Dev	9.451

Turn on : 51.2419

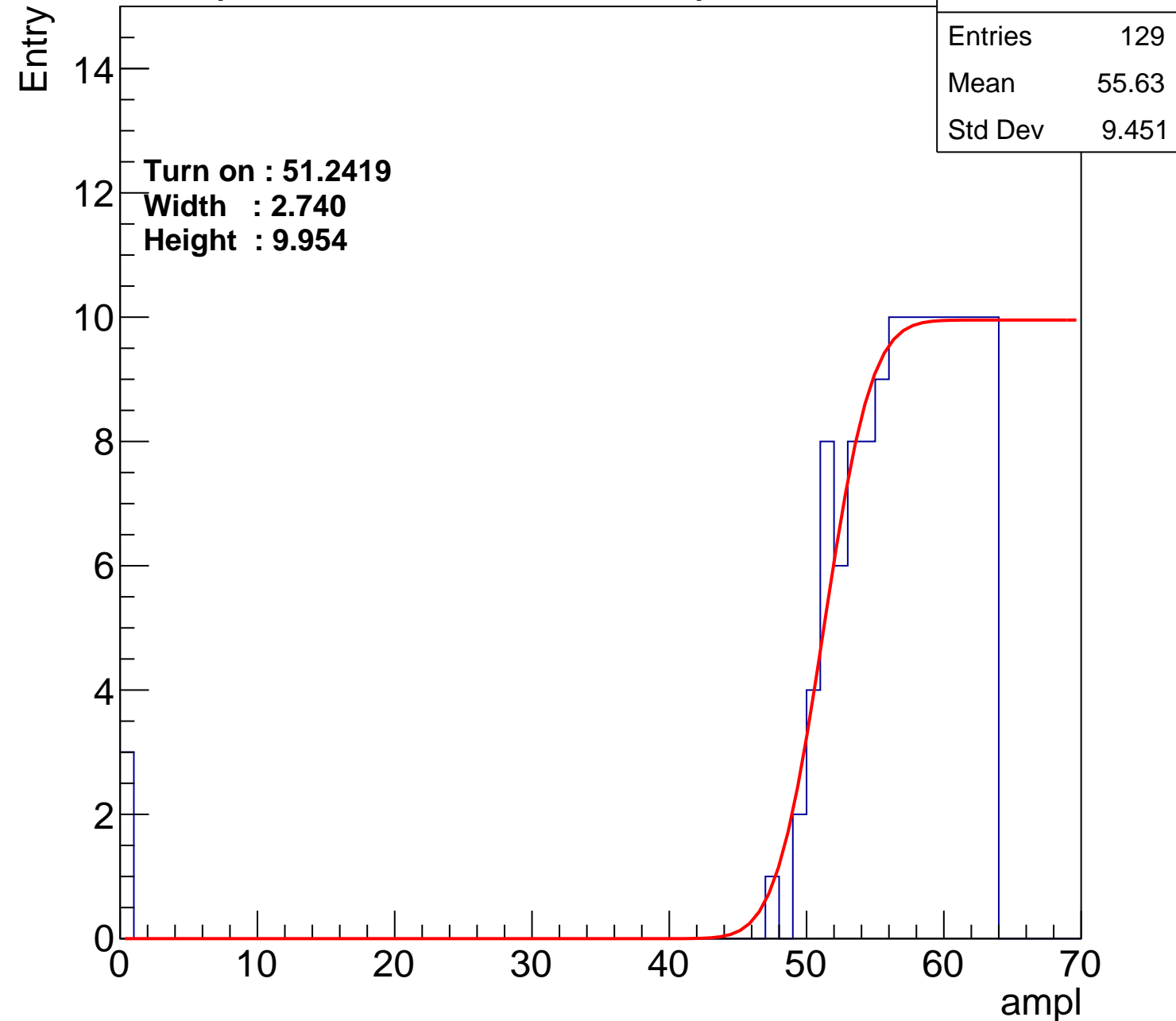
Width : 2.740

Height : 9.954

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch31

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	53.85
Std Dev	13.18

Turn on : 51.4582

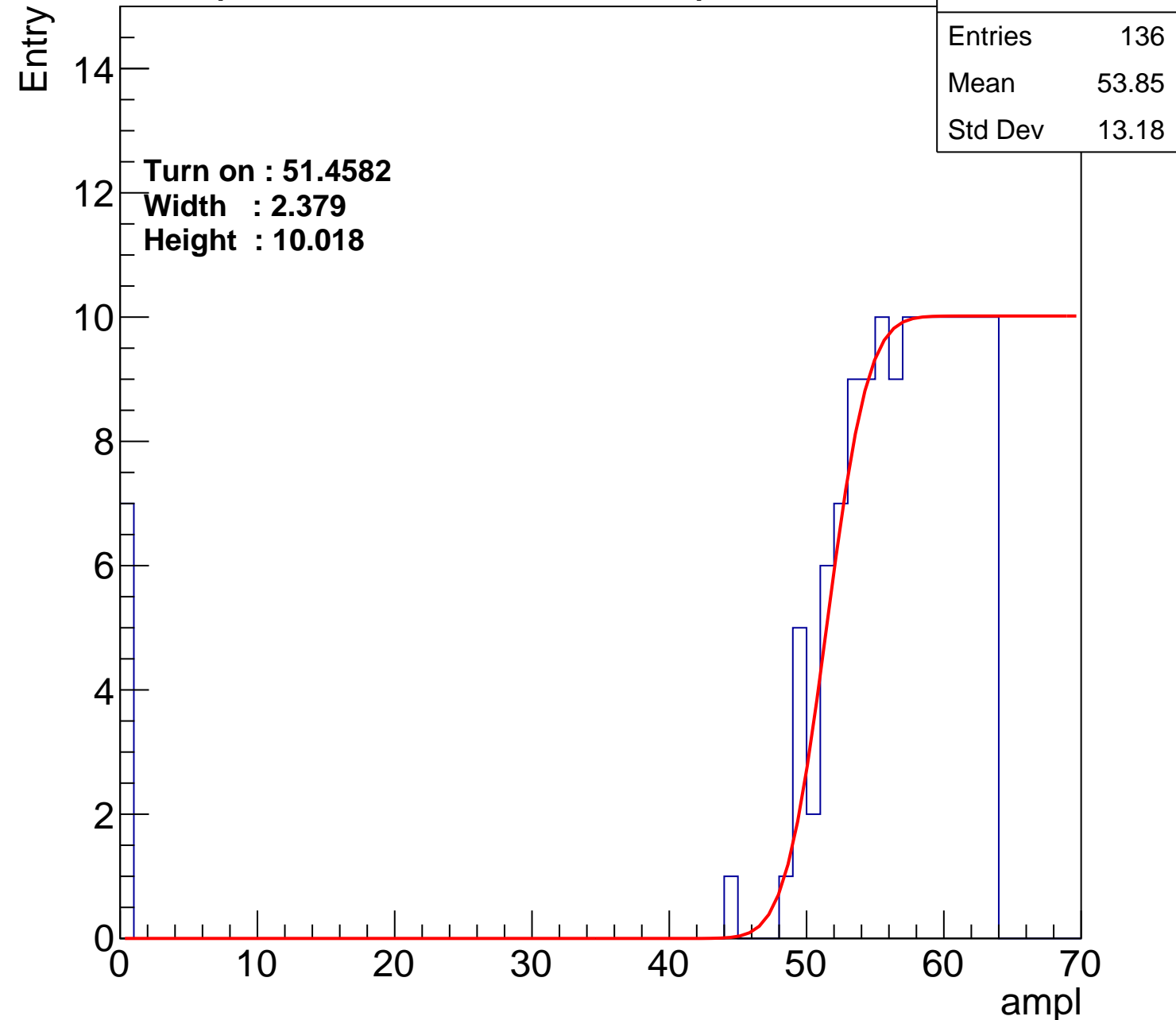
Width : 2.379

Height : 10.018

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch32

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	155
Mean	54.88
Std Dev	7.882

Turn on : 49.0124

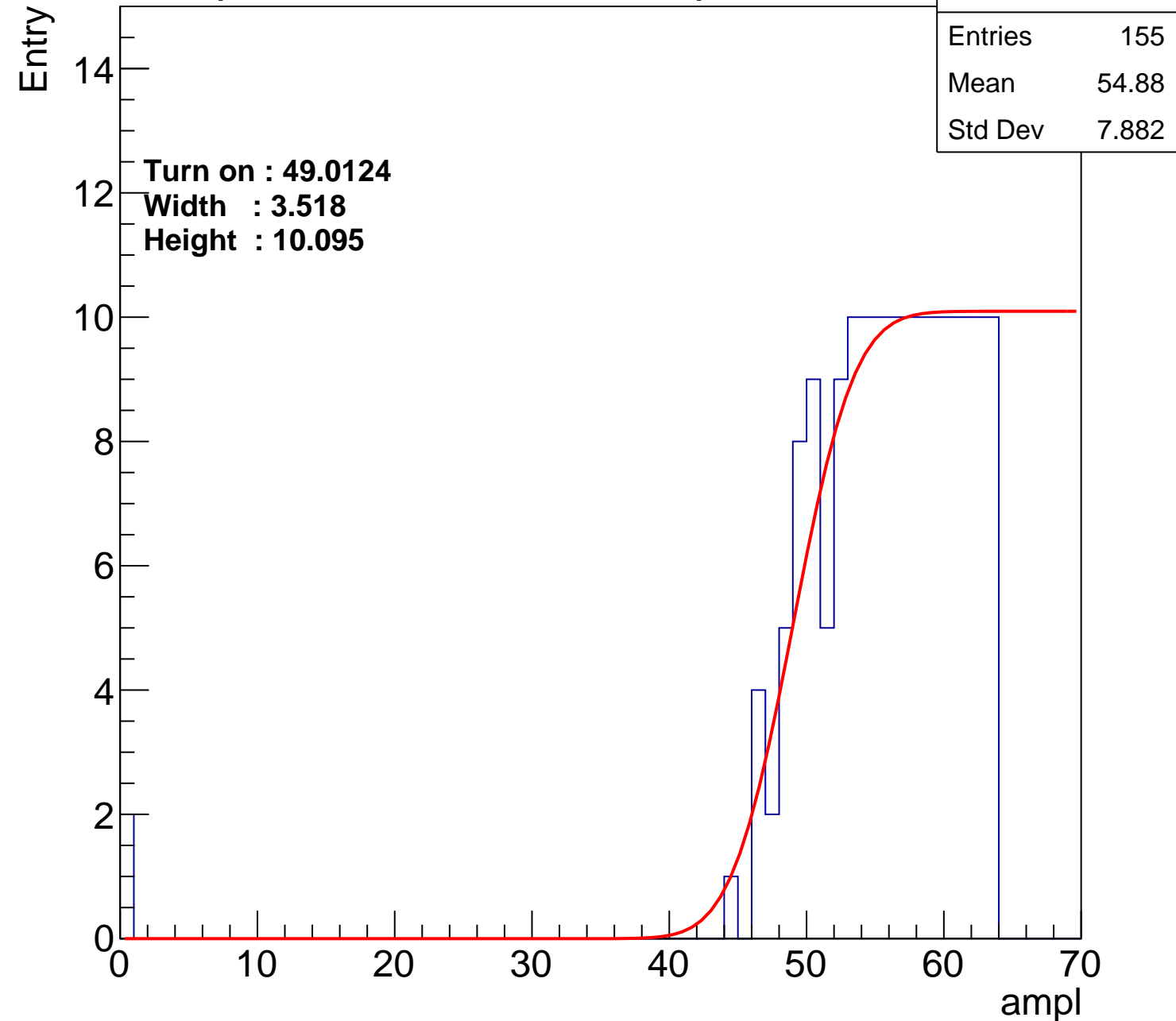
Width : 3.518

Height : 10.095

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch33

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	148
Mean	54.45
Std Dev	10.17

Turn on : 49.5624

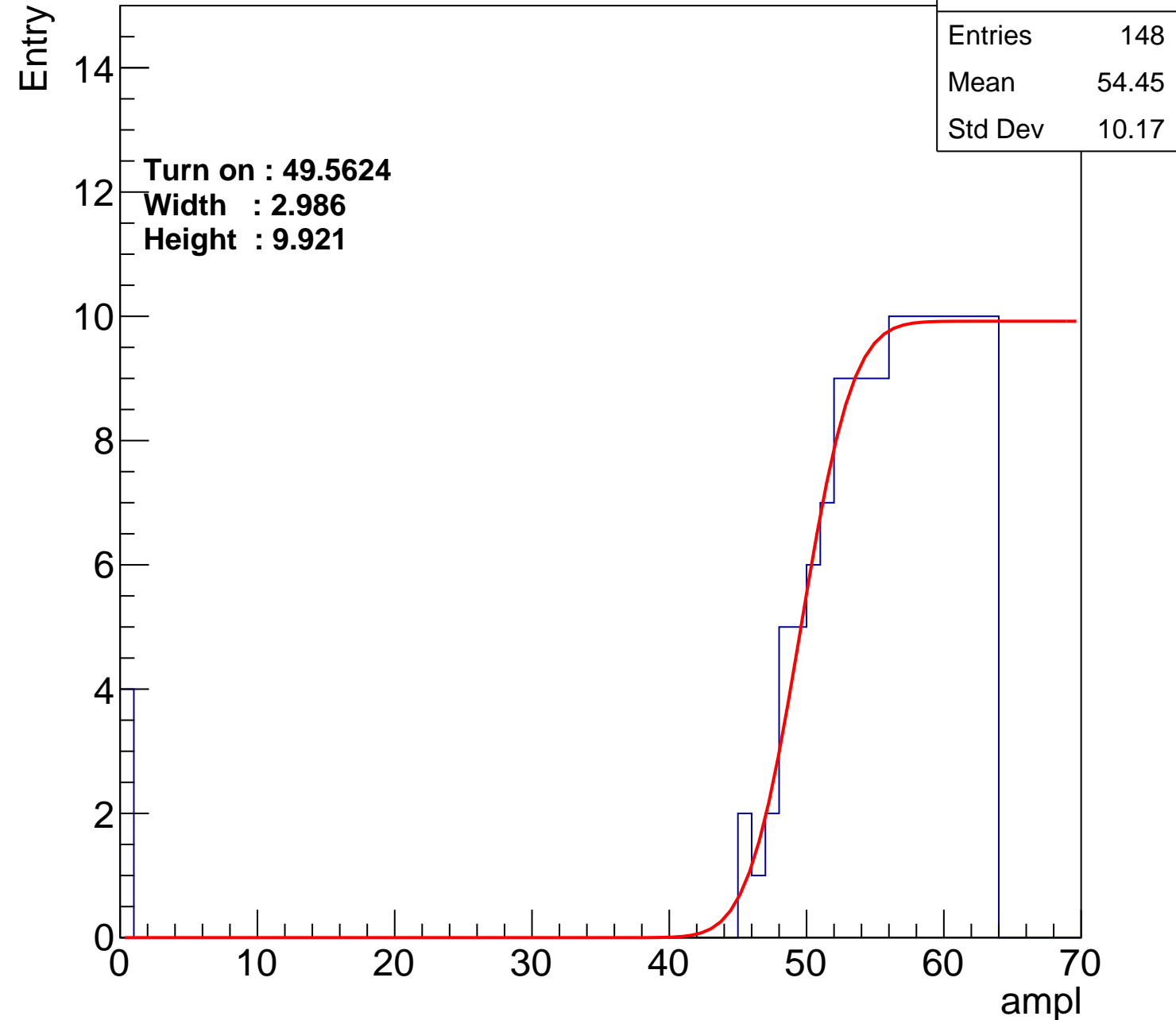
Width : 2.986

Height : 9.921

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch34

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.56
Std Dev	10.28

Turn on : 49.9133

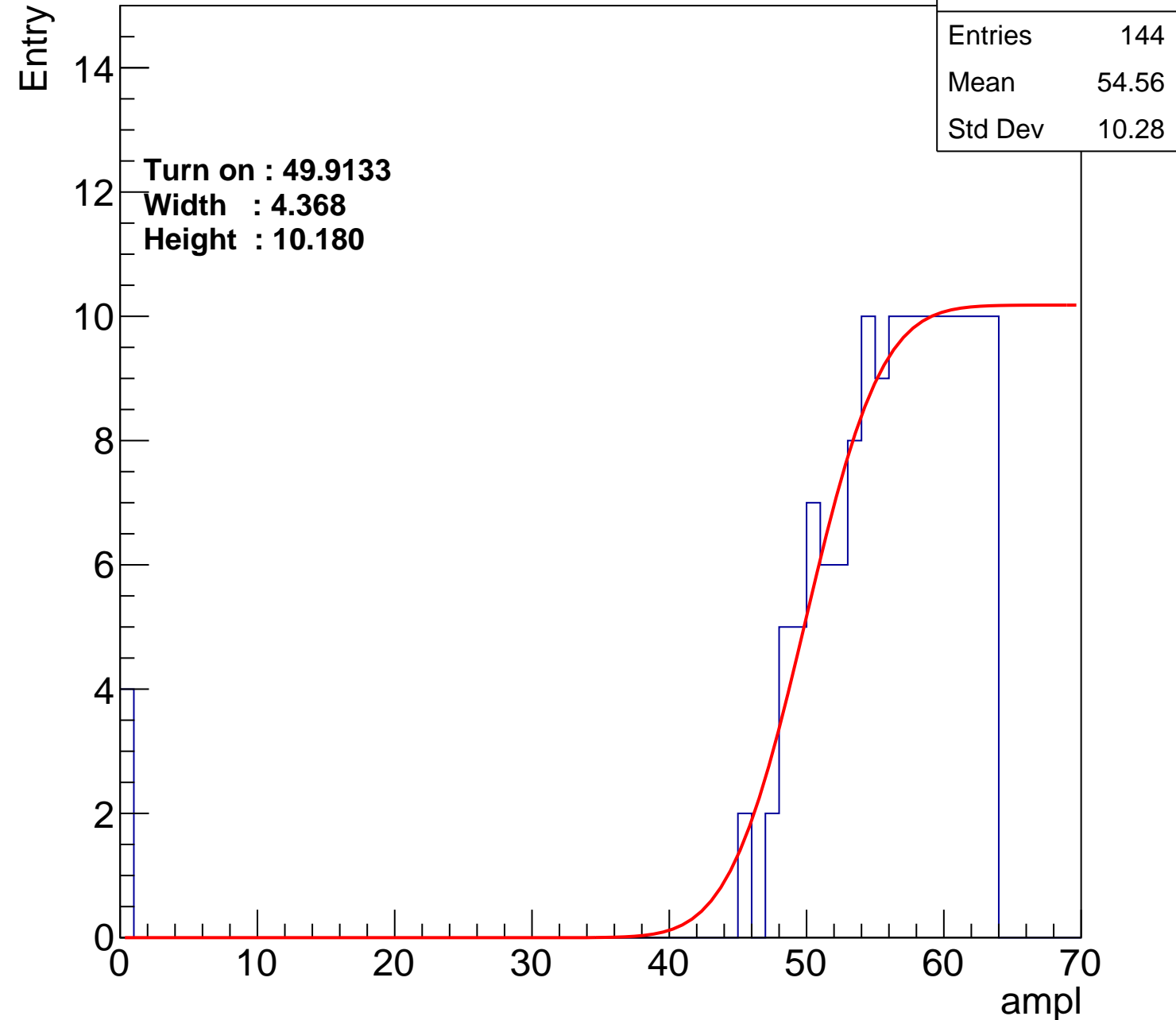
Width : 4.368

Height : 10.180

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch35

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	54.87
Std Dev	10.32

Turn on : 50.8277

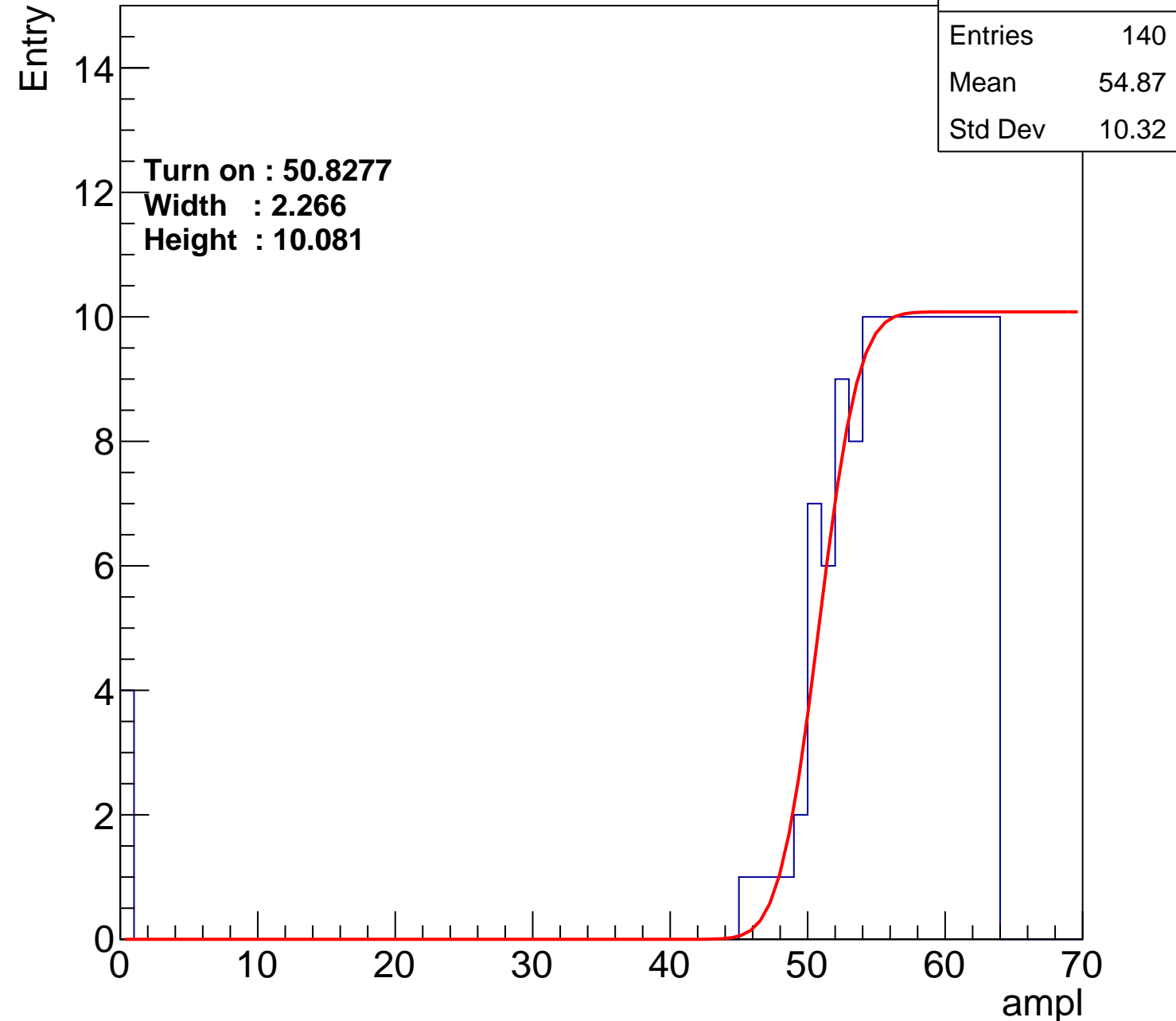
Width : 2.266

Height : 10.081

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch36

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	56.43
Std Dev	8.197

**Turn on : 53.0157**

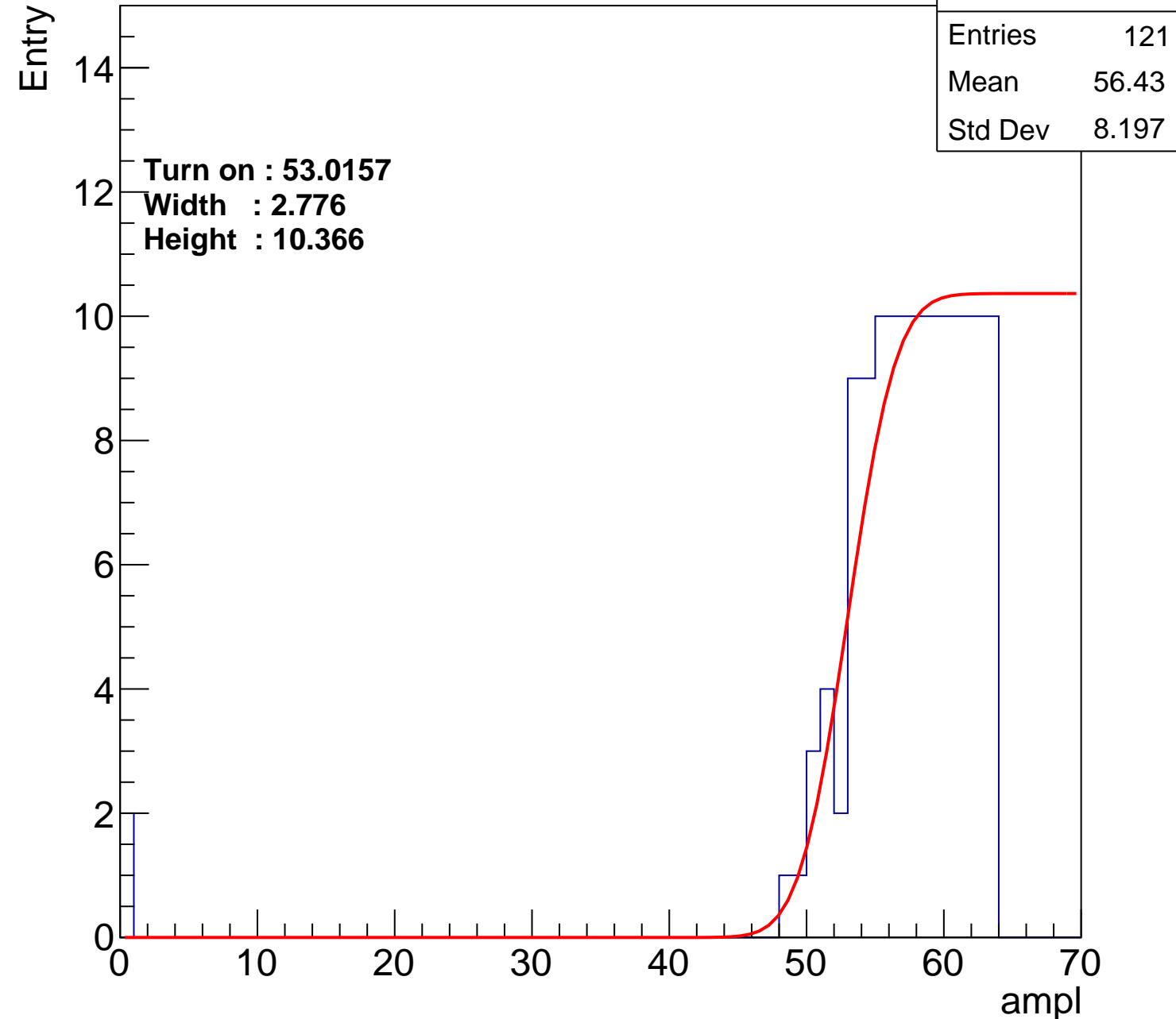
**Width : 2.776**

**Height : 10.366**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch37

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.37
Std Dev	11.34

Turn on : 51.5524

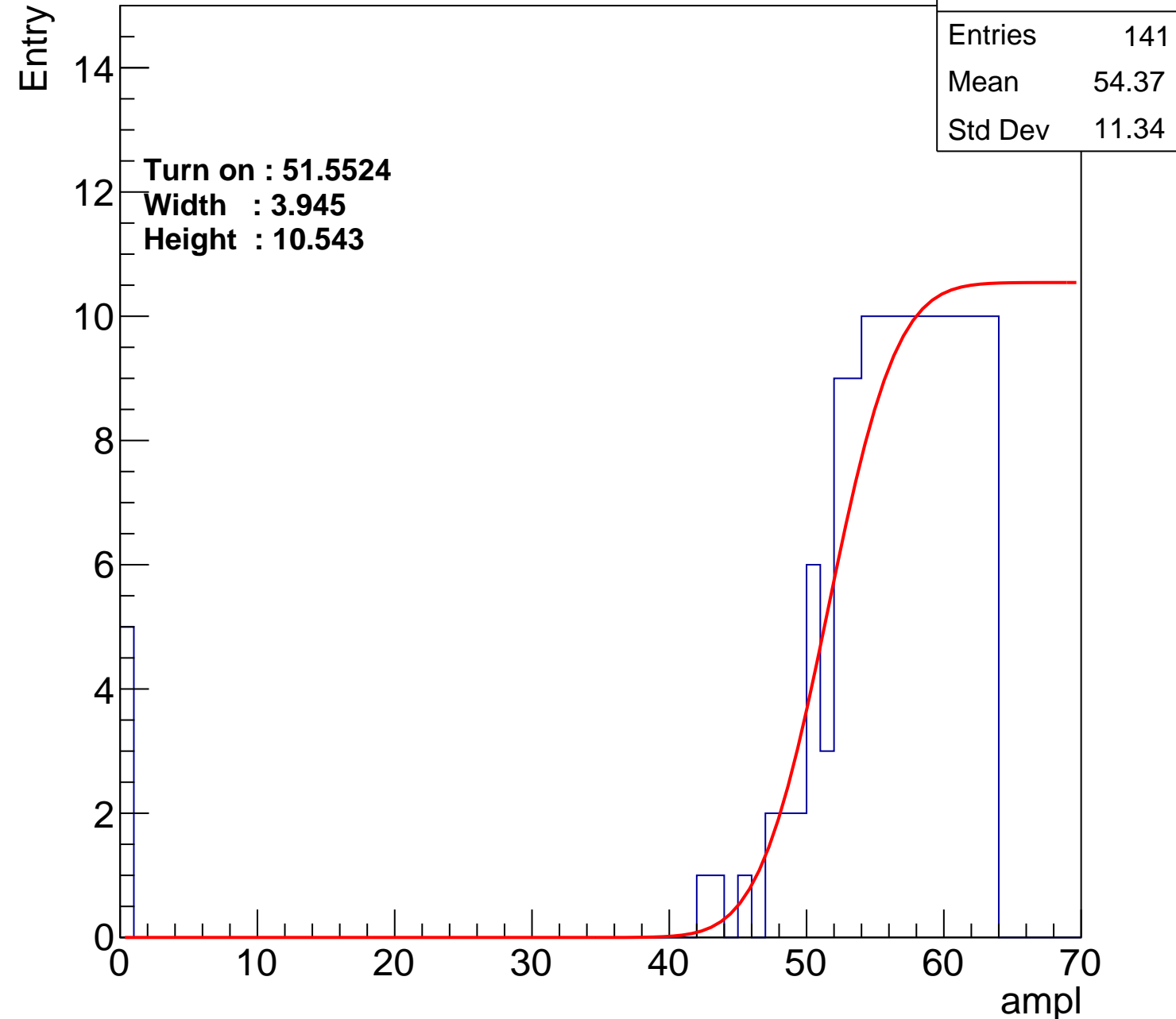
Width : 3.945

Height : 10.543

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch38

calib\_packv5\_040323\_1717.root, FC#2, port C3

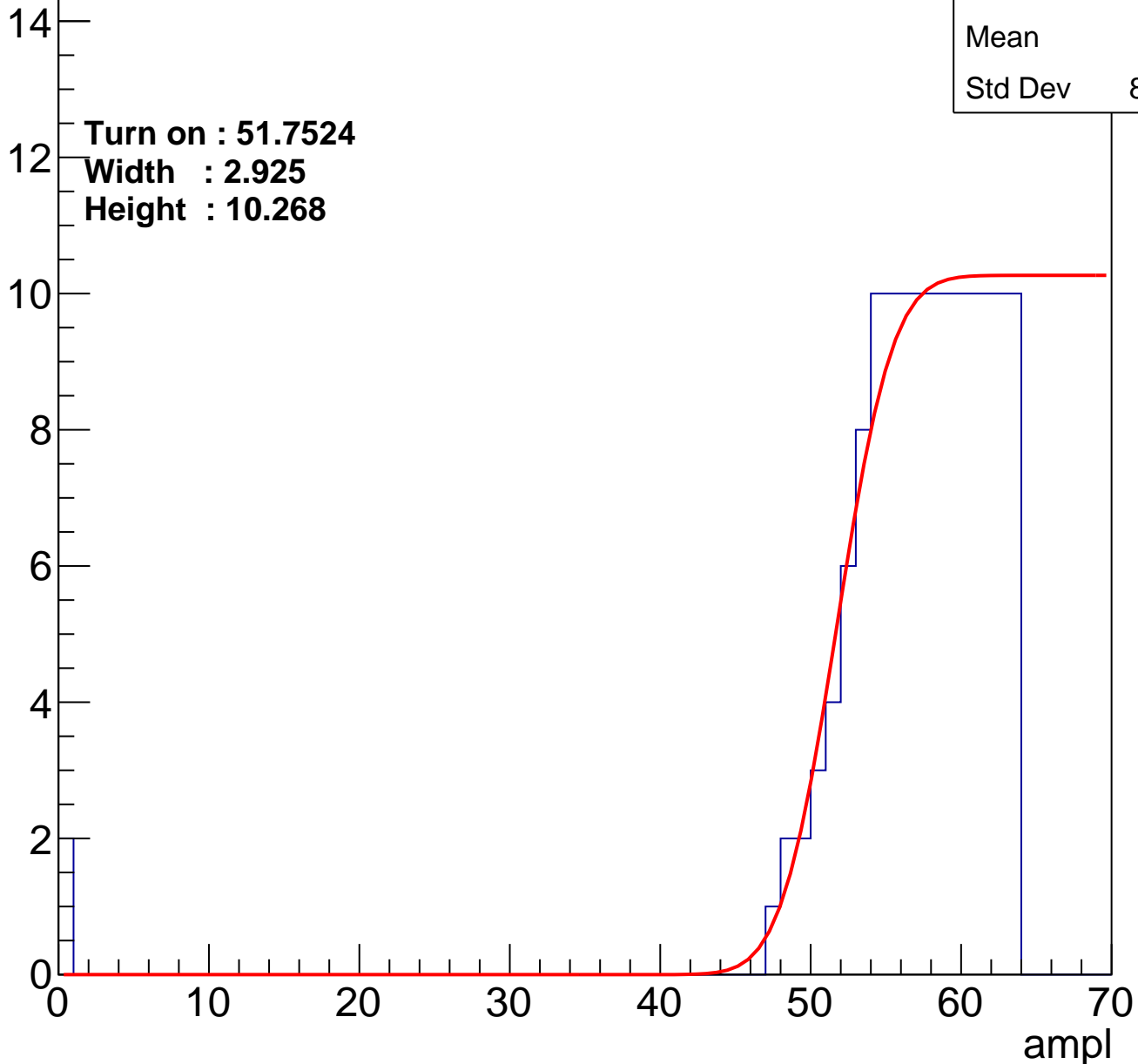
Entries	128
Mean	56.1
Std Dev	8.103

Turn on : 51.7524

Width : 2.925

Height : 10.268

Entry





# B0L103S, U19-ch39

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.15
Std Dev	10.58

Turn on : 52.4072

Width : 0.151

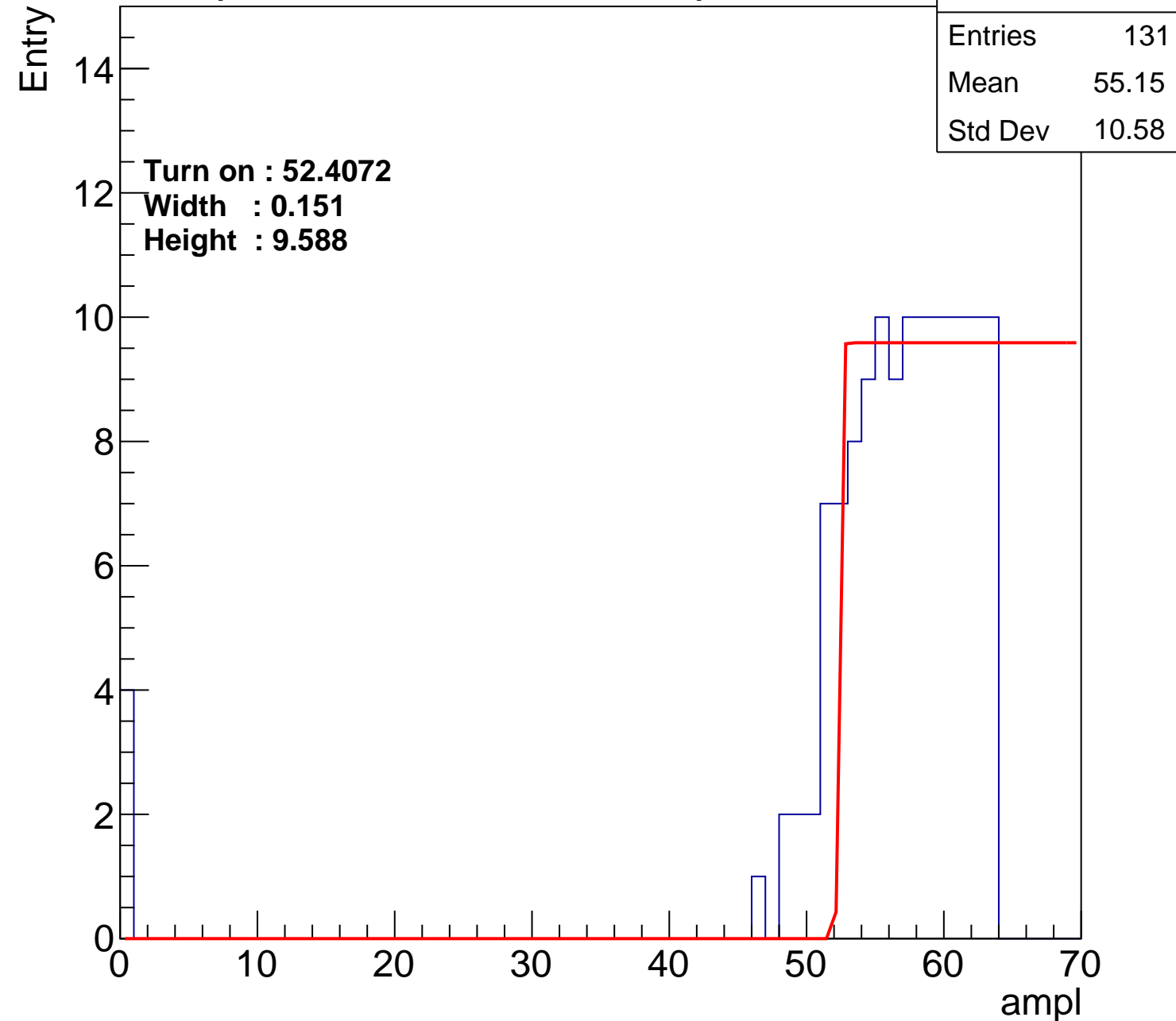
Height : 9.588

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U19-ch40

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	121
Mean	55.47
Std Dev	10.93

Turn on : 52.6346

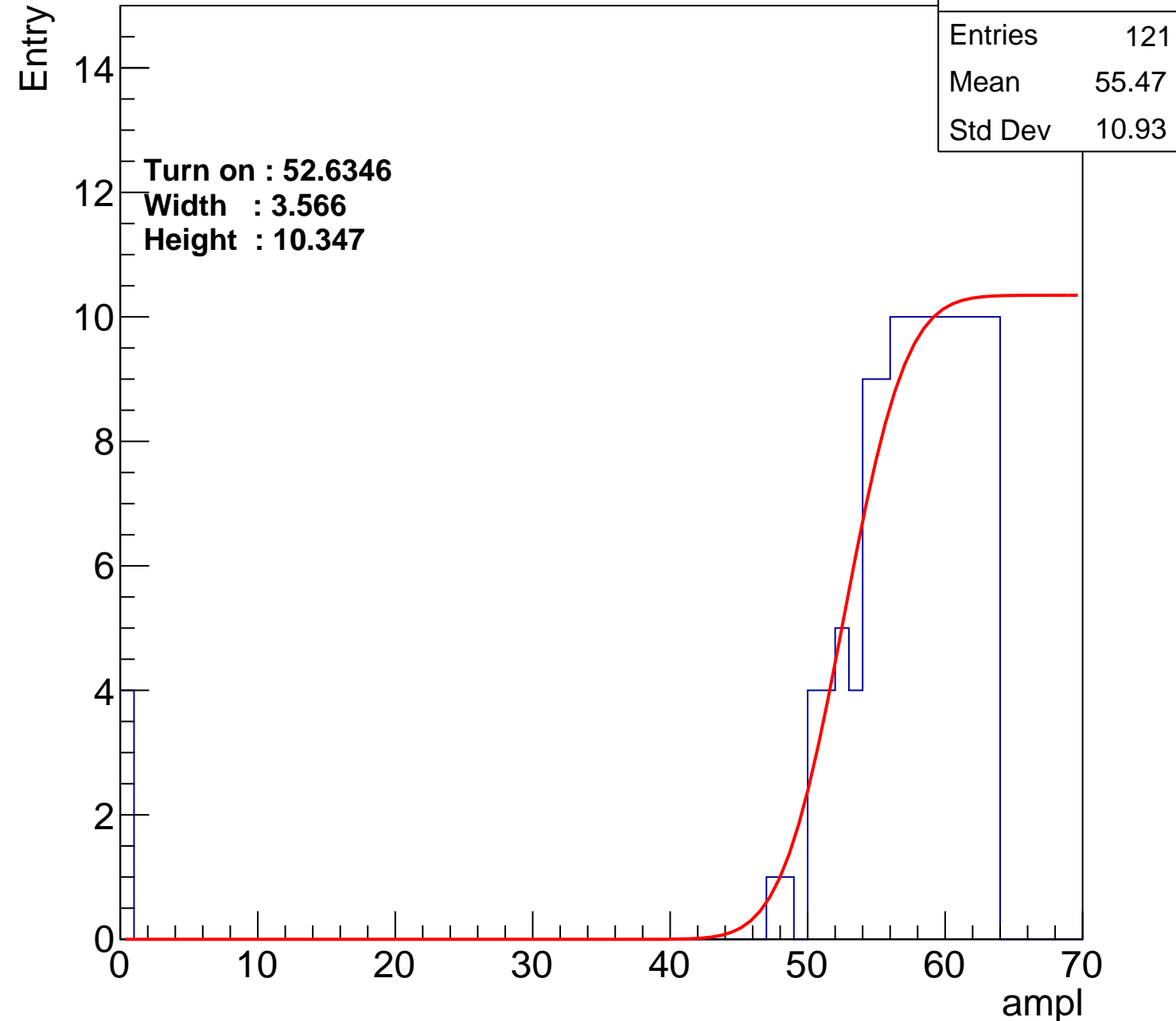
Width : 3.566

Height : 10.347

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch41

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	56.23
Std Dev	6.491

Turn on : 50.8647

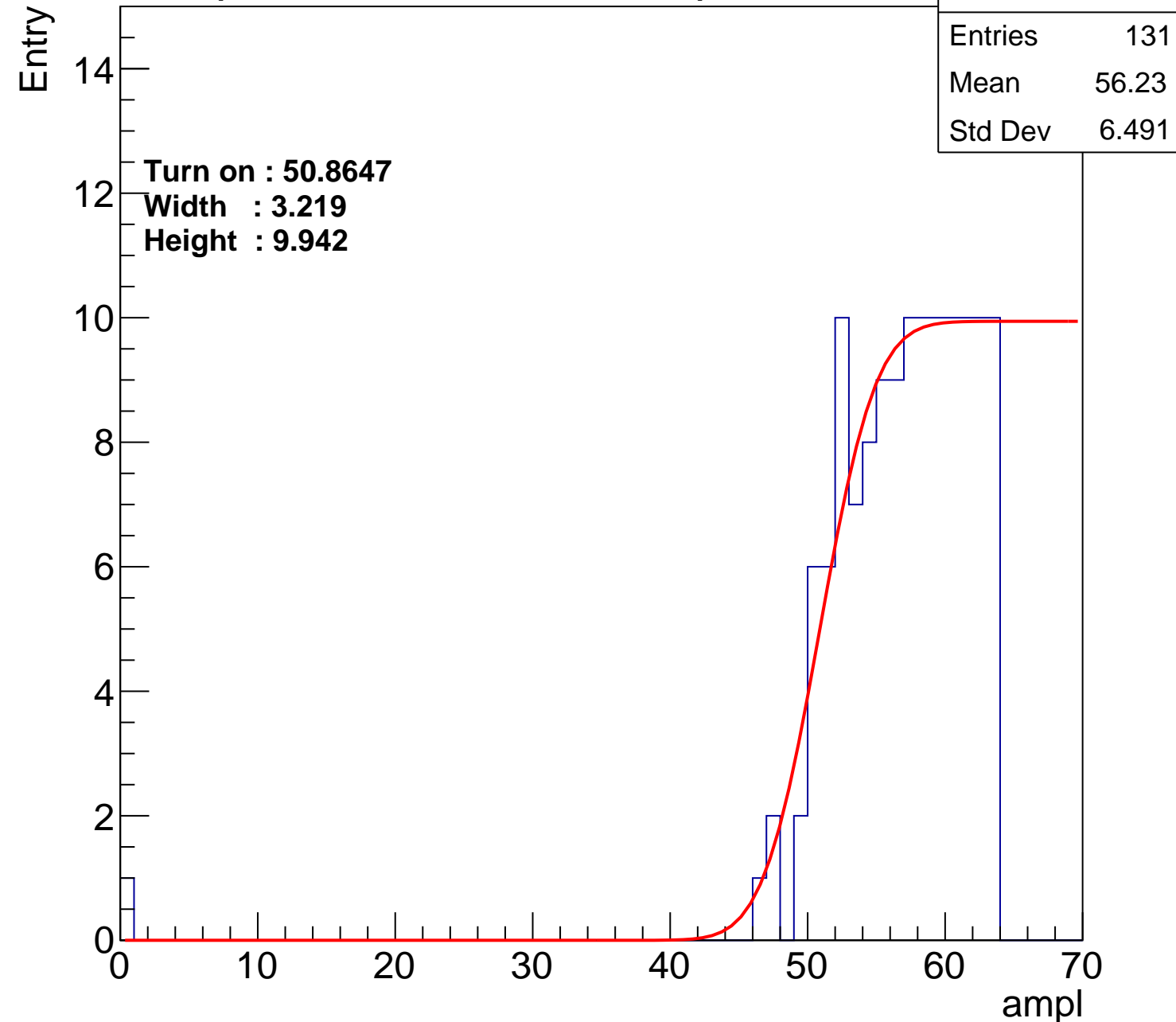
Width : 3.219

Height : 9.942

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch42

calib\_packv5\_040323\_1717.root, FC#2, port C3

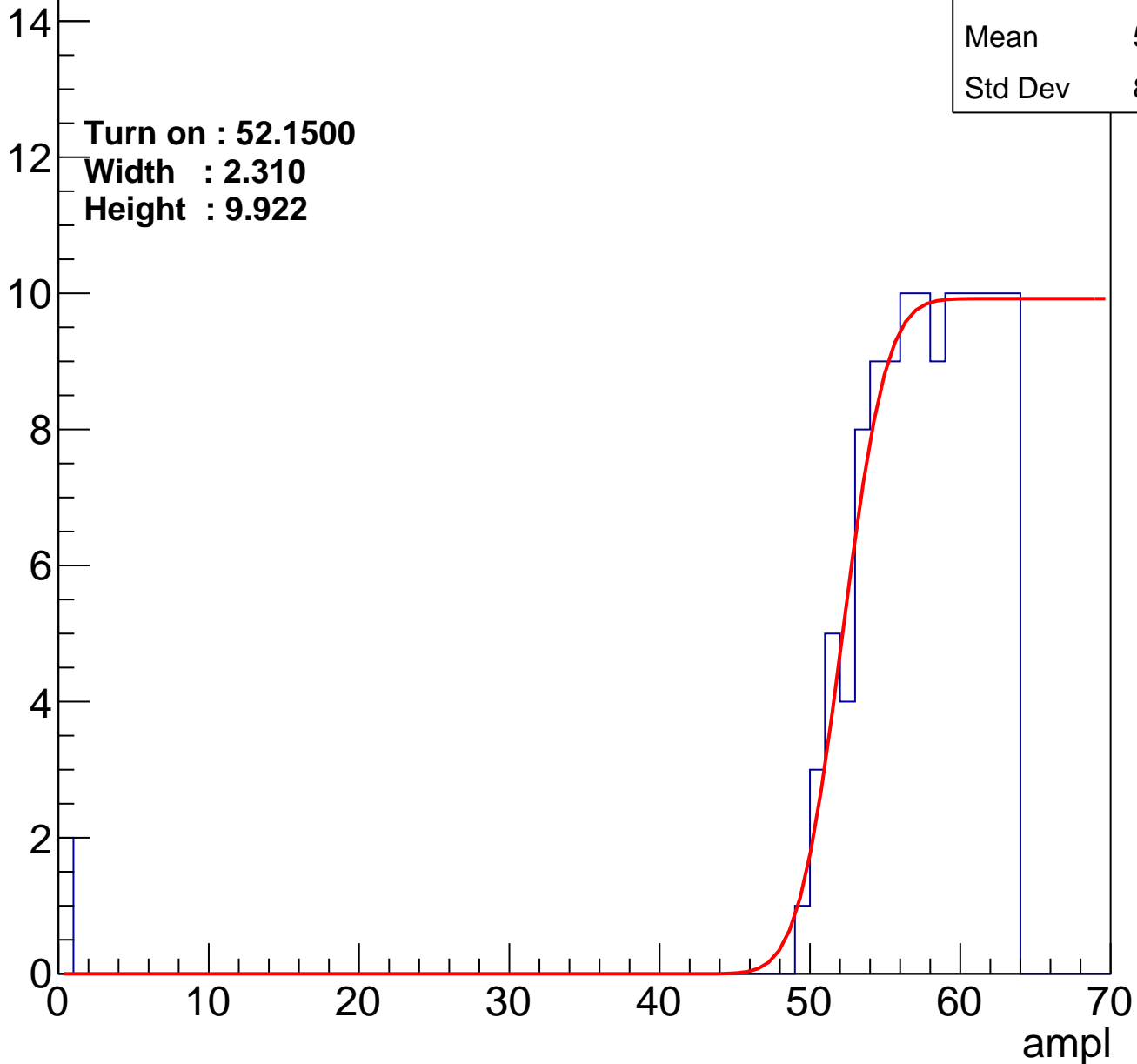
Entries	120
Mean	56.41
Std Dev	8.221

Turn on : 52.1500

Width : 2.310

Height : 9.922

Entry



# B0L103S, U19-ch43

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.99
Std Dev	8.155

Turn on : 51.4562

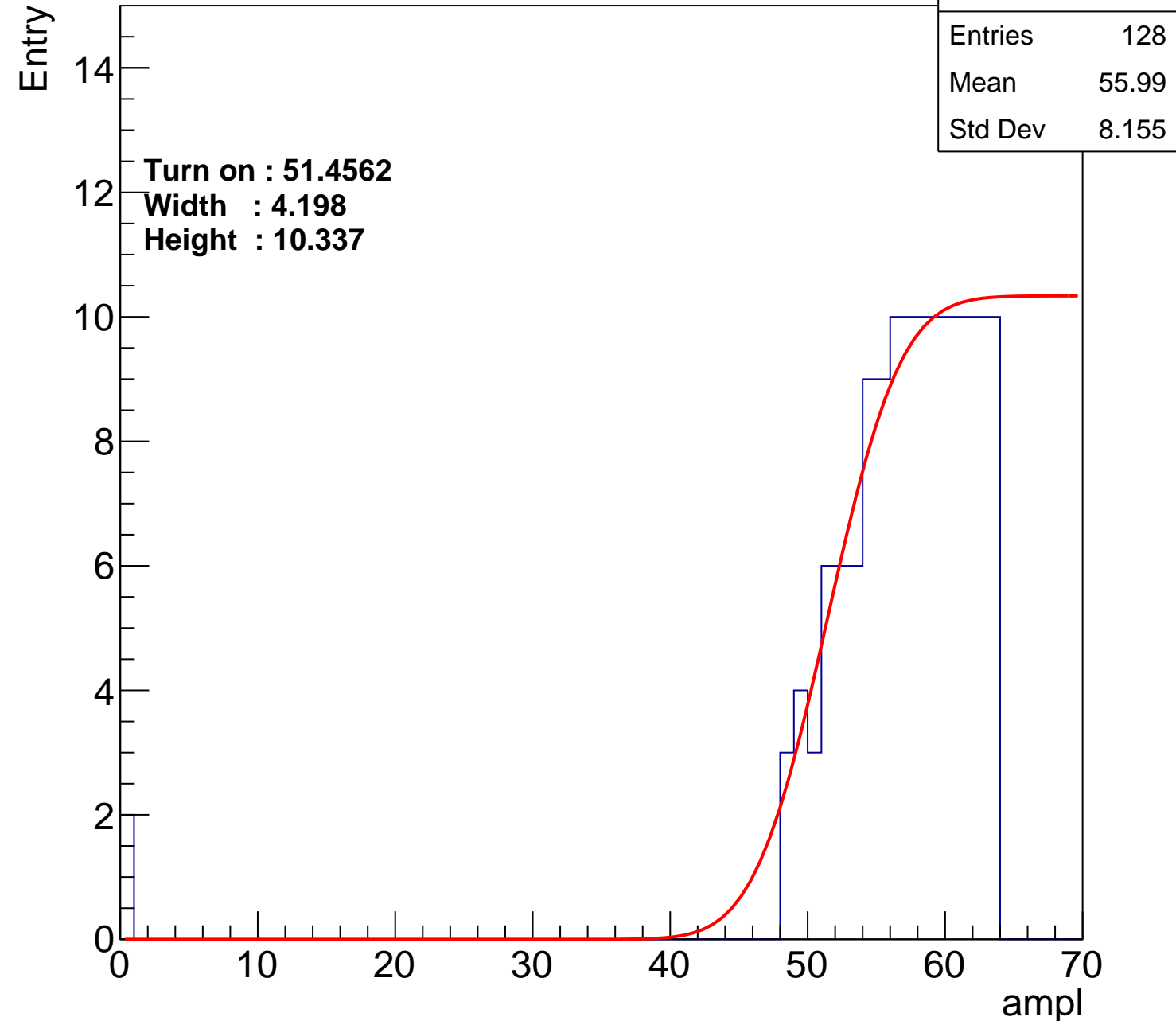
Width : 4.198

Height : 10.337

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch44

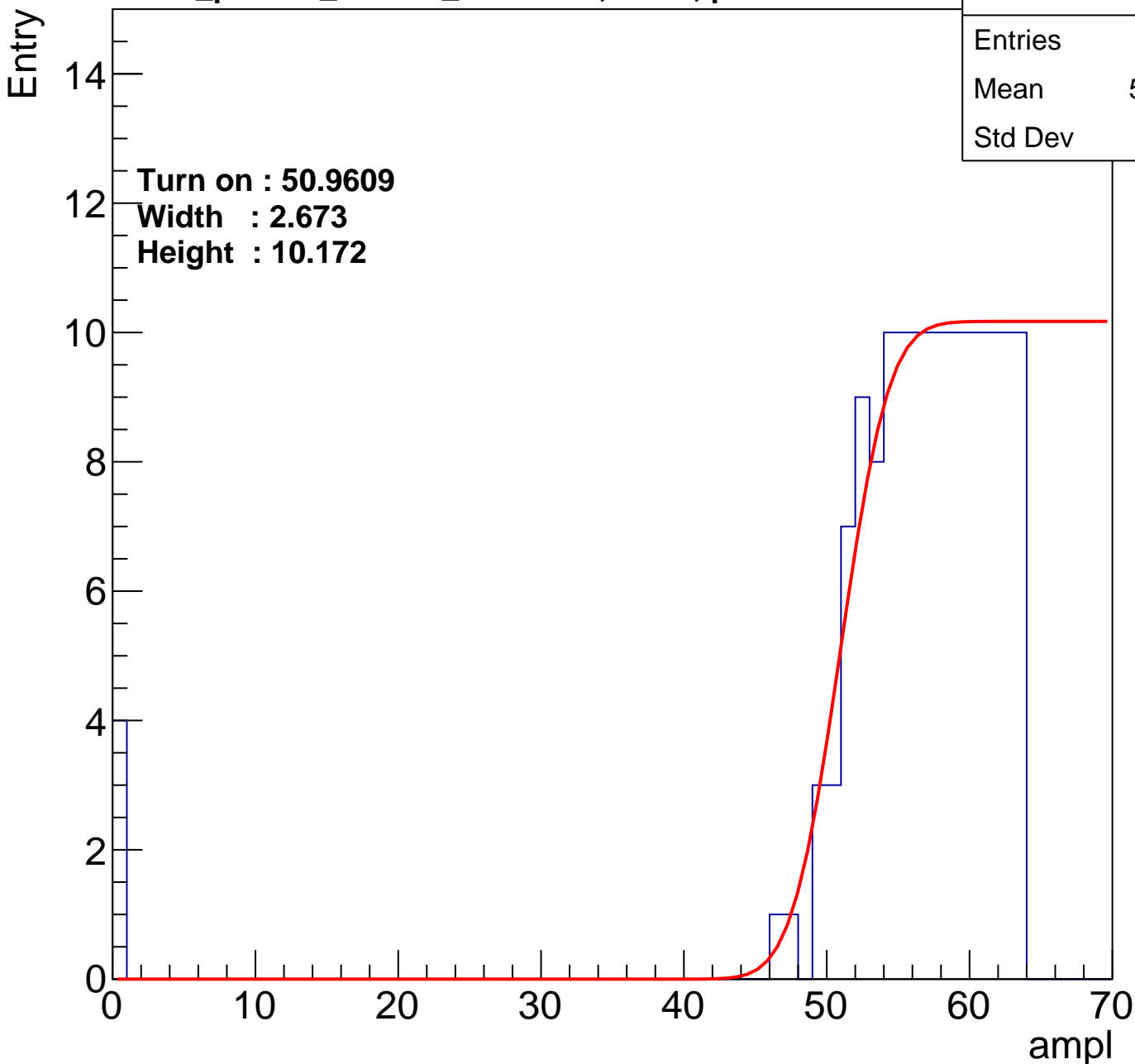
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	136
Mean	55.07
Std Dev	10.4

**Turn on : 50.9609**

**Width : 2.673**

**Height : 10.172**



# B0L103S, U19-ch45

calib\_packv5\_040323\_1717.root, FC#2, port C3

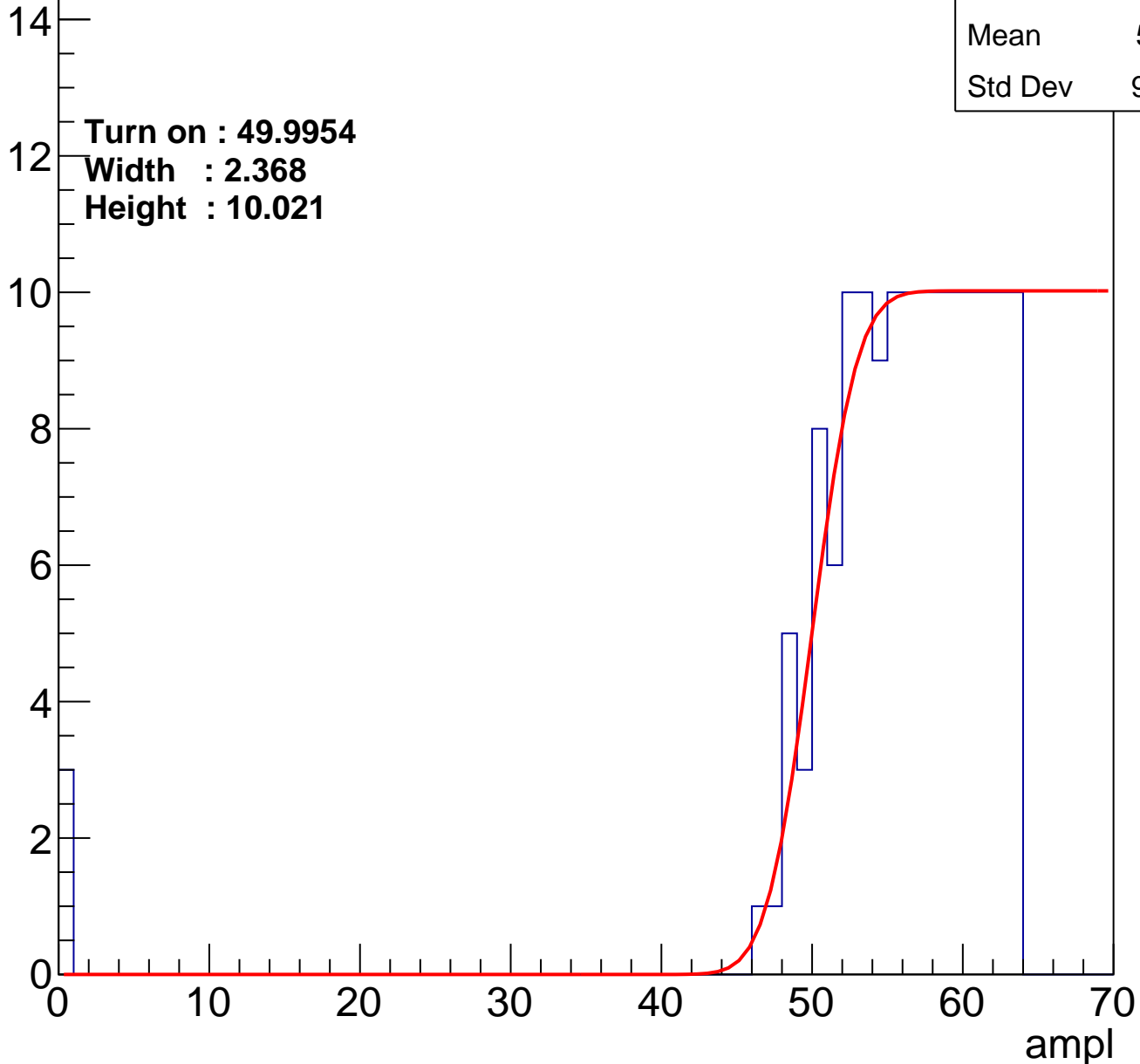
Entries	146
Mean	55.01
Std Dev	9.084

Turn on : 49.9954

Width : 2.368

Height : 10.021

Entry



# B0L103S, U19-ch46

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.98
Std Dev	8.066

Turn on : 51.2362

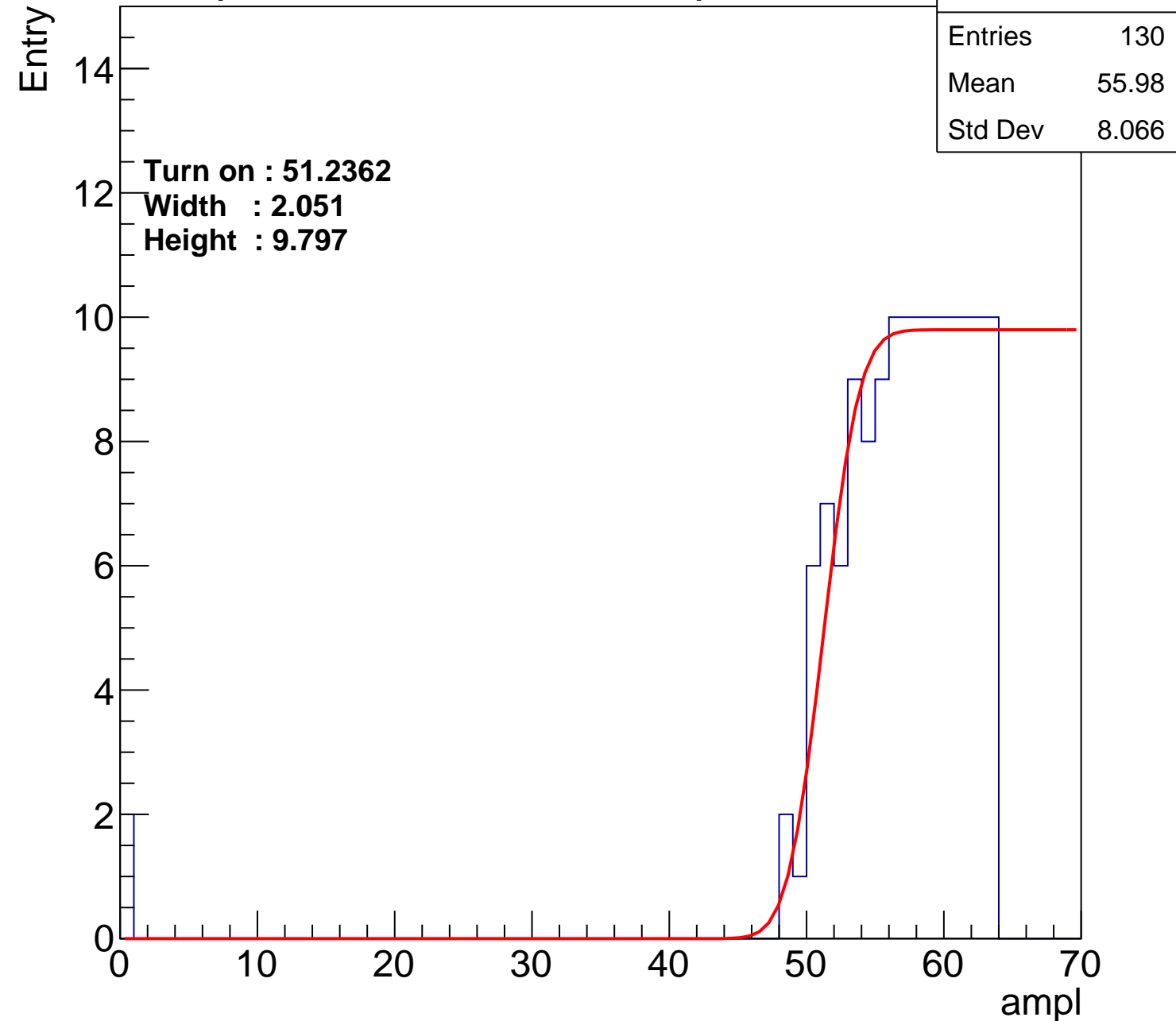
Width : 2.051

Height : 9.797

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch47

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	132
Mean	55.53
Std Dev	9.36

Turn on : 50.7580

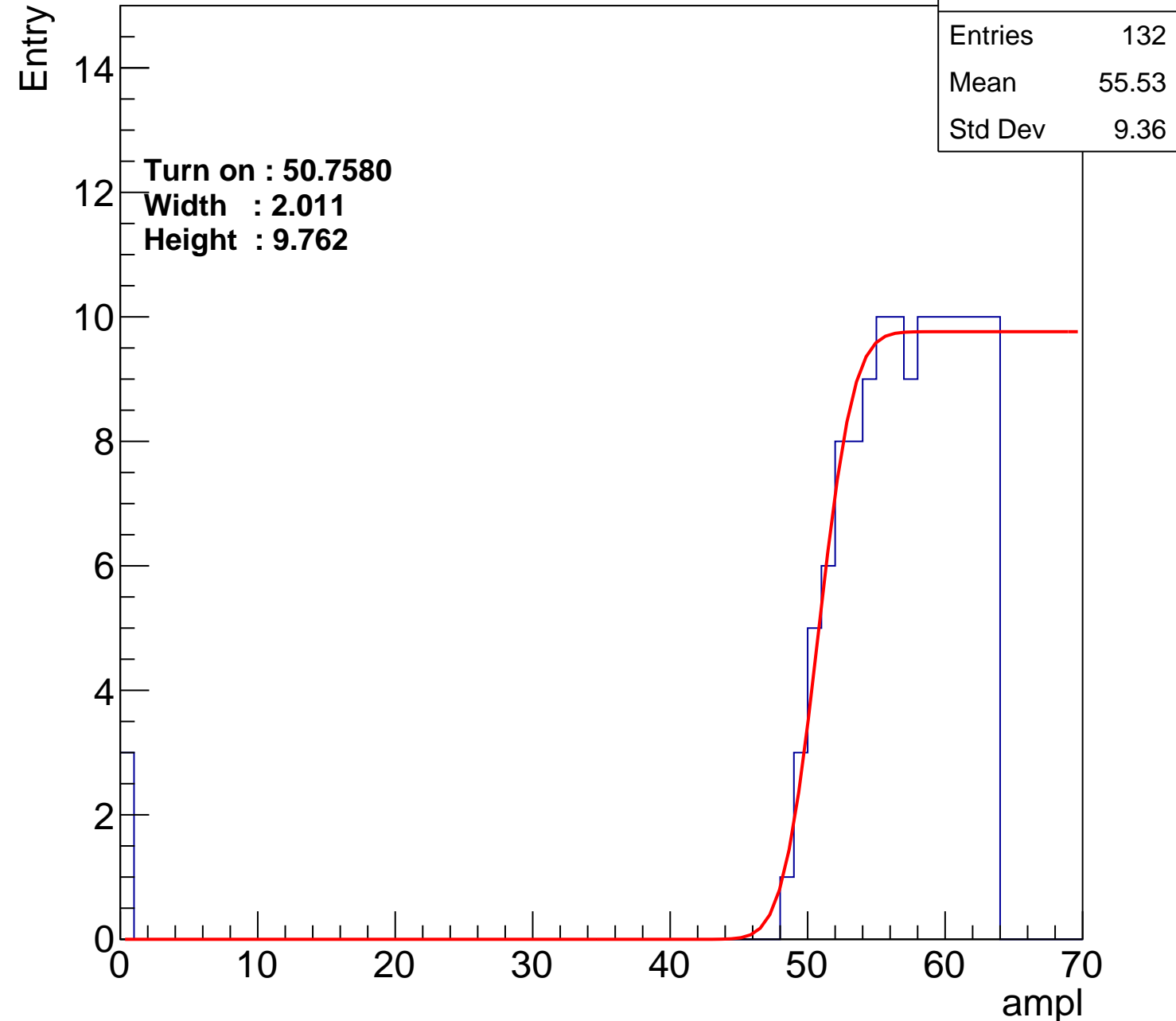
Width : 2.011

Height : 9.762

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch48

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	117
Mean	56.45
Std Dev	8.341

Turn on : 52.7569

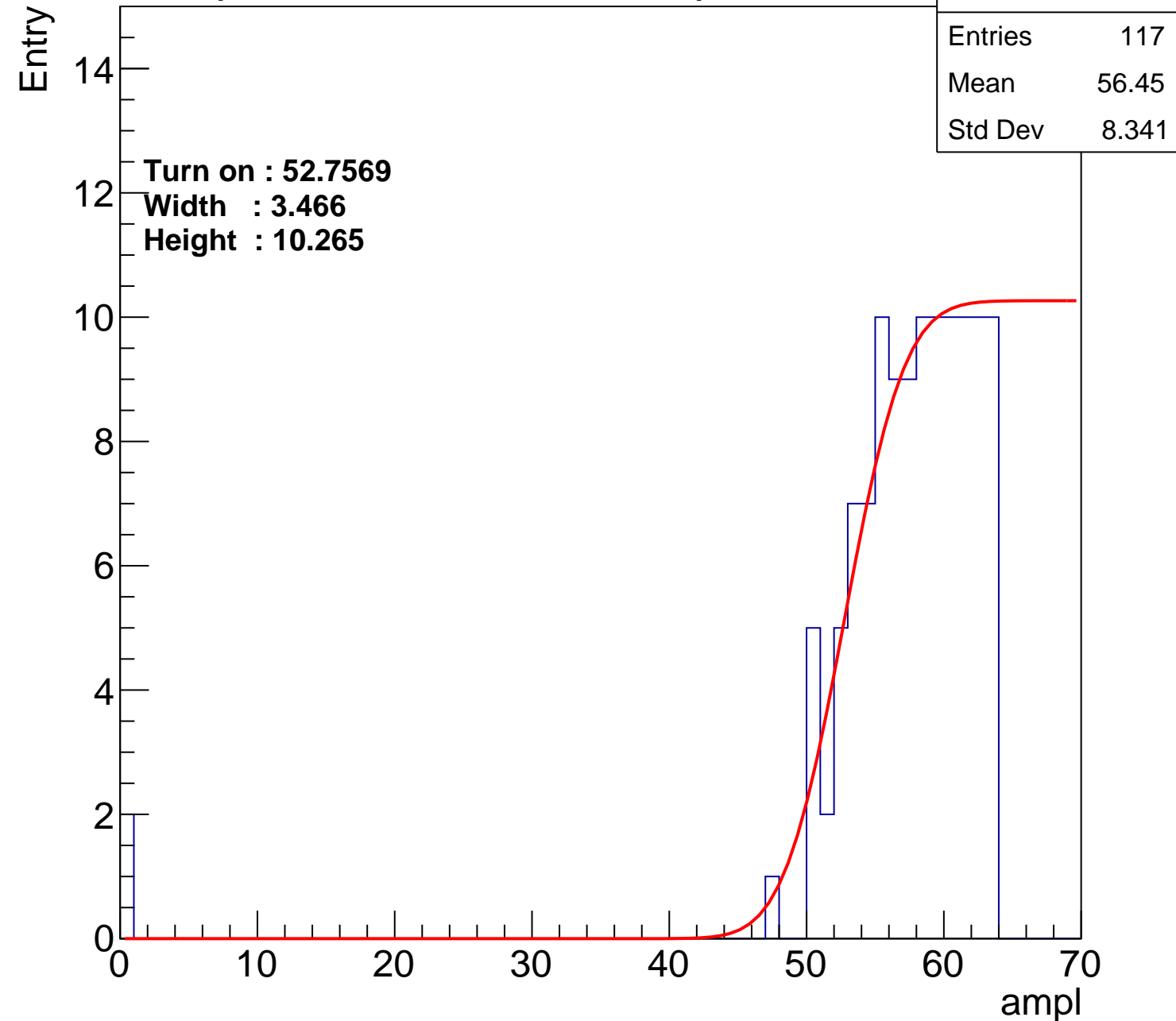
Width : 3.466

Height : 10.265

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch49

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.54
Std Dev	9.474

Turn on : 52.0427

Width : 3.046

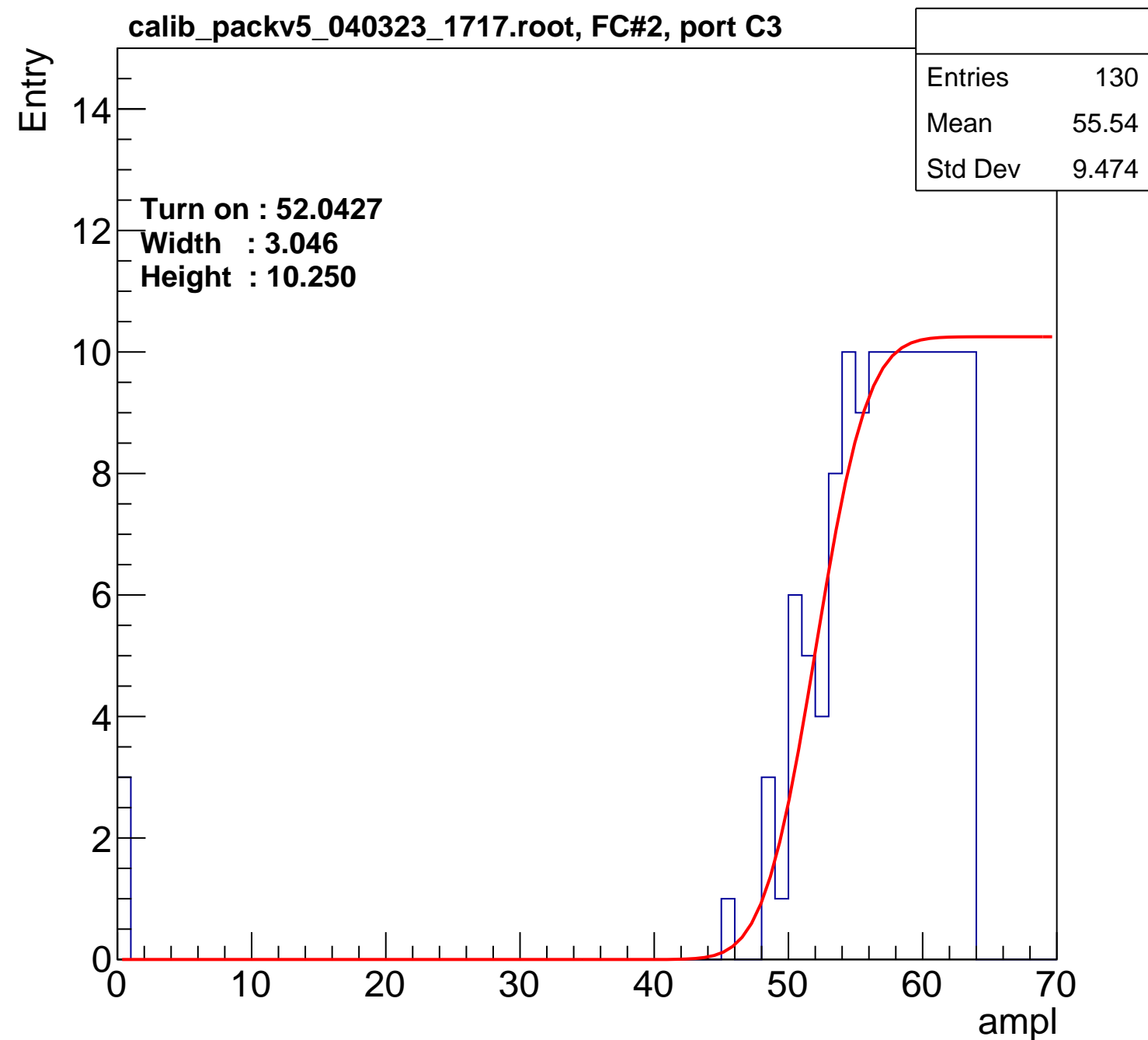
Height : 10.250

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

0 10 20 30 40 50 60 70



# B0L103S, U19-ch50

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	55.3
Std Dev	10.96

Turn on : 54.0851

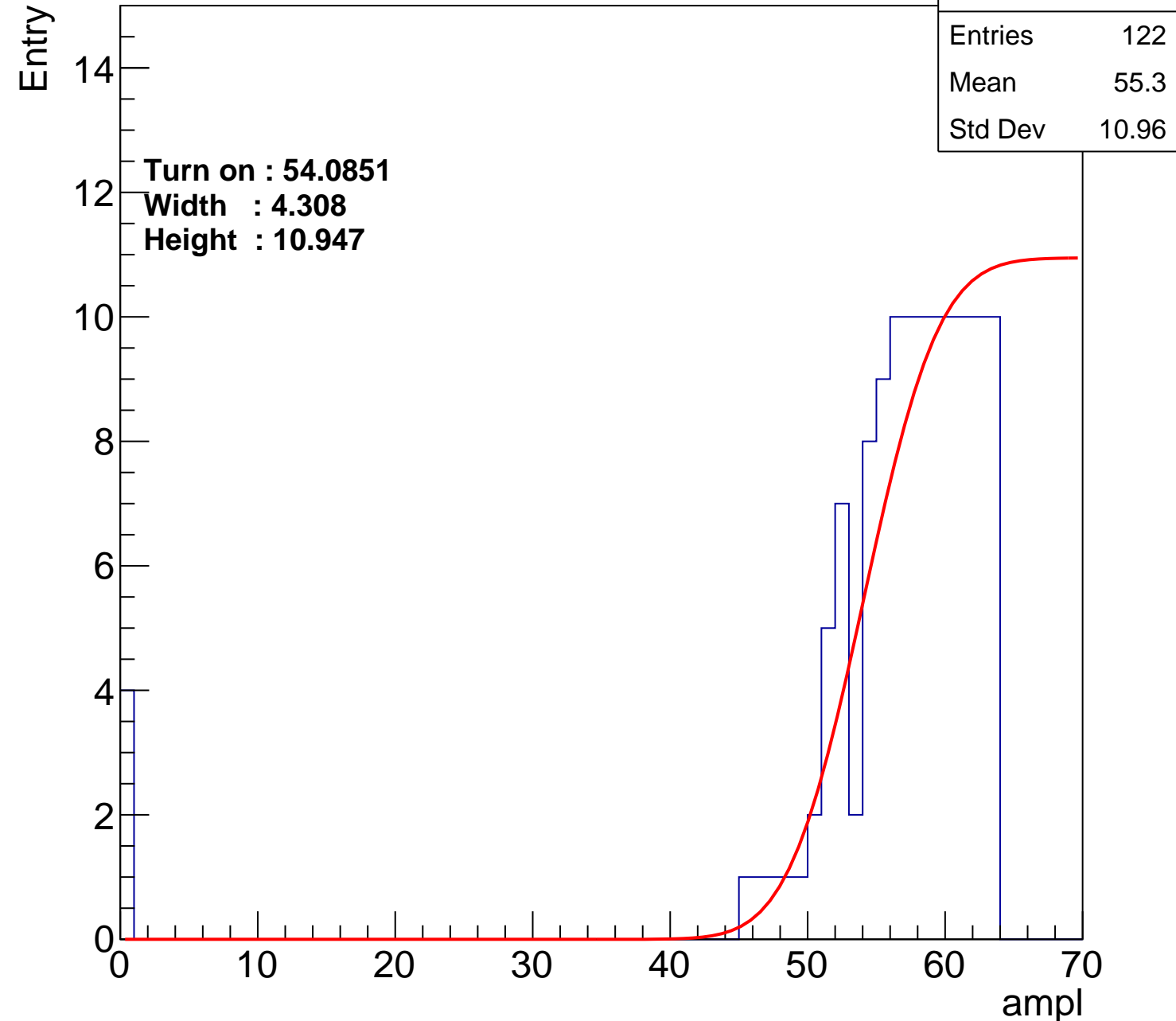
Width : 4.308

Height : 10.947

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch51

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	54.04
Std Dev	13.67

Turn on : 52.9678

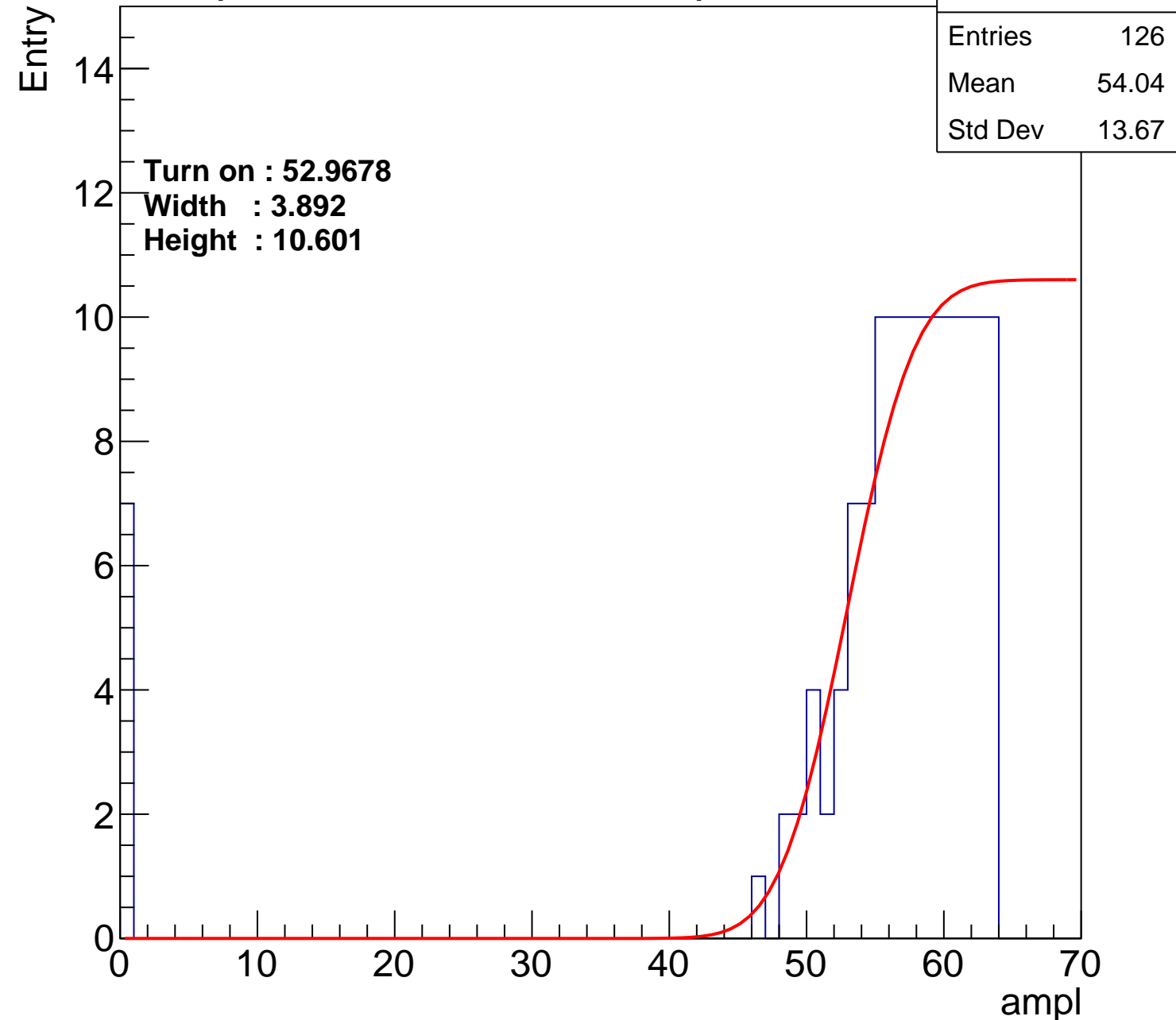
Width : 3.892

Height : 10.601

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch52

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	129
Mean	55.66
Std Dev	9.438

Turn on : 50.5169

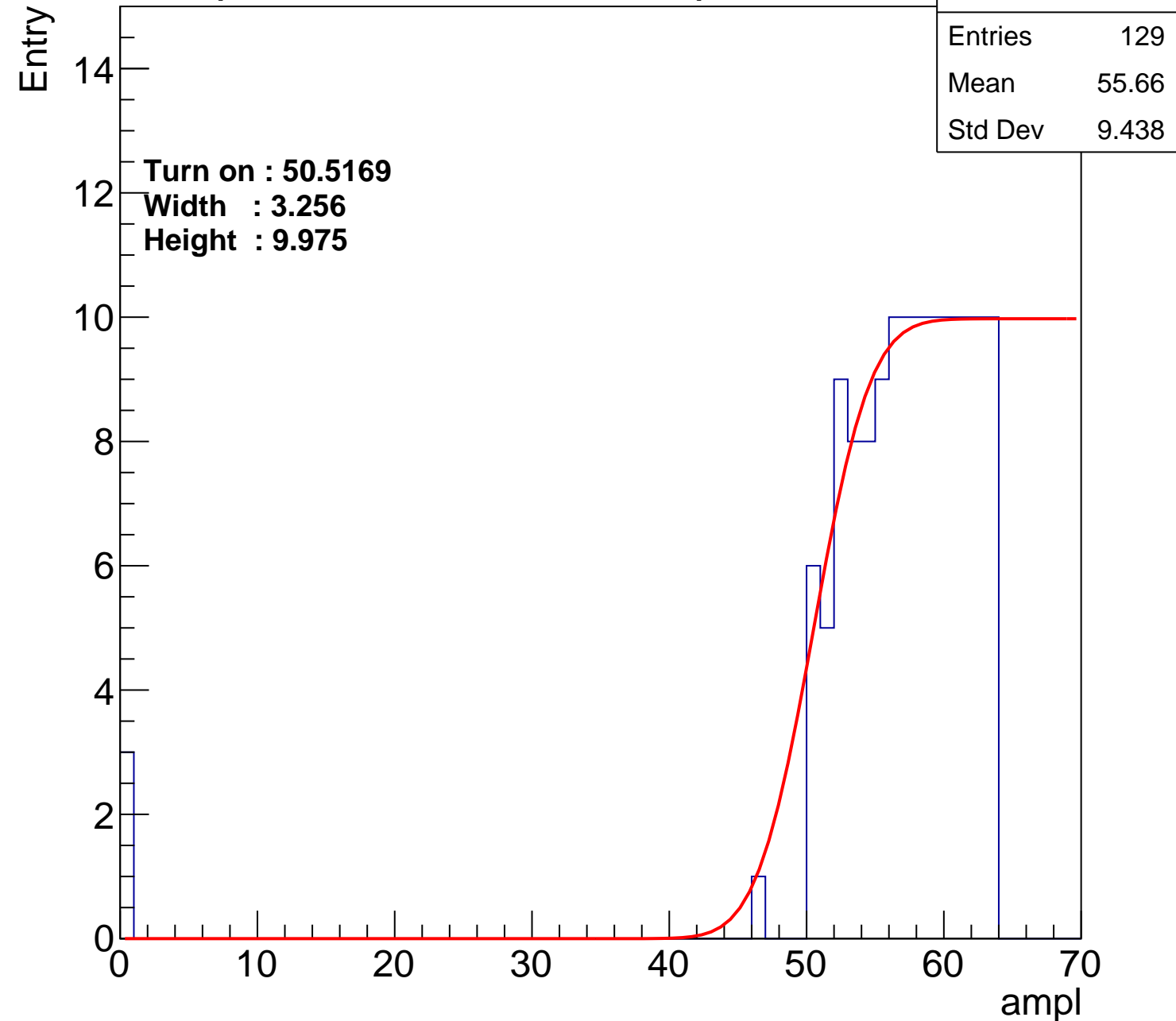
Width : 3.256

Height : 9.975

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch53

calib\_packv5\_040323\_1717.root, FC#2, port C3

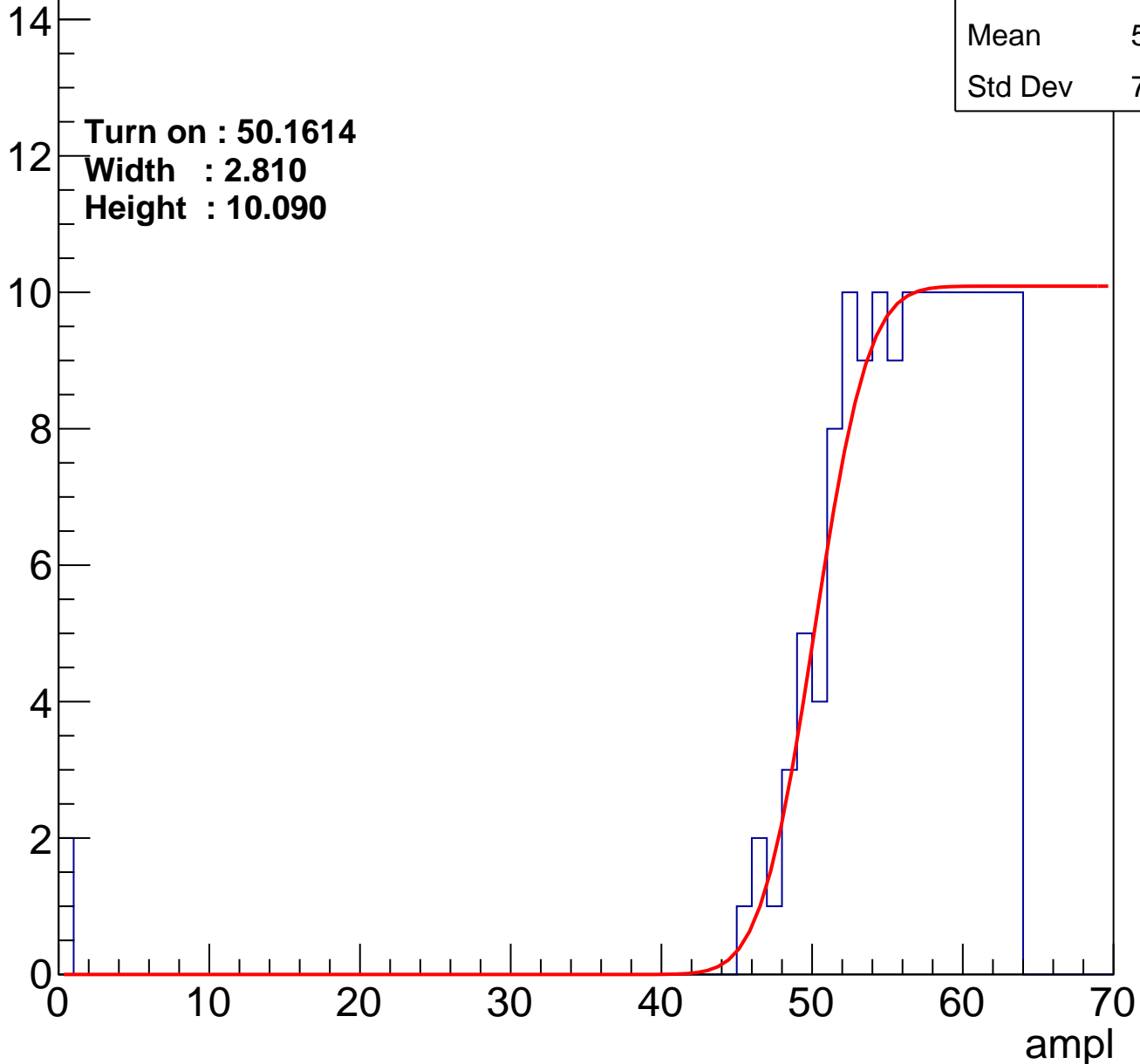
Entries	144
Mean	55.37
Std Dev	7.944

Turn on : 50.1614

Width : 2.810

Height : 10.090

Entry



# B0L103S, U19-ch54

calib\_packv5\_040323\_1717.root, FC#2, port C3

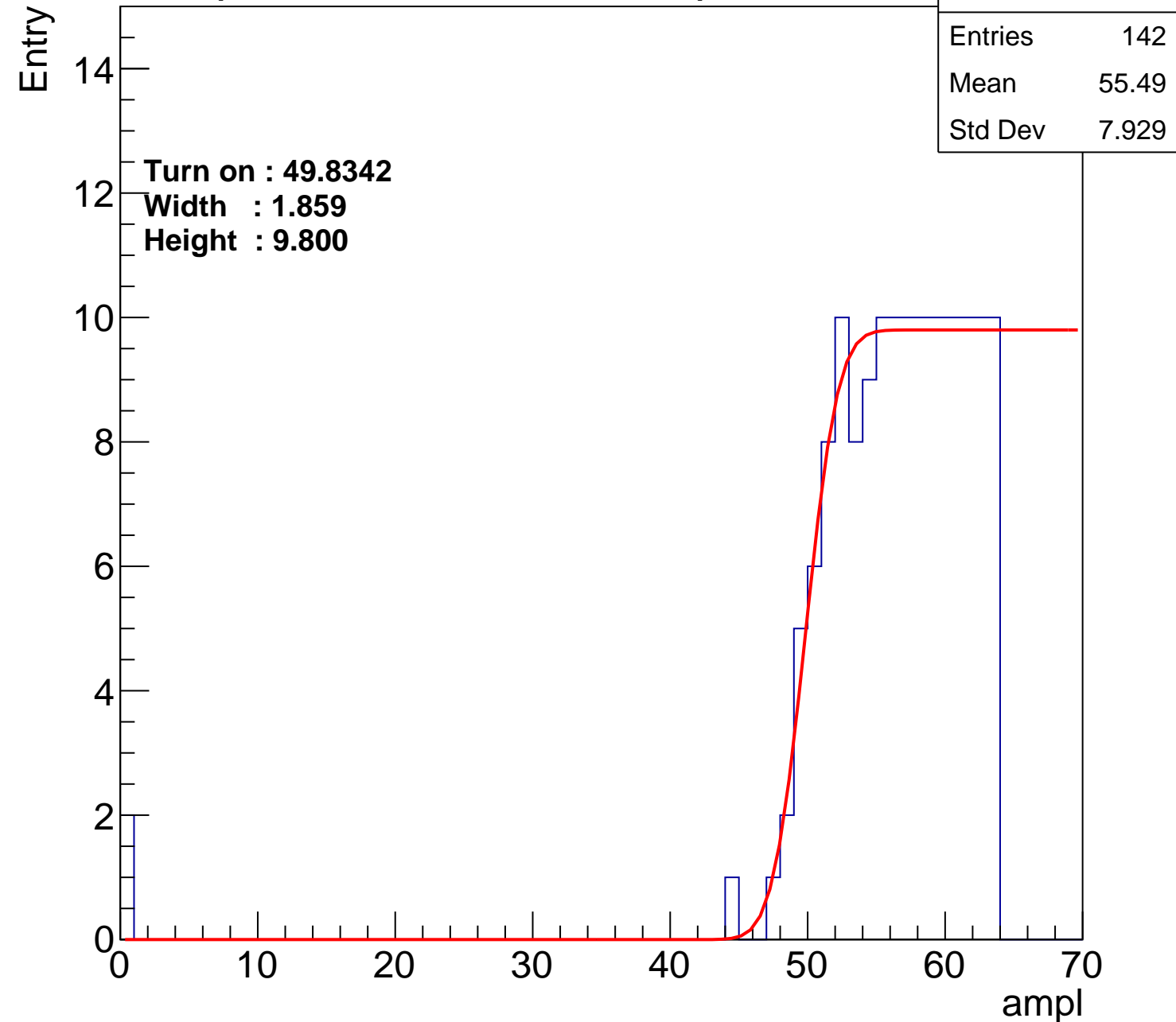
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 49.8342  
Width : 1.859  
Height : 9.800

Entries	142
Mean	55.49
Std Dev	7.929

ampl





# B0L103S, U19-ch55

calib\_packv5\_040323\_1717.root, FC#2, port C3

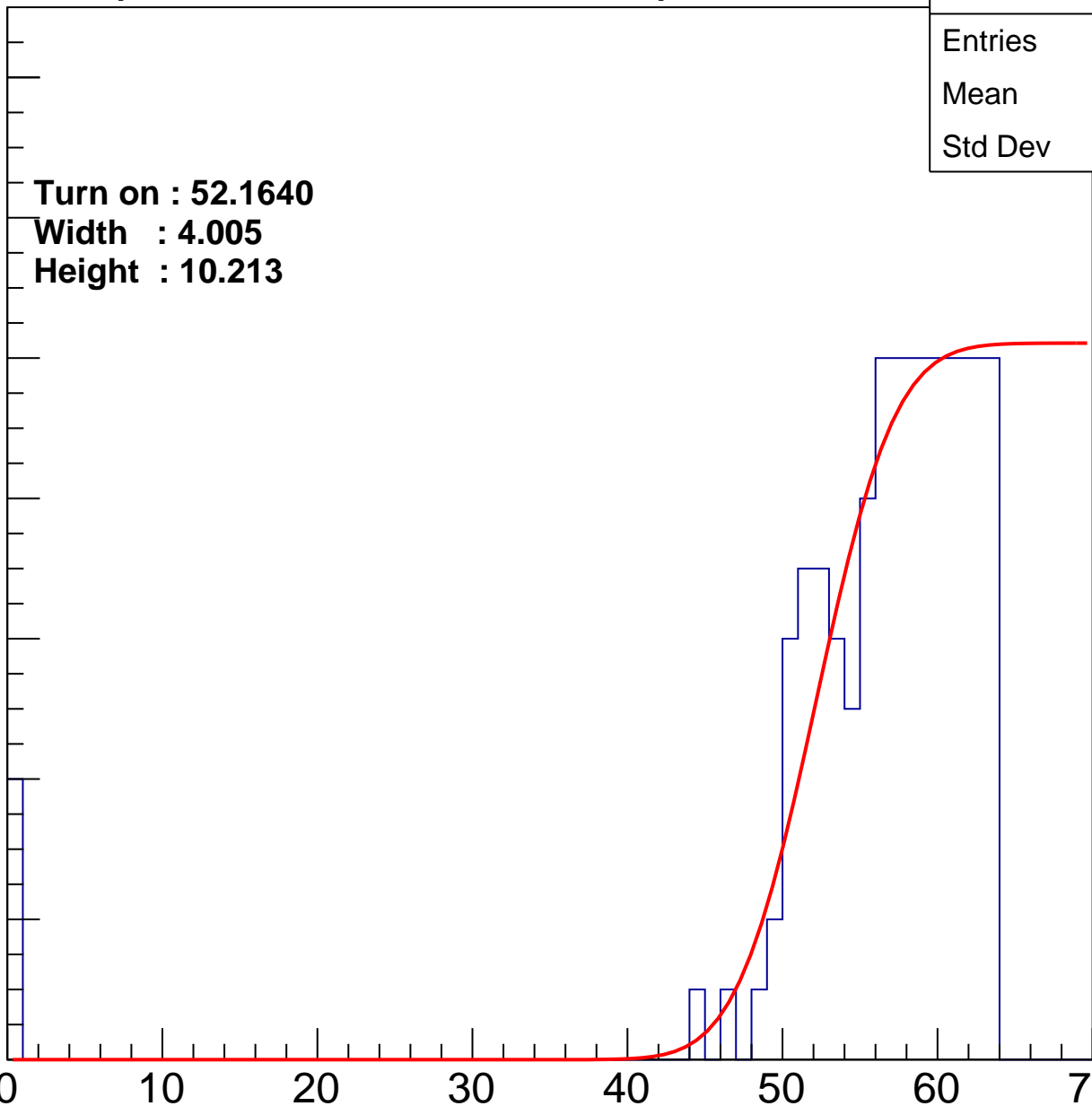
Entry

14  
12  
10  
8  
6  
4  
2  
0

Turn on : 52.1640  
Width : 4.005  
Height : 10.213

Entries	128
Mean	55.04
Std Dev	10.76

ampl



# B0L103S, U19-ch56

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.55
Std Dev	9.455

Turn on : 51.4878

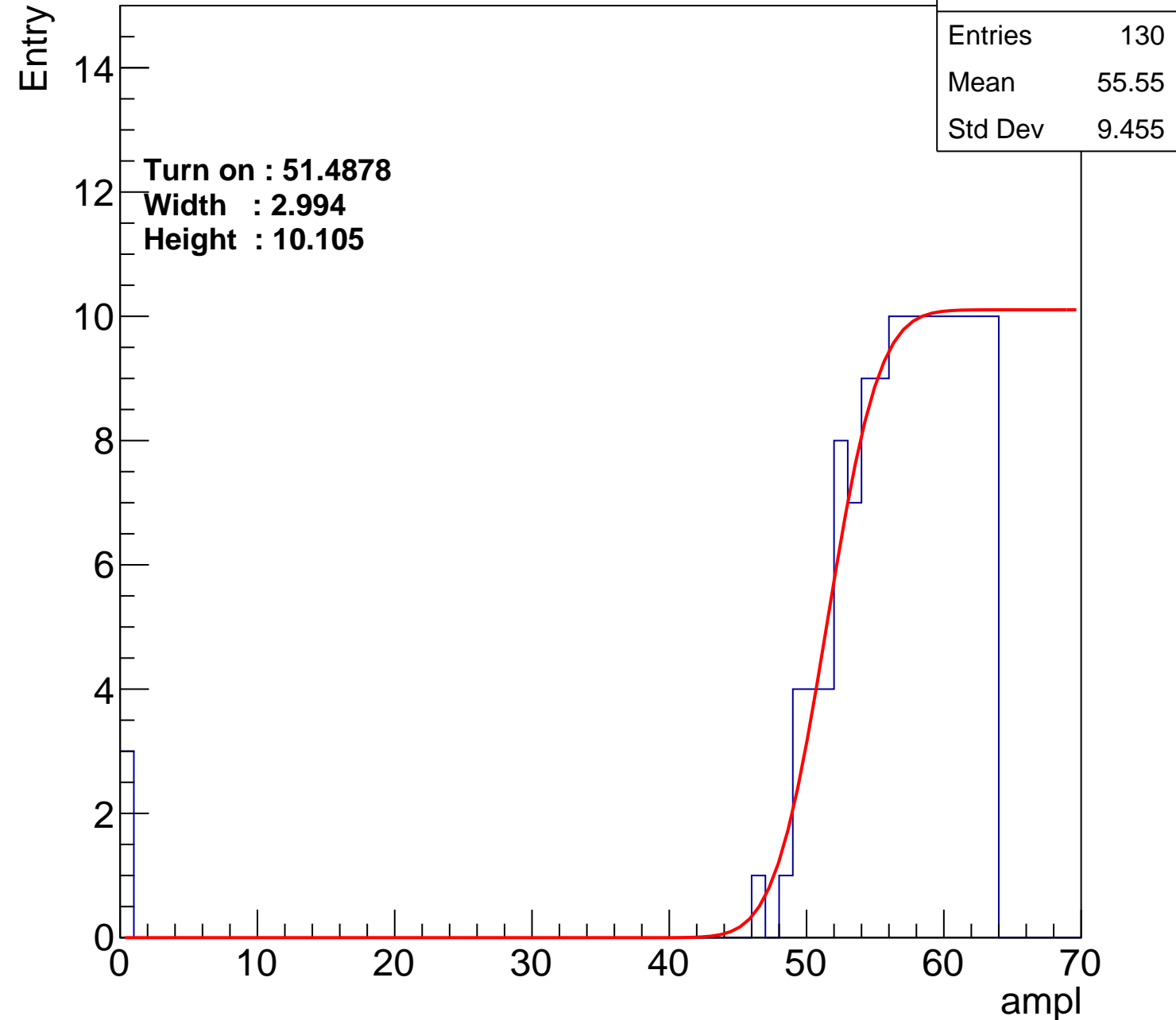
Width : 2.994

Height : 10.105

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch57

calib\_packv5\_040323\_1717.root, FC#2, port C3

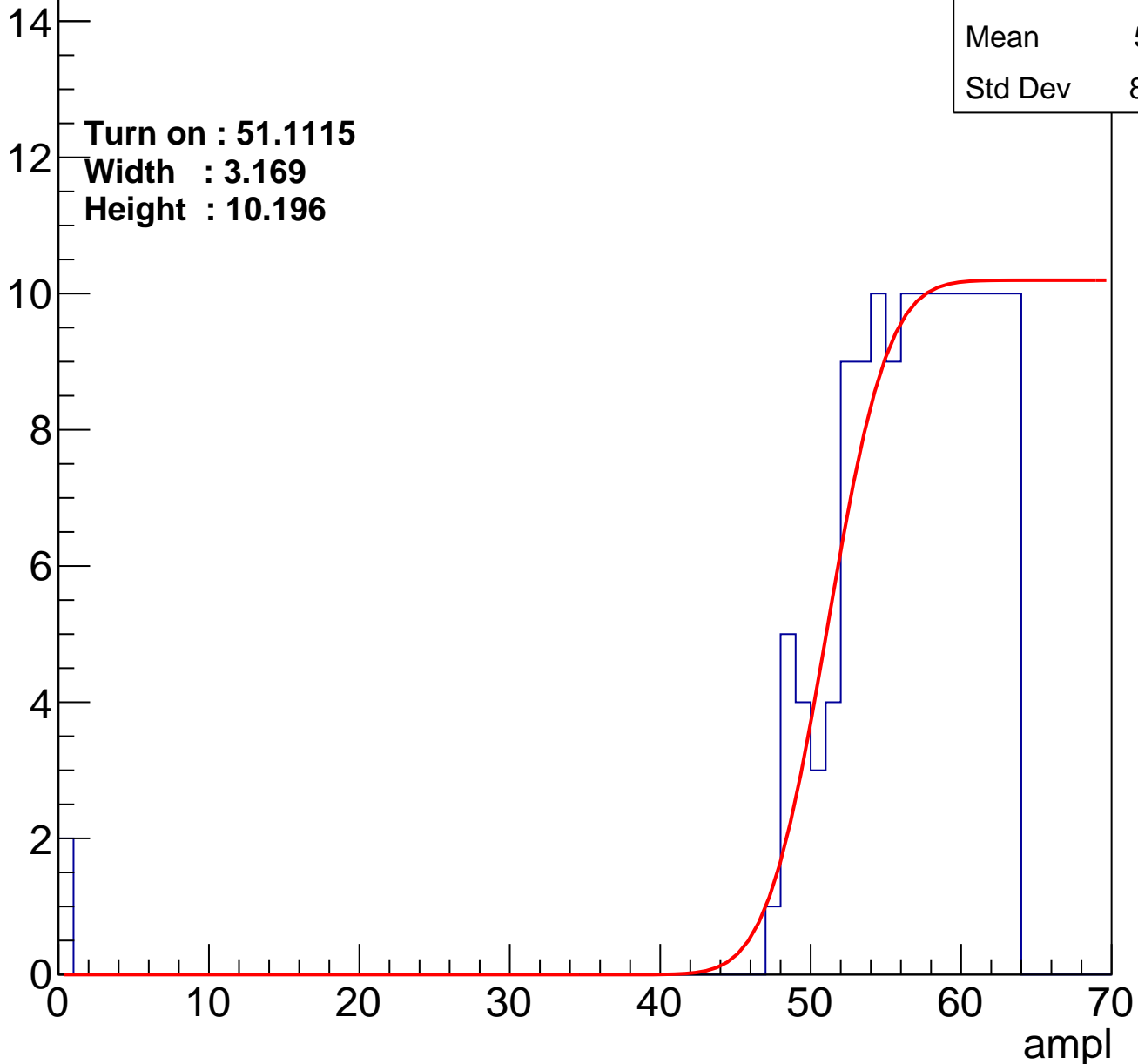
Entries	136
Mean	55.71
Std Dev	8.016

Turn on : 51.1115

Width : 3.169

Height : 10.196

Entry



# B0L103S, U19-ch58

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	54.69
Std Dev	10.17

Turn on : 49.7040

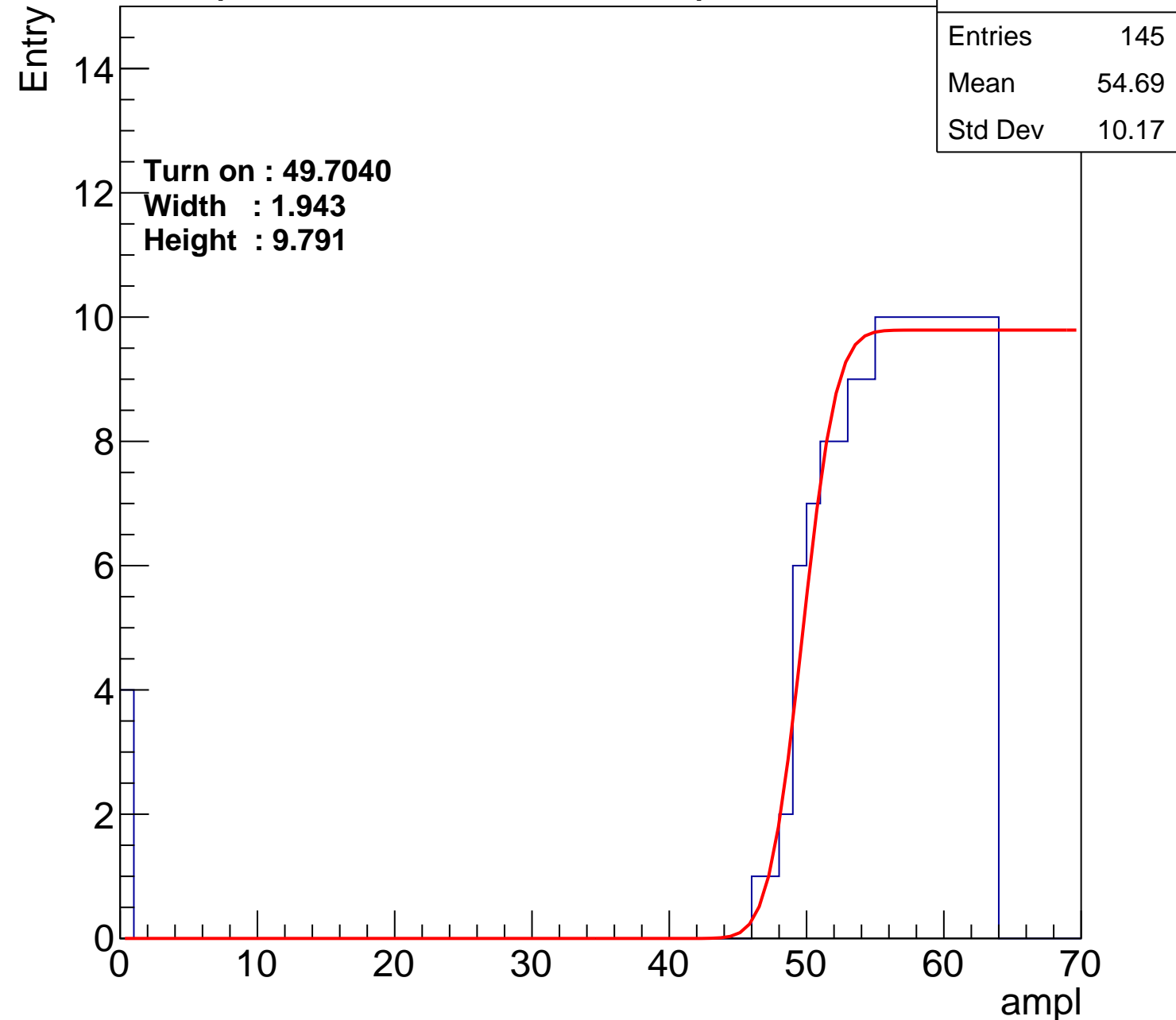
Width : 1.943

Height : 9.791

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch59

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.96
Std Dev	6.556

Turn on : 50.9335

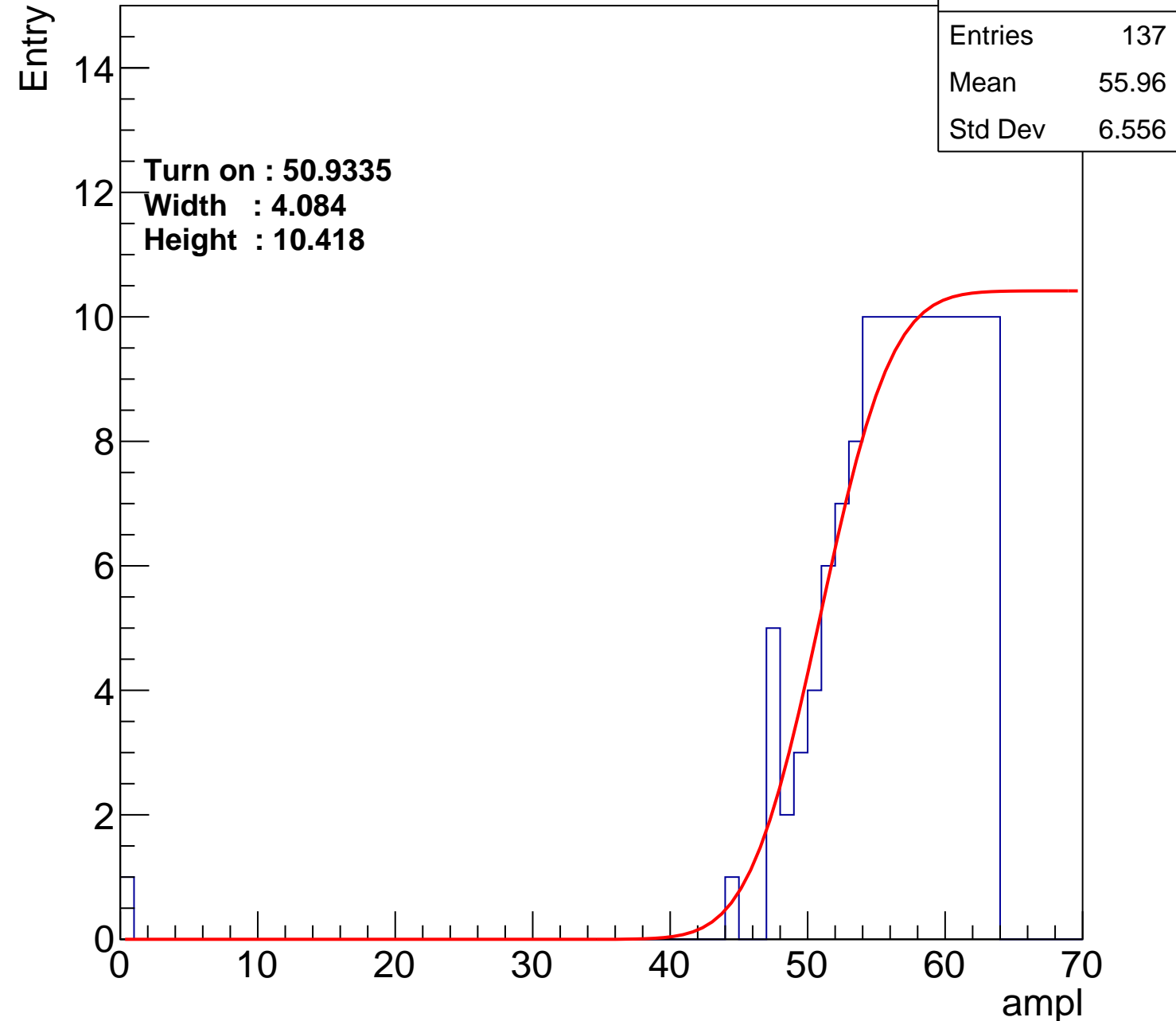
Width : 4.084

Height : 10.418

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch60

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	107
Mean	56.29
Std Dev	10.19

Turn on : 54.2211

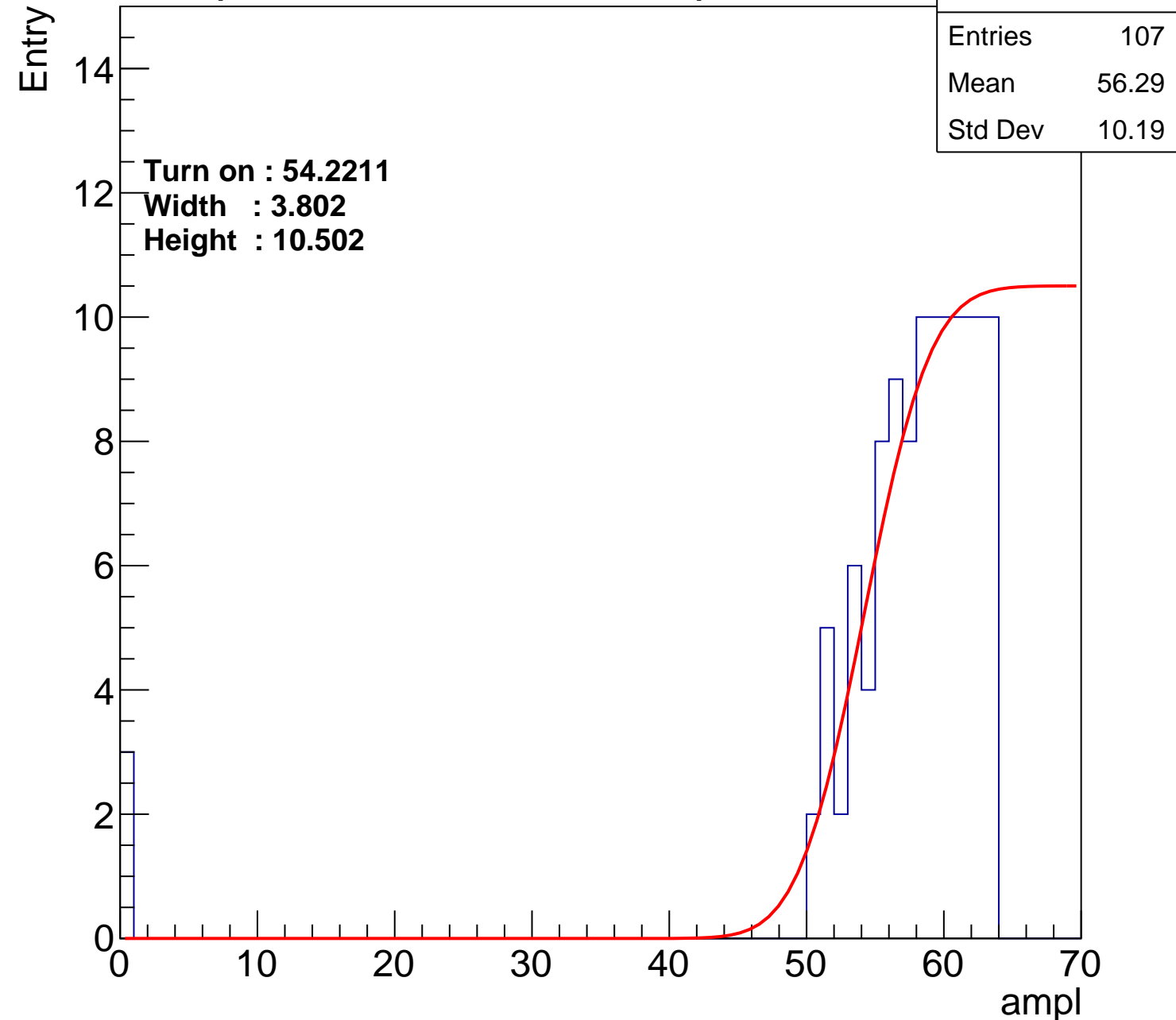
Width : 3.802

Height : 10.502

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch61

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	139
Mean	54.83
Std Dev	10.36

Turn on : 50.8055

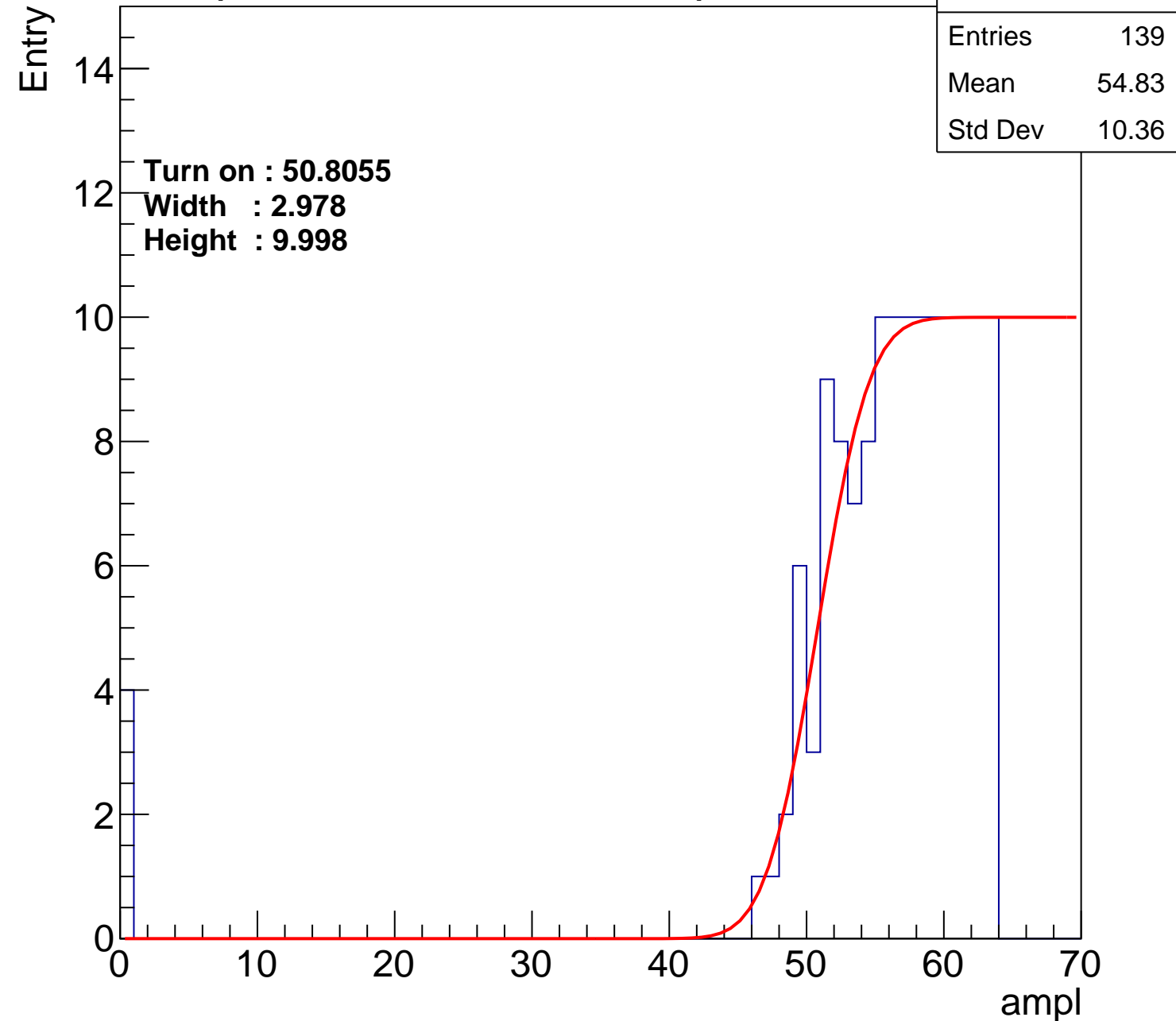
Width : 2.978

Height : 9.998

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch62

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	128
Mean	55.33
Std Dev	10.65

Turn on : 51.9755

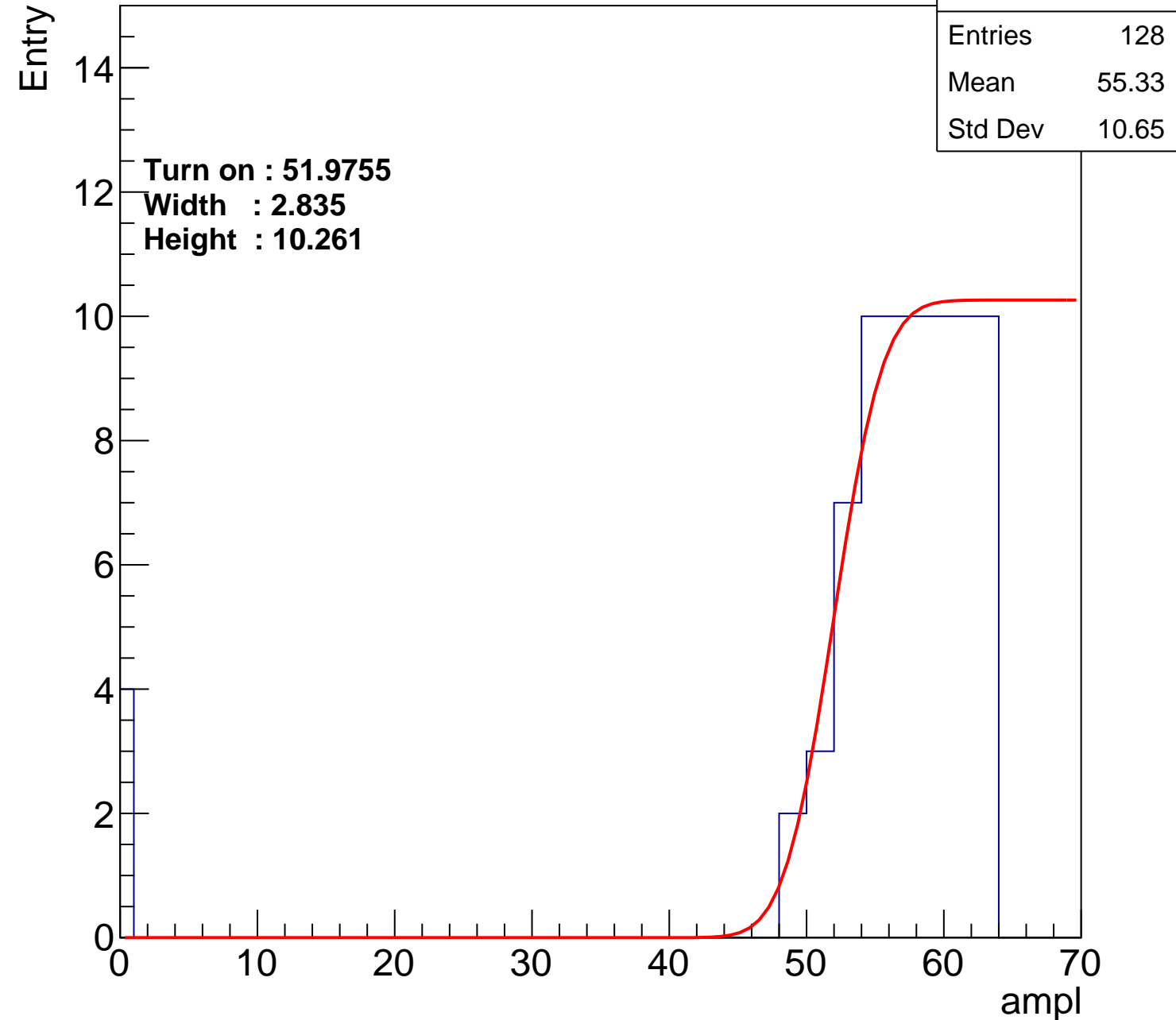
Width : 2.835

Height : 10.261

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch63

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.59
Std Dev	11.44

Turn on : 50.9522

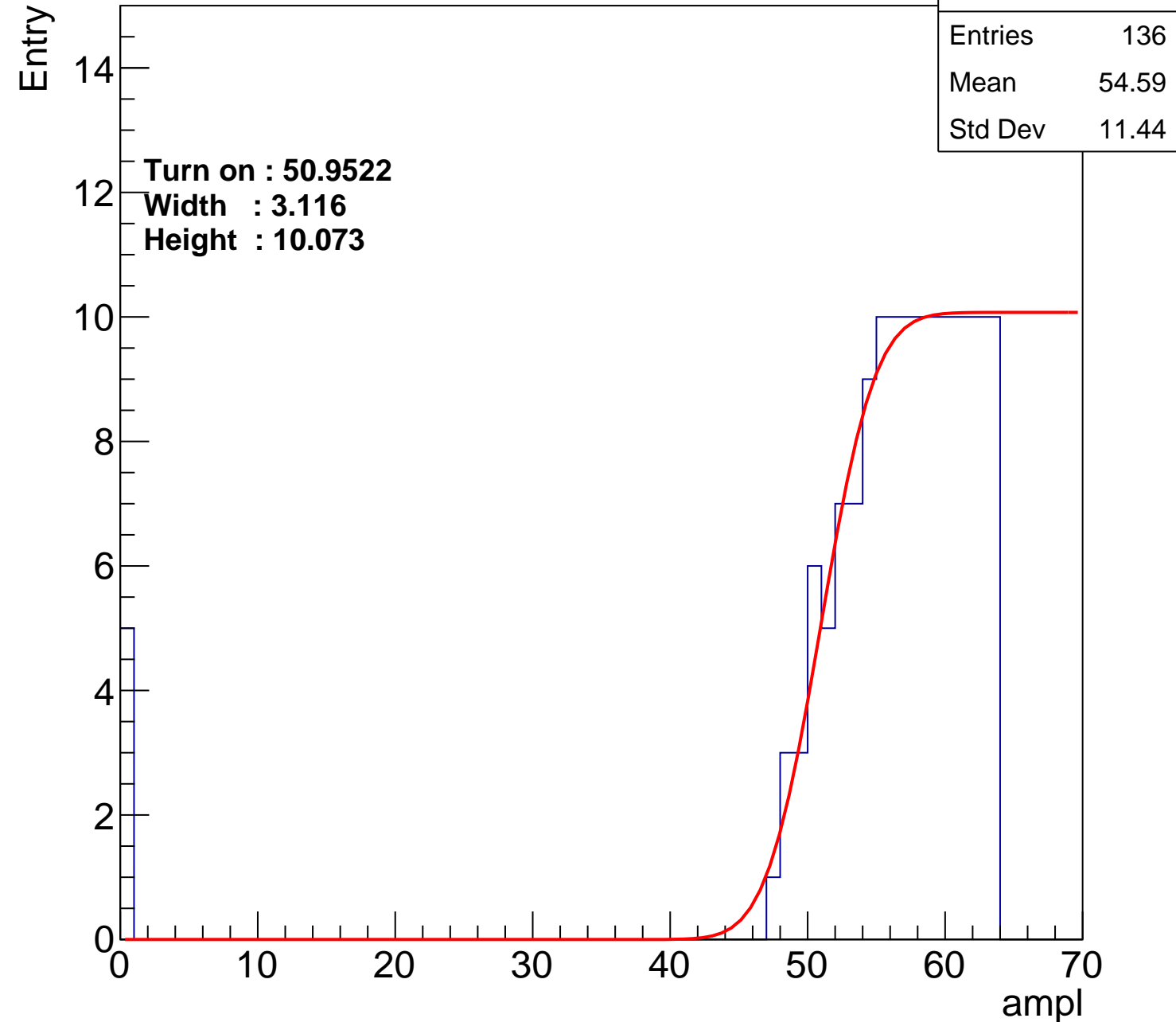
Width : 3.116

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch64

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	112
Mean	56.04
Std Dev	10.06

Turn on : 53.7911

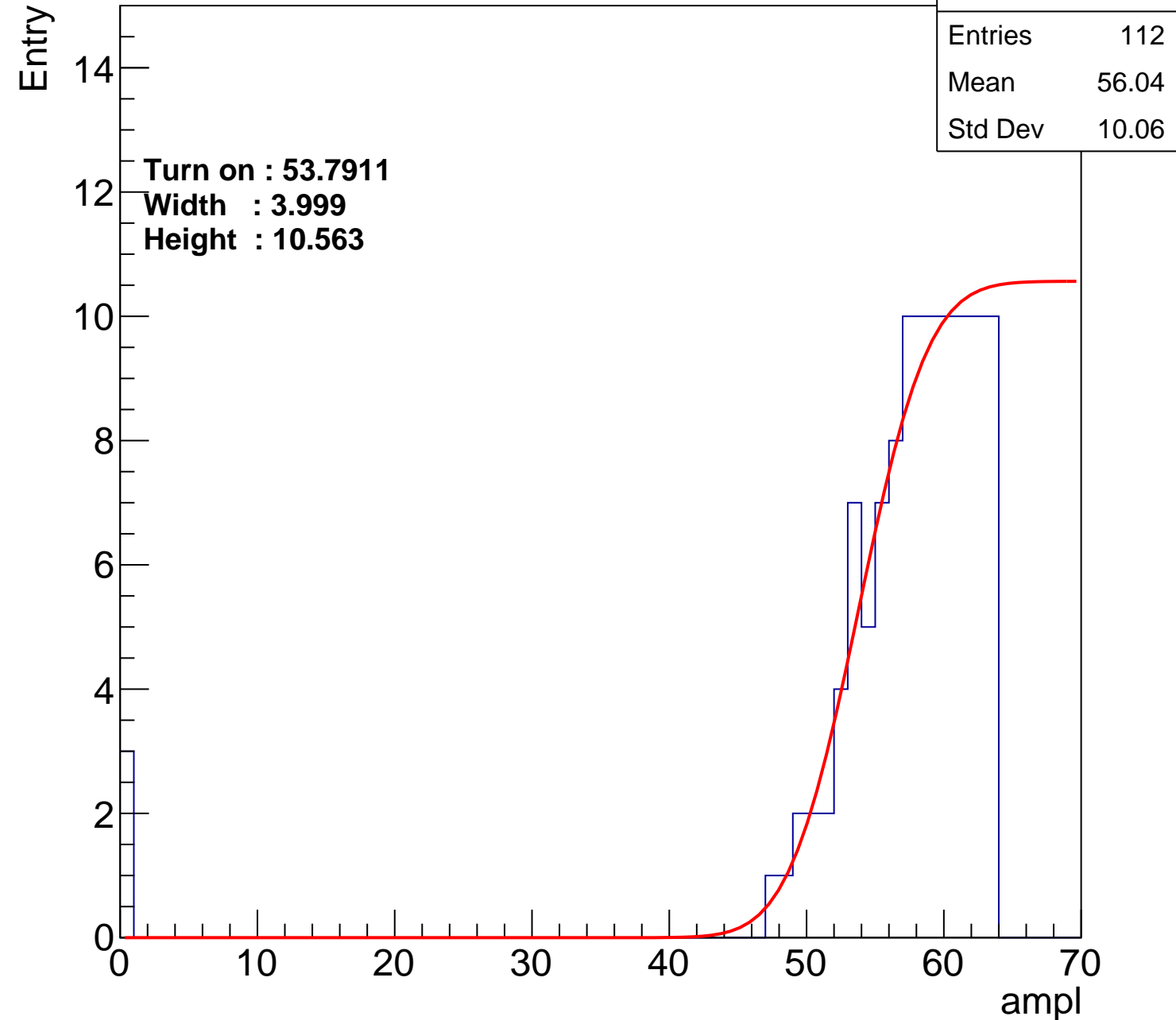
Width : 3.999

Height : 10.563

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch65

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	55.73
Std Dev	6.456

Turn on : 49.6955

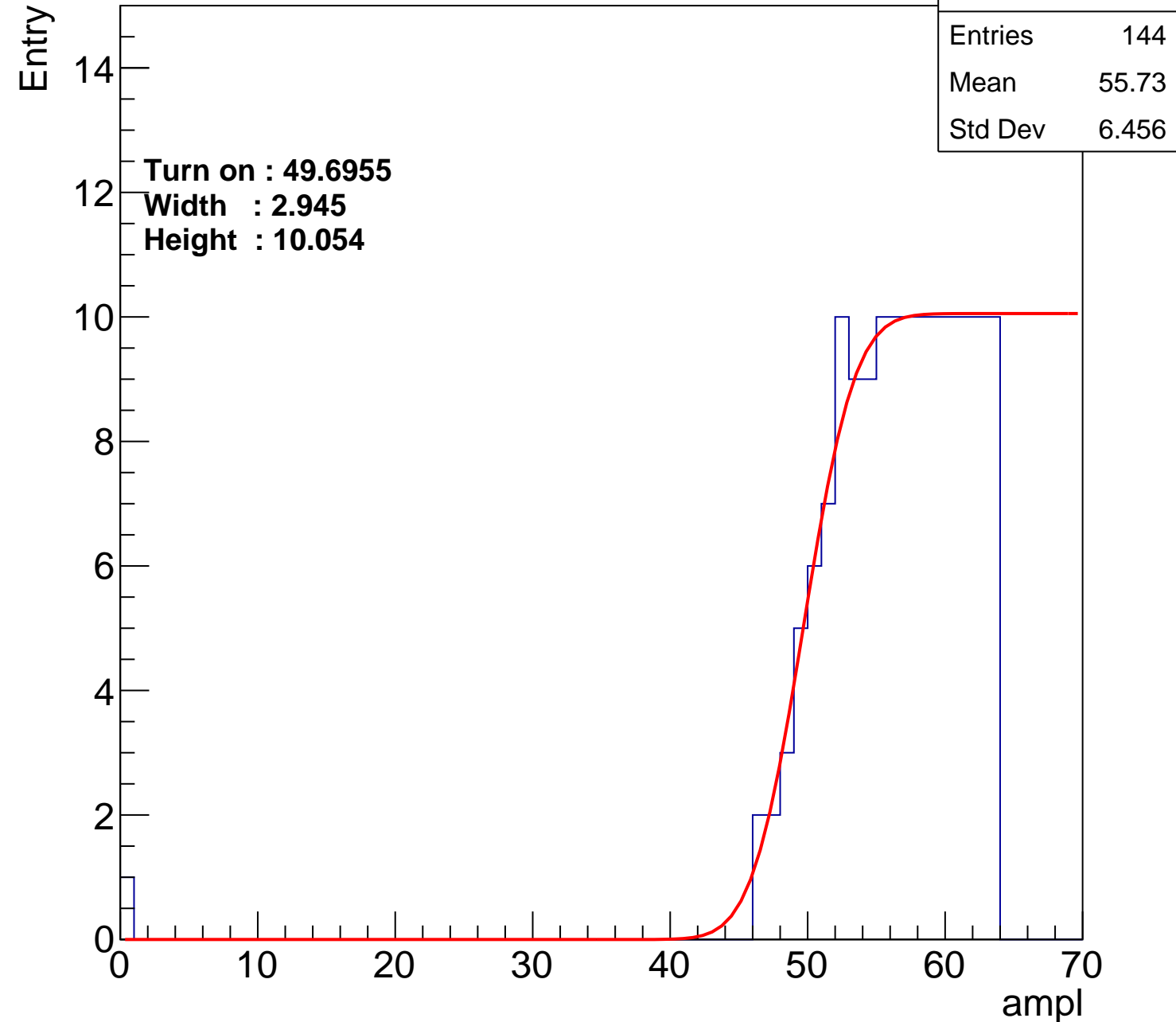
Width : 2.945

Height : 10.054

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch66

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	54.41
Std Dev	11.34

Turn on : 50.7816

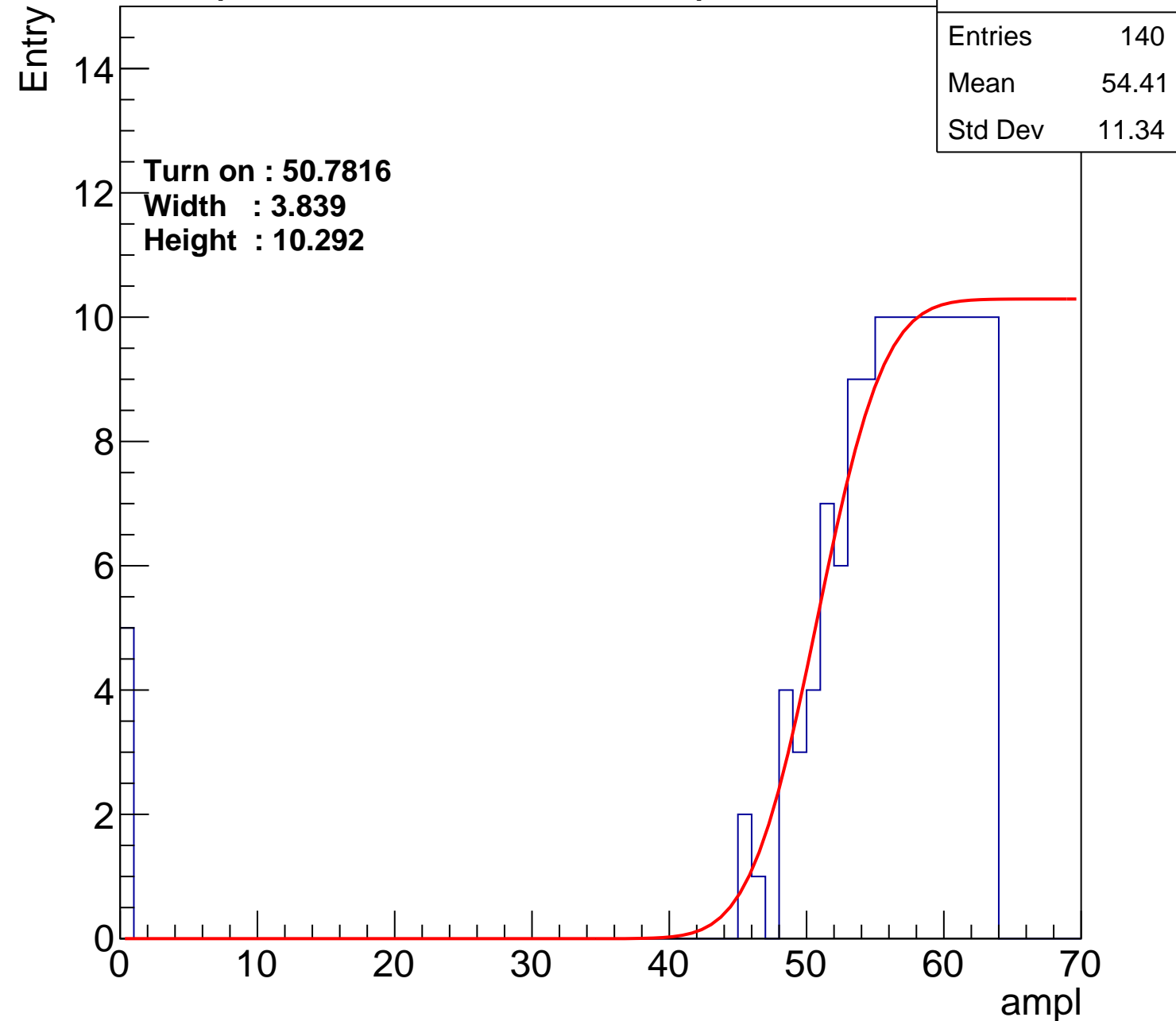
Width : 3.839

Height : 10.292

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch67

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.95
Std Dev	8.063

Turn on : 51.2767

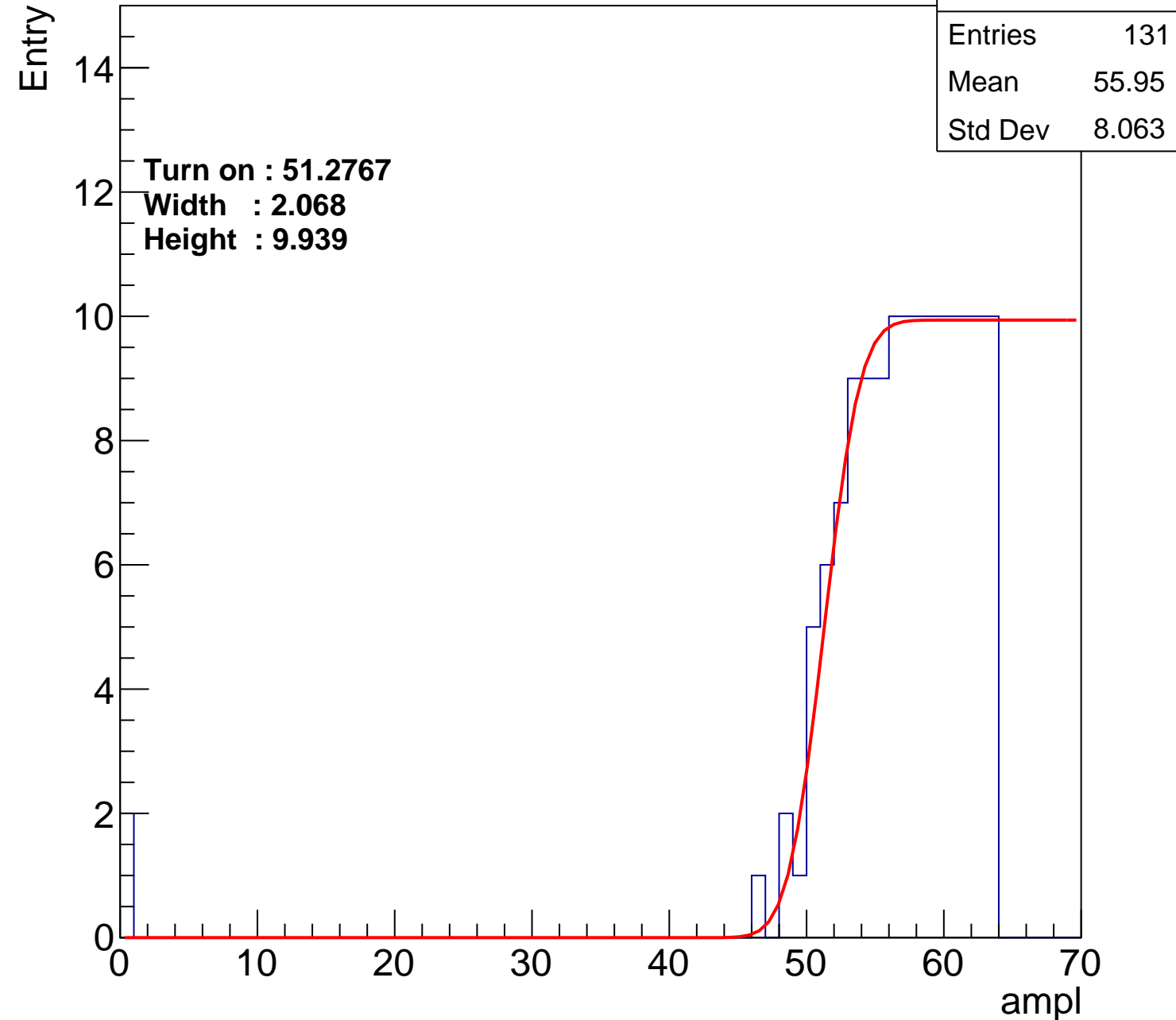
Width : 2.068

Height : 9.939

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch68

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	56.27
Std Dev	6.405

Turn on : 51.0684

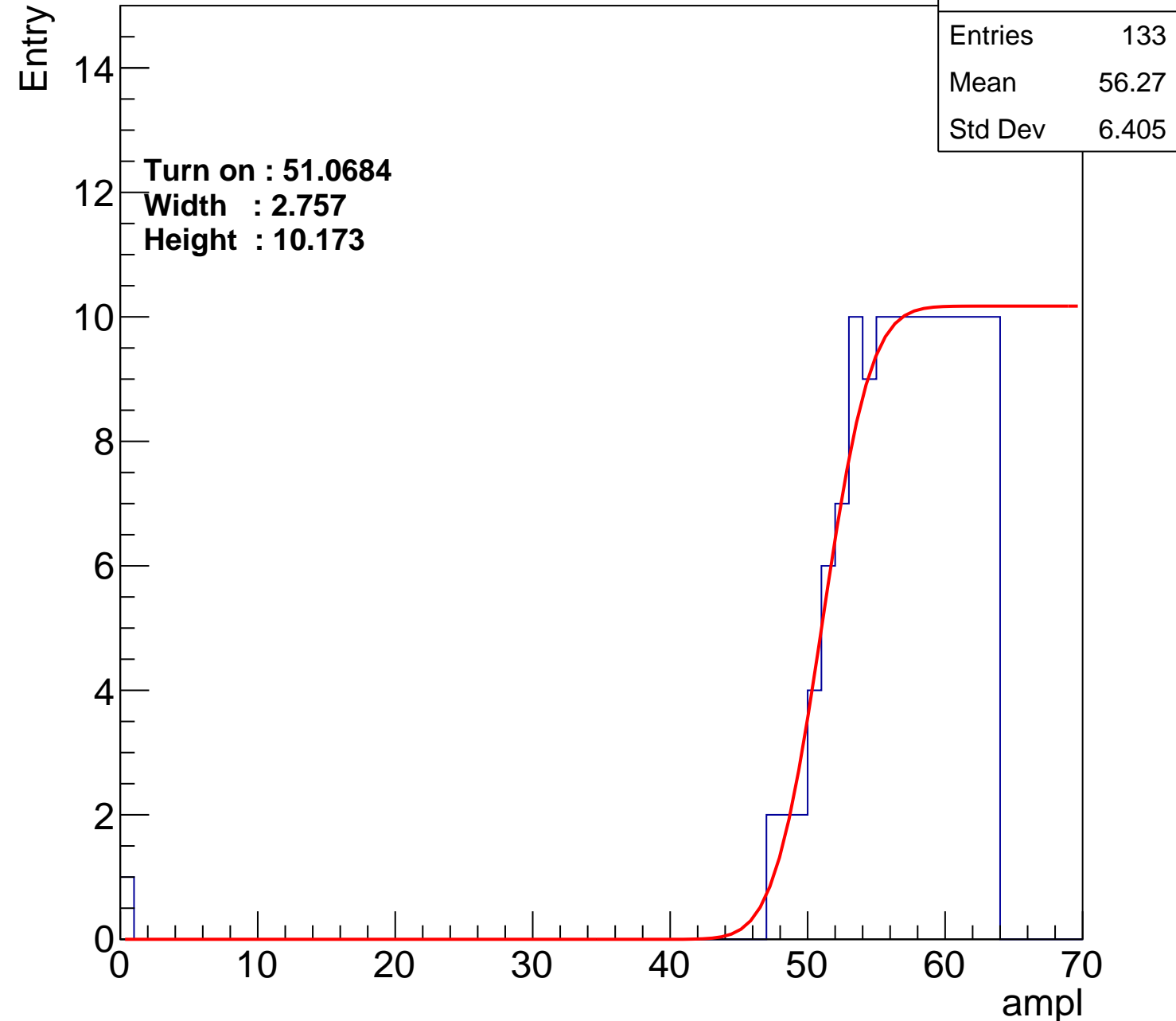
Width : 2.757

Height : 10.173

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch69

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	136
Mean	54.65
Std Dev	11.42

Turn on : 51.4663

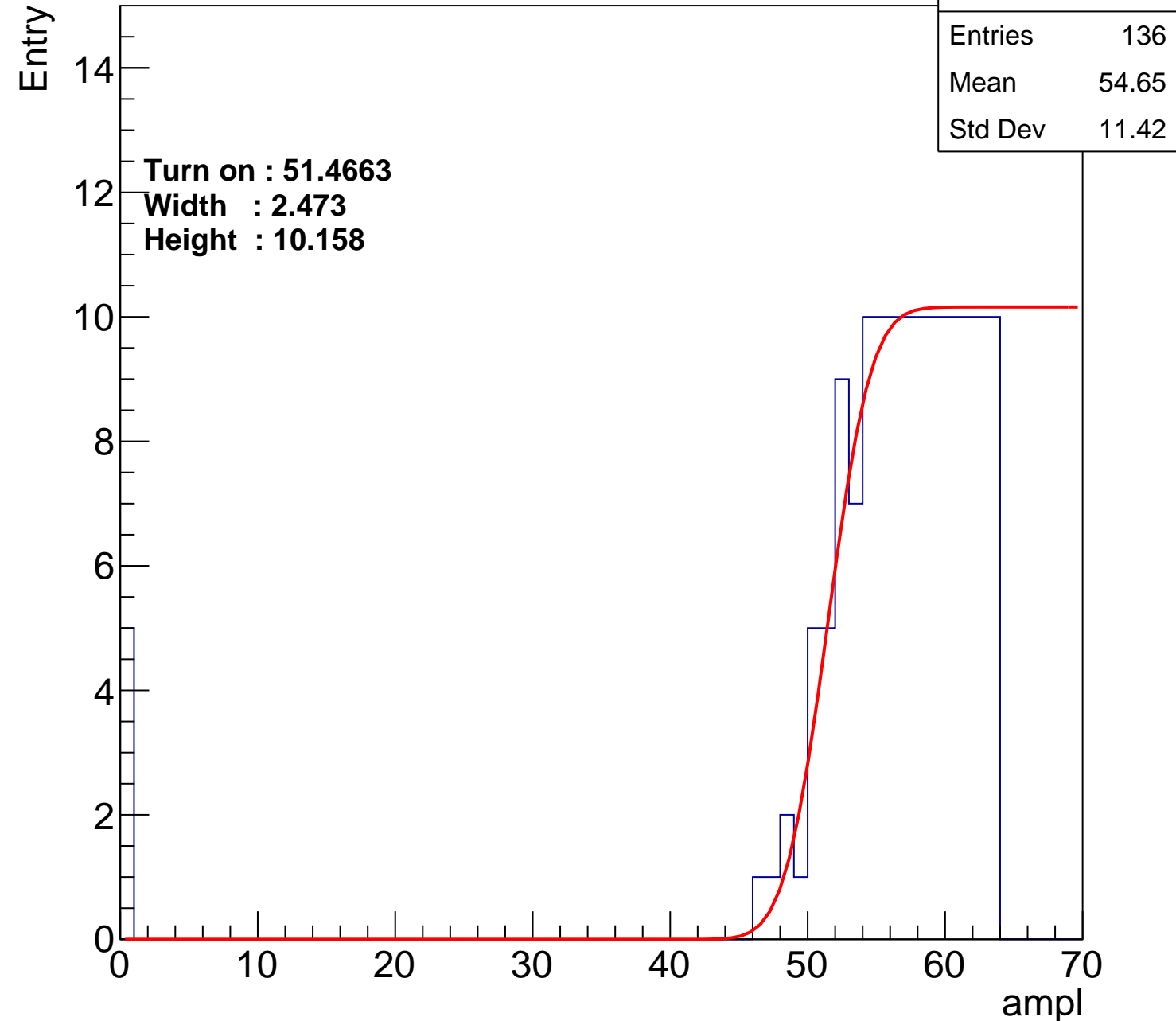
Width : 2.473

Height : 10.158

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch70

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	155
Mean	54.81
Std Dev	7.925

Turn on : 48.9839

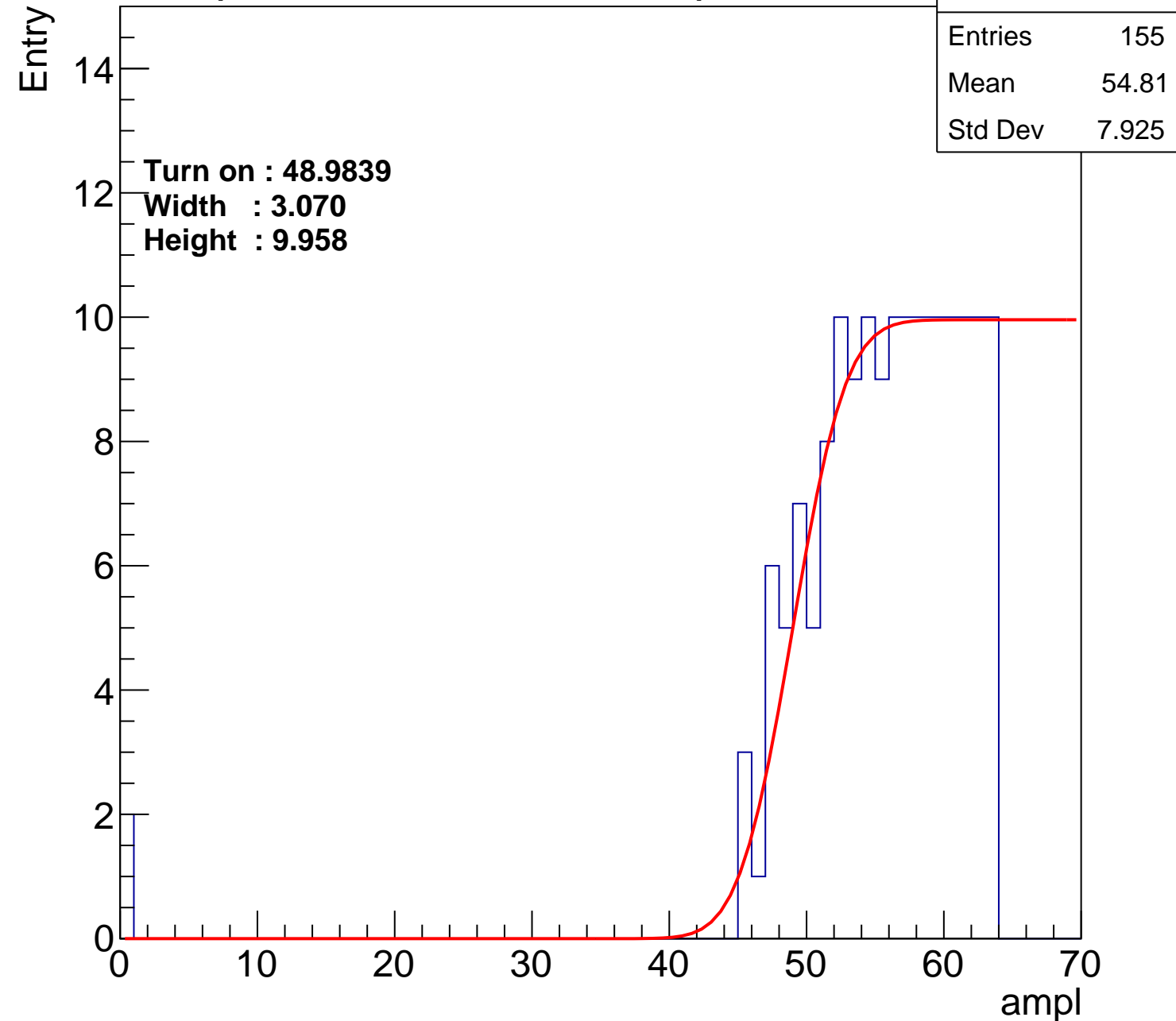
Width : 3.070

Height : 9.958

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch71

calib\_packv5\_040323\_1717.root, FC#2, port C3

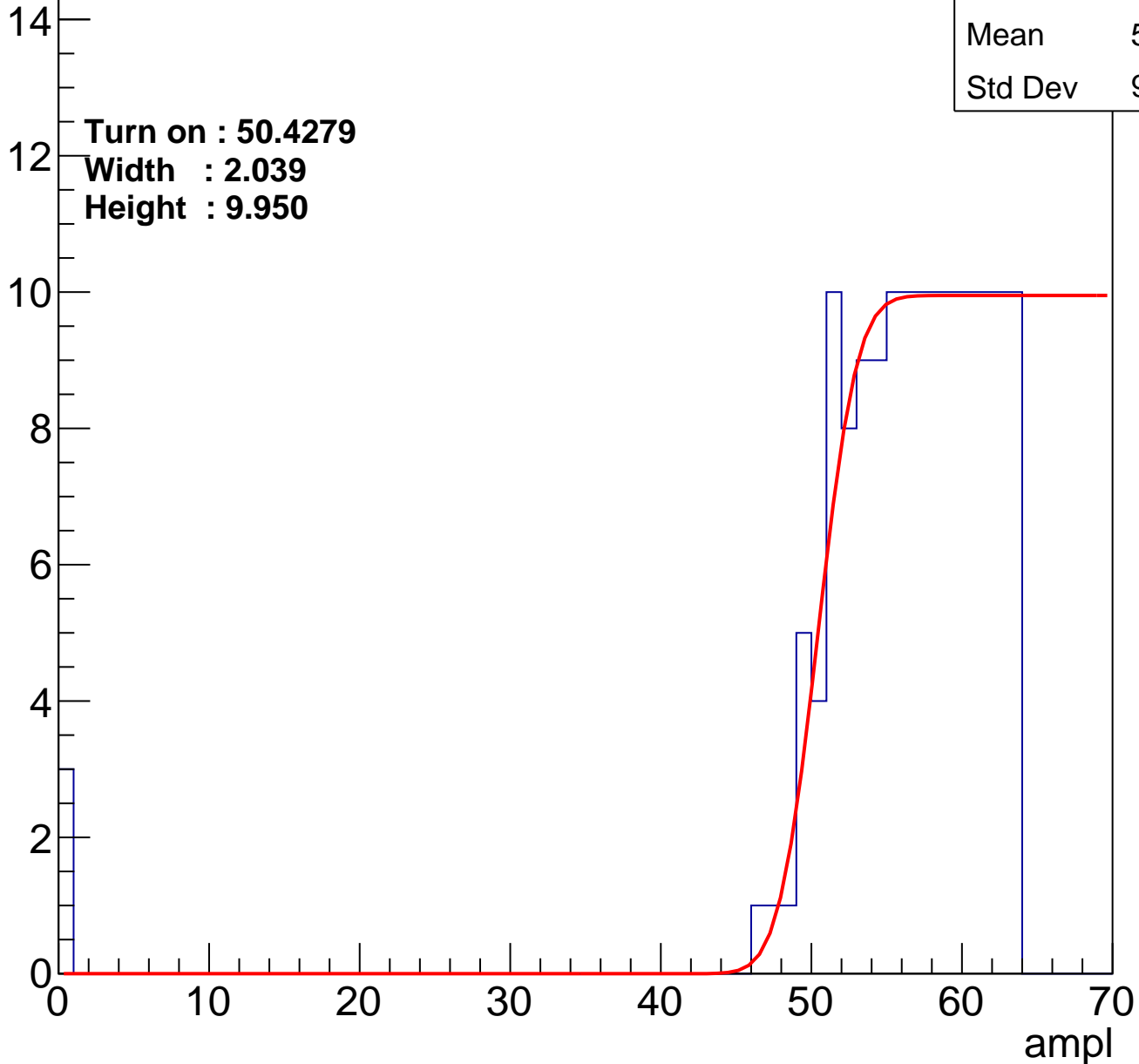
Entries	141
Mean	55.21
Std Dev	9.171

Turn on : 50.4279

Width : 2.039

Height : 9.950

Entry



# B0L103S, U19-ch72

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.98
Std Dev	9.335

Turn on : 50.6106

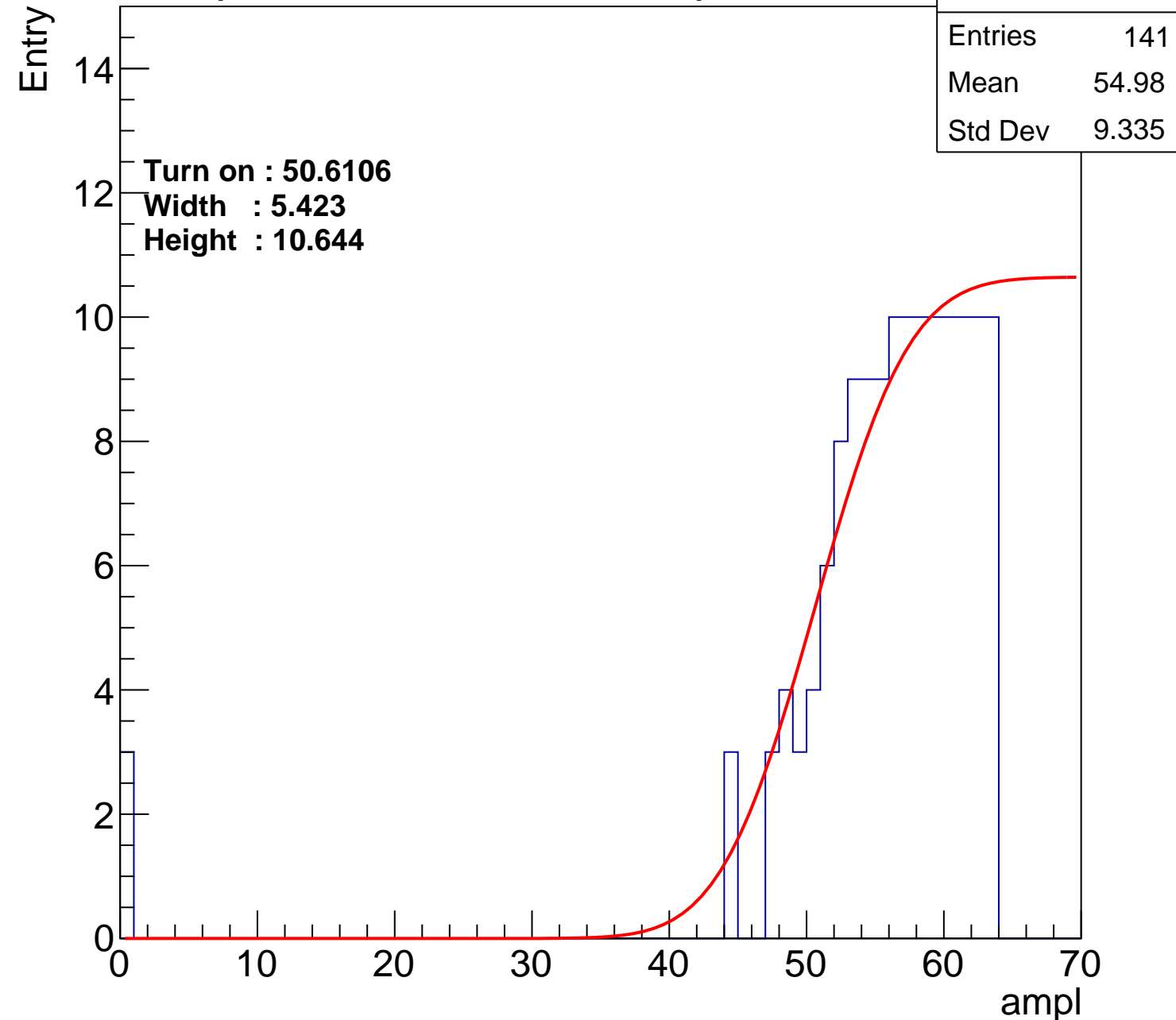
Width : 5.423

Height : 10.644

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch73

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.2
Std Dev	9.355

Turn on : 50.9086

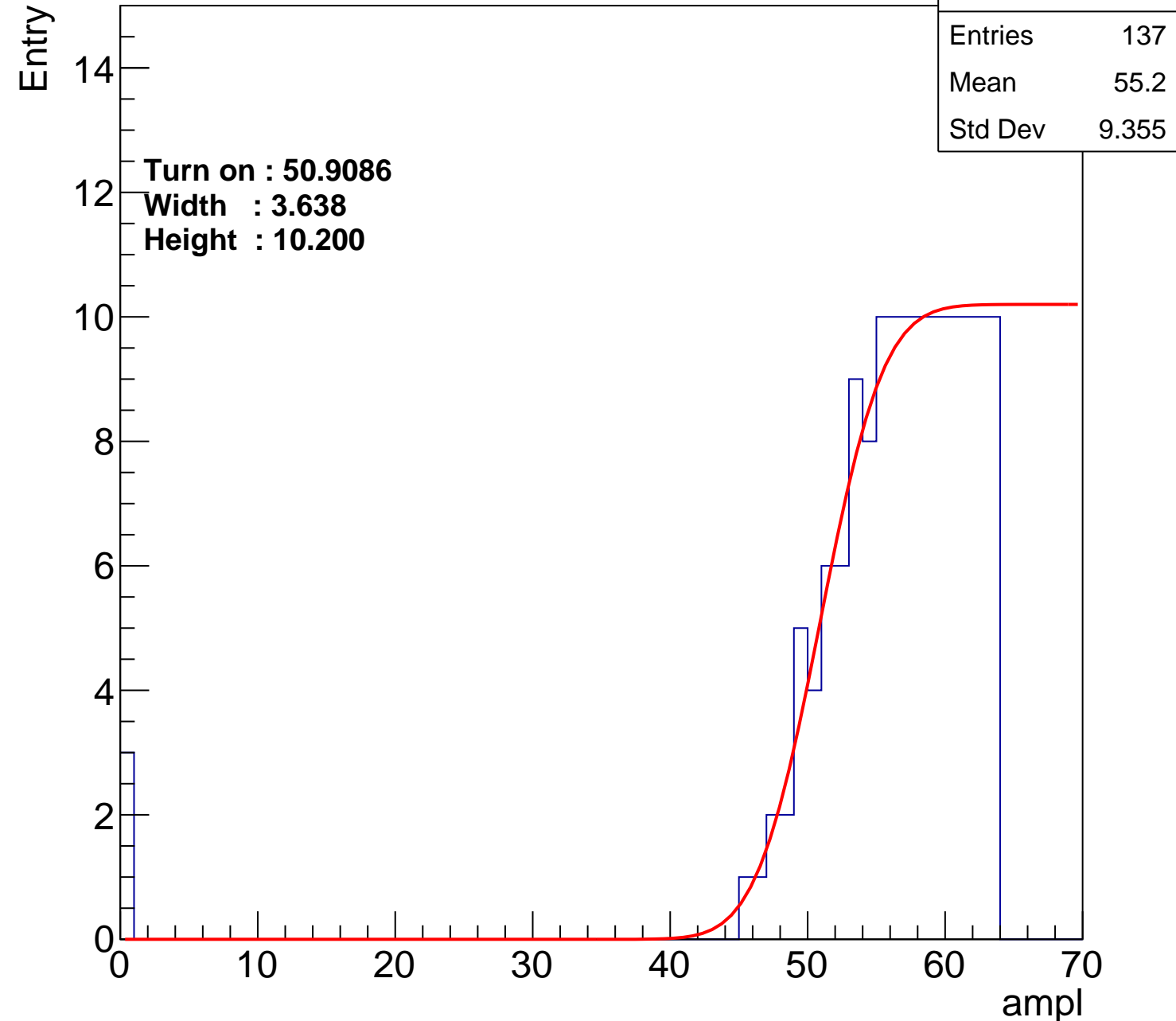
Width : 3.638

Height : 10.200

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch74

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	143
Mean	55.47
Std Dev	7.897

Turn on : 49.7011

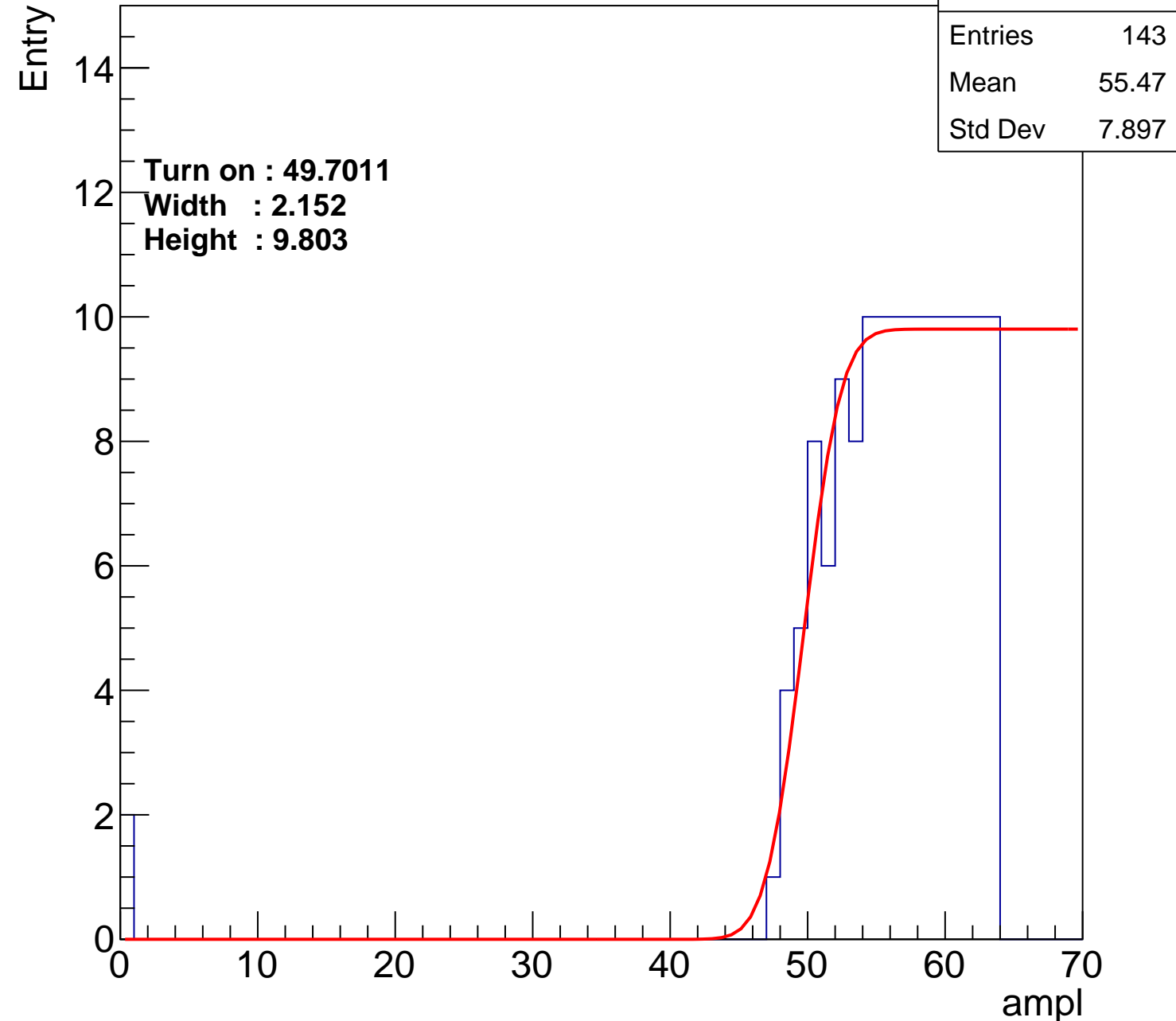
Width : 2.152

Height : 9.803

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch75

calib\_packv5\_040323\_1717.root, FC#2, port C3

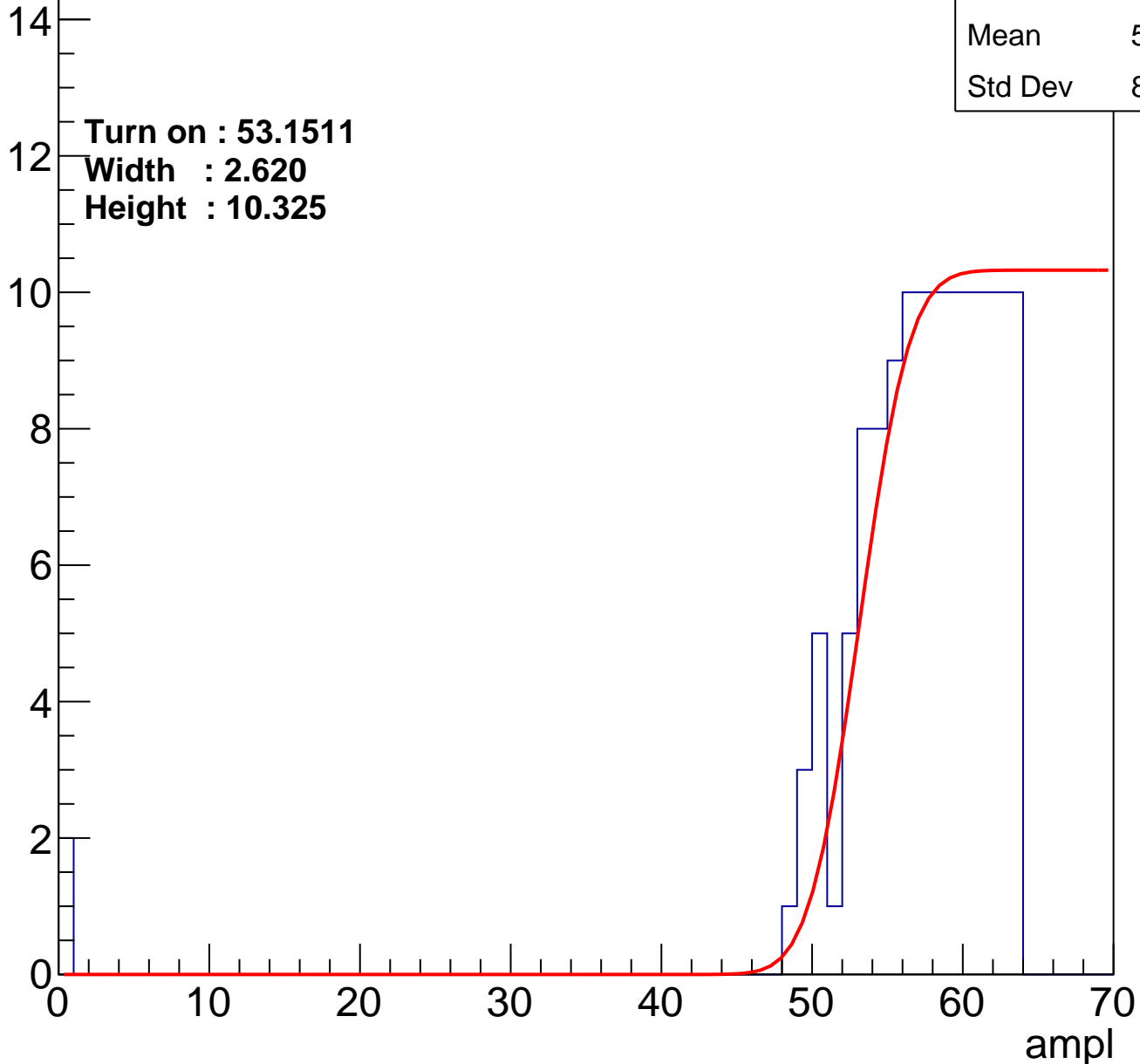
Entries	122
Mean	56.29
Std Dev	8.234

Turn on : 53.1511

Width : 2.620

Height : 10.325

Entry



# B0L103S, U19-ch76

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	134
Mean	54.66
Std Dev	11.5

Turn on : 51.3083

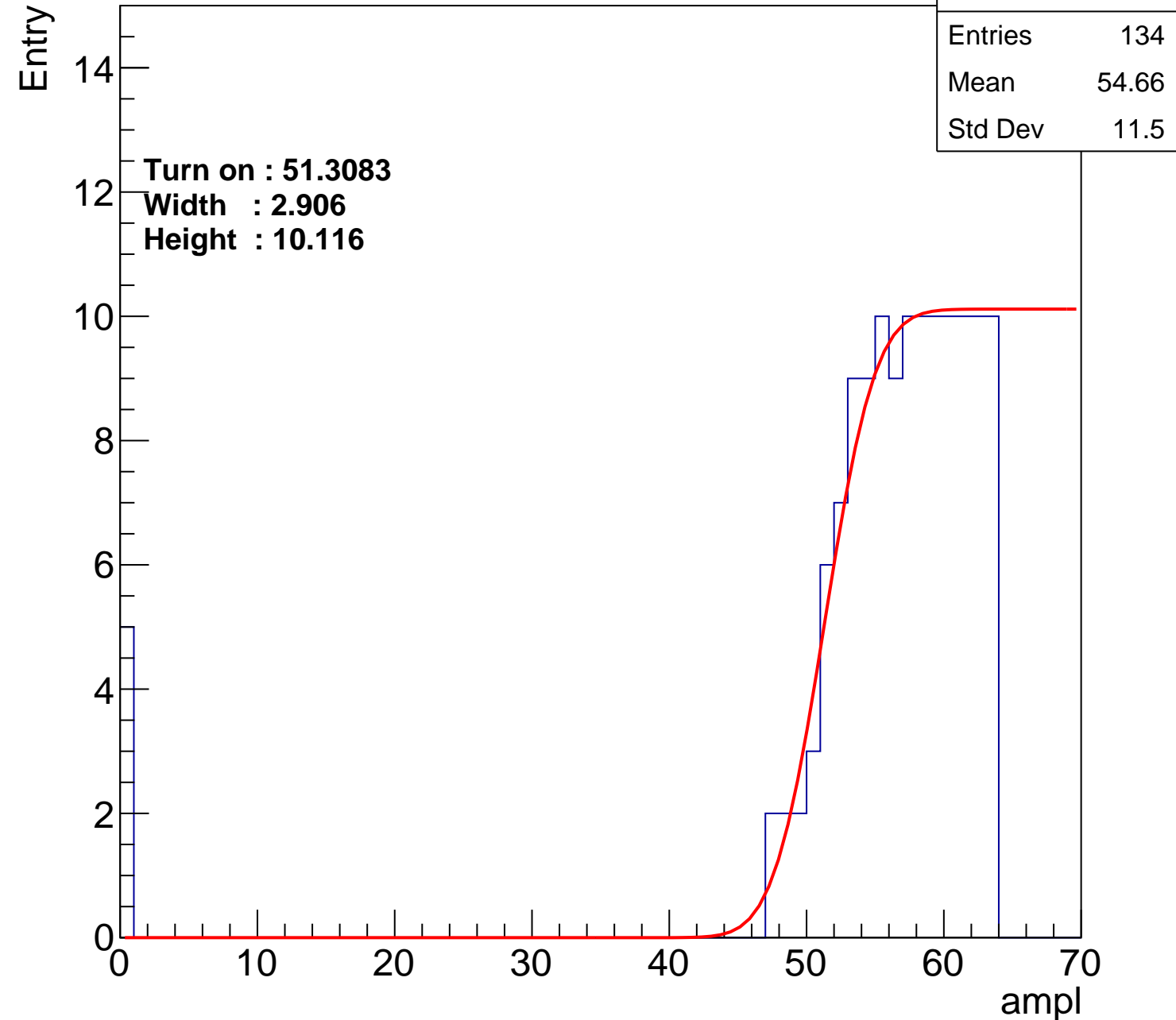
Width : 2.906

Height : 10.116

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch77

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	151
Mean	55.31
Std Dev	6.598

Turn on : 49.3972

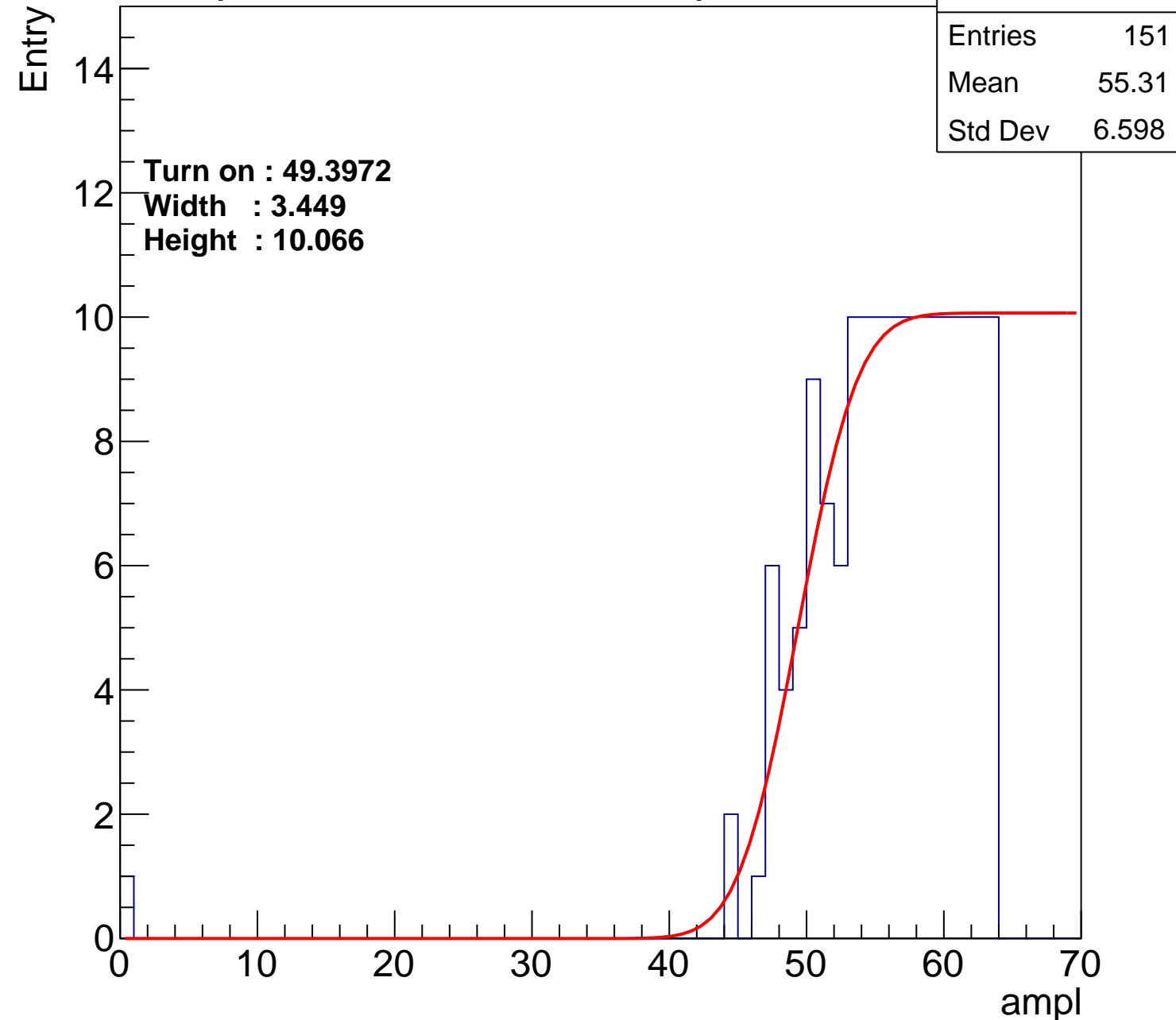
Width : 3.449

Height : 10.066

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch78

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.88
Std Dev	8.162

Turn on : 51.2967

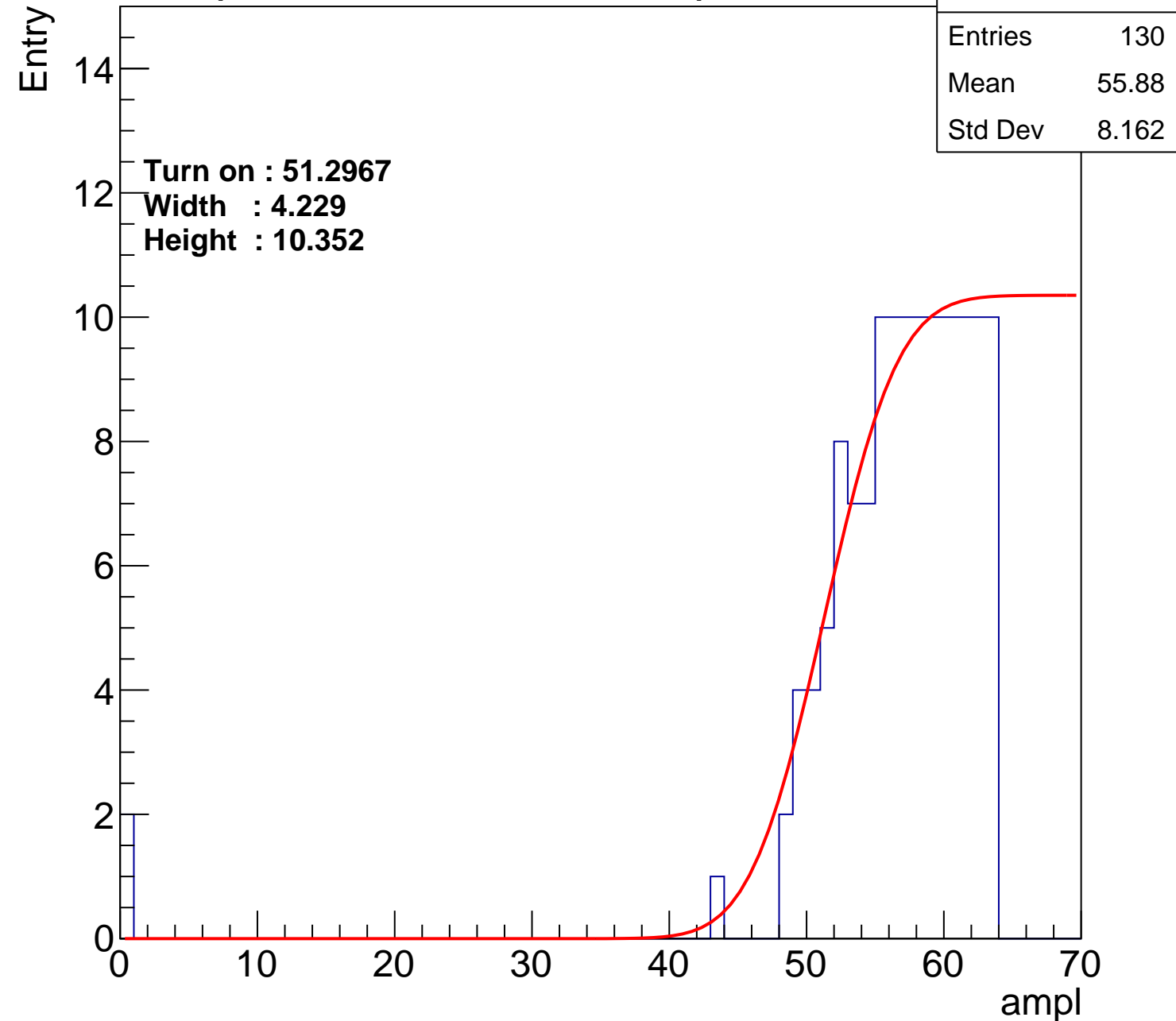
Width : 4.229

Height : 10.352

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch79

calib\_packv5\_040323\_1717.root, FC#2, port C3

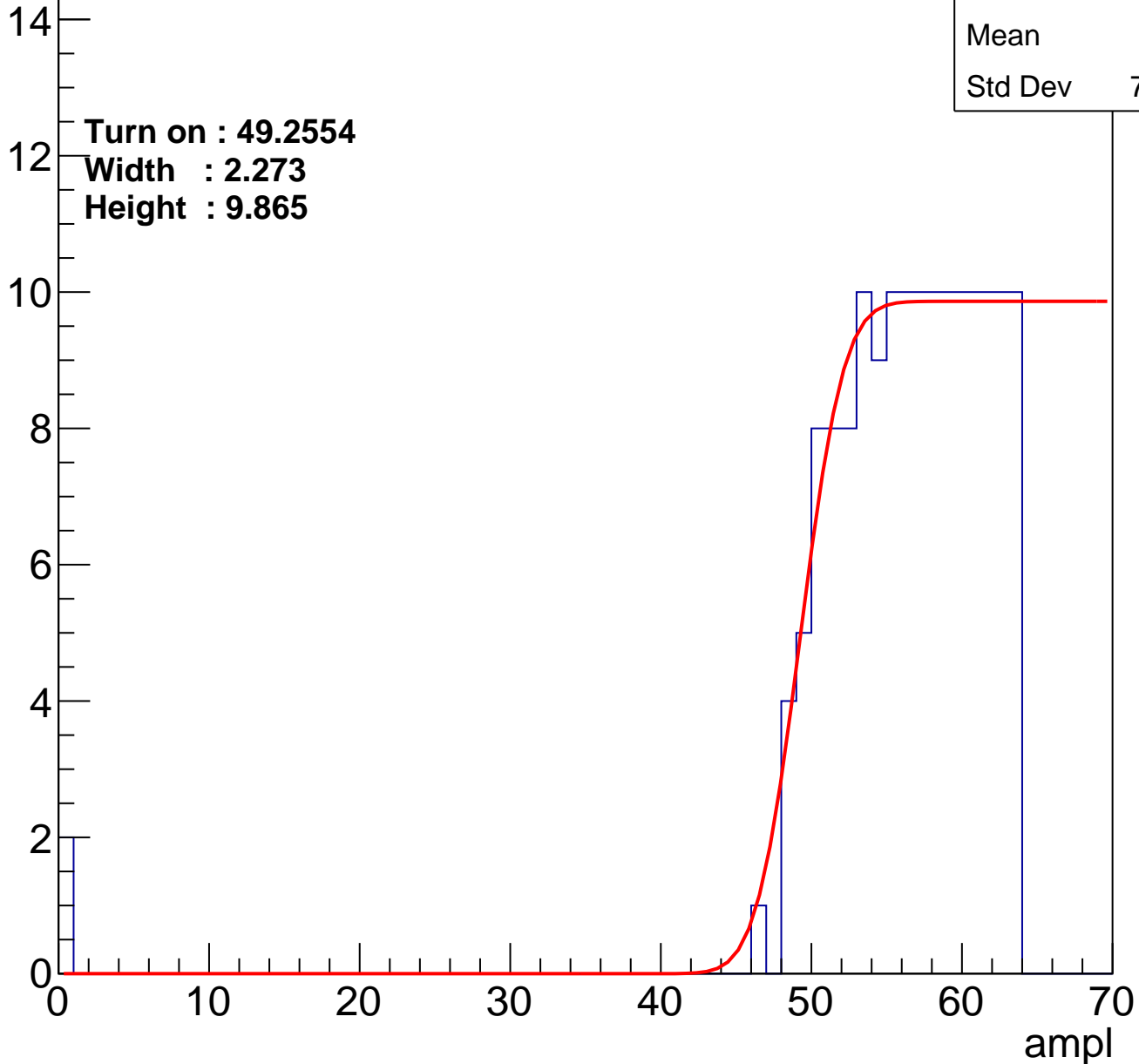
Entries	145
Mean	55.4
Std Dev	7.866

Turn on : 49.2554

Width : 2.273

Height : 9.865

Entry



# B0L103S, U19-ch80

calib\_packv5\_040323\_1717.root, FC#2, port C3

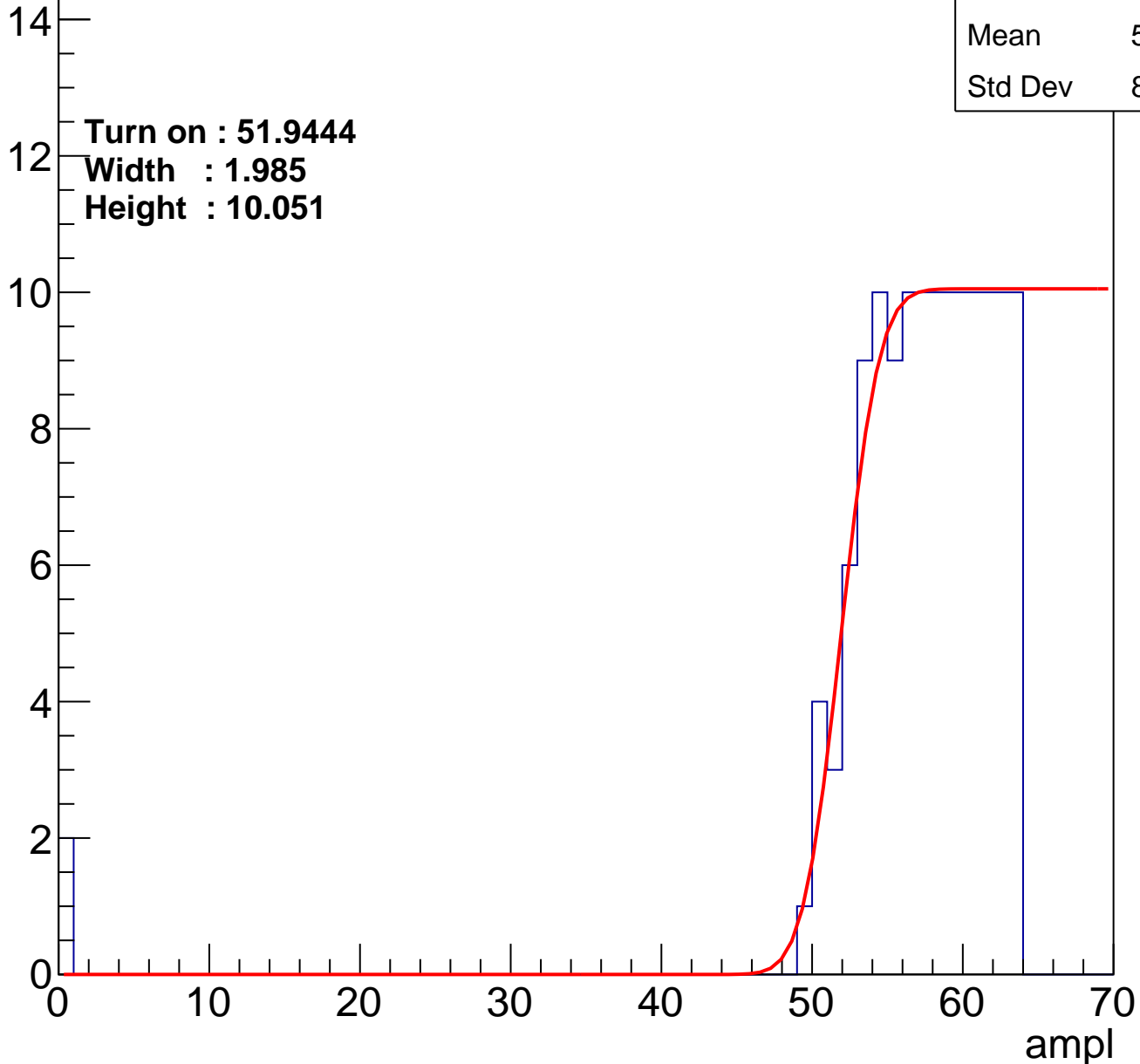
Entries	124
Mean	56.34
Std Dev	8.108

Turn on : 51.9444

Width : 1.985

Height : 10.051

Entry



# B0L103S, U19-ch81

**calib\_packv5\_040323\_1717.root, FC#2, port C3**

Entries	118
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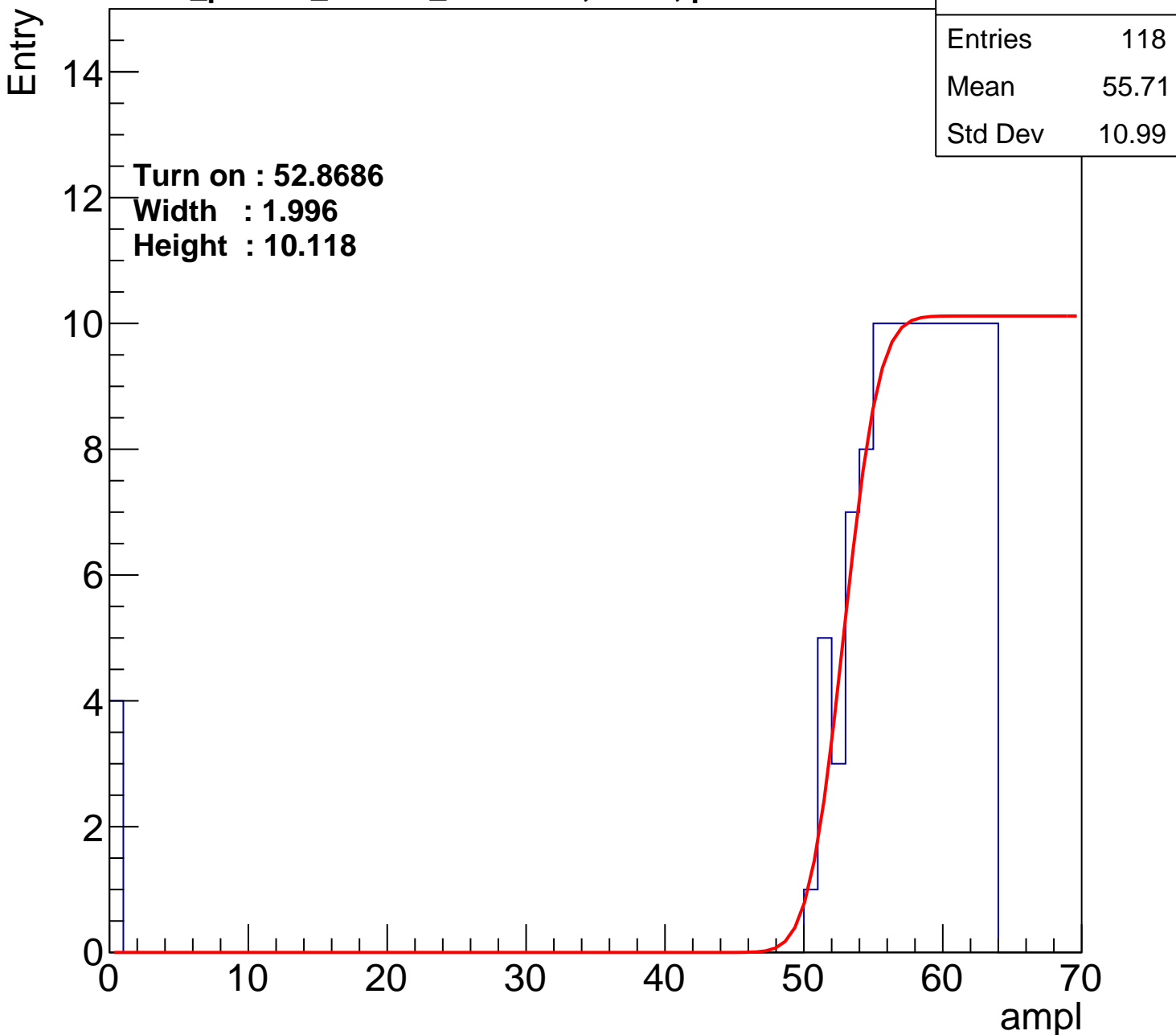
Mean	55.71
------	-------

Std Dev	10.99
---------	-------

**Turn on : 52.8686**

**Width : 1.996**

**Height : 10.118**



# B0L103S, U19-ch82

calib\_packv5\_040323\_1717.root, FC#2, port C3

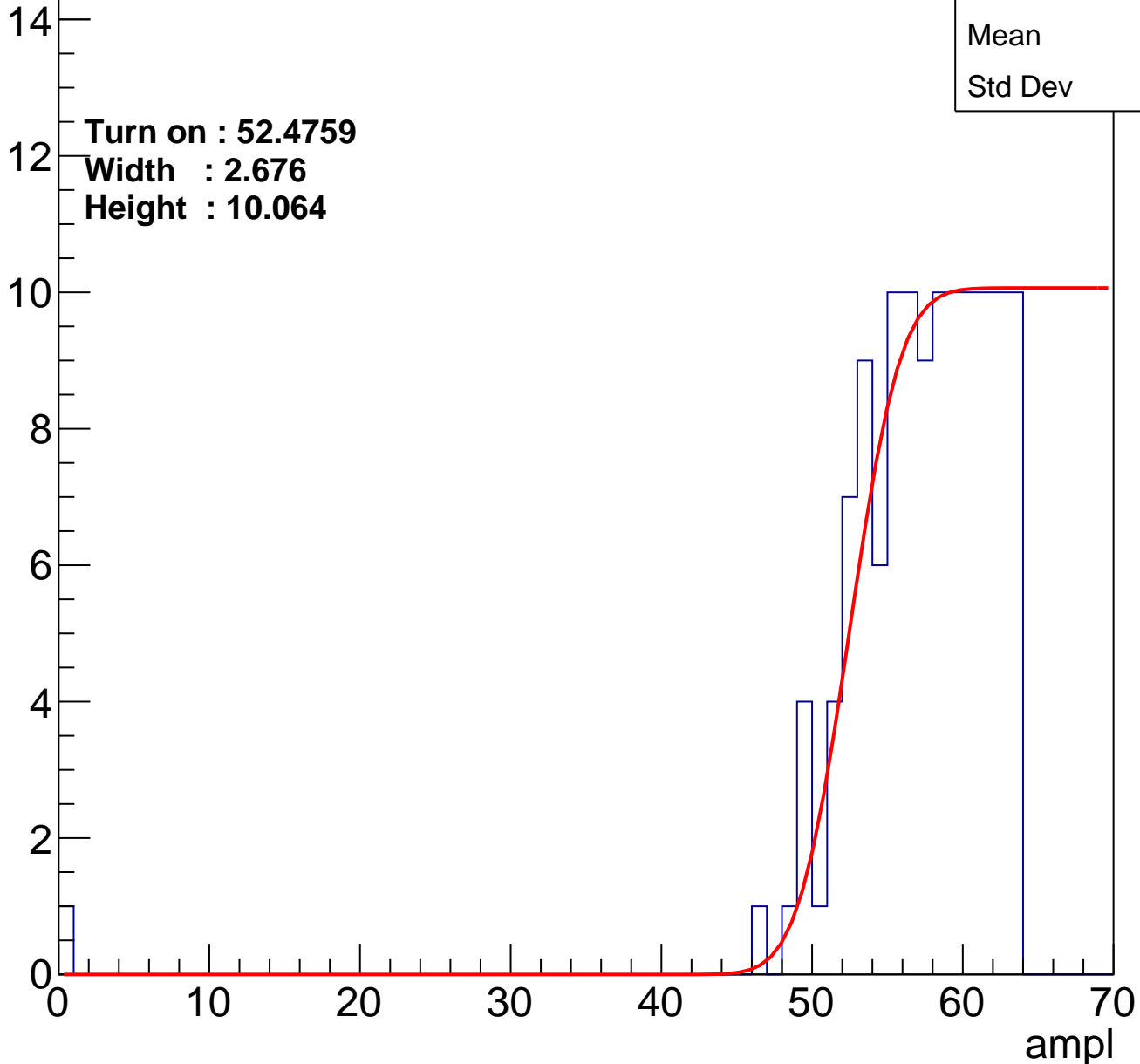
Entries	123
Mean	56.6
Std Dev	6.51

Turn on : 52.4759

Width : 2.676

Height : 10.064

Entry



# B0L103S, U19-ch83

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	119
Mean	55.91
Std Dev	9.801

Turn on : 53.2339

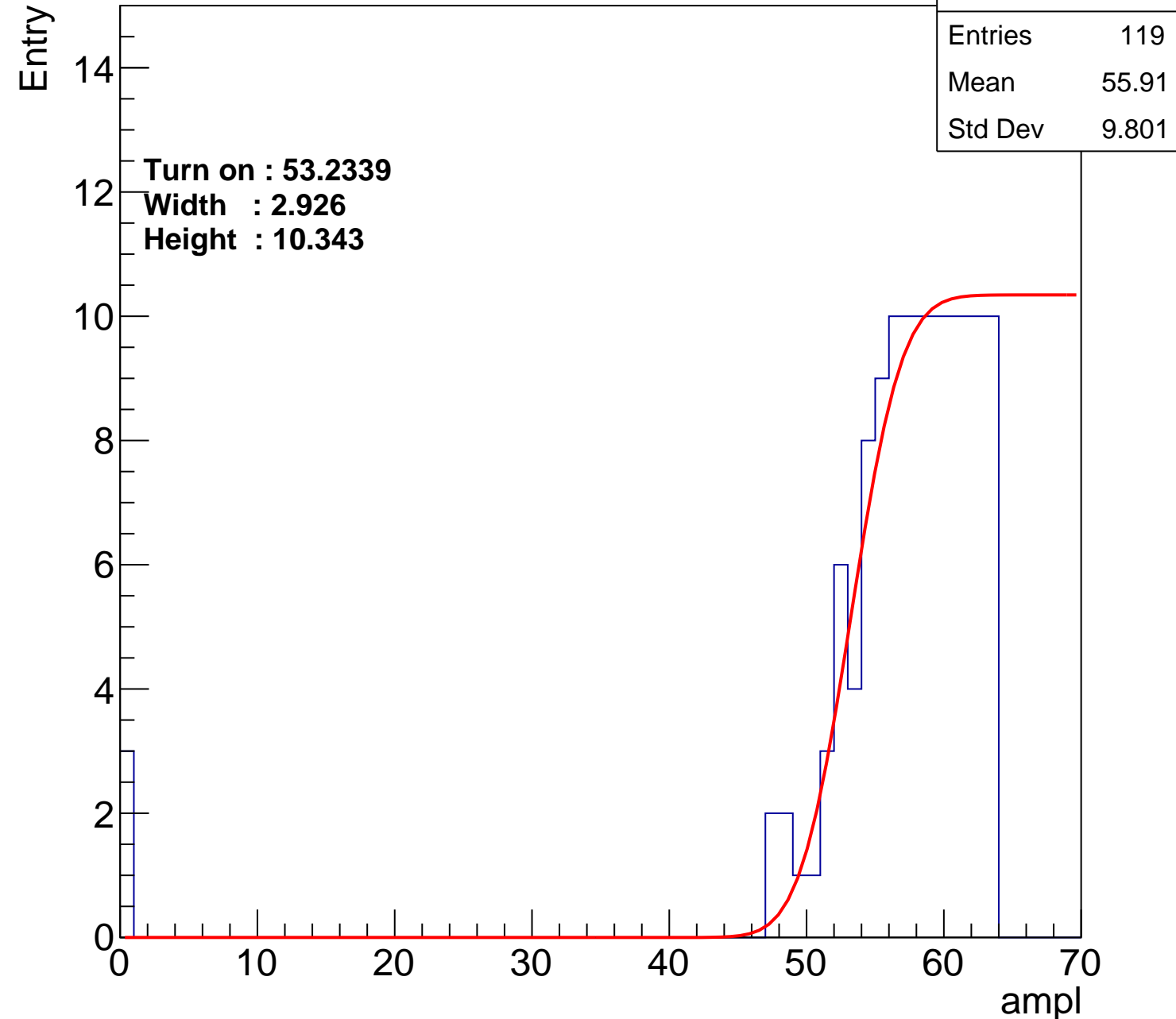
Width : 2.926

Height : 10.343

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch84

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	138
Mean	54.5
Std Dev	11.38

Turn on : 50.8079

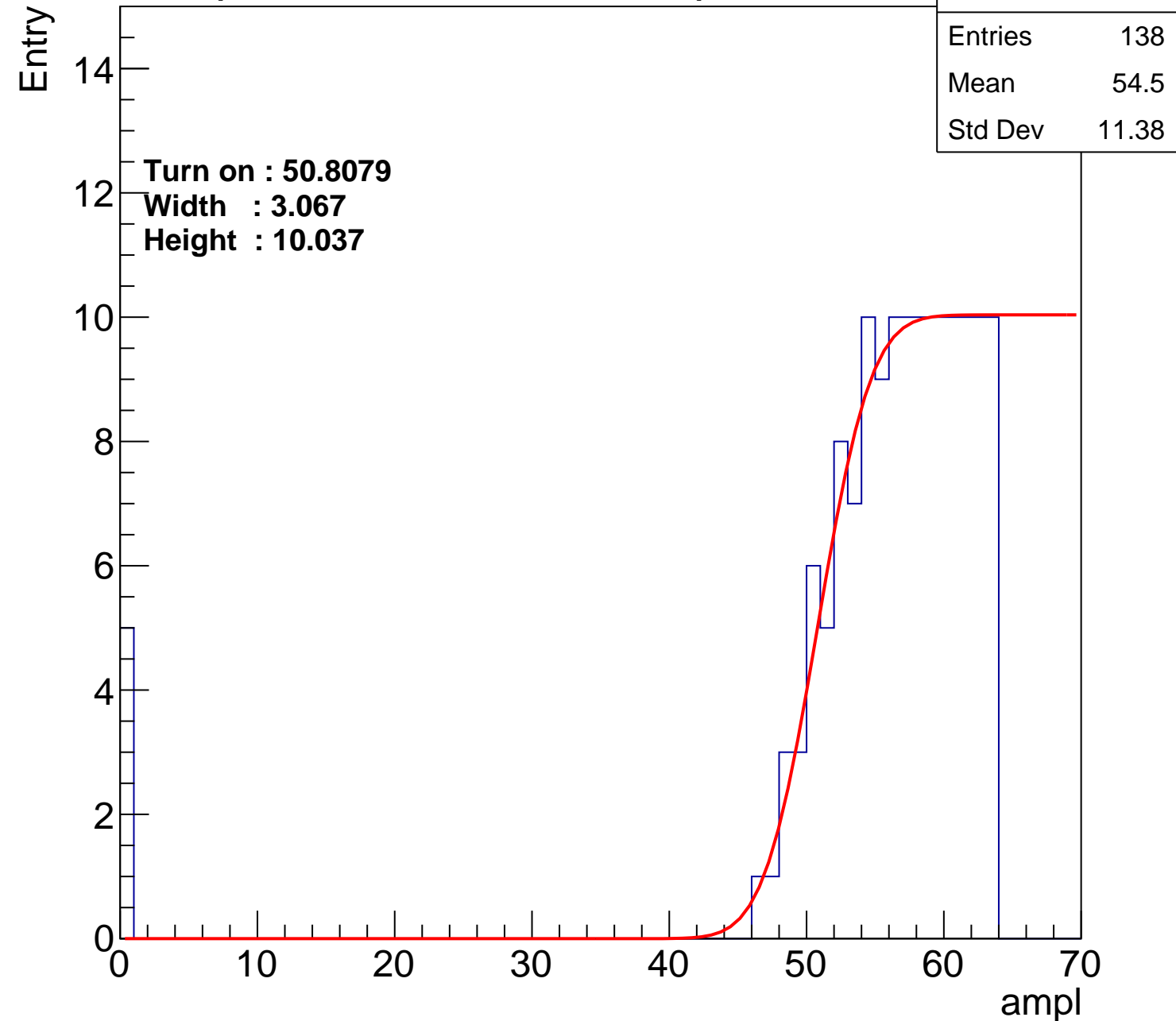
Width : 3.067

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch85

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	54.84
Std Dev	11.64

Turn on : 51.9904

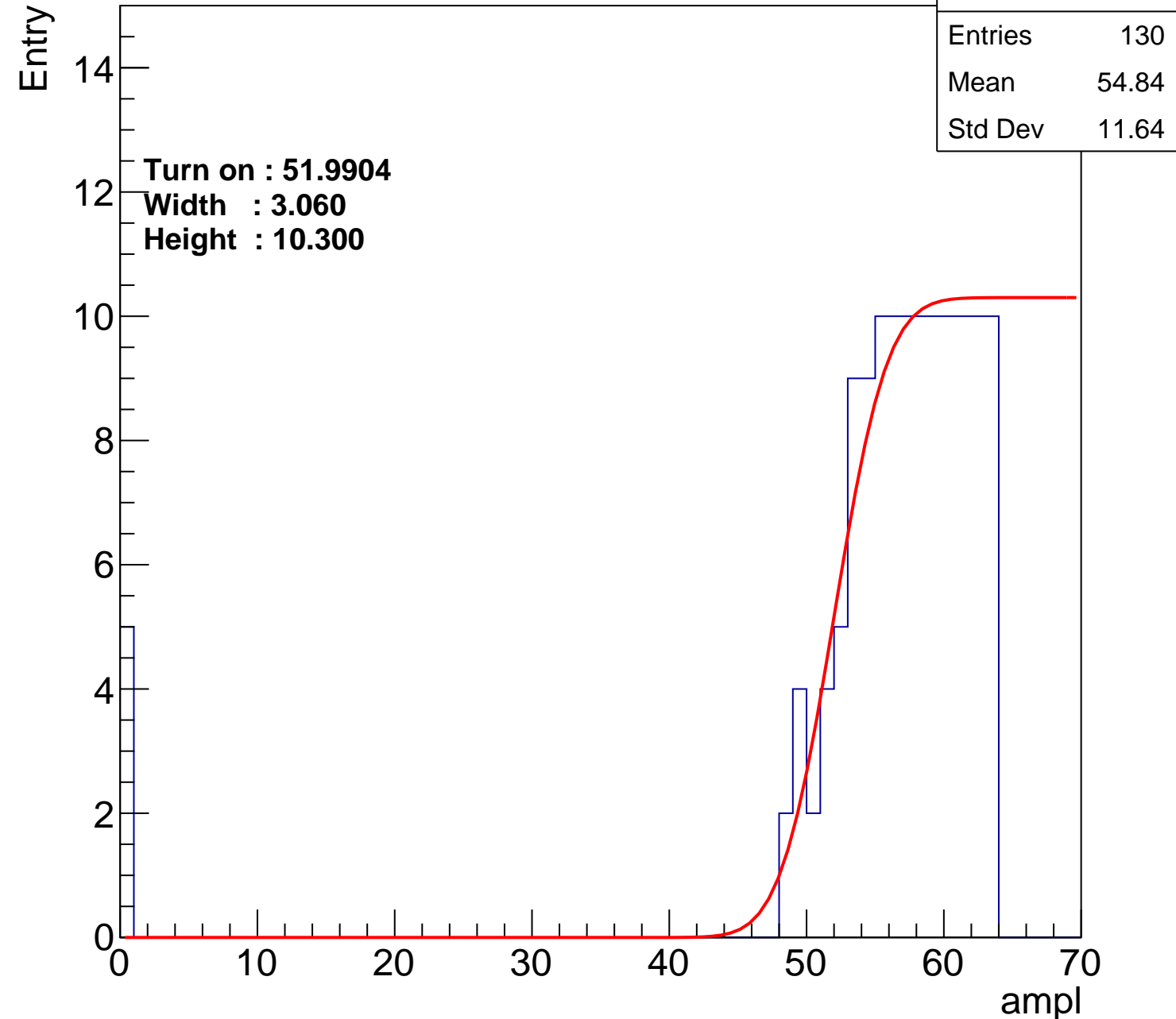
Width : 3.060

Height : 10.300

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch86

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.95
Std Dev	9.149

Turn on : 50.5145

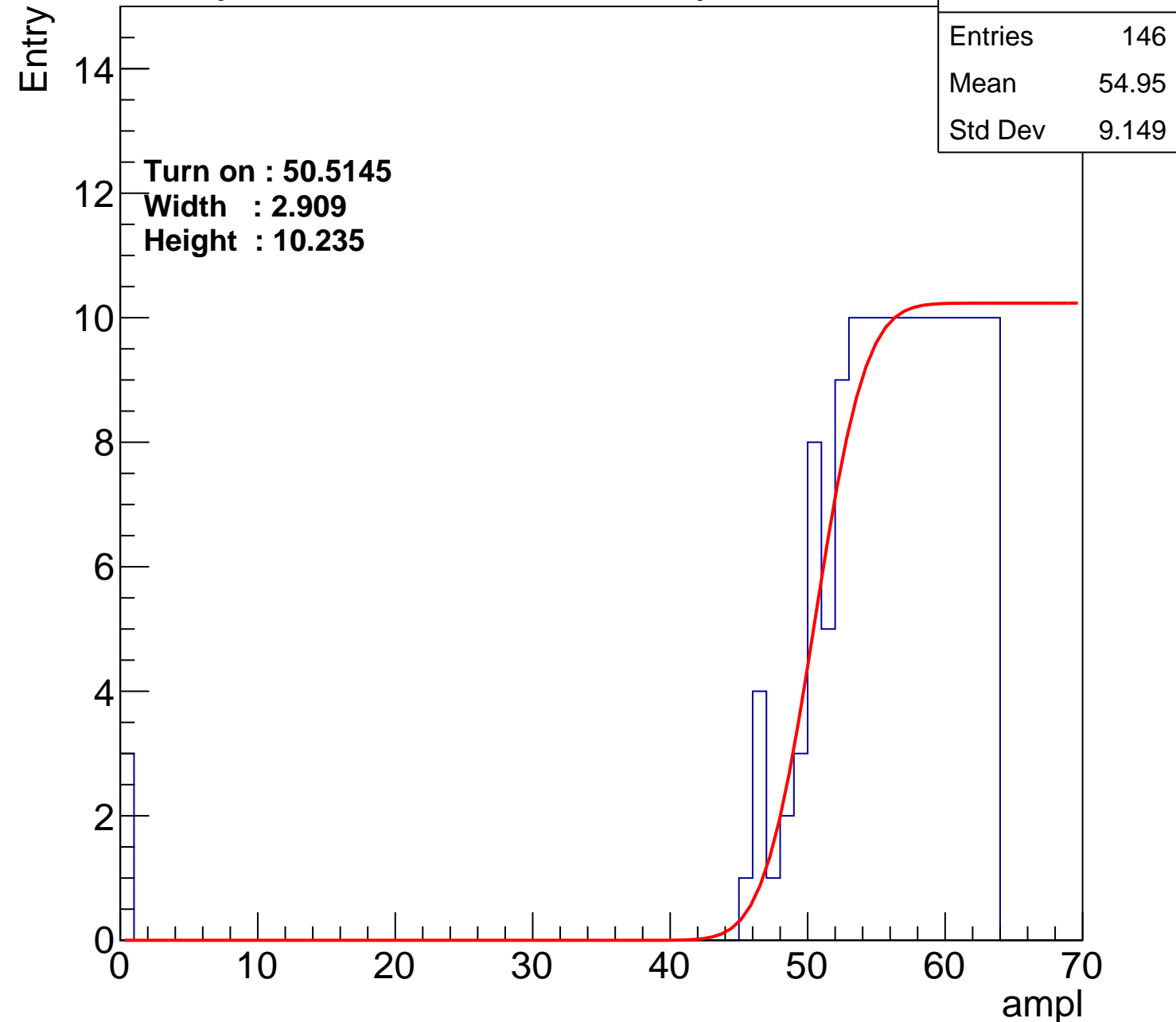
Width : 2.909

Height : 10.235

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch87

calib\_packv5\_040323\_1717.root, FC#2, port C3

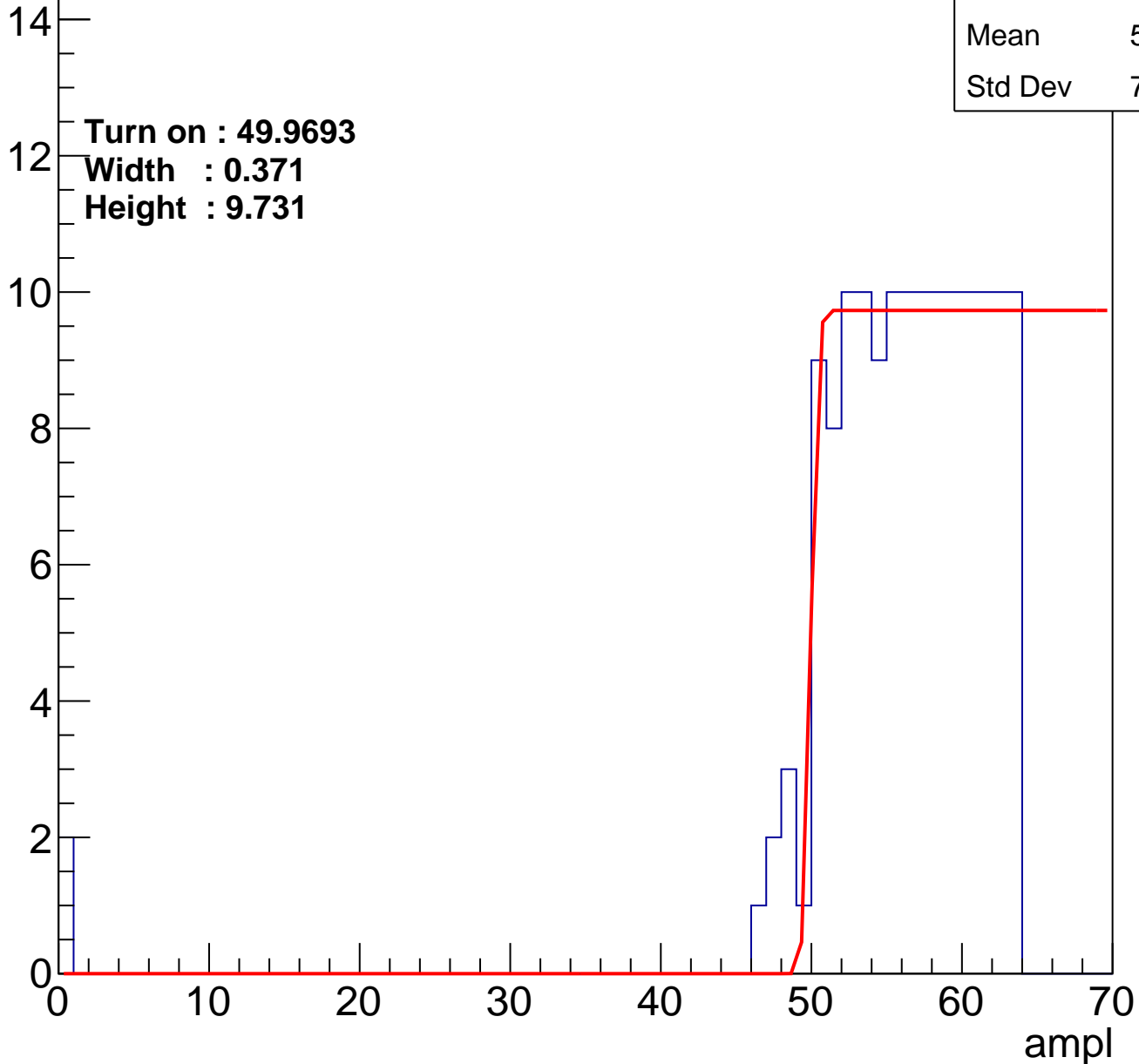
Entries	145
Mean	55.43
Std Dev	7.855

Turn on : 49.9693

Width : 0.371

Height : 9.731

Entry



# B0L103S, U19-ch88

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	149
Mean	54.77
Std Dev	9.091

Turn on : 48.9286

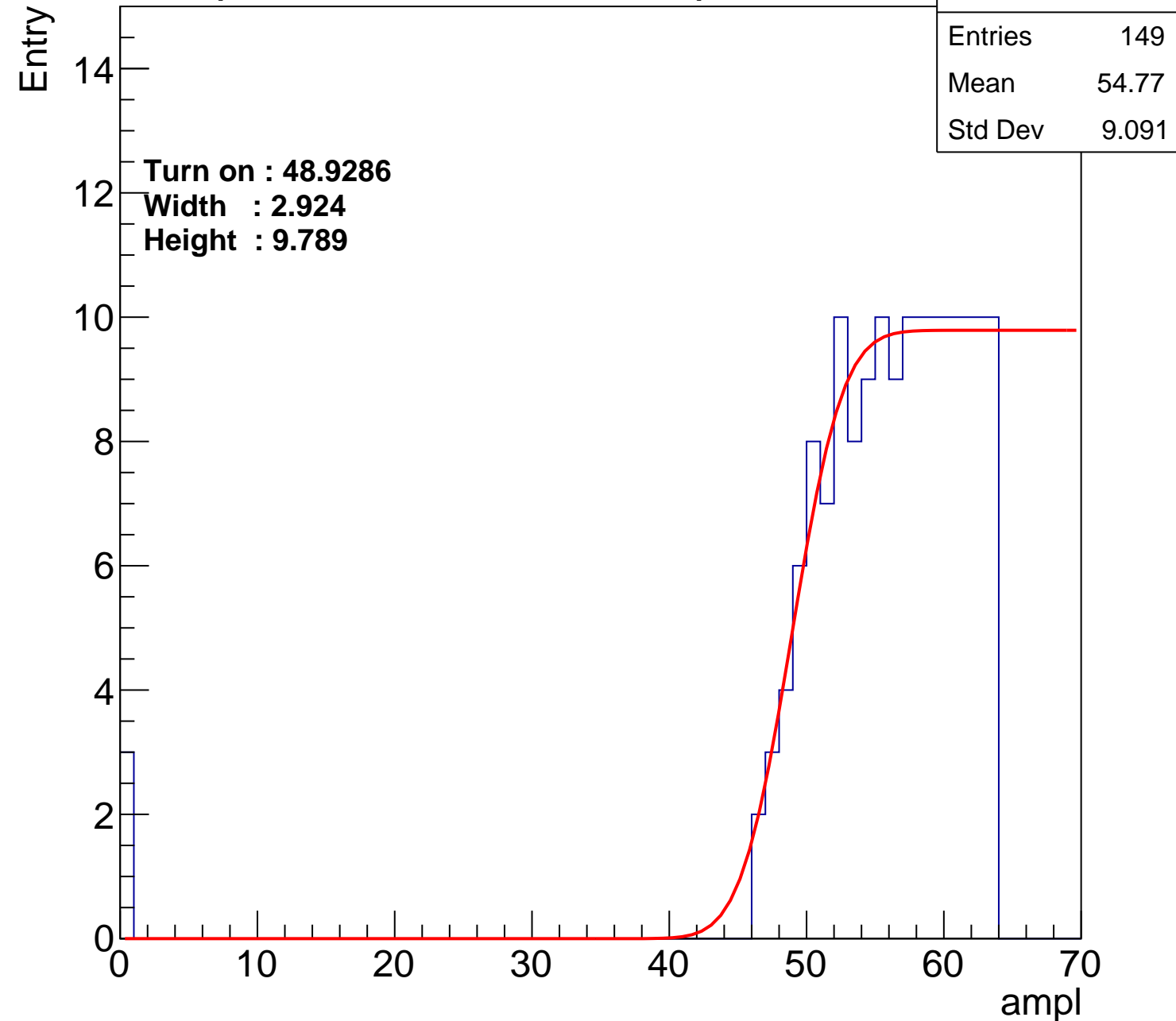
Width : 2.924

Height : 9.789

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch89

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.86
Std Dev	9.209

Turn on : 50.3780

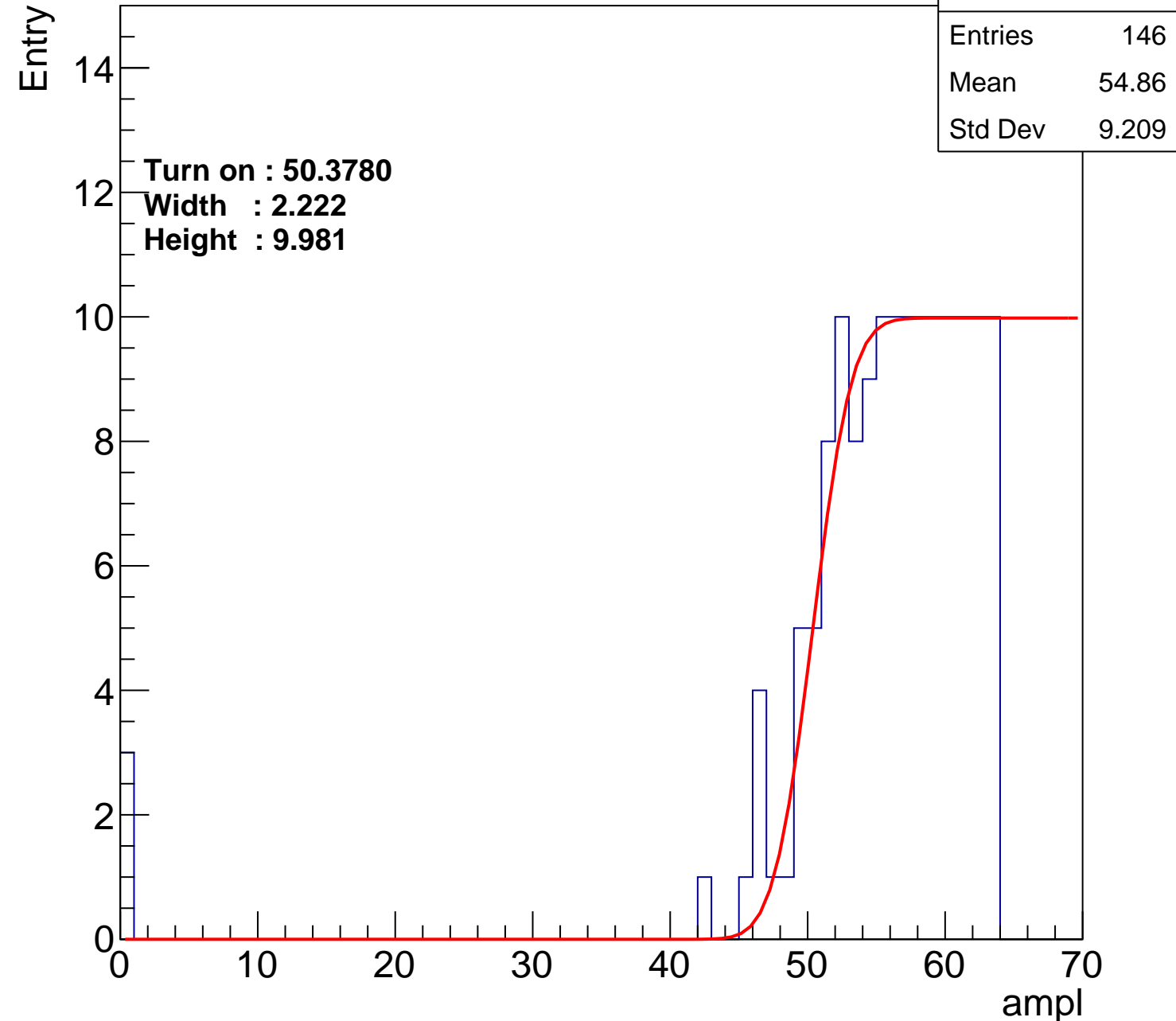
Width : 2.222

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch90

calib\_packv5\_040323\_1717.root, FC#2, port C3

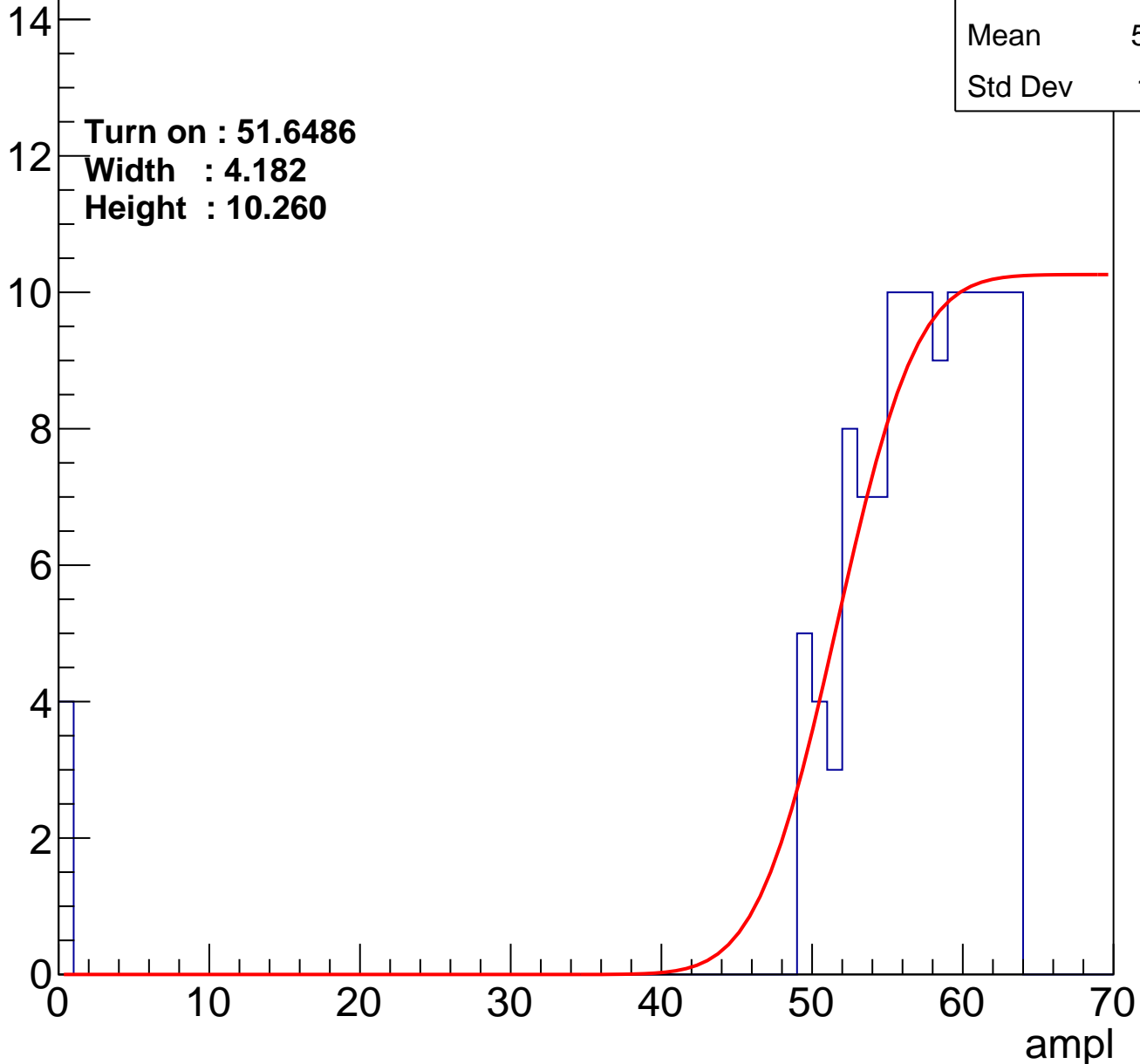
Entries	127
Mean	55.24
Std Dev	10.71

Turn on : 51.6486

Width : 4.182

Height : 10.260

Entry



# B0L103S, U19-ch91

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.22
Std Dev	10.59

Turn on : 51.5296

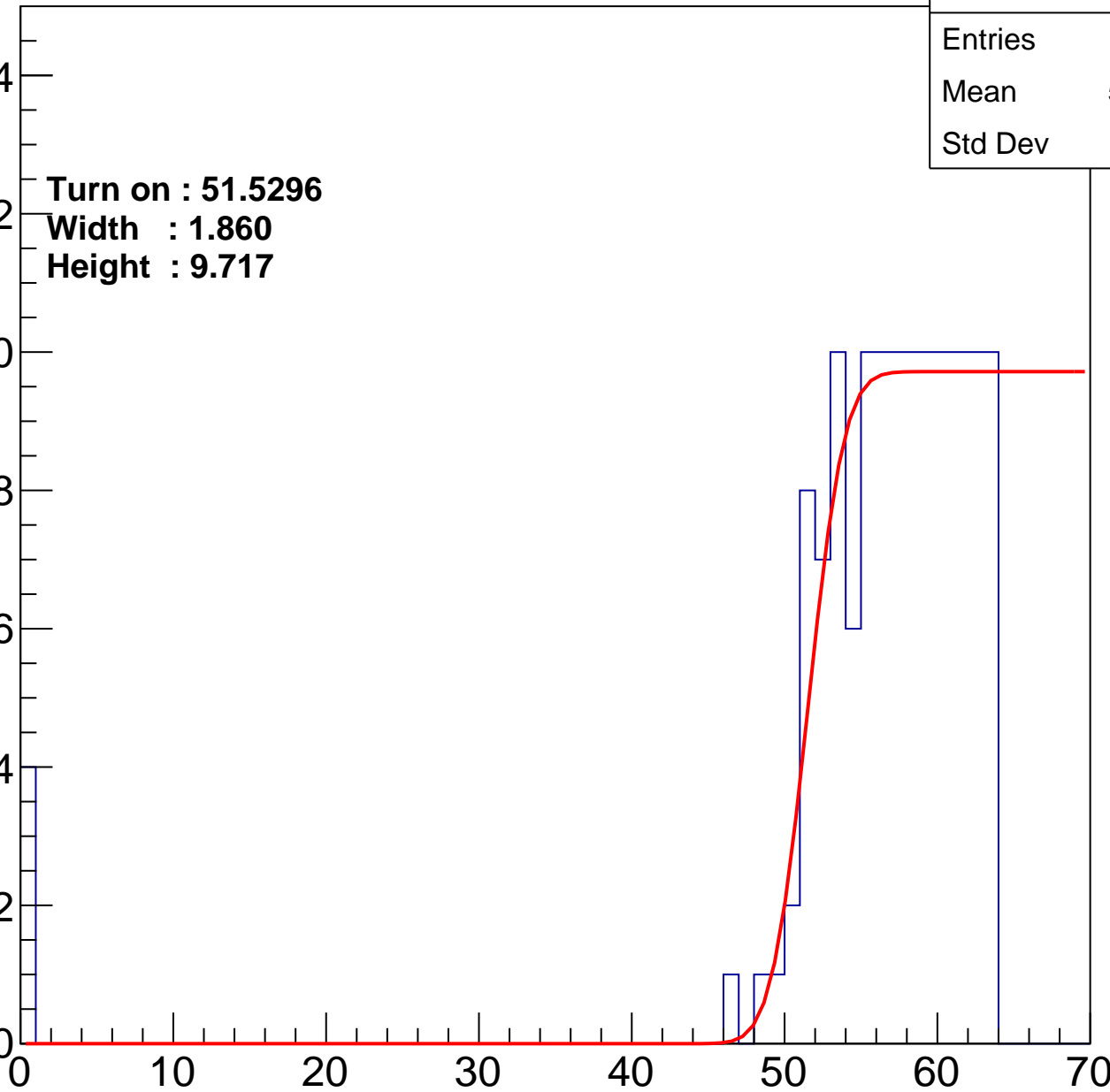
Width : 1.860

Height : 9.717

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch92

calib\_packv5\_040323\_1717.root, FC#2, port C3

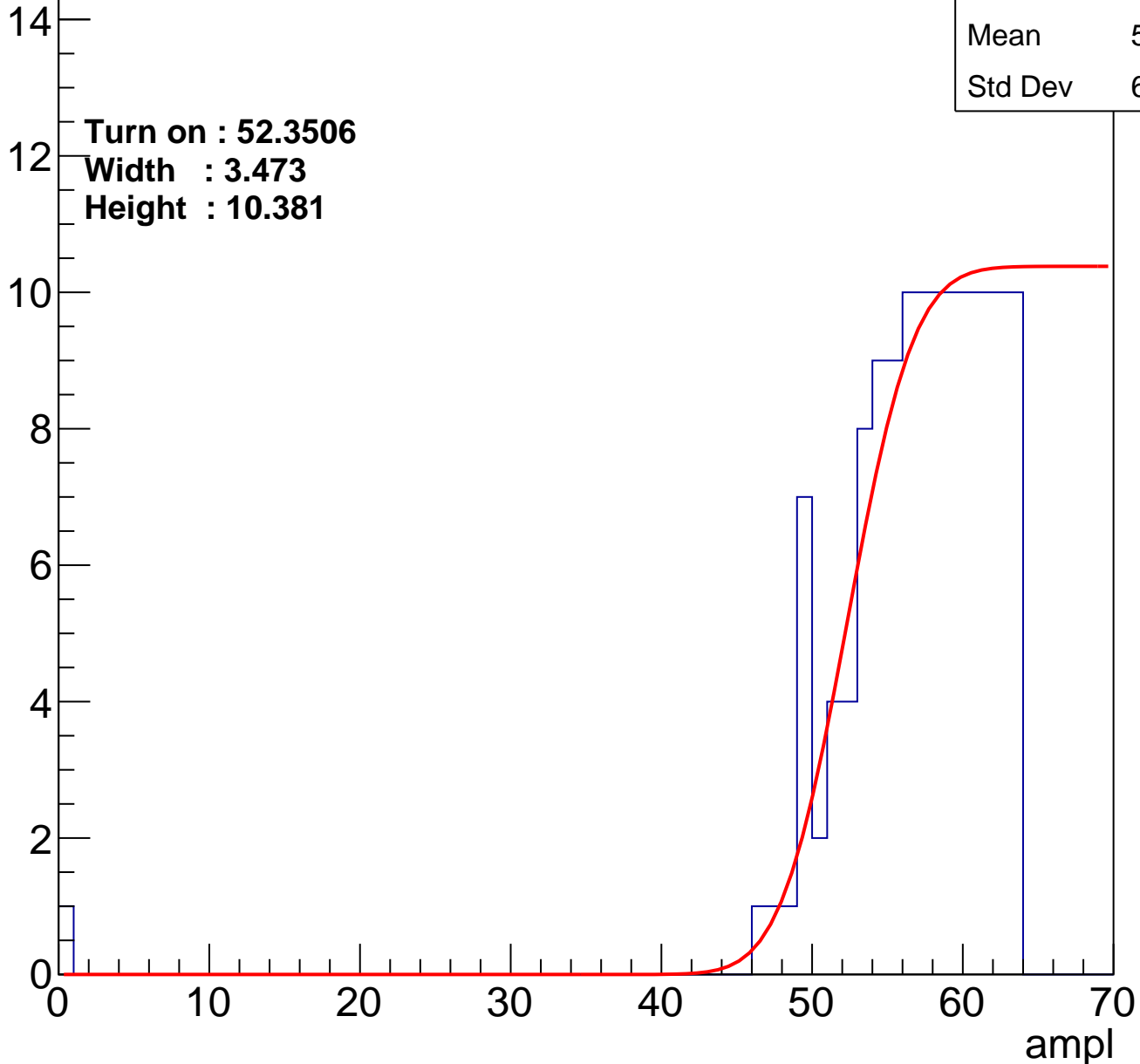
Entries	127
Mean	56.39
Std Dev	6.556

Turn on : 52.3506

Width : 3.473

Height : 10.381

Entry



# B0L103S, U19-ch93

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	110
Mean	54.39
Std Dev	14.55

Turn on : 55.0160

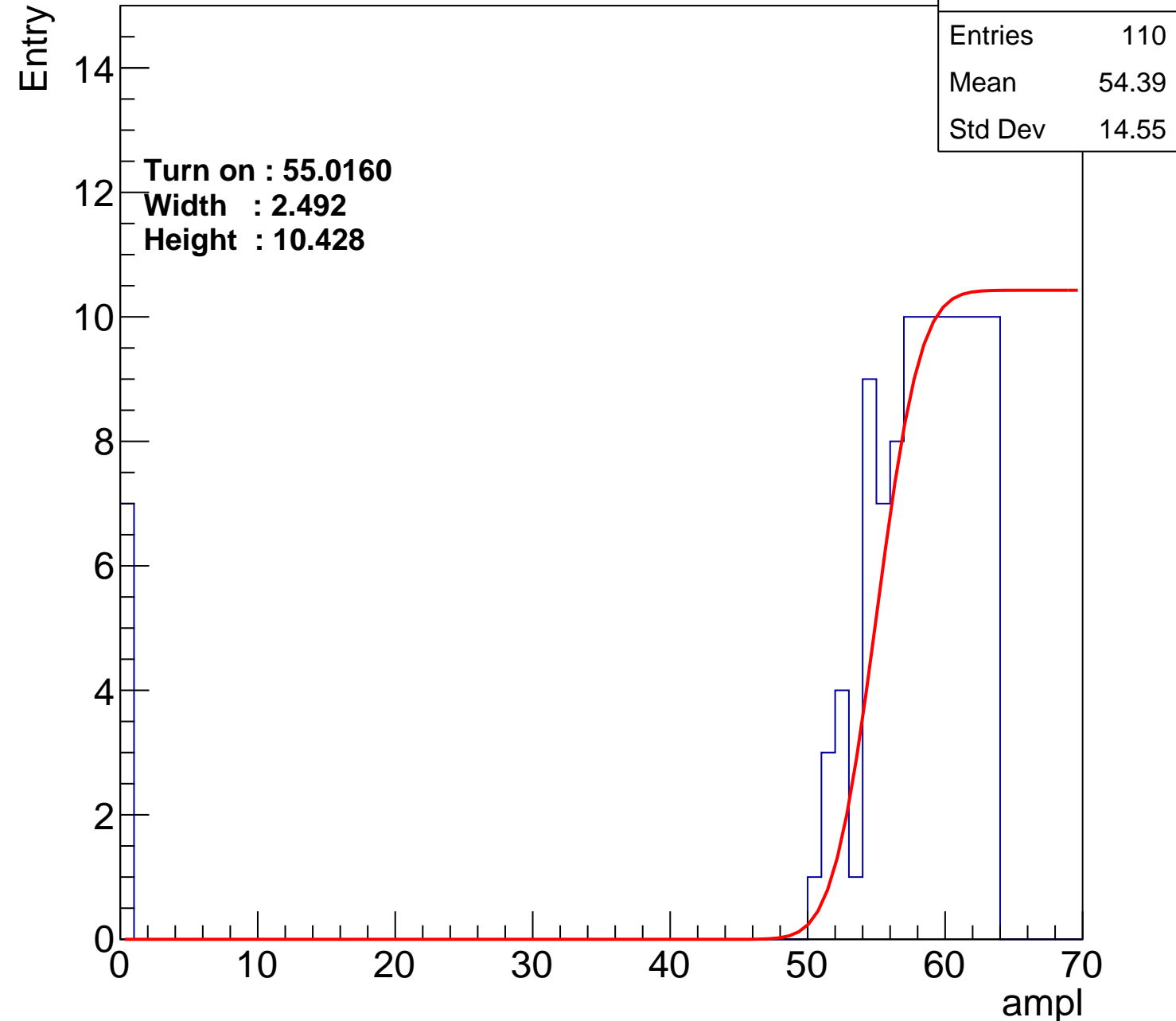
Width : 2.492

Height : 10.428

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch94

calib\_packv5\_040323\_1717.root, FC#2, port C3

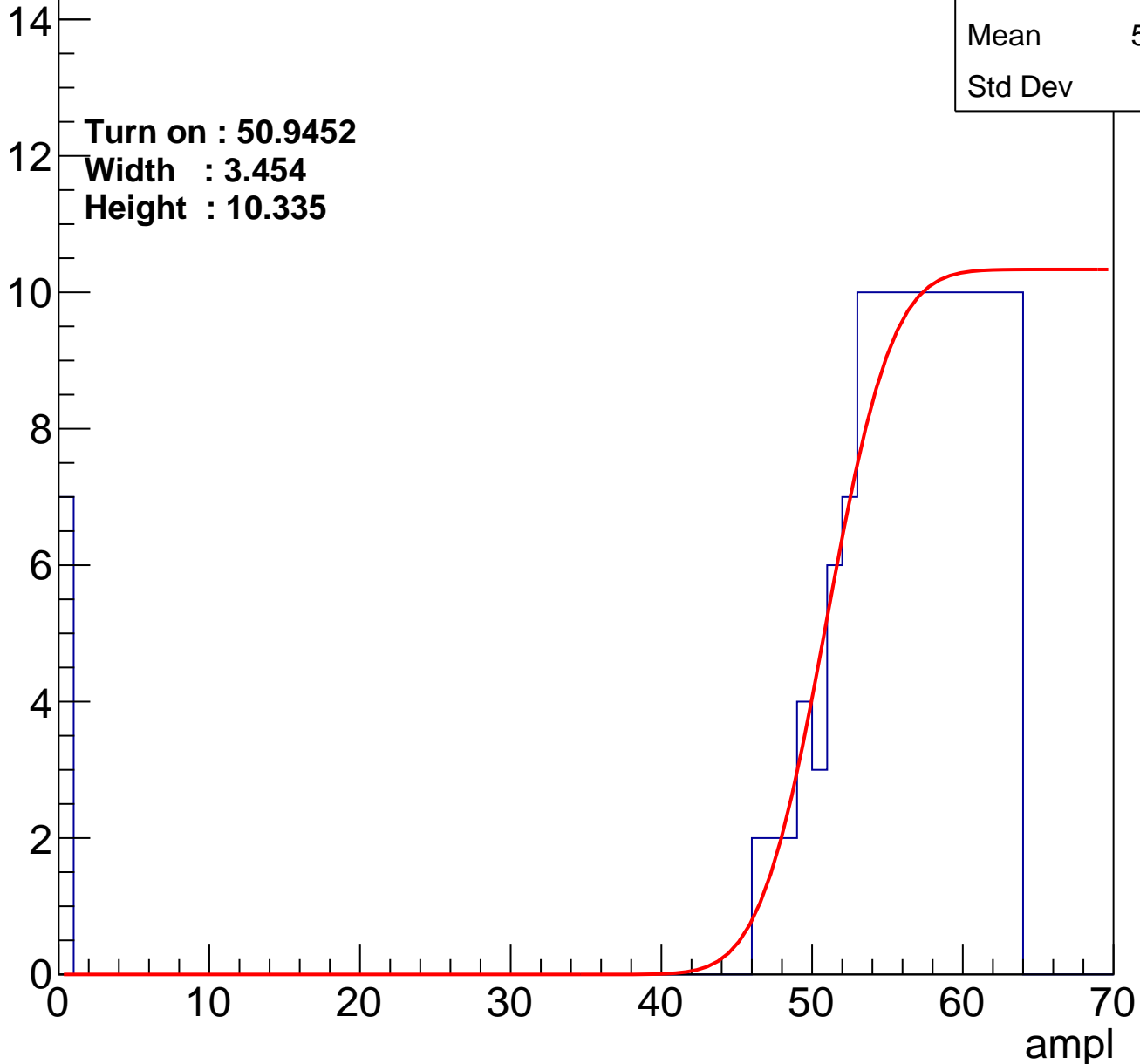
Entries	143
Mean	53.69
Std Dev	12.9

Turn on : 50.9452

Width : 3.454

Height : 10.335

Entry





# B0L103S, U19-ch95

calib\_packv5\_040323\_1717.root, FC#2, port C3

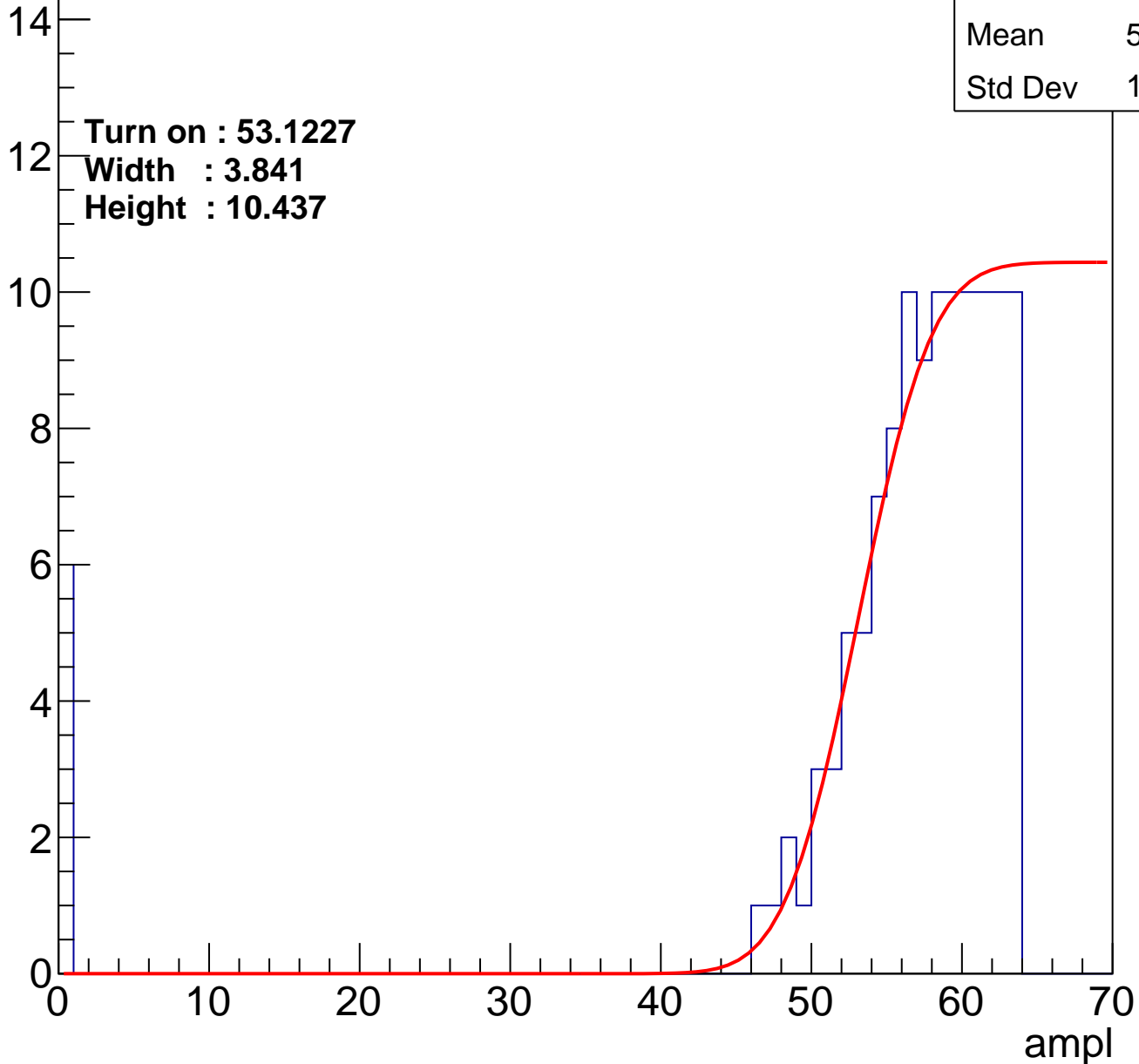
Entries	121
Mean	54.44
Std Dev	13.05

Turn on : 53.1227

Width : 3.841

Height : 10.437

Entry



# B0L103S, U19-ch96

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	144
Mean	54.68
Std Dev	10.24

Turn on : 50.4819

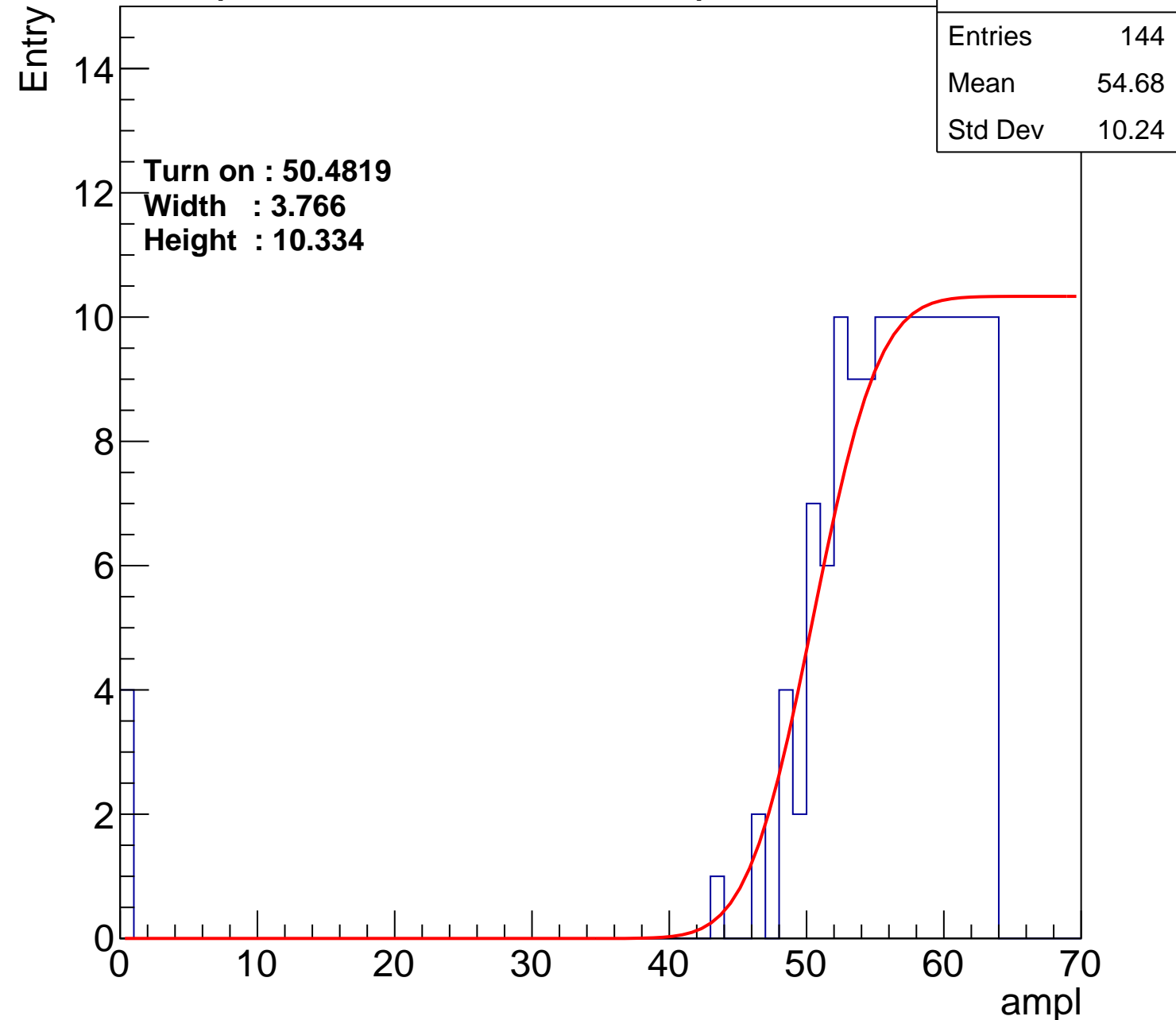
Width : 3.766

Height : 10.334

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch97

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	137
Mean	55.65
Std Dev	8.03

Turn on : 51.1069

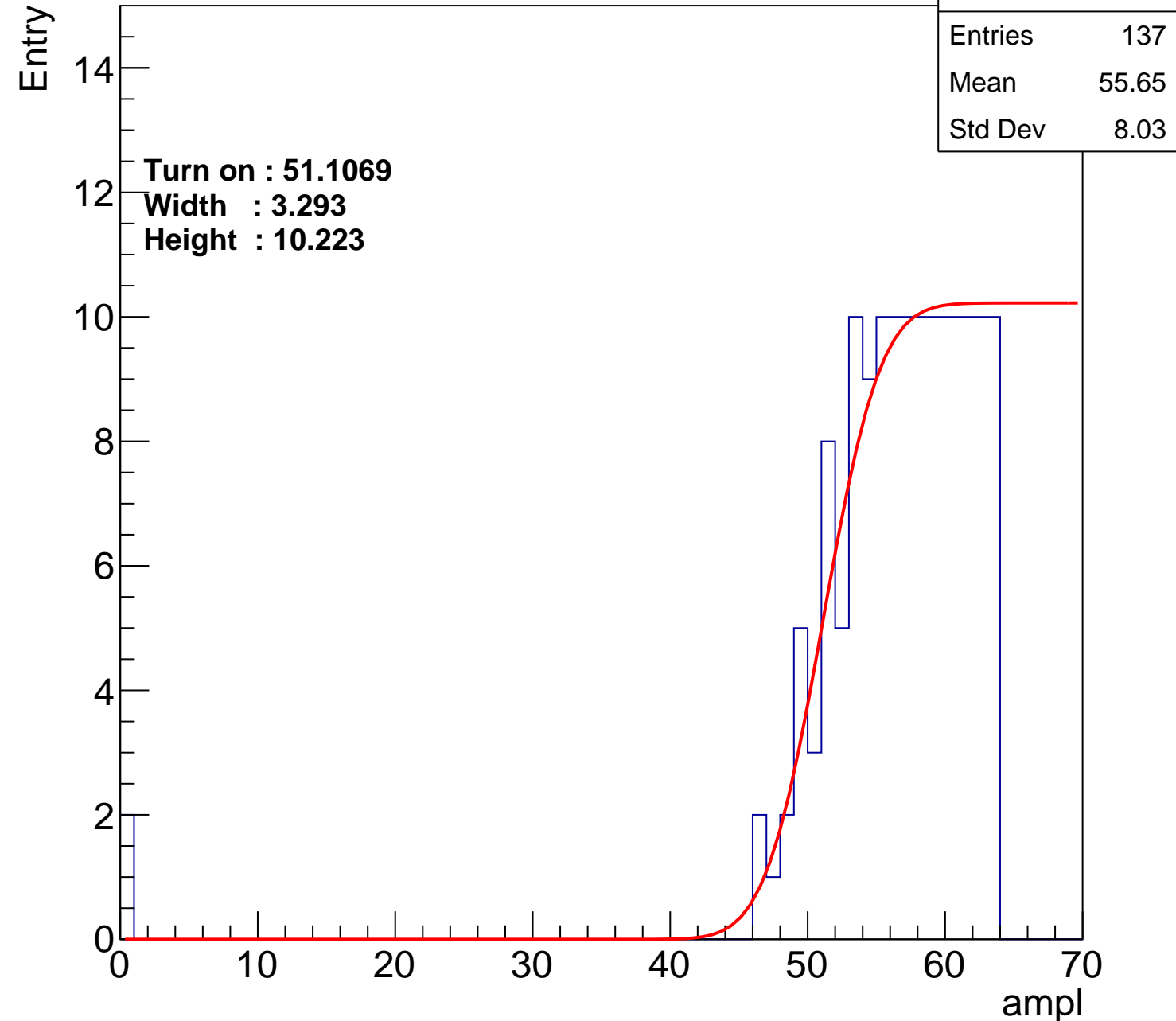
Width : 3.293

Height : 10.223

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch98

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	54.29
Std Dev	12.57

Turn on : 51.8673

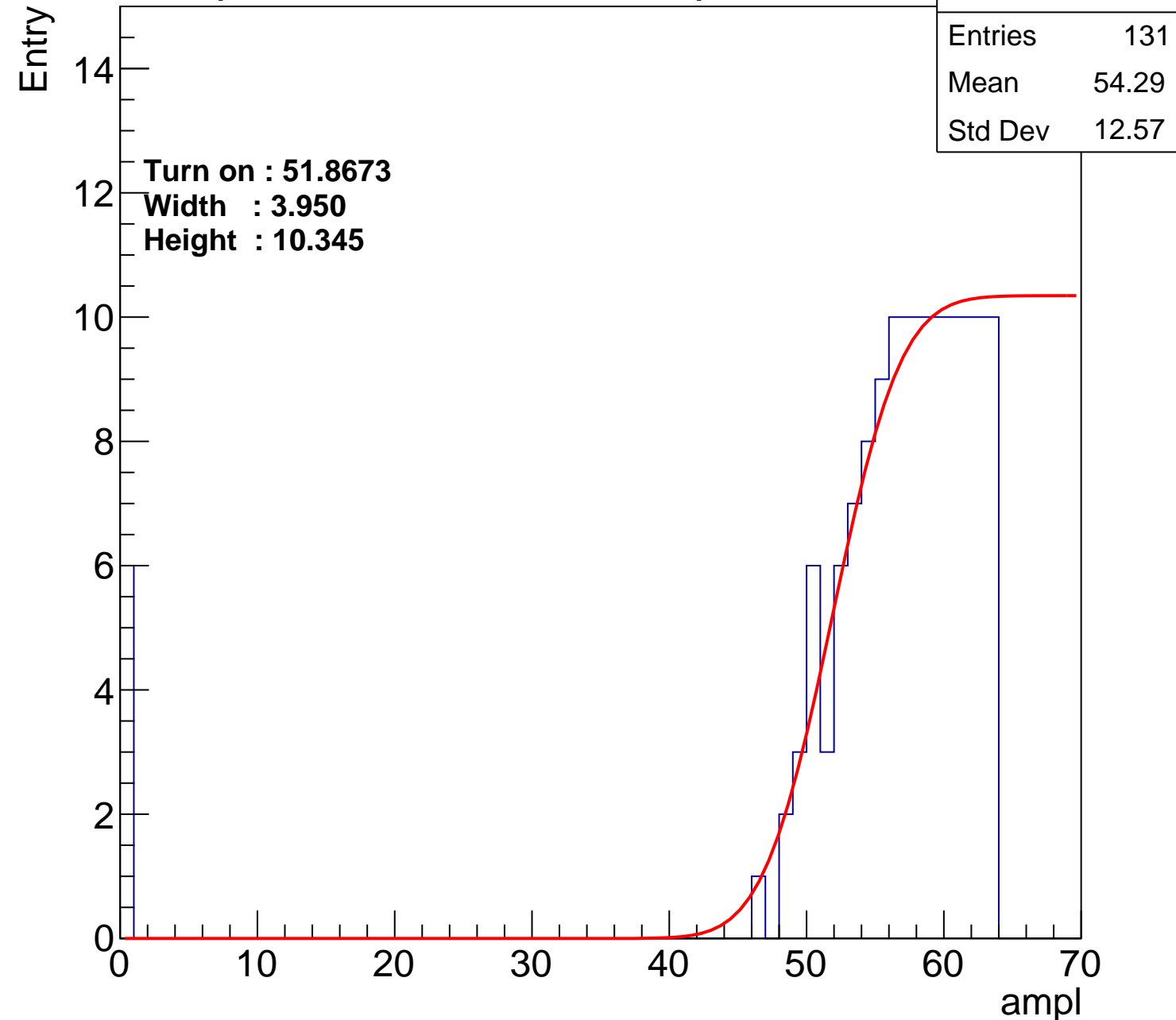
Width : 3.950

Height : 10.345

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch99

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	55.66
Std Dev	9.731

Turn on : 52.4271

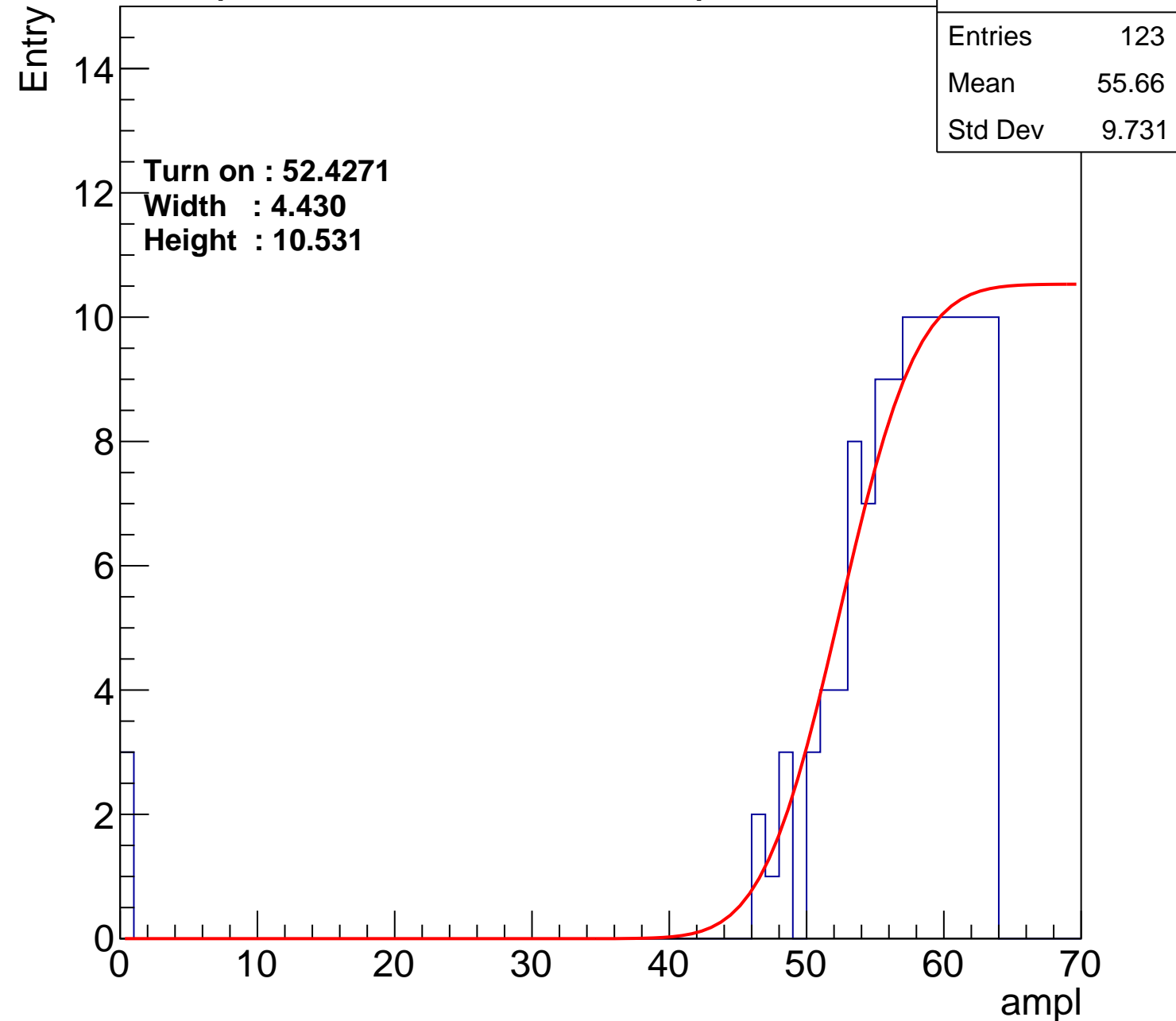
Width : 4.430

Height : 10.531

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch100

calib\_packv5\_040323\_1717.root, FC#2, port C3

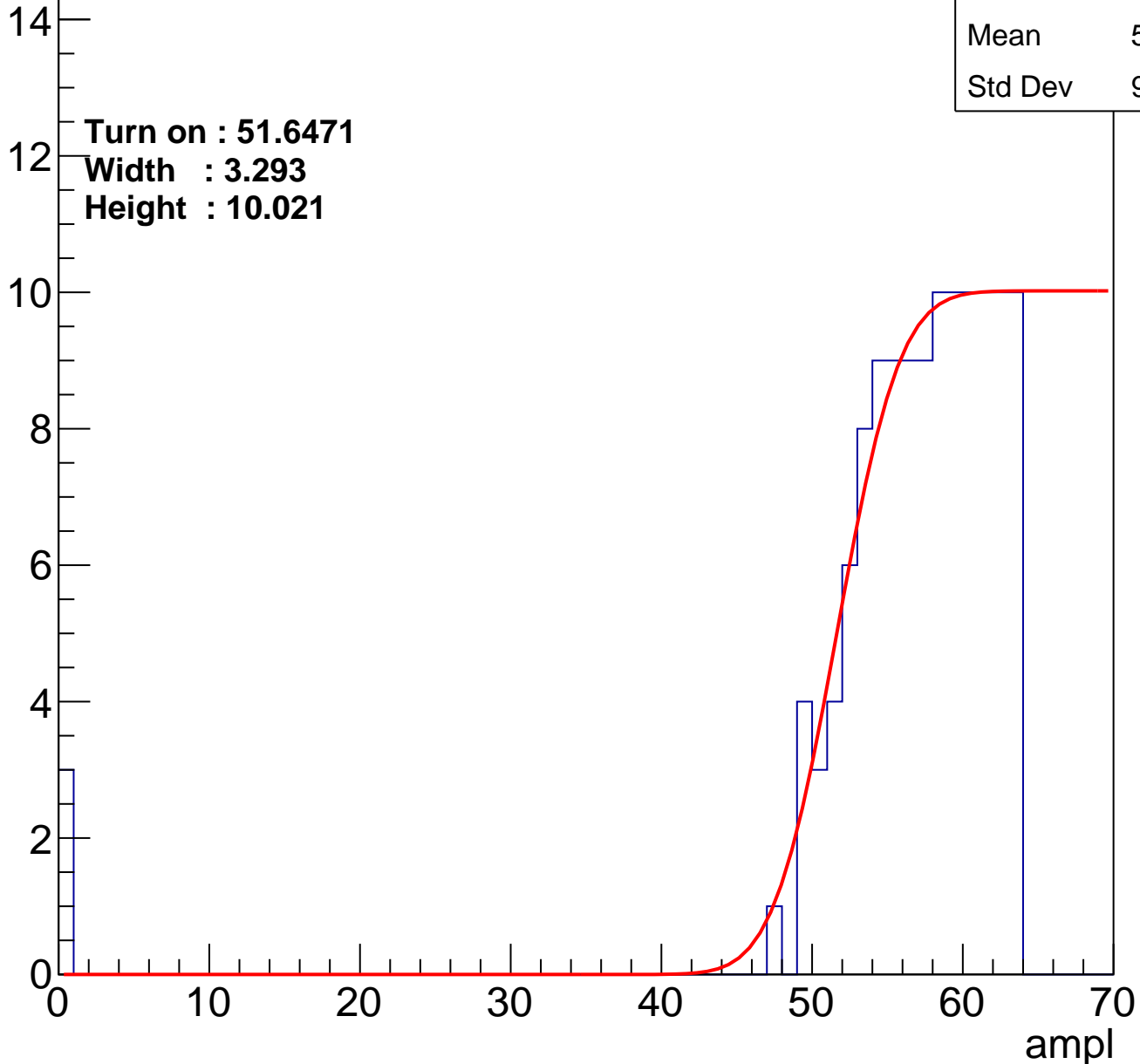
Entries	125
Mean	55.69
Std Dev	9.589

Turn on : 51.6471

Width : 3.293

Height : 10.021

Entry



# B0L103S, U19-ch101

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	54.27
Std Dev	12.48

Turn on : 52.2390

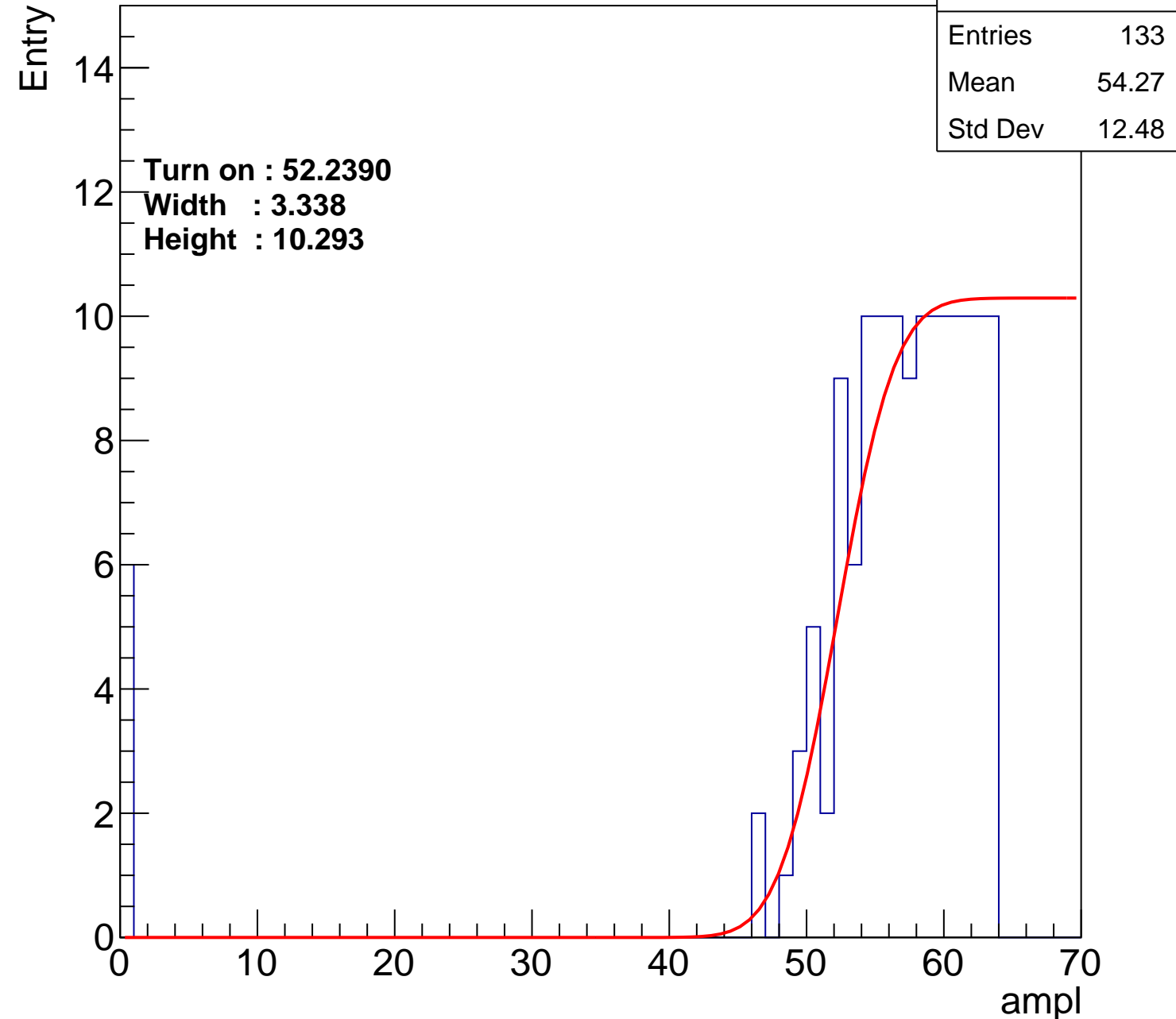
Width : 3.338

Height : 10.293

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch102

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	53.8
Std Dev	10.94

Turn on : 49.7648

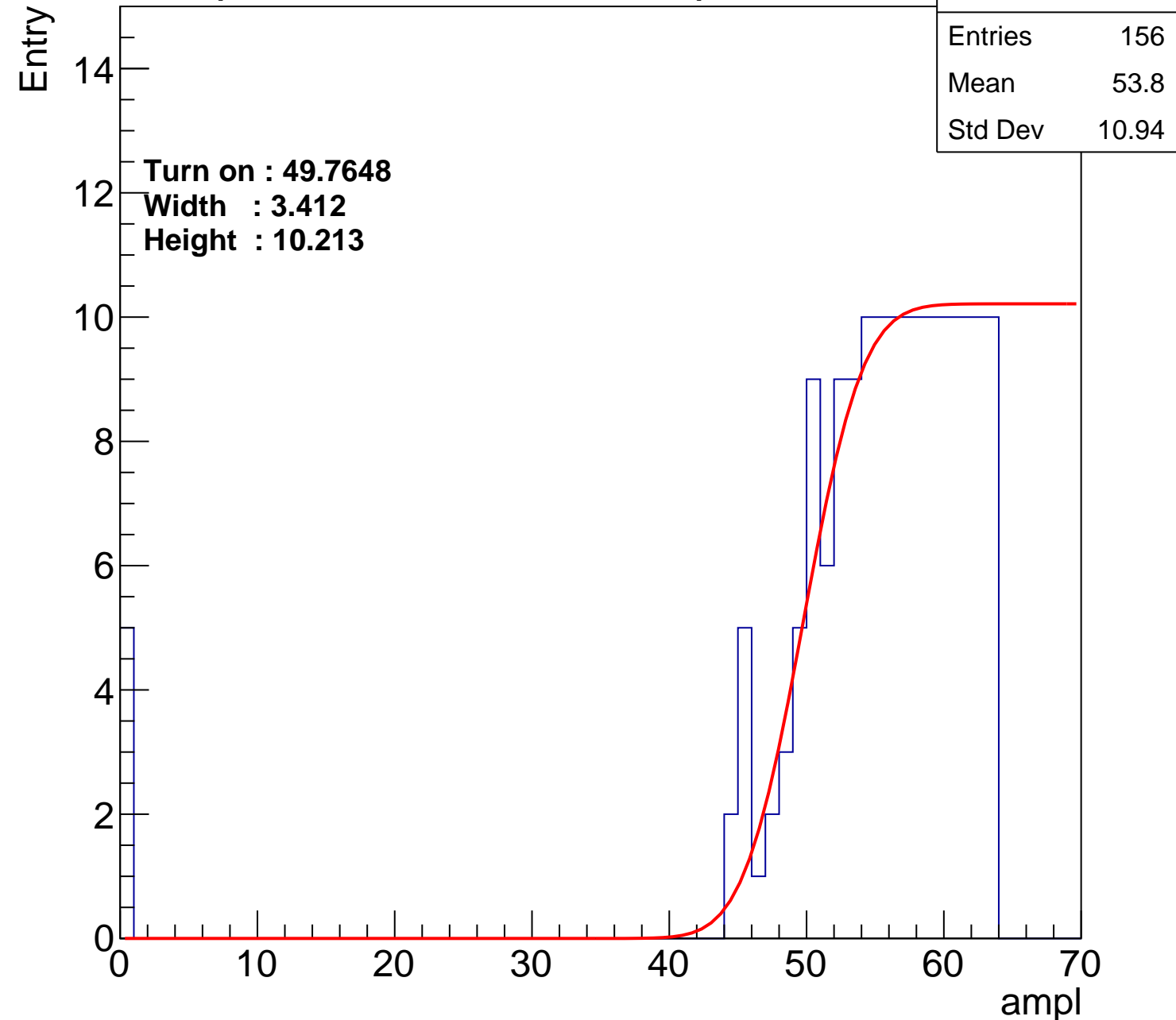
Width : 3.412

Height : 10.213

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch103

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	54.85
Std Dev	11.79

Turn on : 52.0921

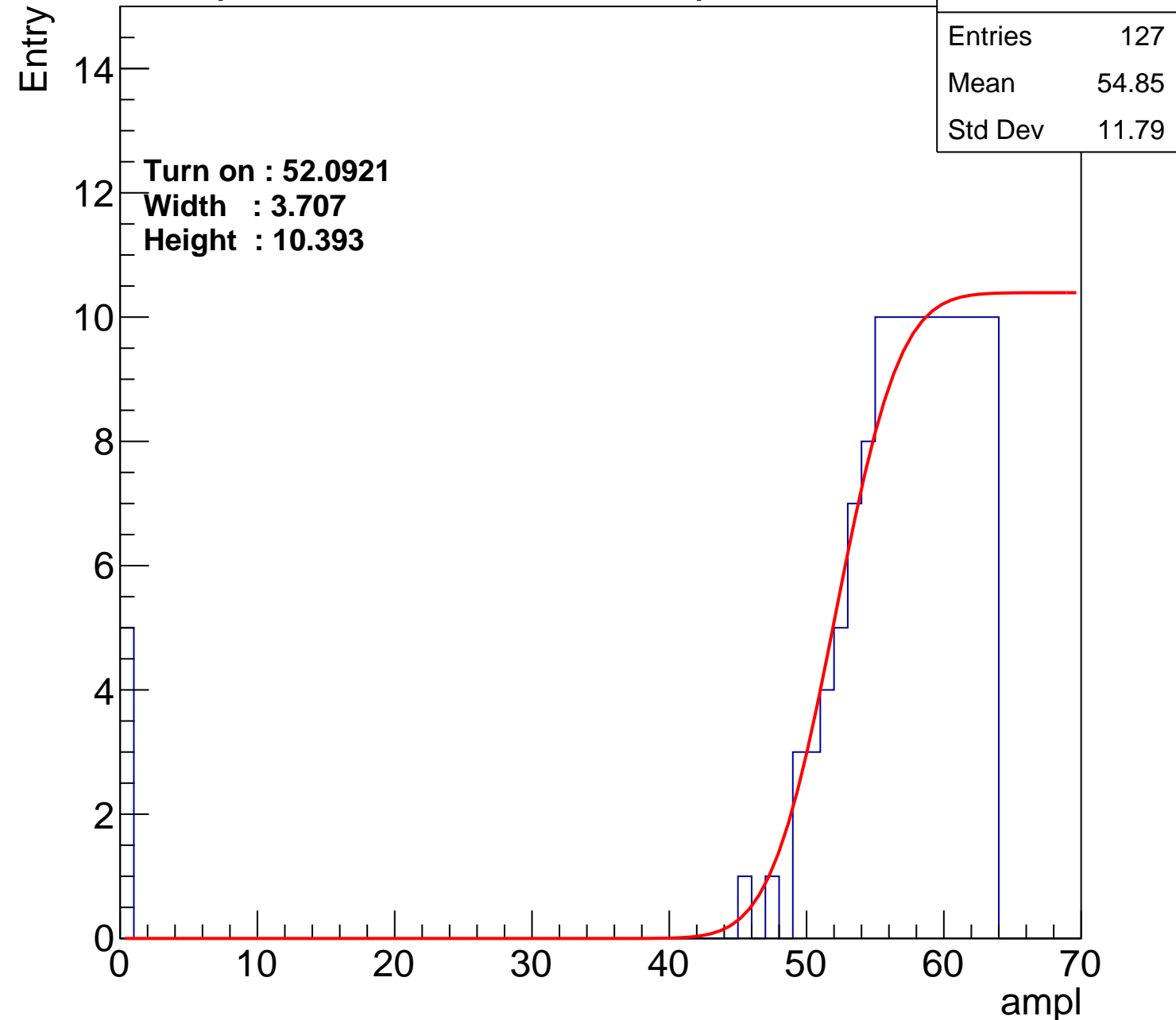
Width : 3.707

Height : 10.393

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch104

calib\_packv5\_040323\_1717.root, FC#2, port C3

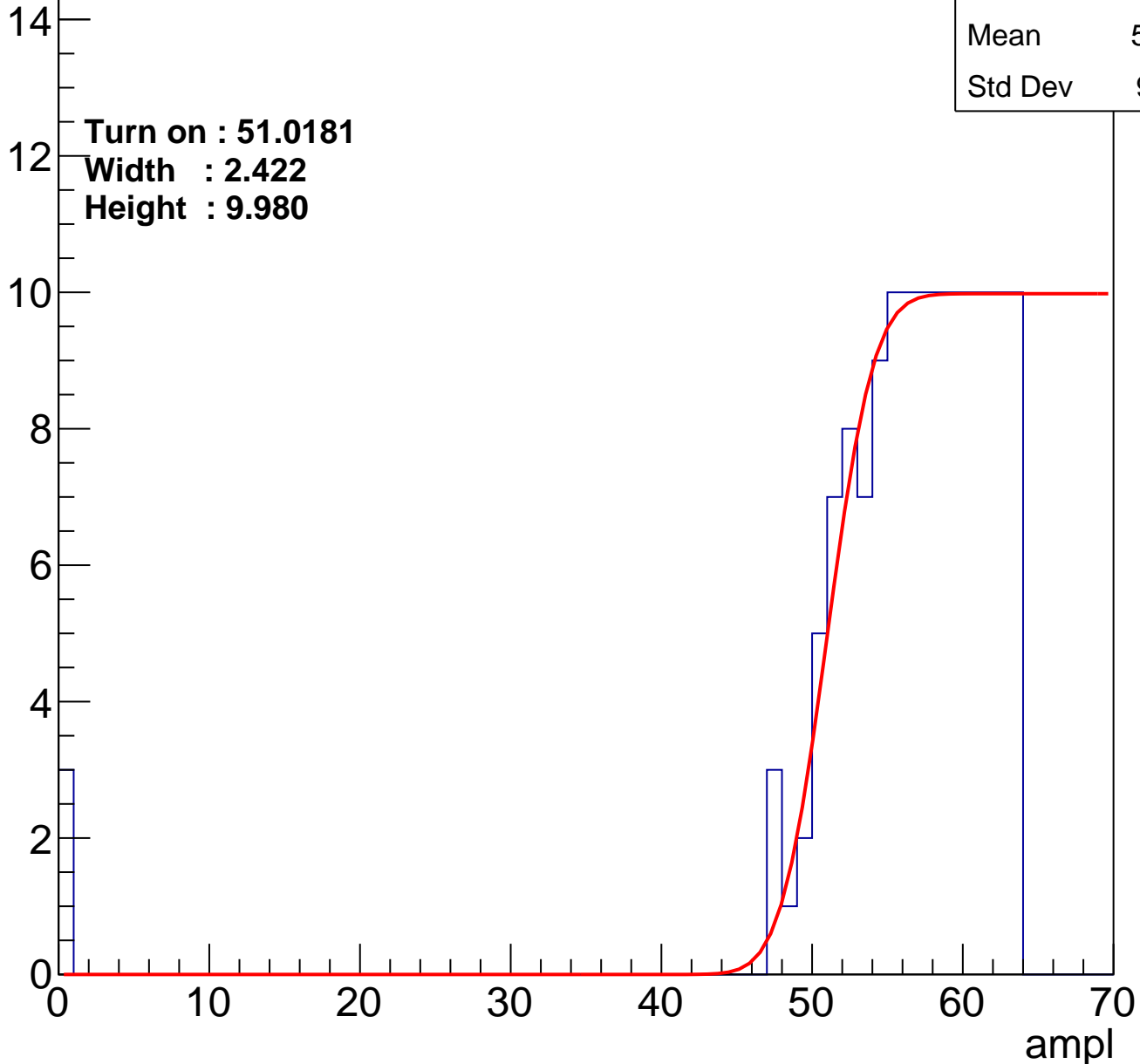
Entries	135
Mean	55.39
Std Dev	9.331

Turn on : 51.0181

Width : 2.422

Height : 9.980

Entry



# B0L103S, U19-ch105

calib\_packv5\_040323\_1717.root, FC#2, port C3

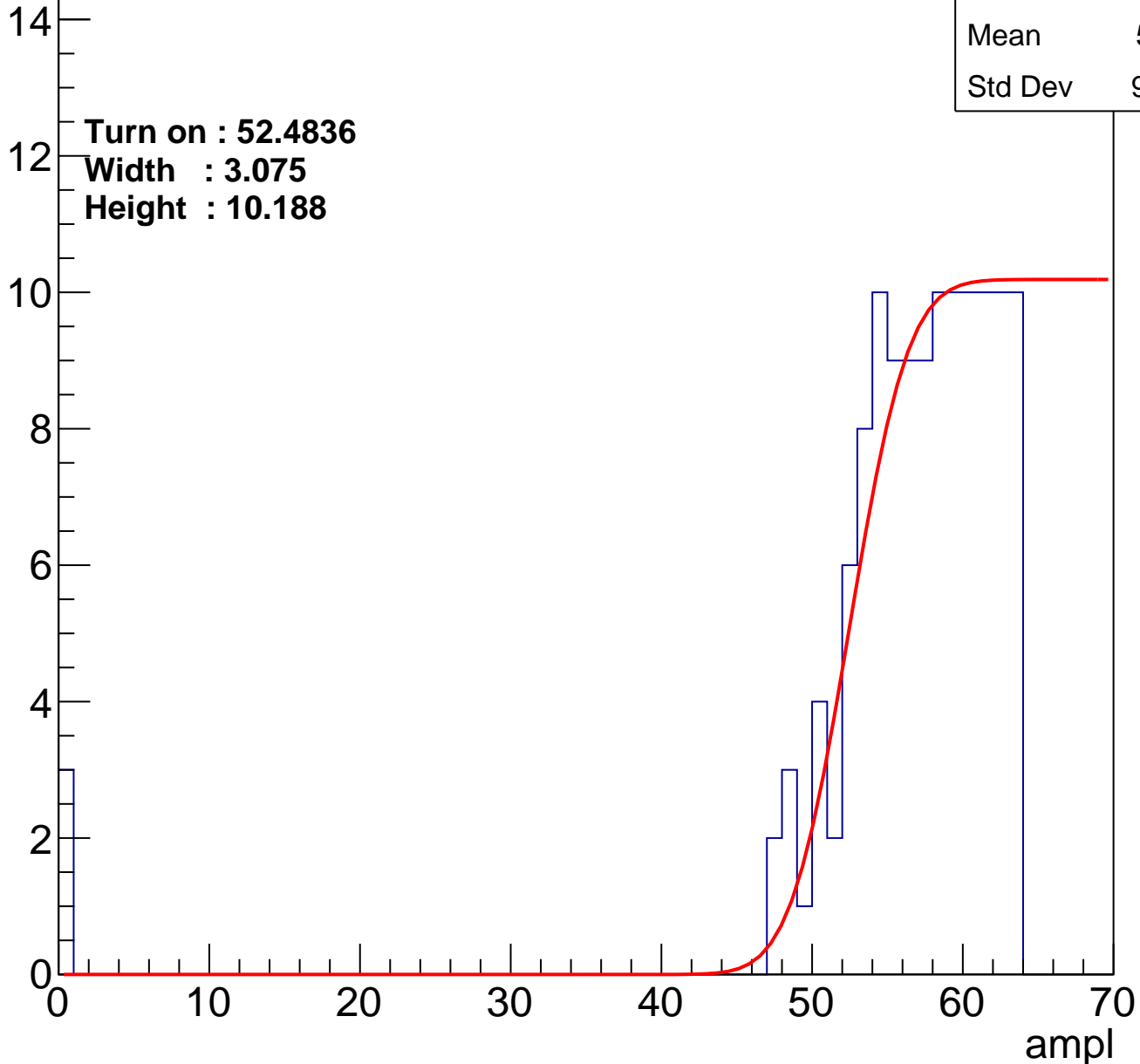
Entries	126
Mean	55.61
Std Dev	9.596

Turn on : 52.4836

Width : 3.075

Height : 10.188

Entry



# B0L103S, U19-ch106

calib\_packv5\_040323\_1717.root, FC#2, port C3

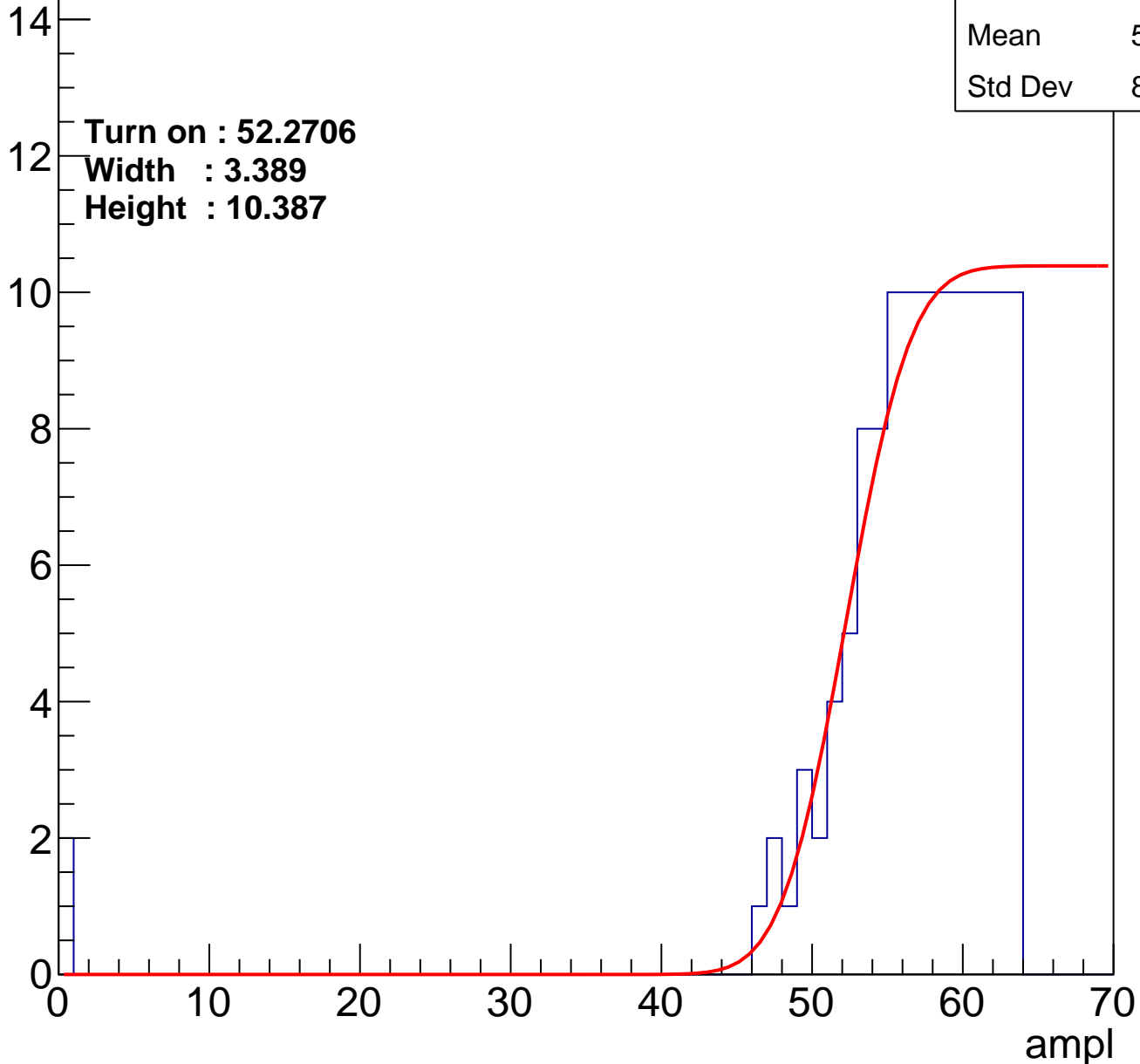
Entries	126
Mean	56.07
Std Dev	8.218

Turn on : 52.2706

Width : 3.389

Height : 10.387

Entry



# B0L103S, U19-ch107

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	156
Mean	54.54
Std Dev	8.978

Turn on : 48.7302

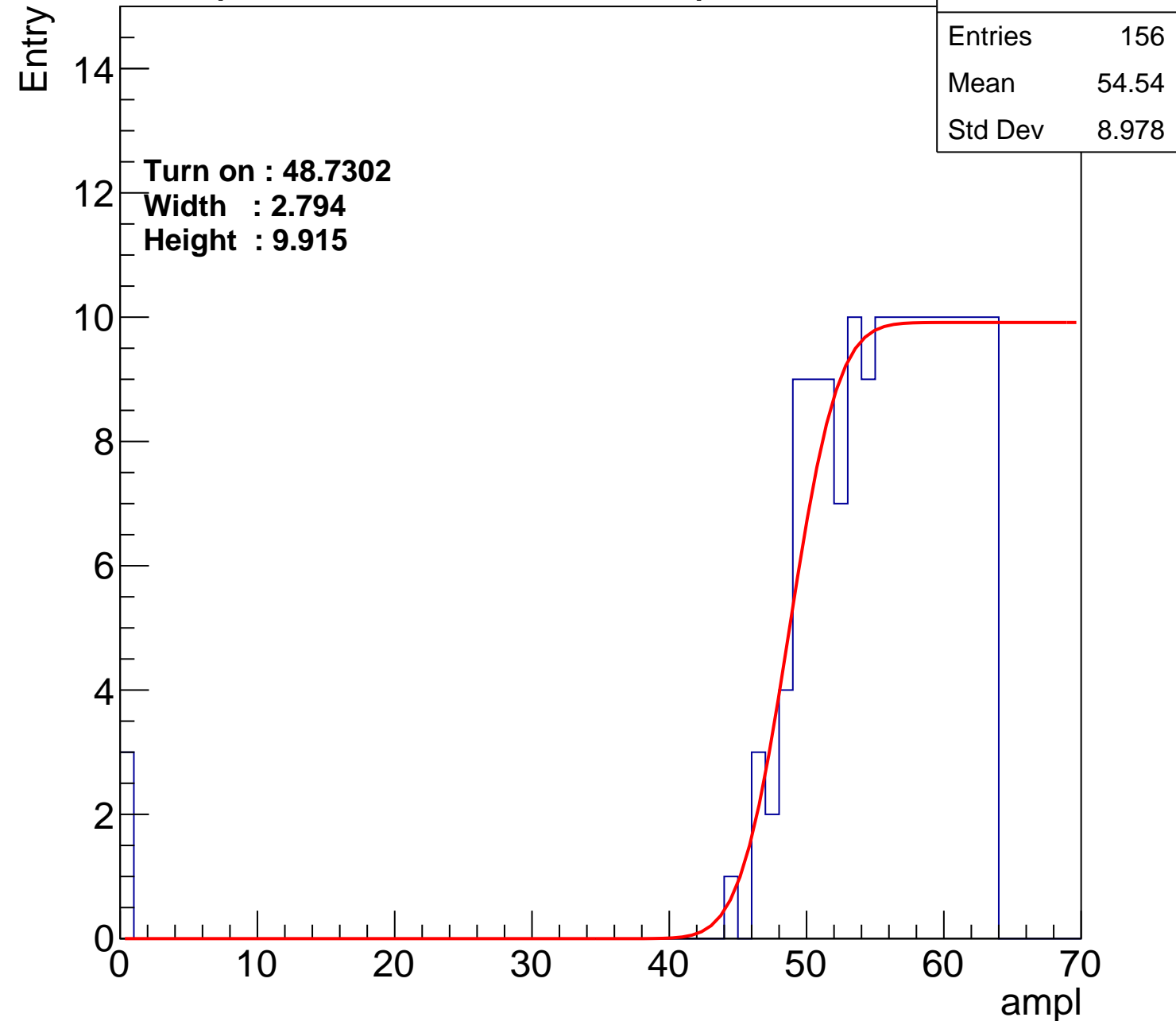
Width : 2.794

Height : 9.915

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch108

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	145
Mean	53.96
Std Dev	12.01

Turn on : 50.0980

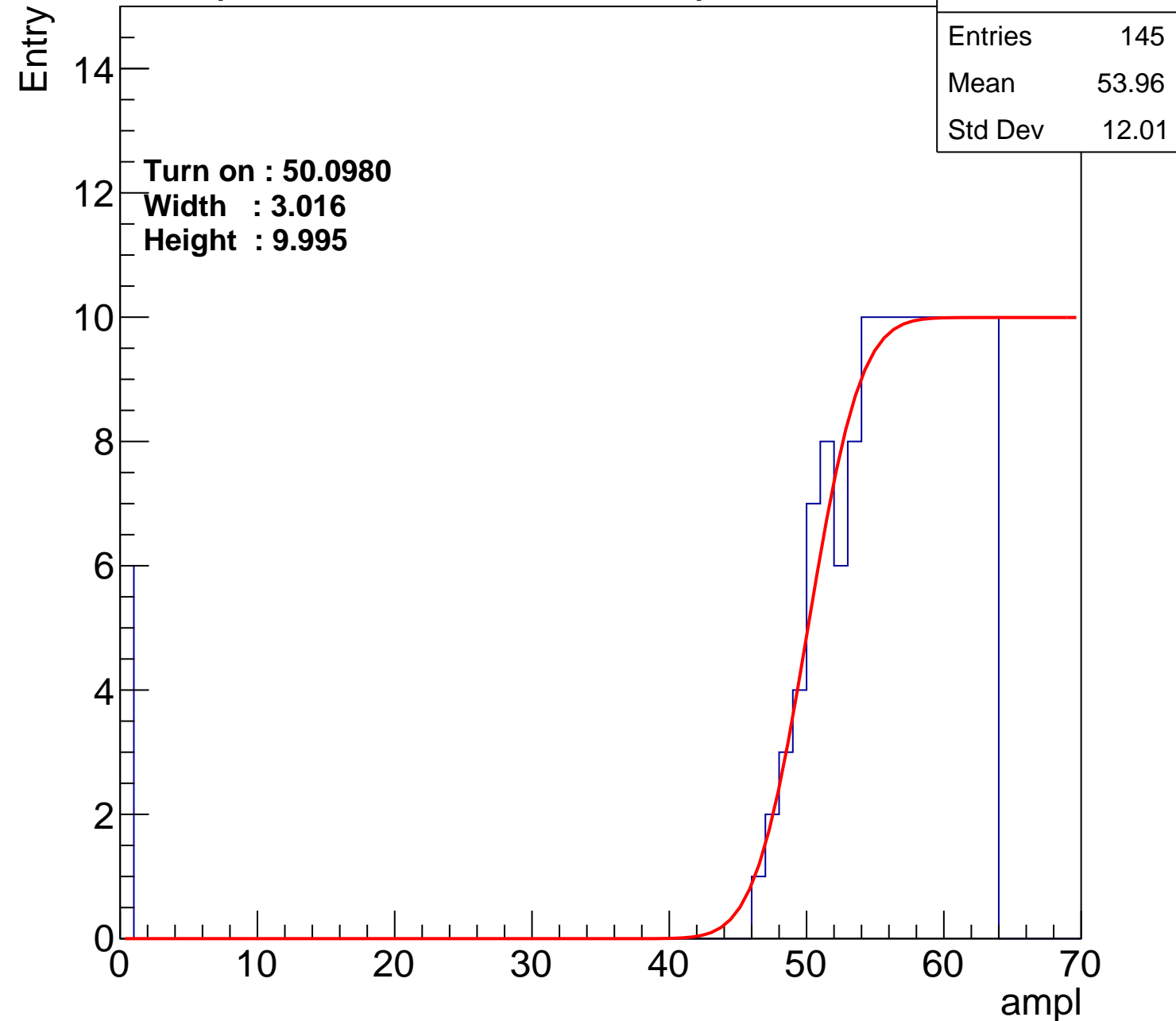
Width : 3.016

Height : 9.995

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch109

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	140
Mean	55.31
Std Dev	8.076

Turn on : 50.8164

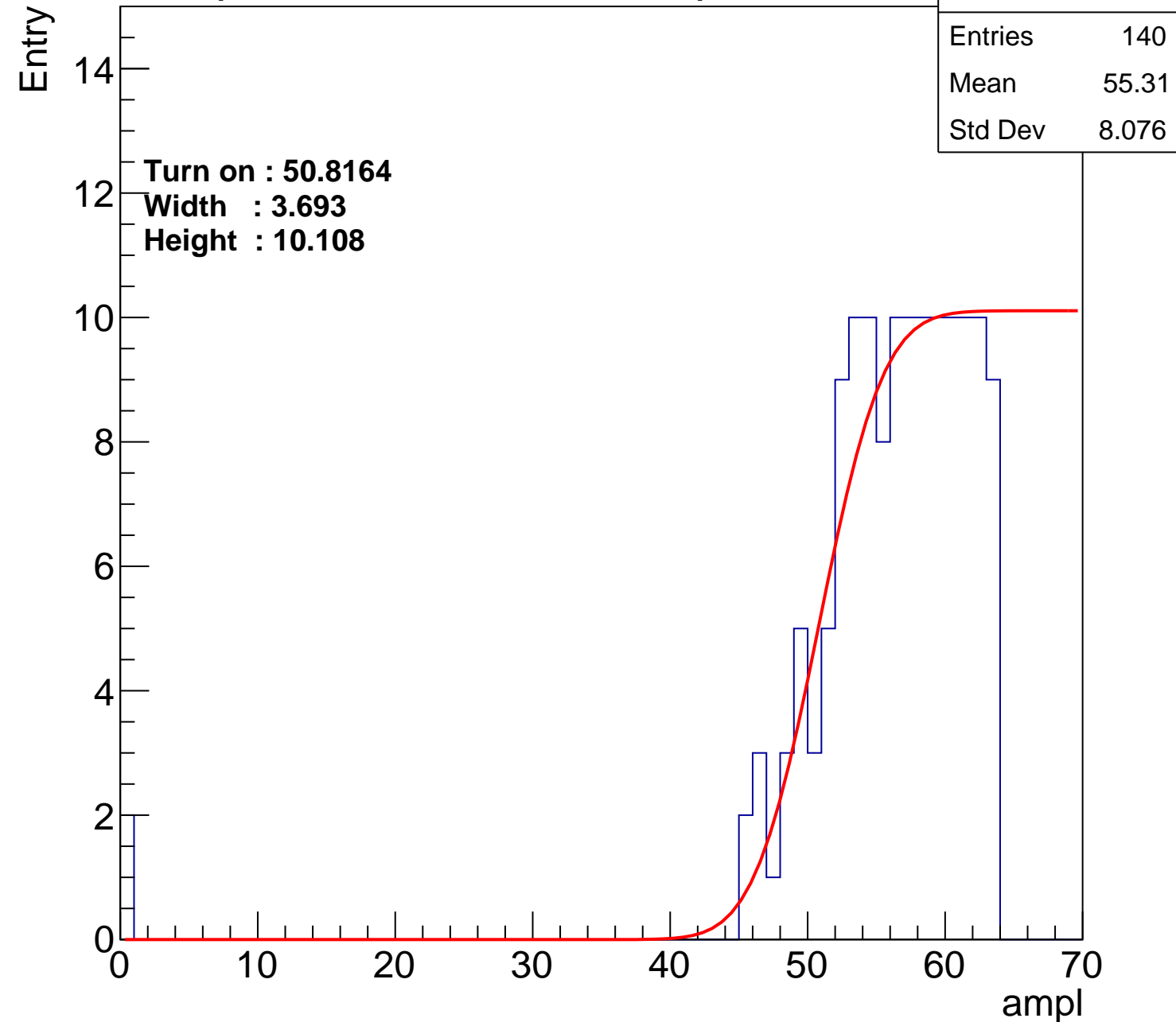
Width : 3.693

Height : 10.108

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch110

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.77
Std Dev	9.616

Turn on : 52.5615

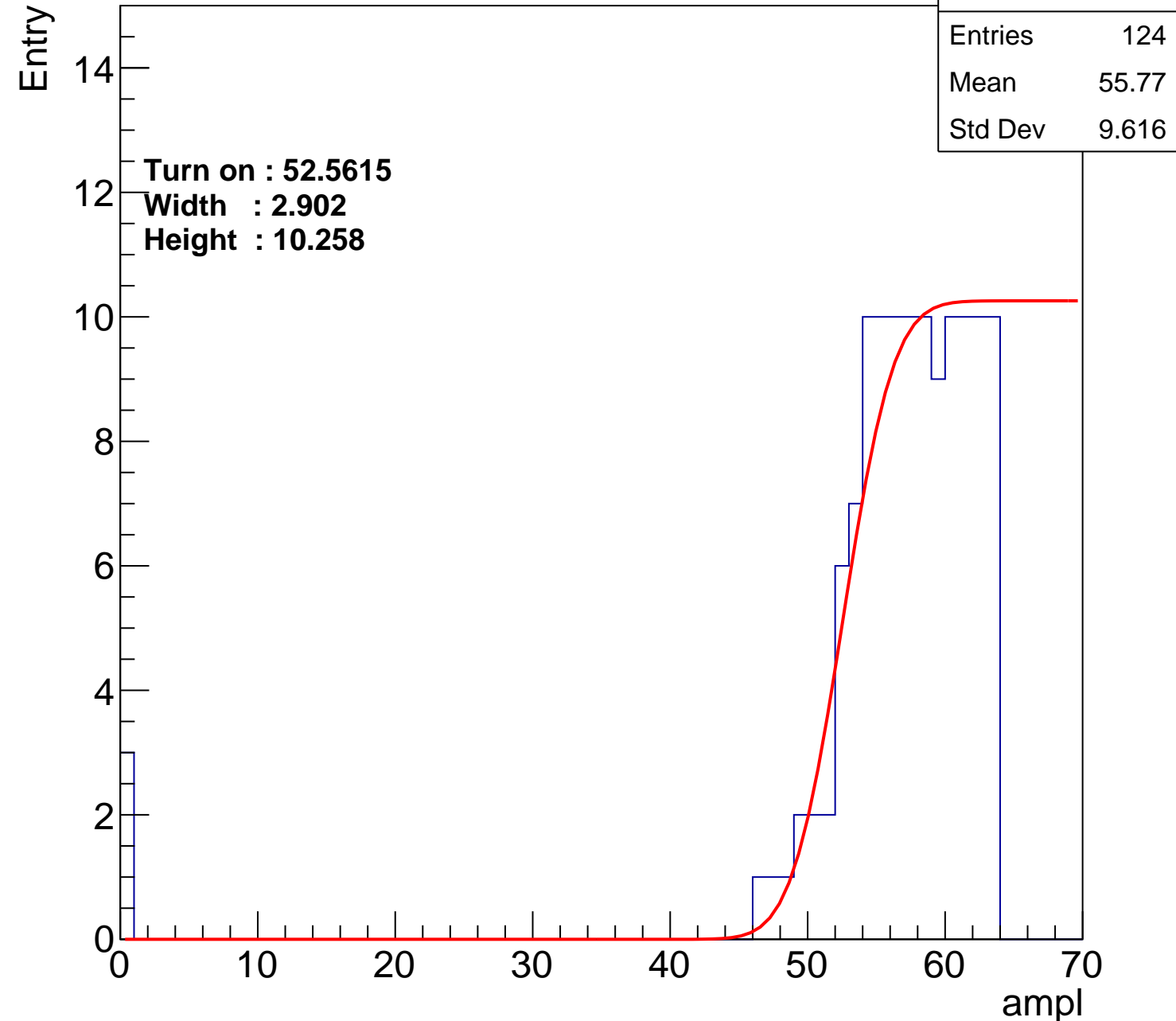
Width : 2.902

Height : 10.258

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch111

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	55.54
Std Dev	9.589

Turn on : 51.2454

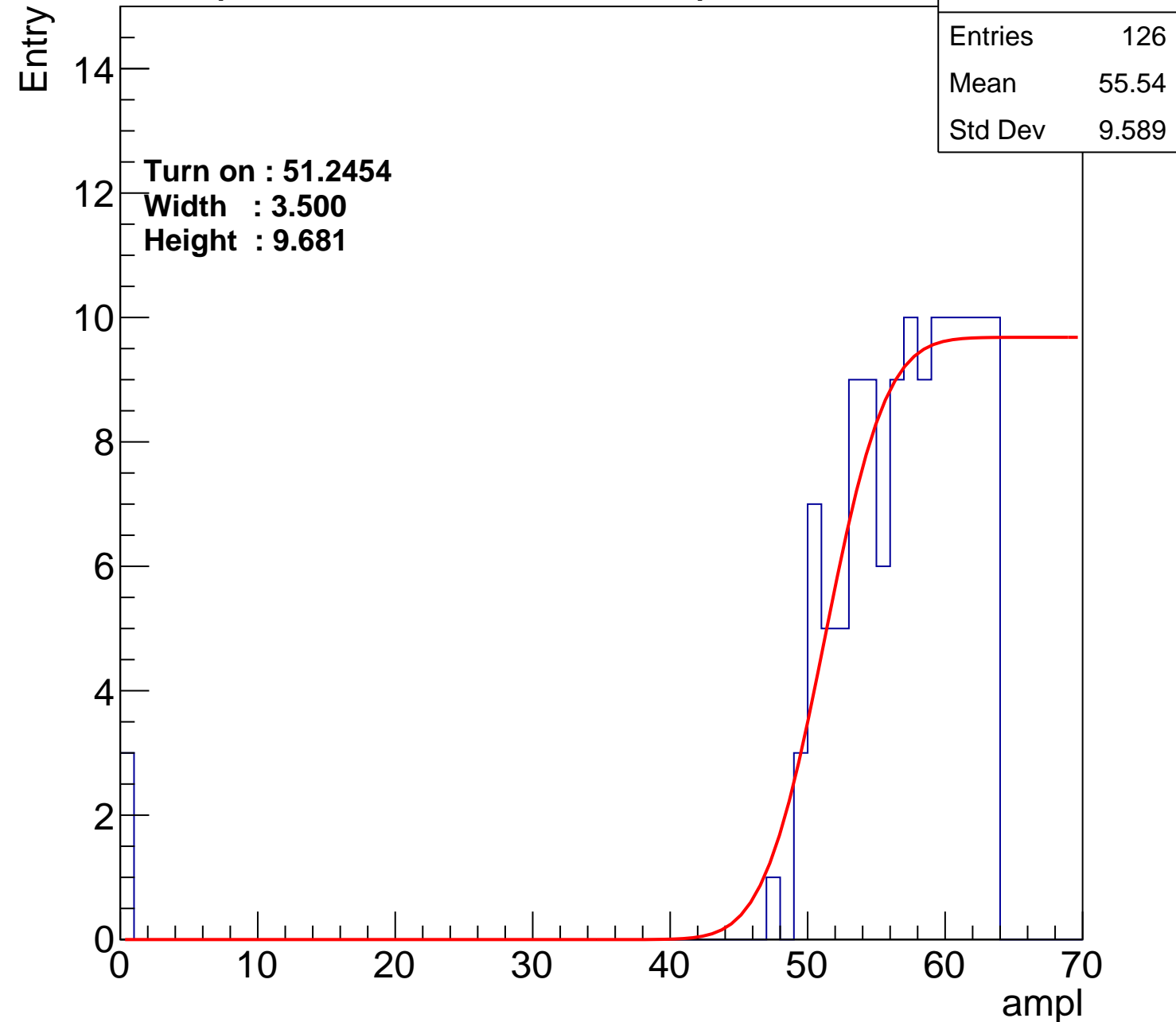
Width : 3.500

Height : 9.681

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch112

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	146
Mean	54.53
Std Dev	10.24

Turn on : 50.7677

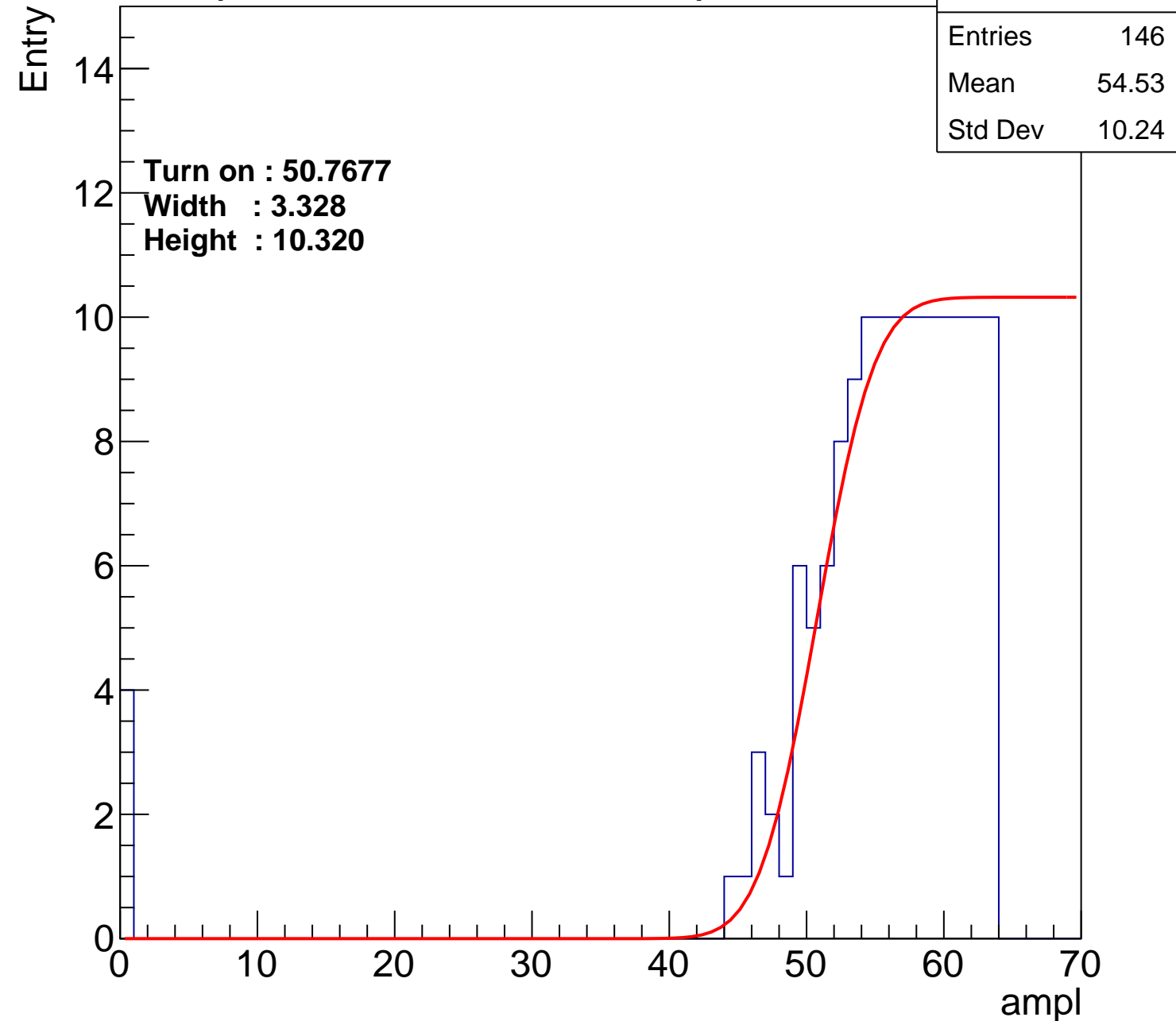
Width : 3.328

Height : 10.320

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch113

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	131
Mean	55.04
Std Dev	10.63

**Turn on : 51.7465**

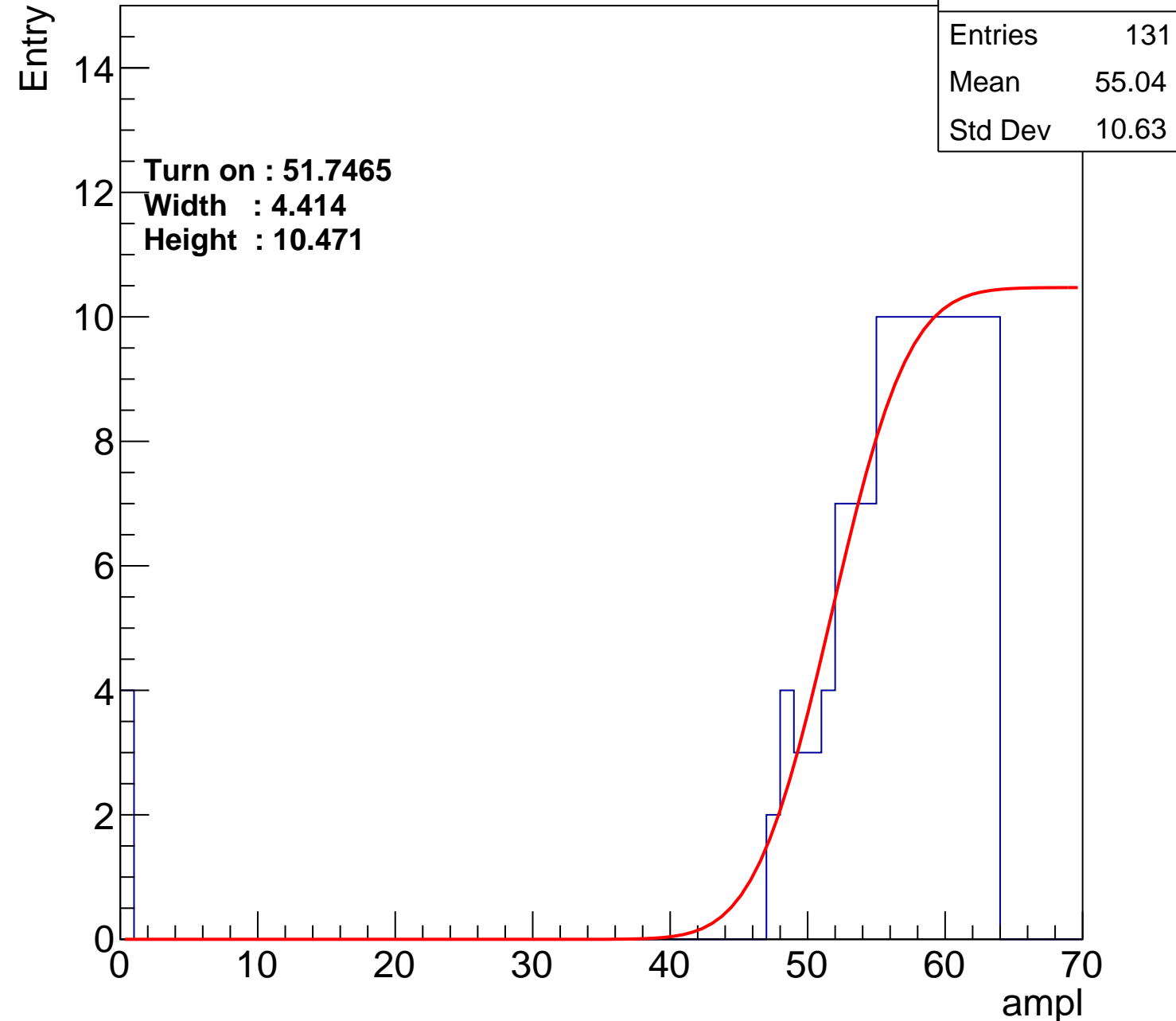
**Width : 4.414**

**Height : 10.471**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch114

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	126
Mean	54.9
Std Dev	11.81

Turn on : 51.7630

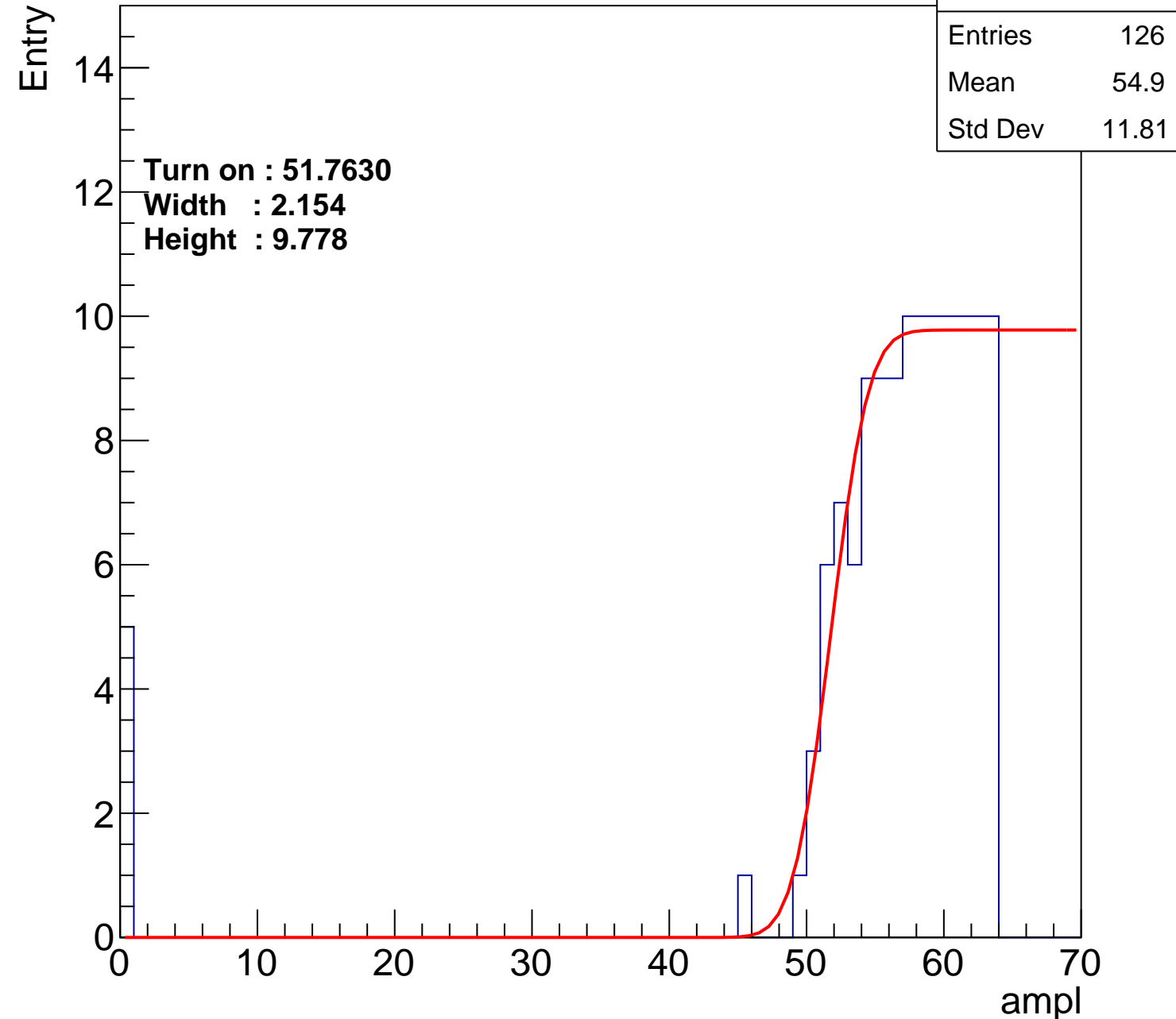
Width : 2.154

Height : 9.778

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch115

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	130
Mean	55.92
Std Dev	8.127

Turn on : 52.0307

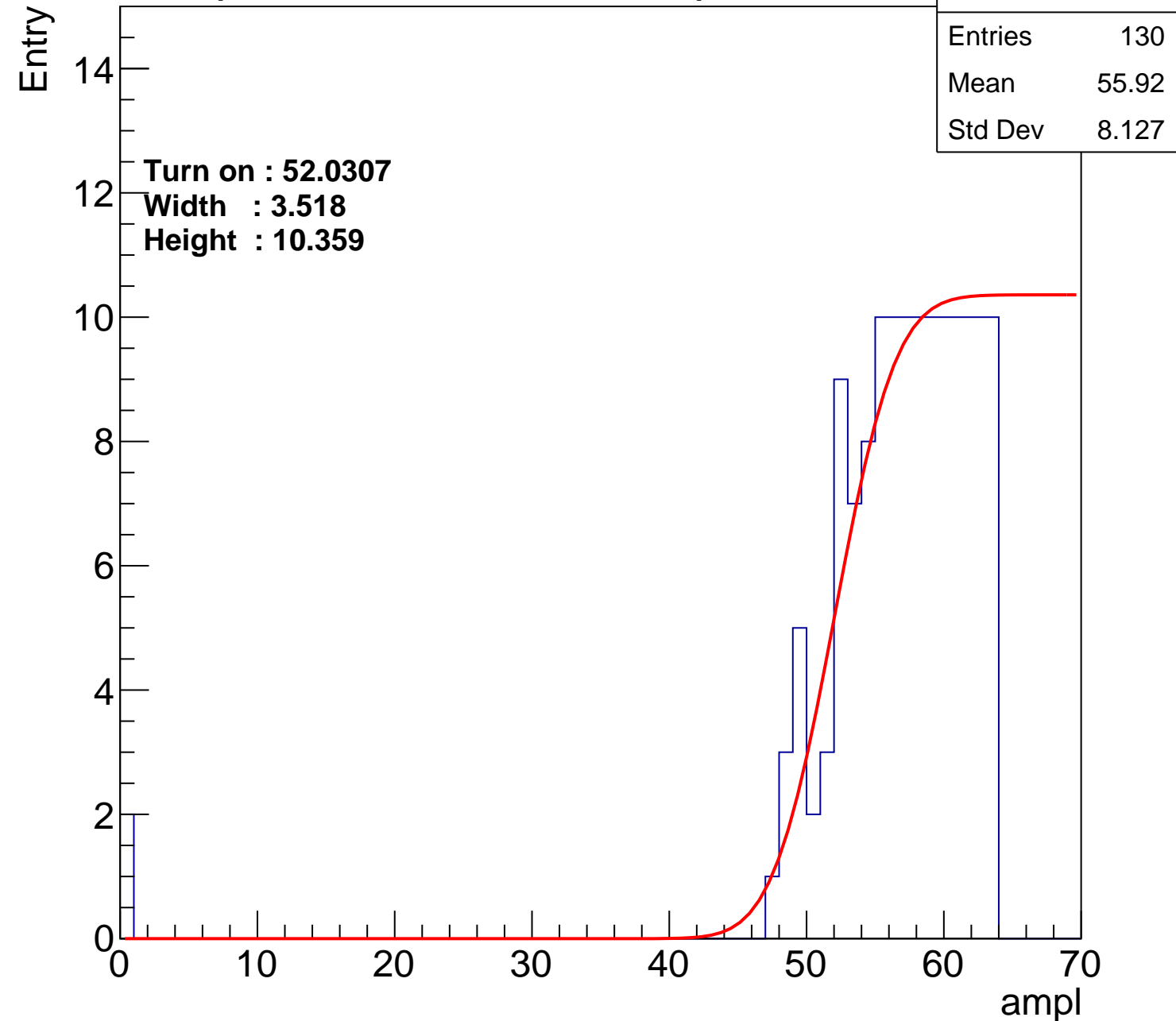
Width : 3.518

Height : 10.359

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch116

calib\_packv5\_040323\_1717.root, FC#2, port C3

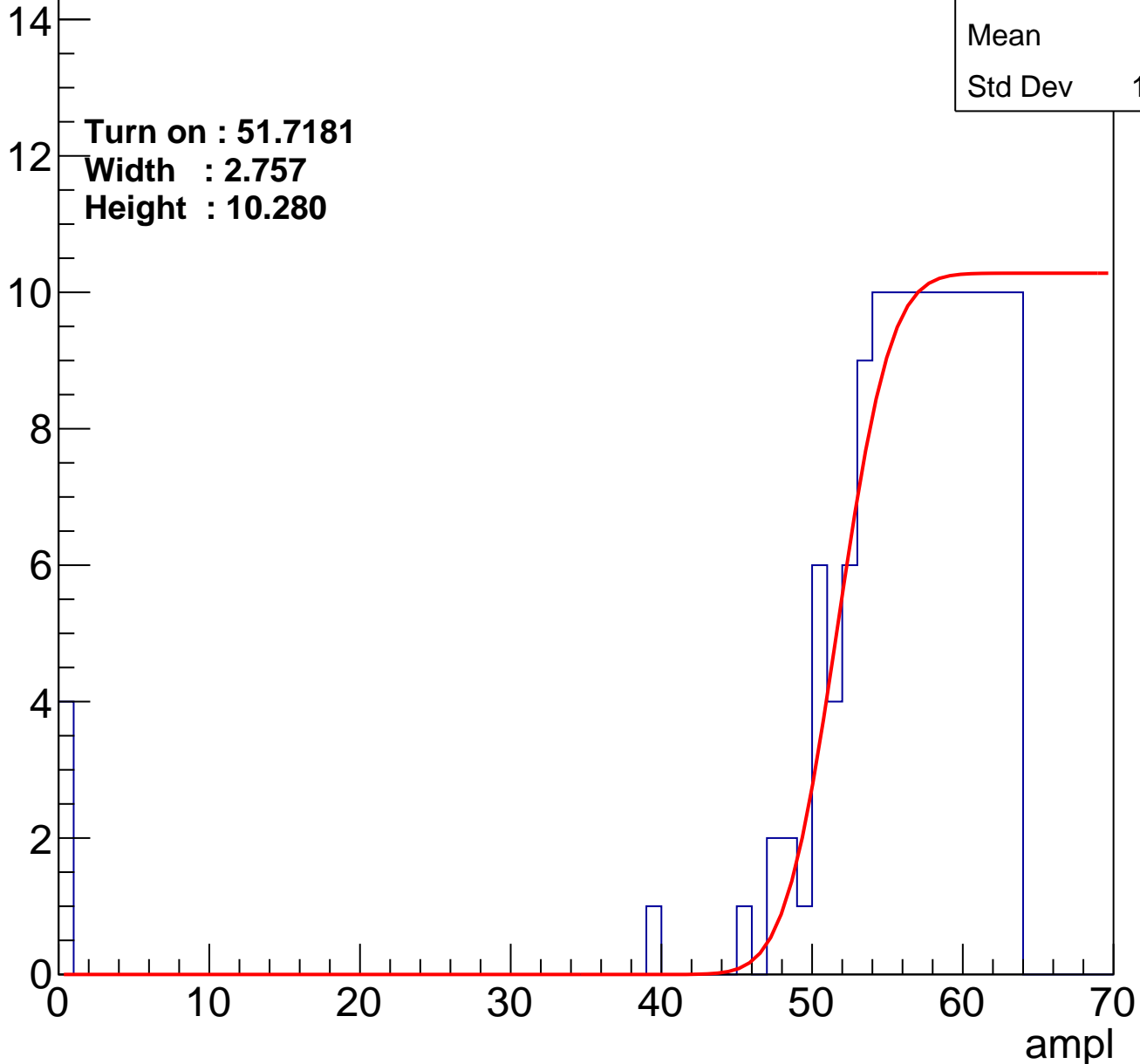
Entries	136
Mean	54.9
Std Dev	10.53

Turn on : 51.7181

Width : 2.757

Height : 10.280

Entry



# B0L103S, U19-ch117

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	123
Mean	56.31
Std Dev	8.169

Turn on : 52.3445

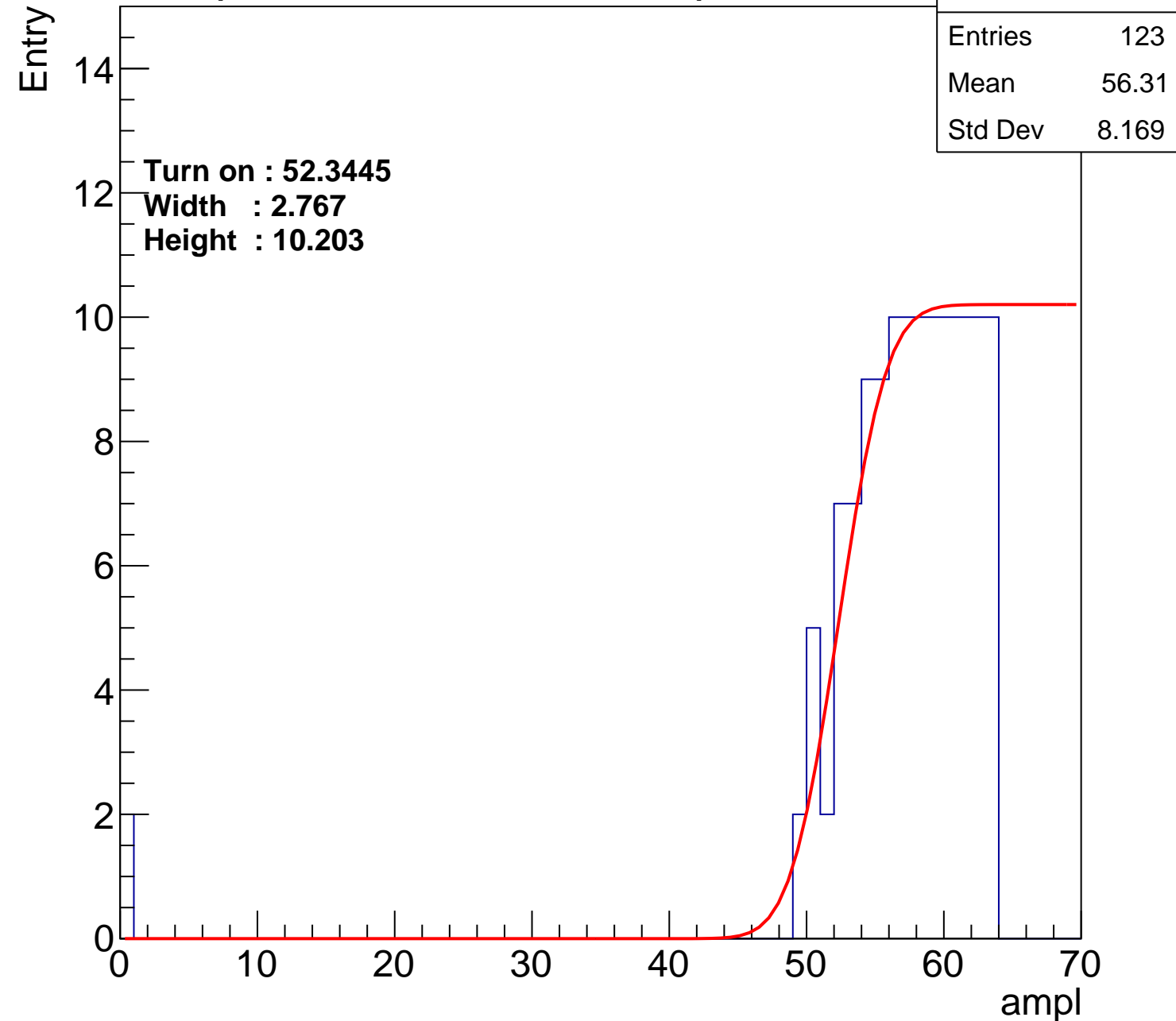
Width : 2.767

Height : 10.203

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch118

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	115
Mean	55.93
Std Dev	9.962

Turn on : 53.3861

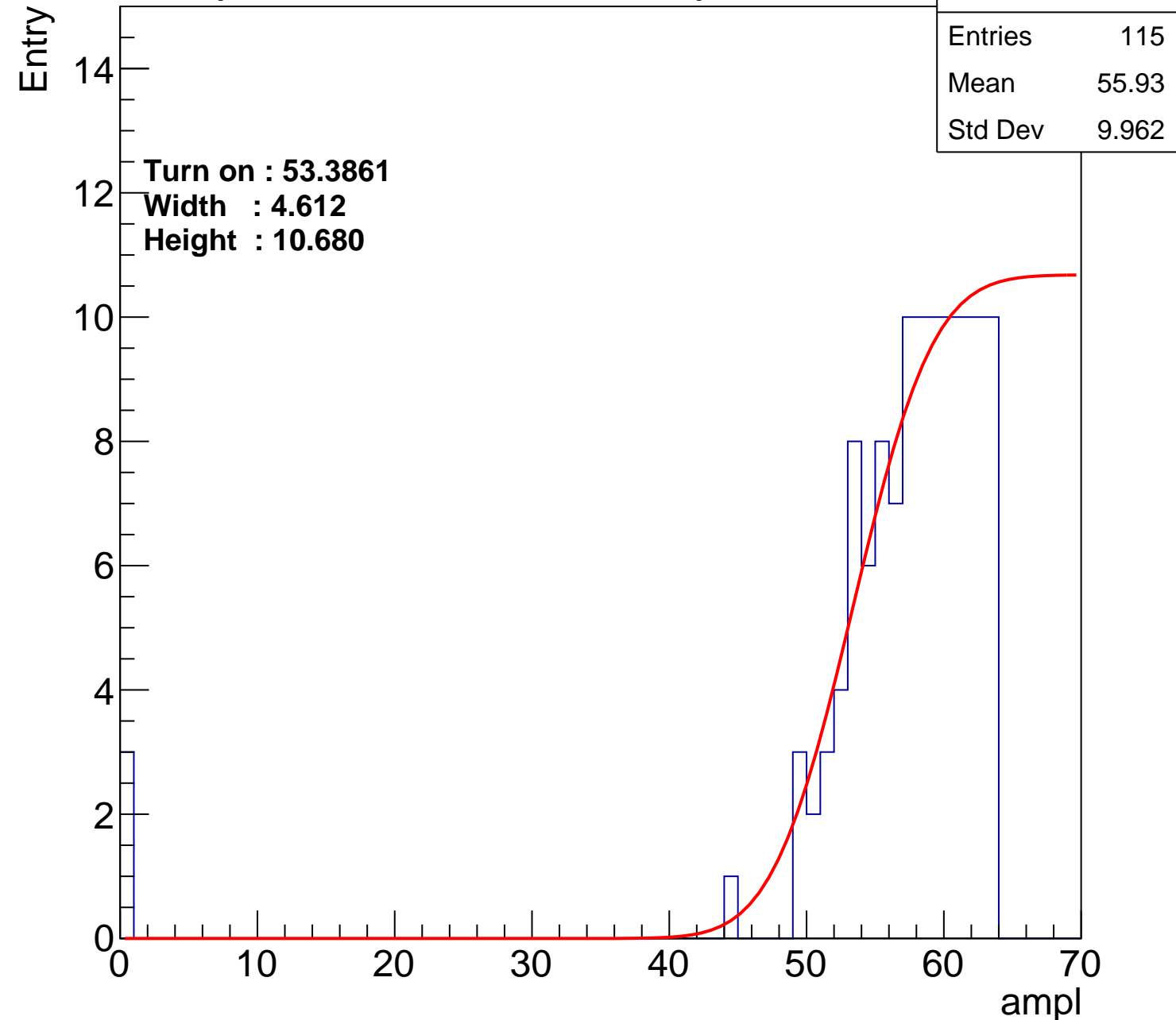
Width : 4.612

Height : 10.680

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch119

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	56.52
Std Dev	6.581

Turn on : 52.1288

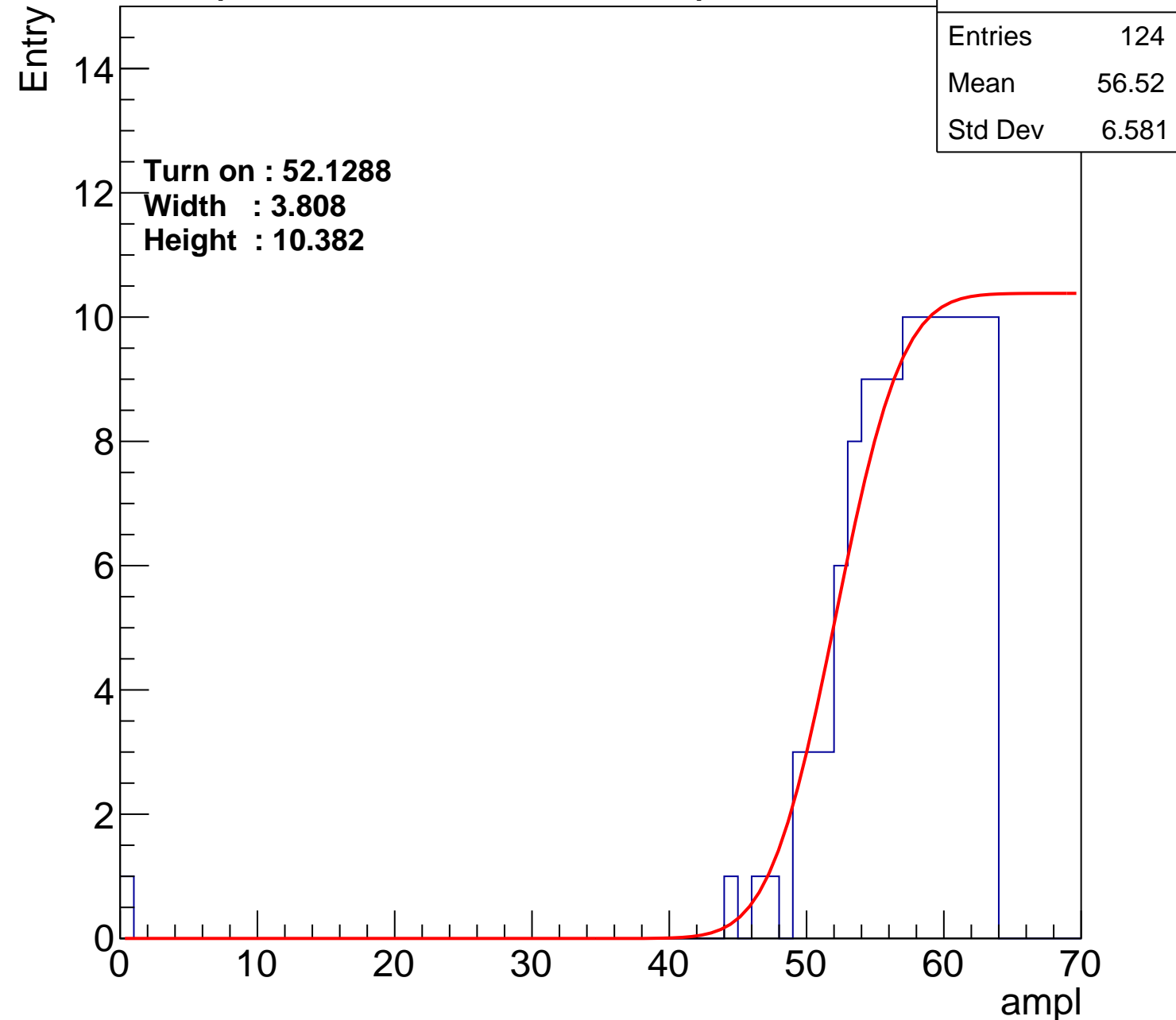
Width : 3.808

Height : 10.382

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch120

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	133
Mean	54.74
Std Dev	11.53

Turn on : 51.7005

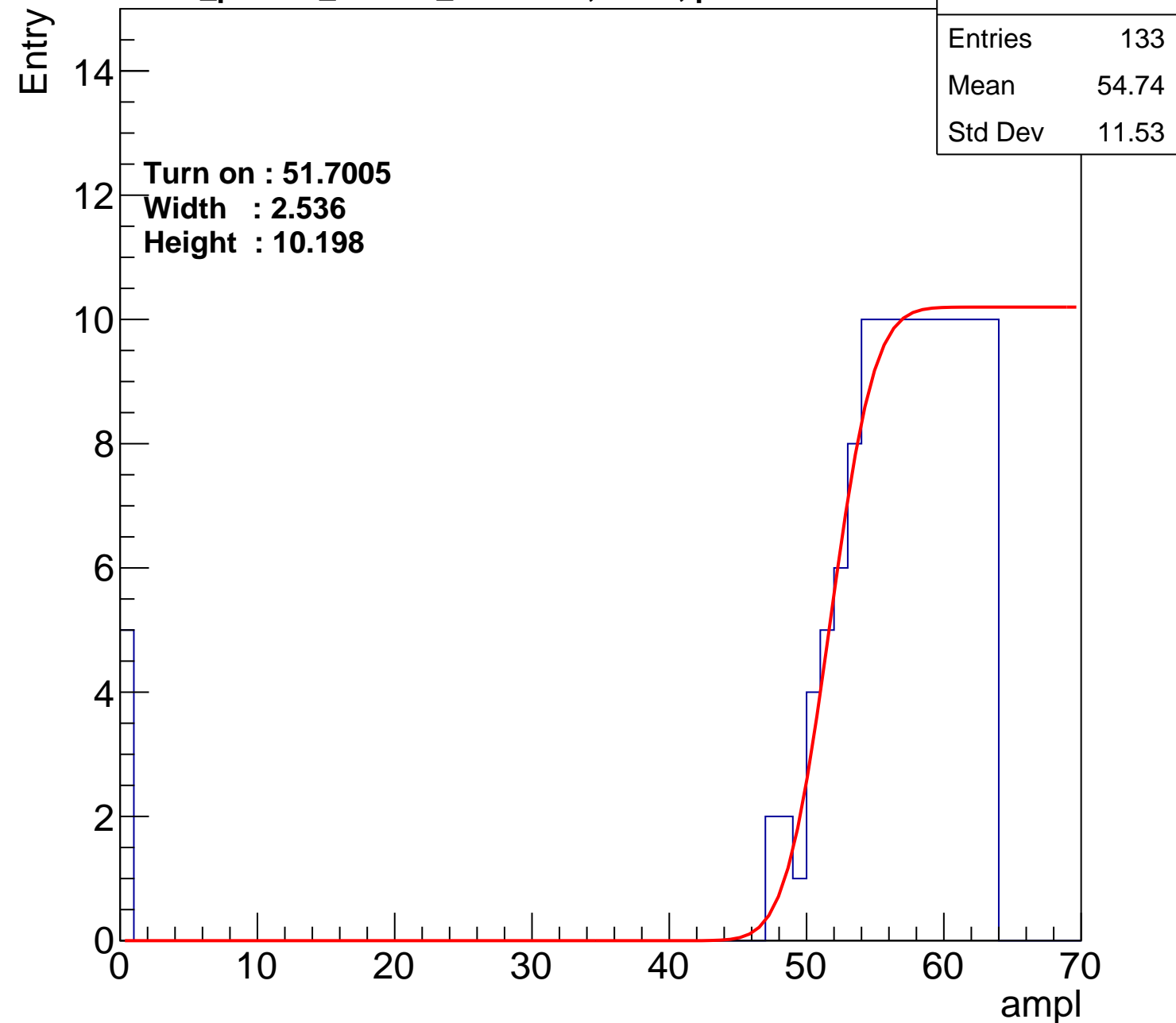
Width : 2.536

Height : 10.198

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch121

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	127
Mean	55.06
Std Dev	10.77

Turn on : 51.9905

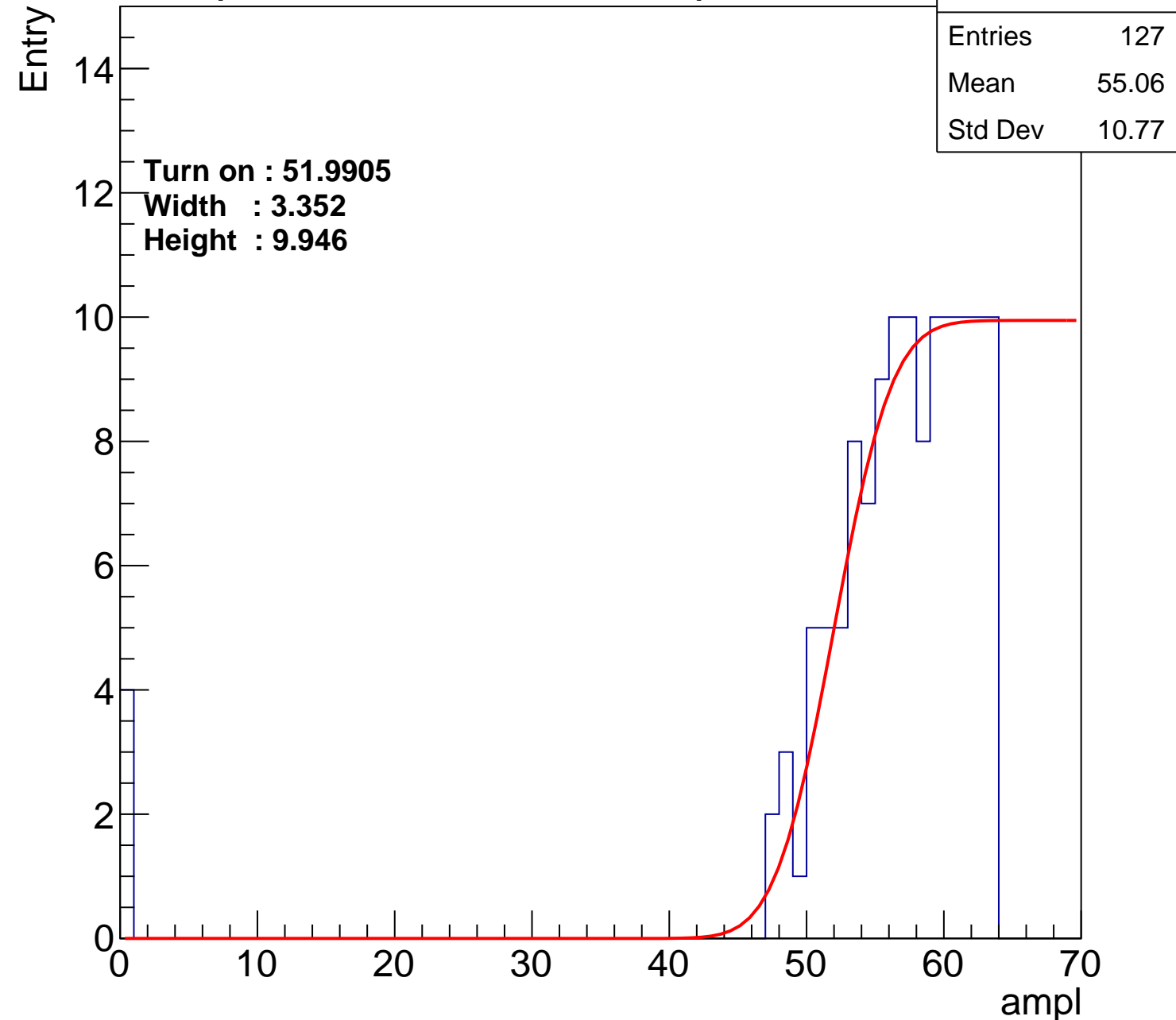
Width : 3.352

Height : 9.946

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch122

calib\_packv5\_040323\_1717.root, FC#2, port C3

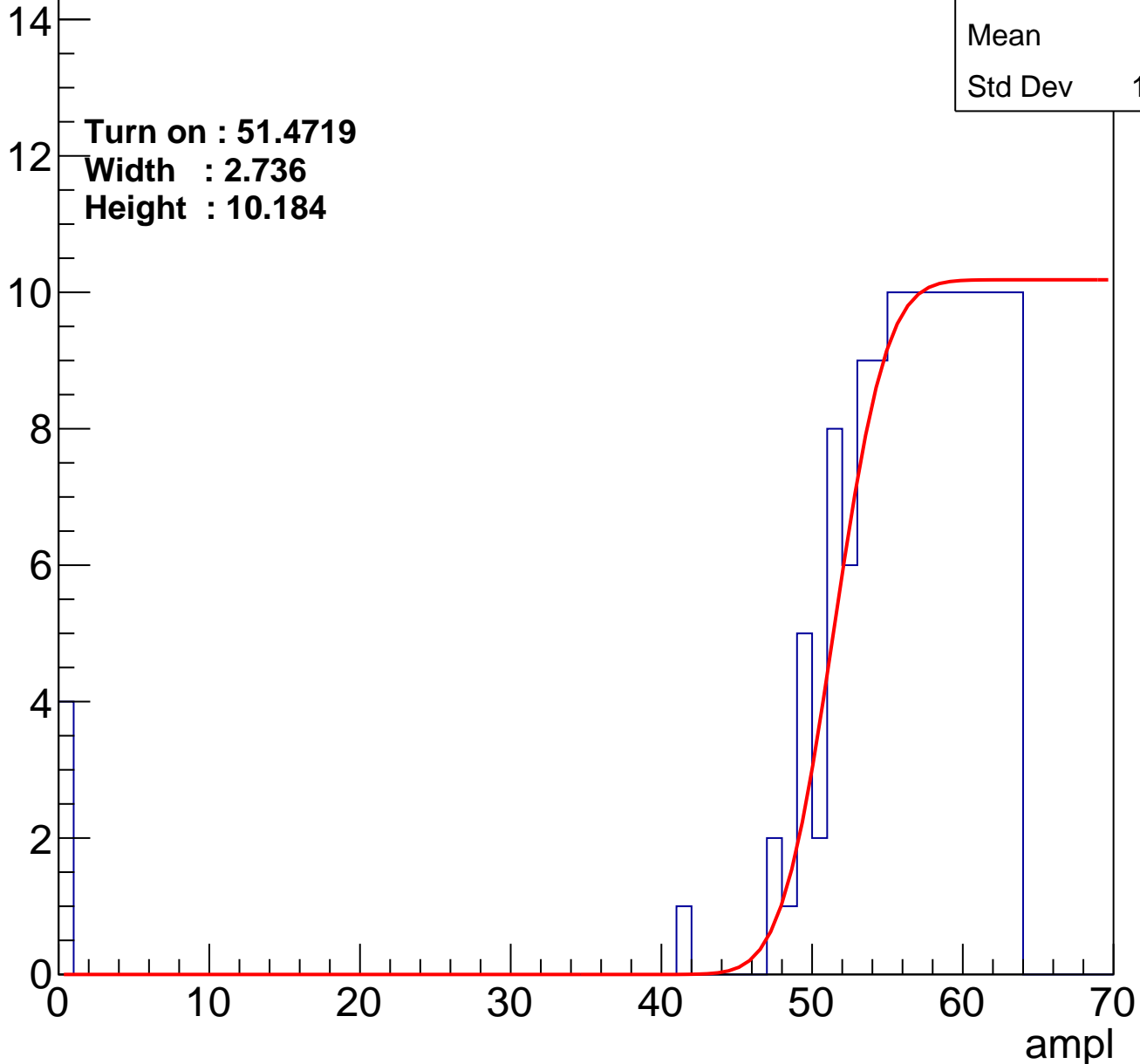
Entries	137
Mean	54.9
Std Dev	10.45

Turn on : 51.4719

Width : 2.736

Height : 10.184

Entry



# B0L103S, U19-ch123

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	124
Mean	55.67
Std Dev	9.651

**Turn on : 52.2757**

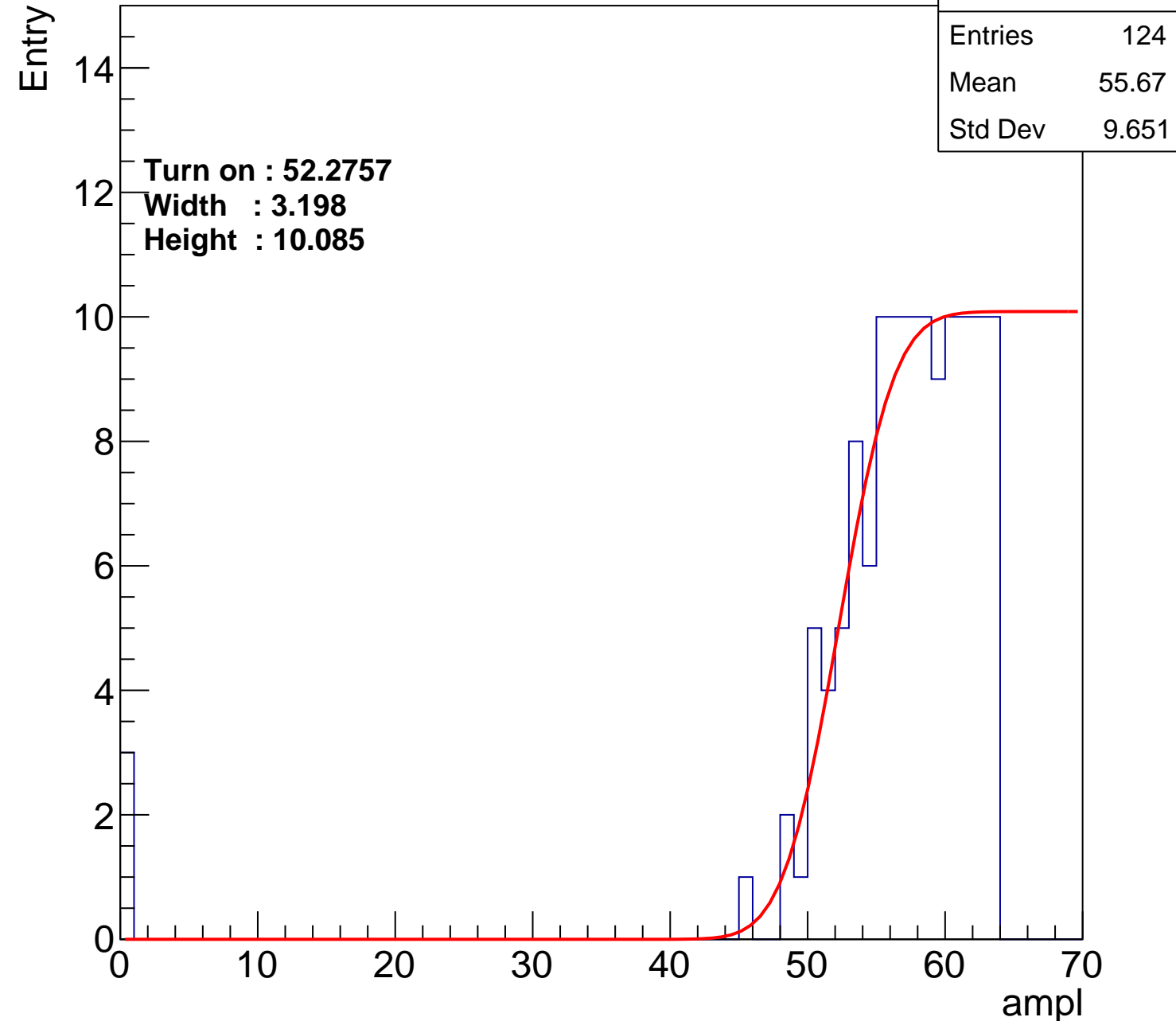
**Width : 3.198**

**Height : 10.085**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch124

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	141
Mean	54.96
Std Dev	10.09

Turn on : 50.6997

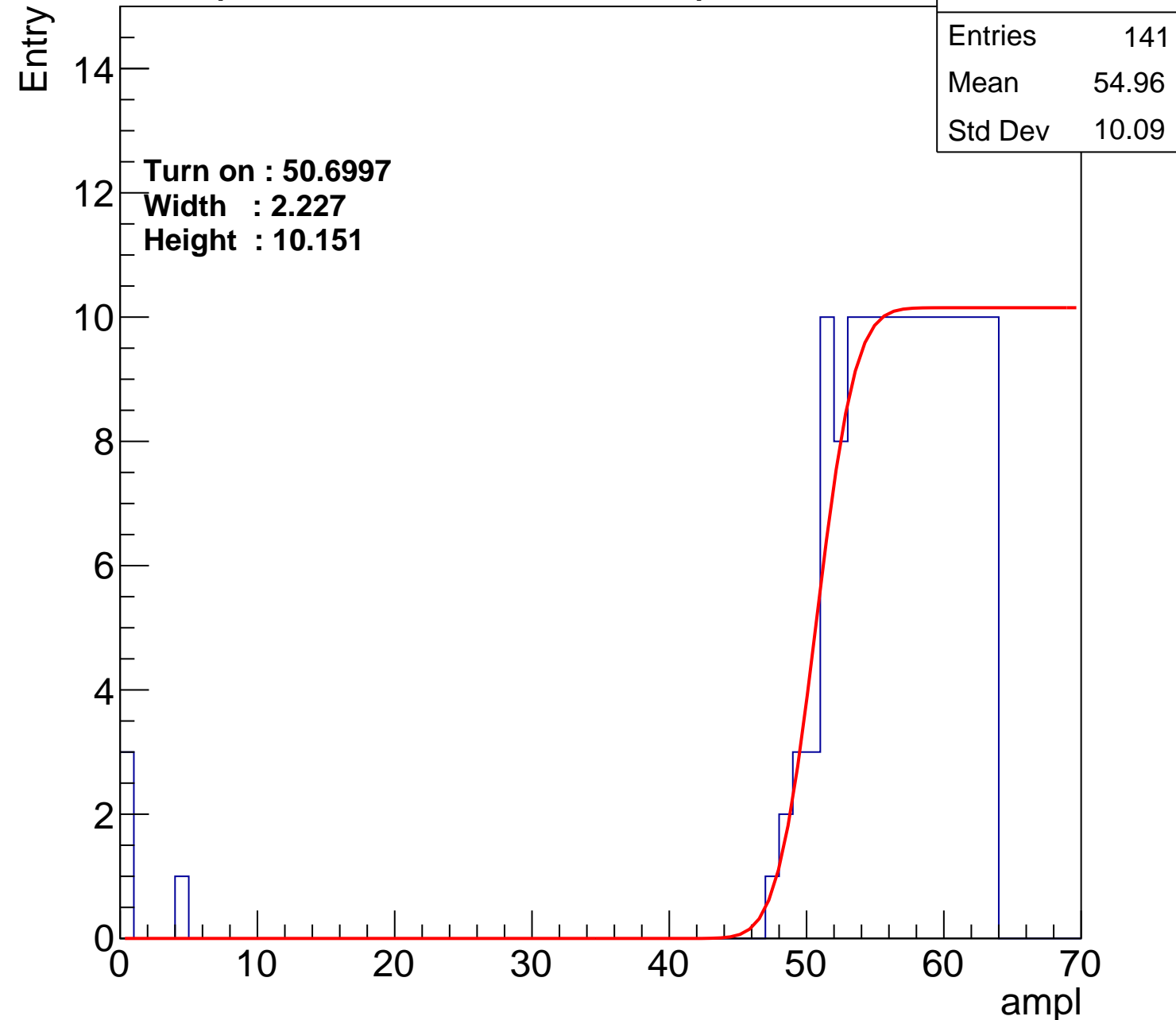
Width : 2.227

Height : 10.151

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch125

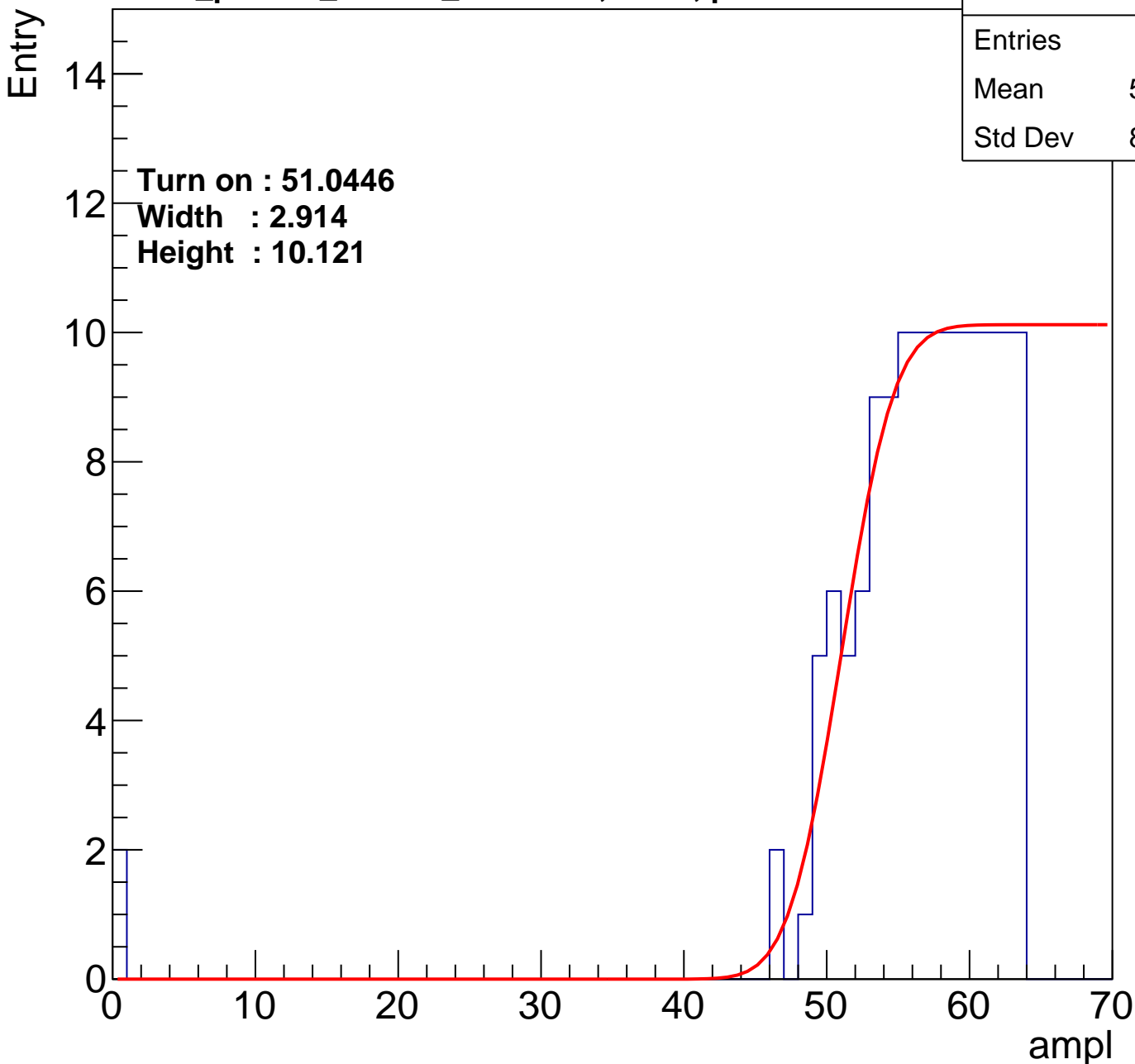
**calib\_packv5\_040323\_1717.root, FC#2, port C3**

**Turn on : 51.0446**

**Width : 2.914**

**Height : 10.121**

Entries	135
Mean	55.74
Std Dev	8.044



# B0L103S, U19-ch126

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	158
Mean	54.09
Std Dev	9.971

Turn on : 48.5076

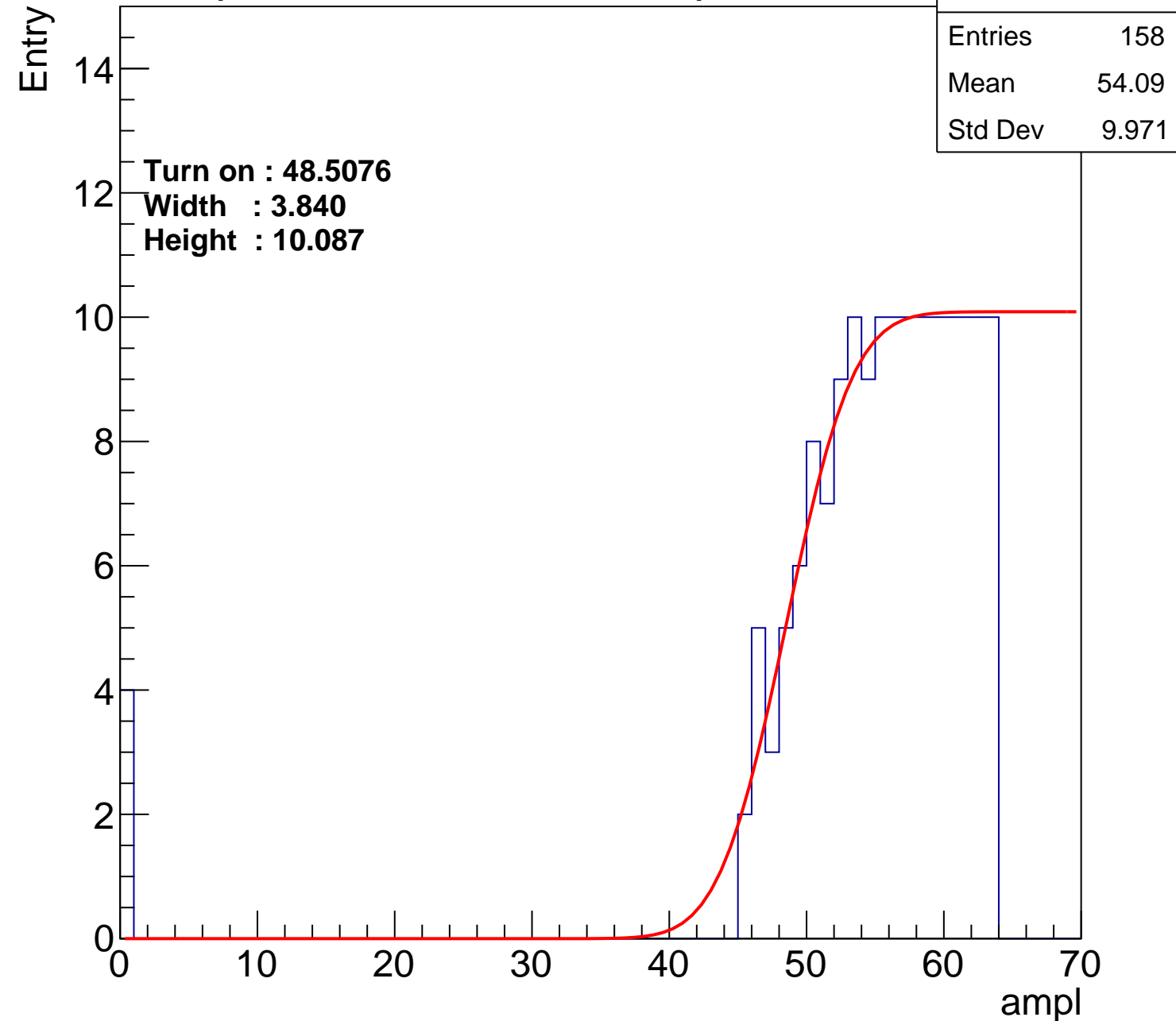
Width : 3.840

Height : 10.087

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L103S, U19-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.09
Std Dev	8.372

Turn on : 54.9322

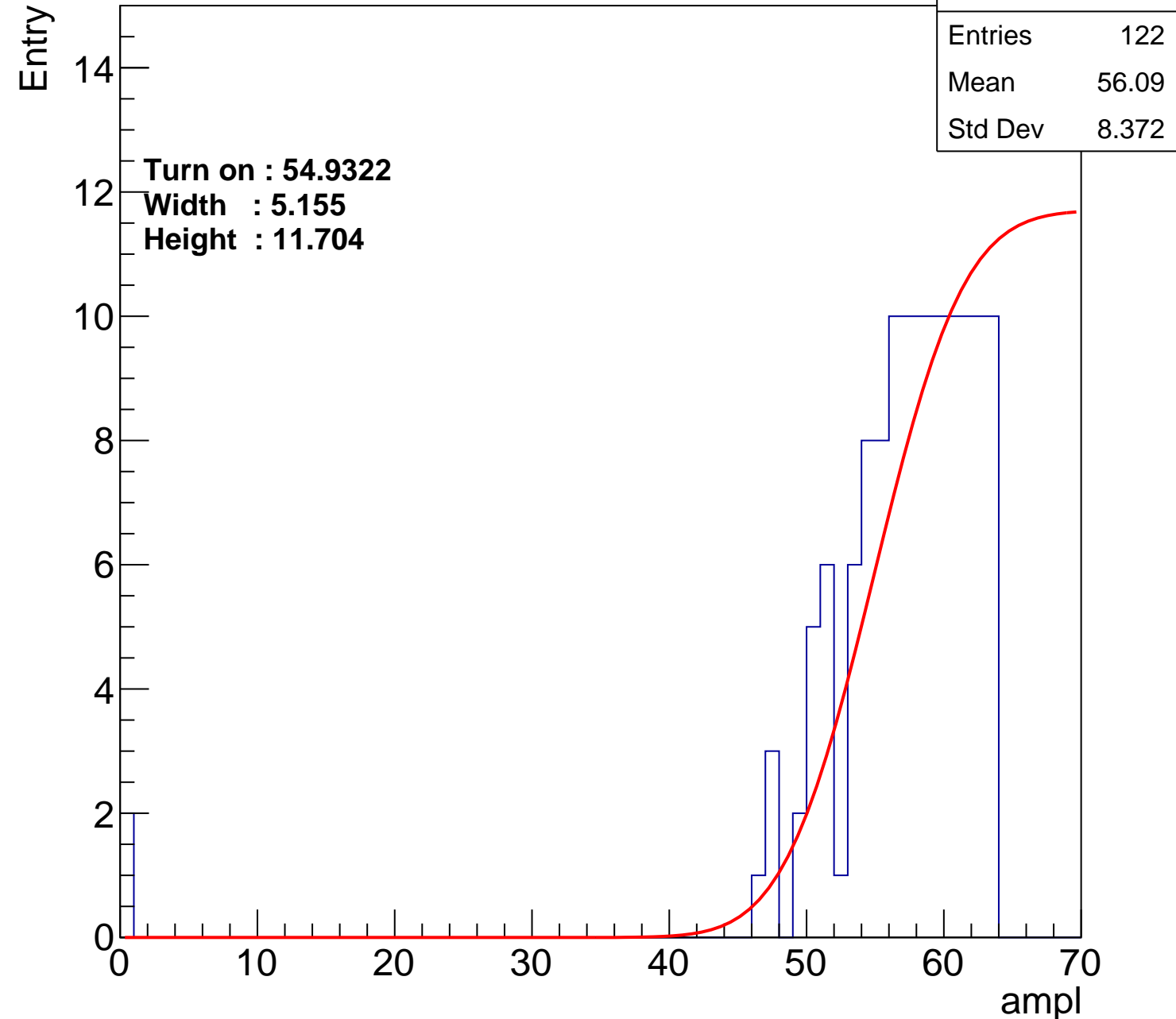
Width : 5.155

Height : 11.704

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L103S, U19-ch127

calib\_packv5\_040323\_1717.root, FC#2, port C3

Entries	122
Mean	56.09
Std Dev	8.372

Turn on : 54.9322

Width : 5.155

Height : 11.704

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

