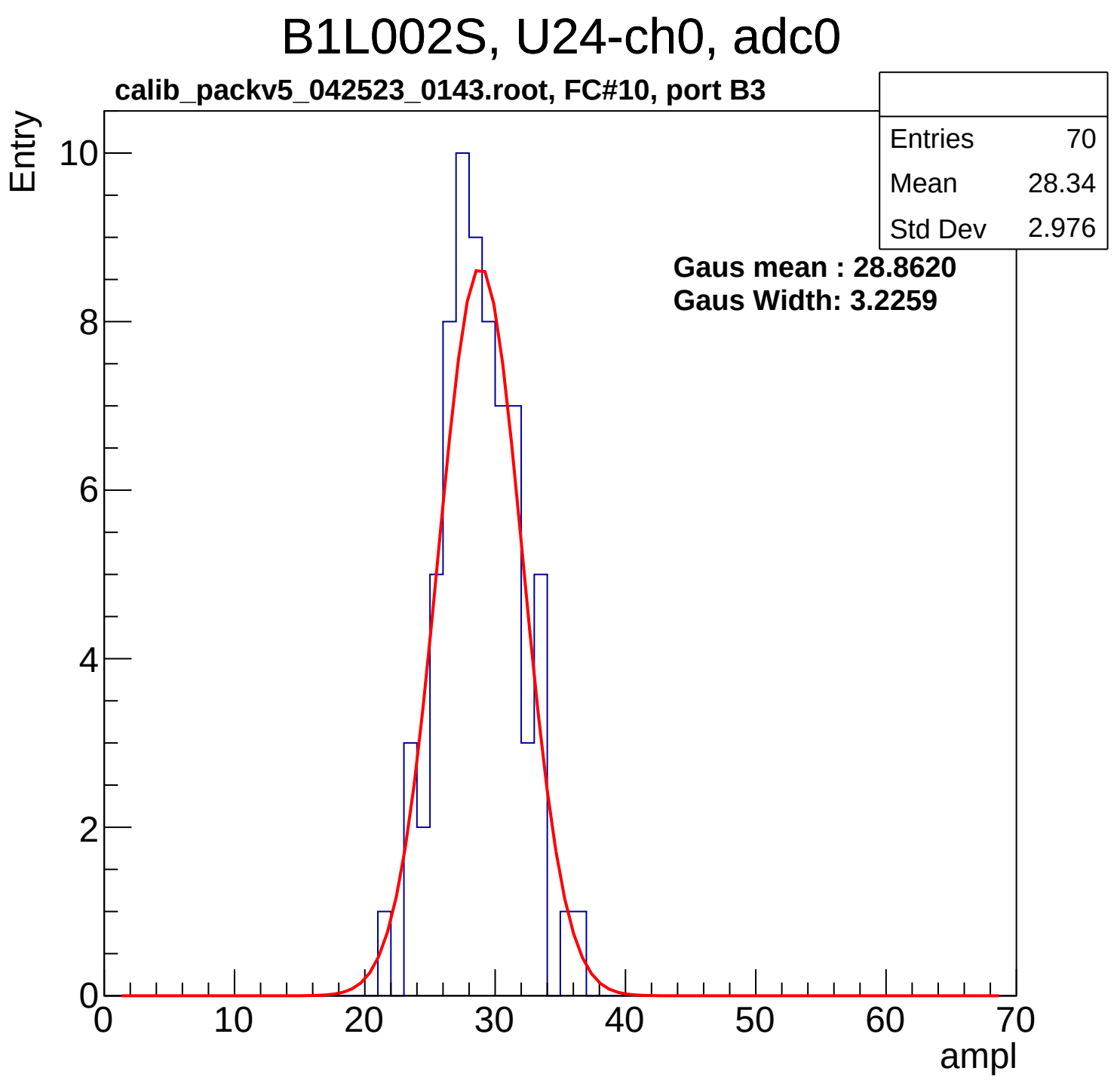
B1L002S, U24-ch0, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.34
Std Dev 2.976
Gaus mean : 28.8620
Gaus Width: 3.2259
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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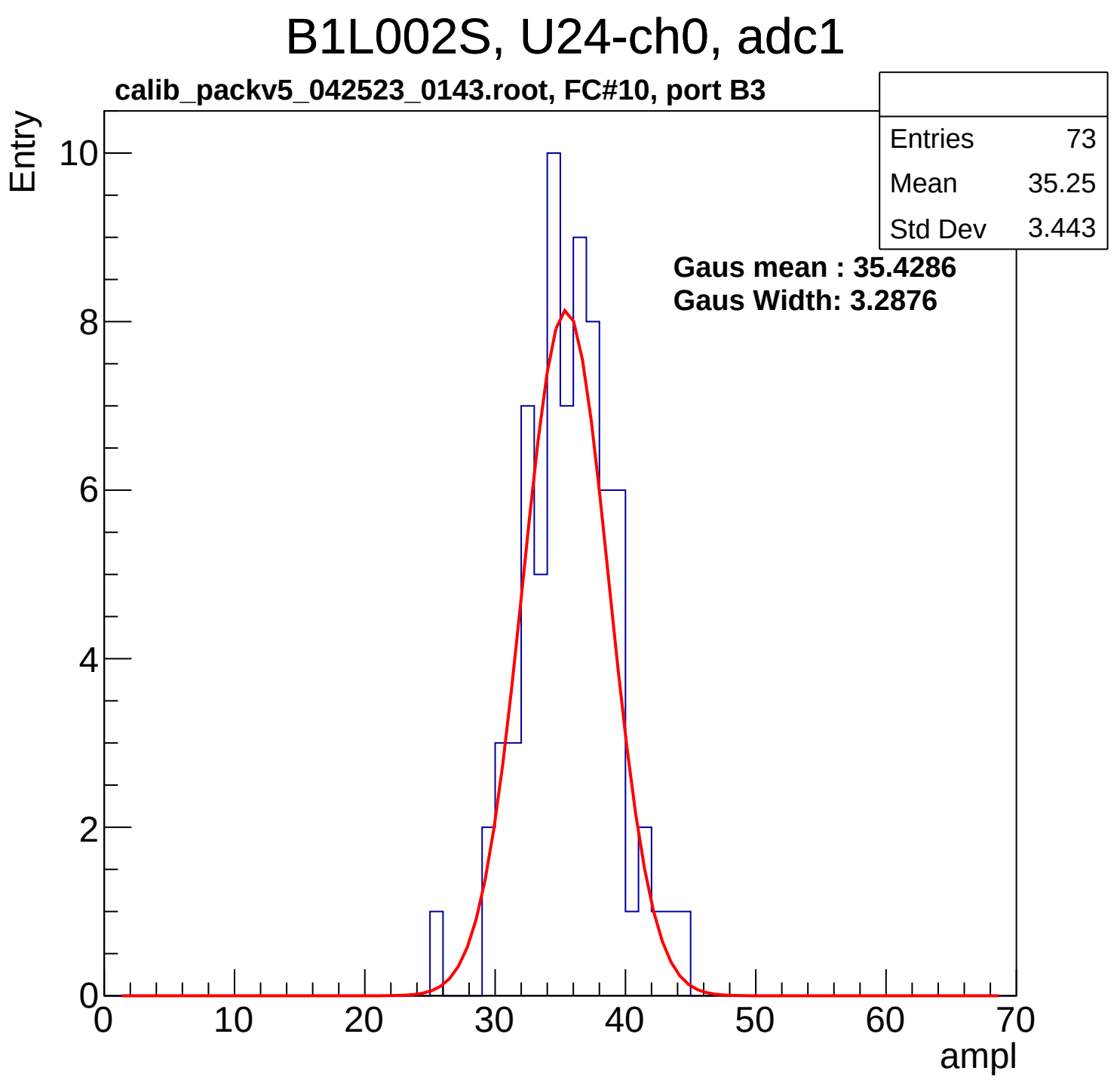
B1L002S, U24-ch0, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.25
Std Dev 3.443
Gaus mean : 35.4286
Gaus Width: 3.2876
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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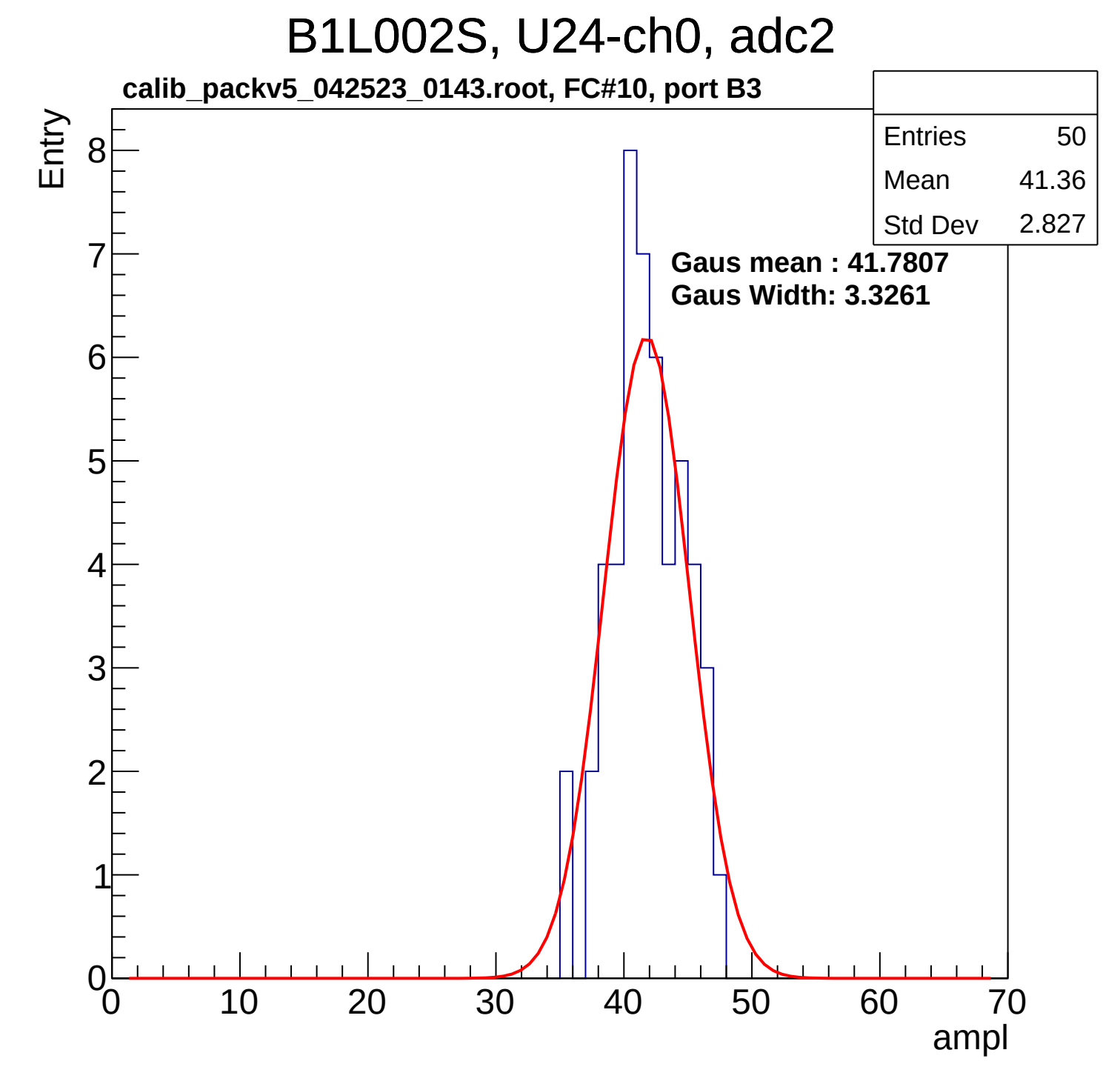
B1L002S, U24-ch0, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 41.36
Std Dev 2.827
Gaus mean : 41.7807
Gaus Width: 3.3261
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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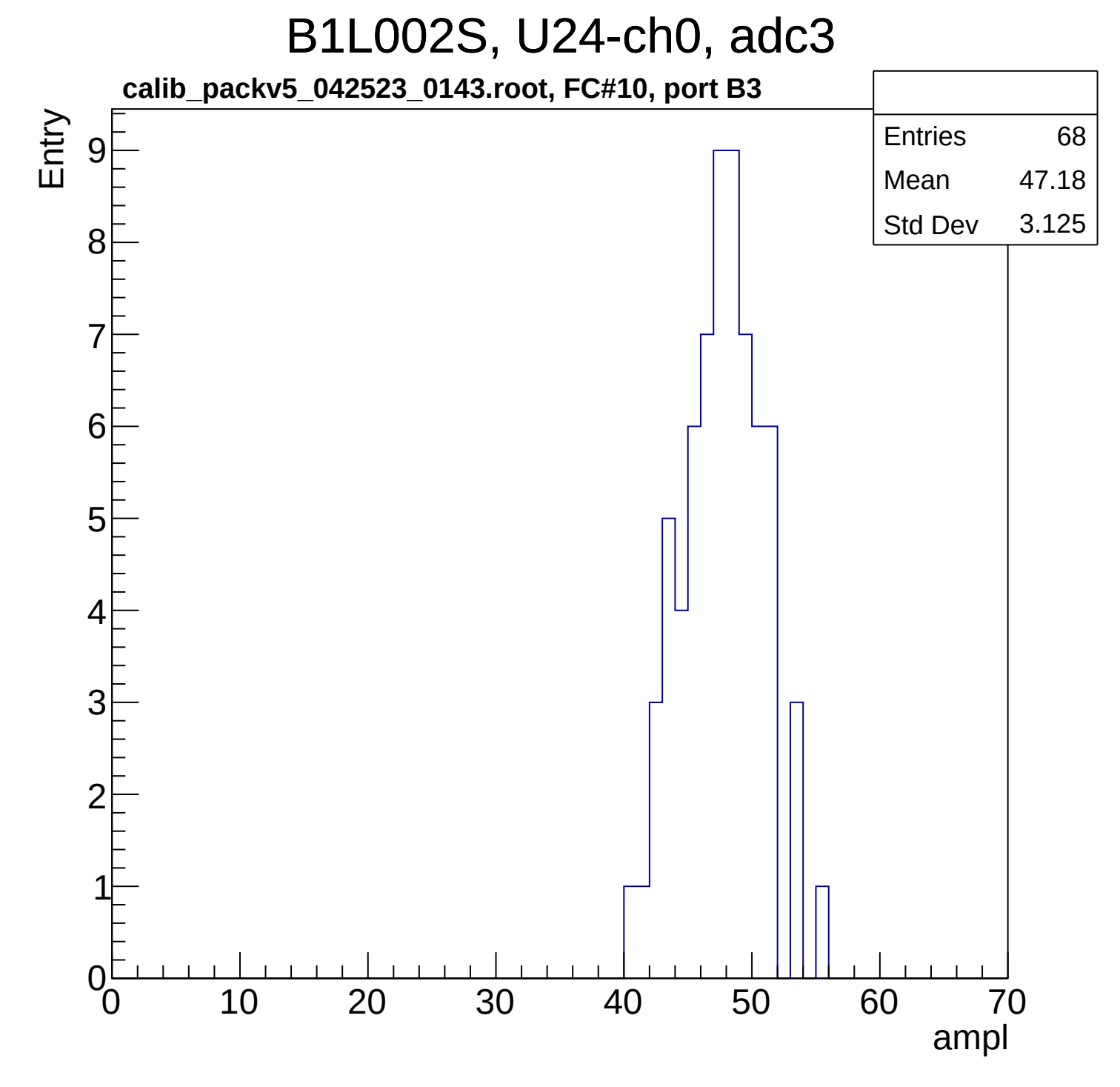
B1L002S, U24-ch0, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.18
Std Dev 3.125
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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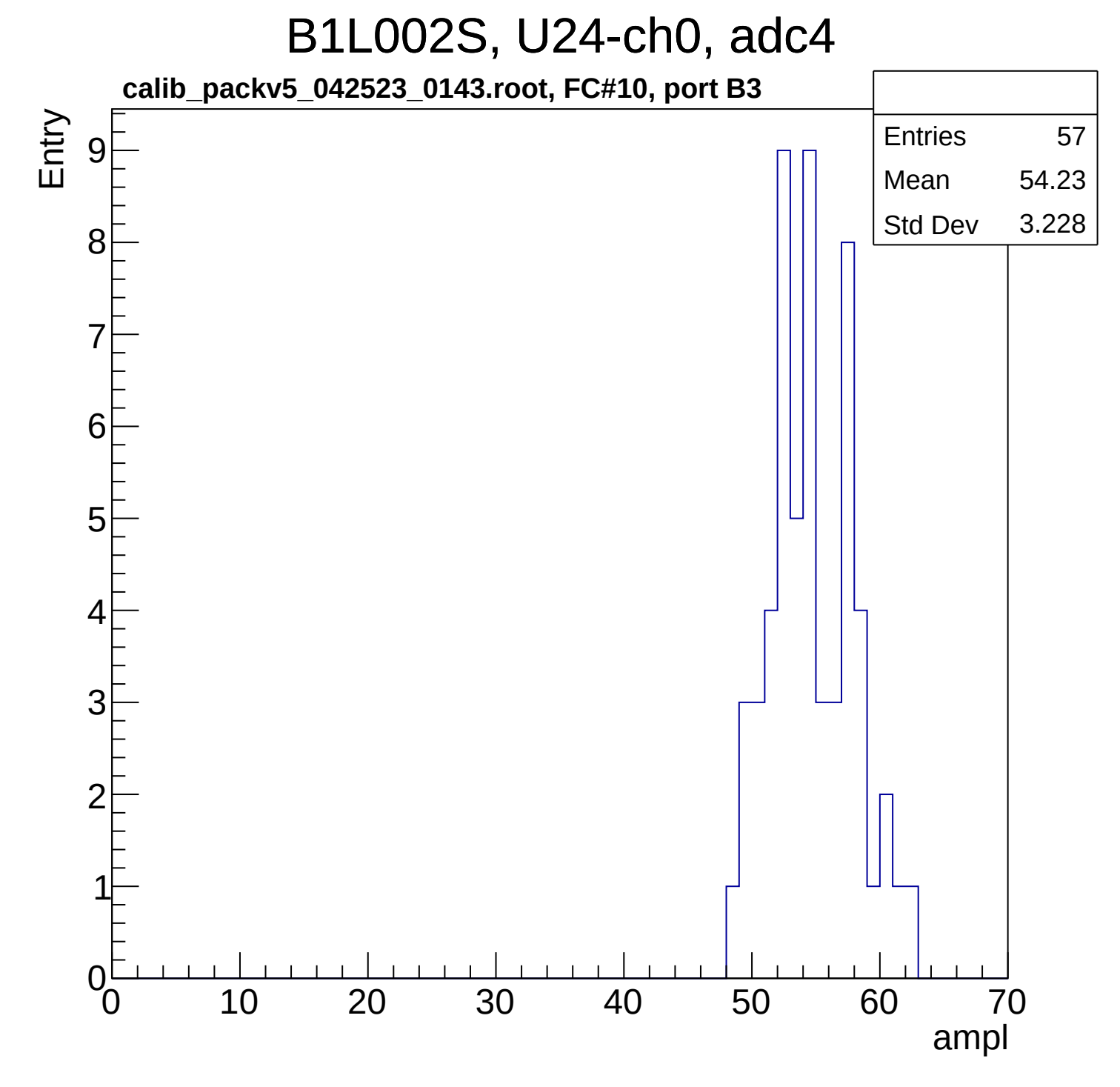
B1L002S, U24-ch0, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 54.23
Std Dev 3.228
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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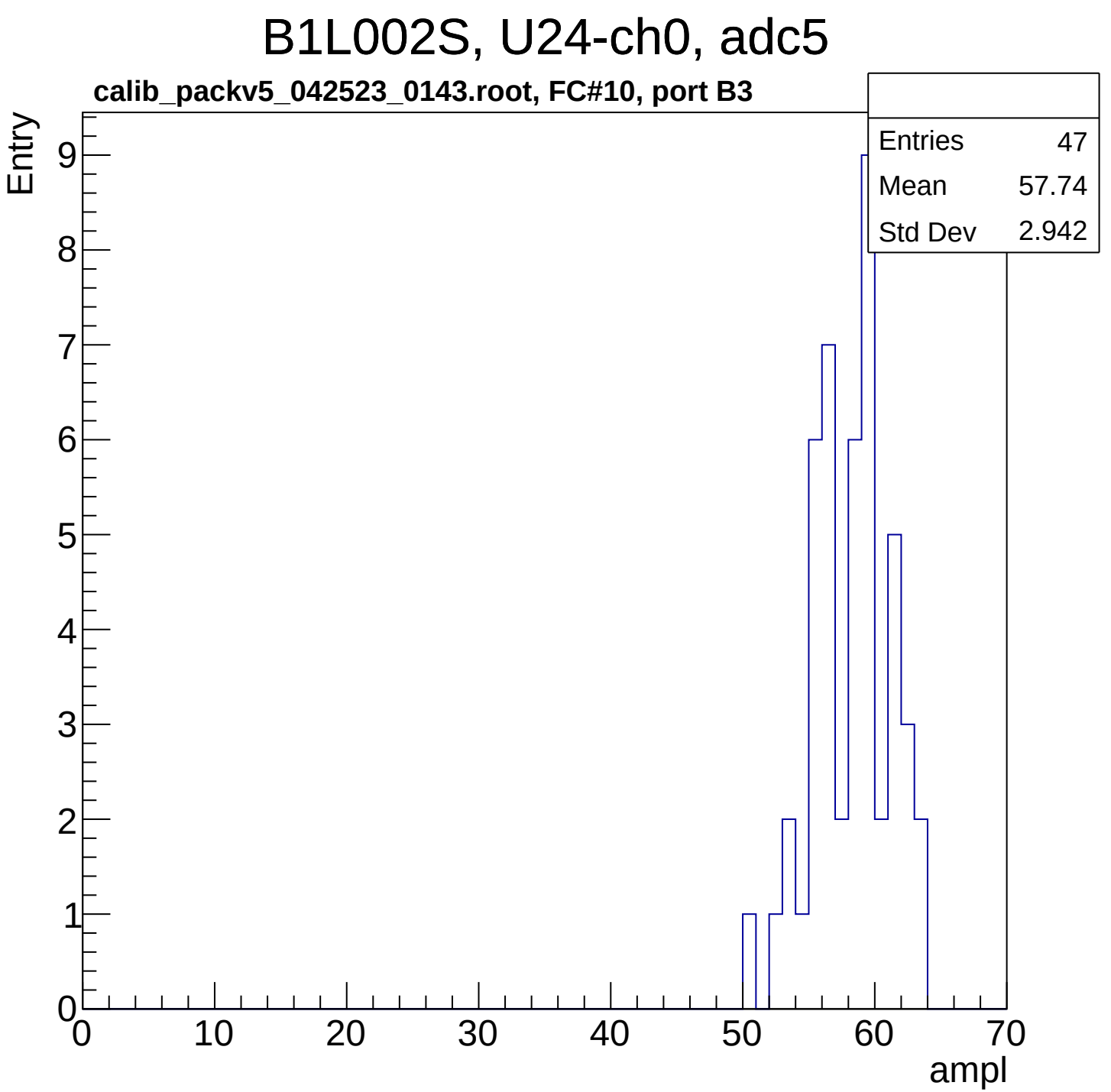
B1L002S, U24-ch0, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 57.74
Std Dev 2.942
Entry
ampl
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4
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6
7
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0
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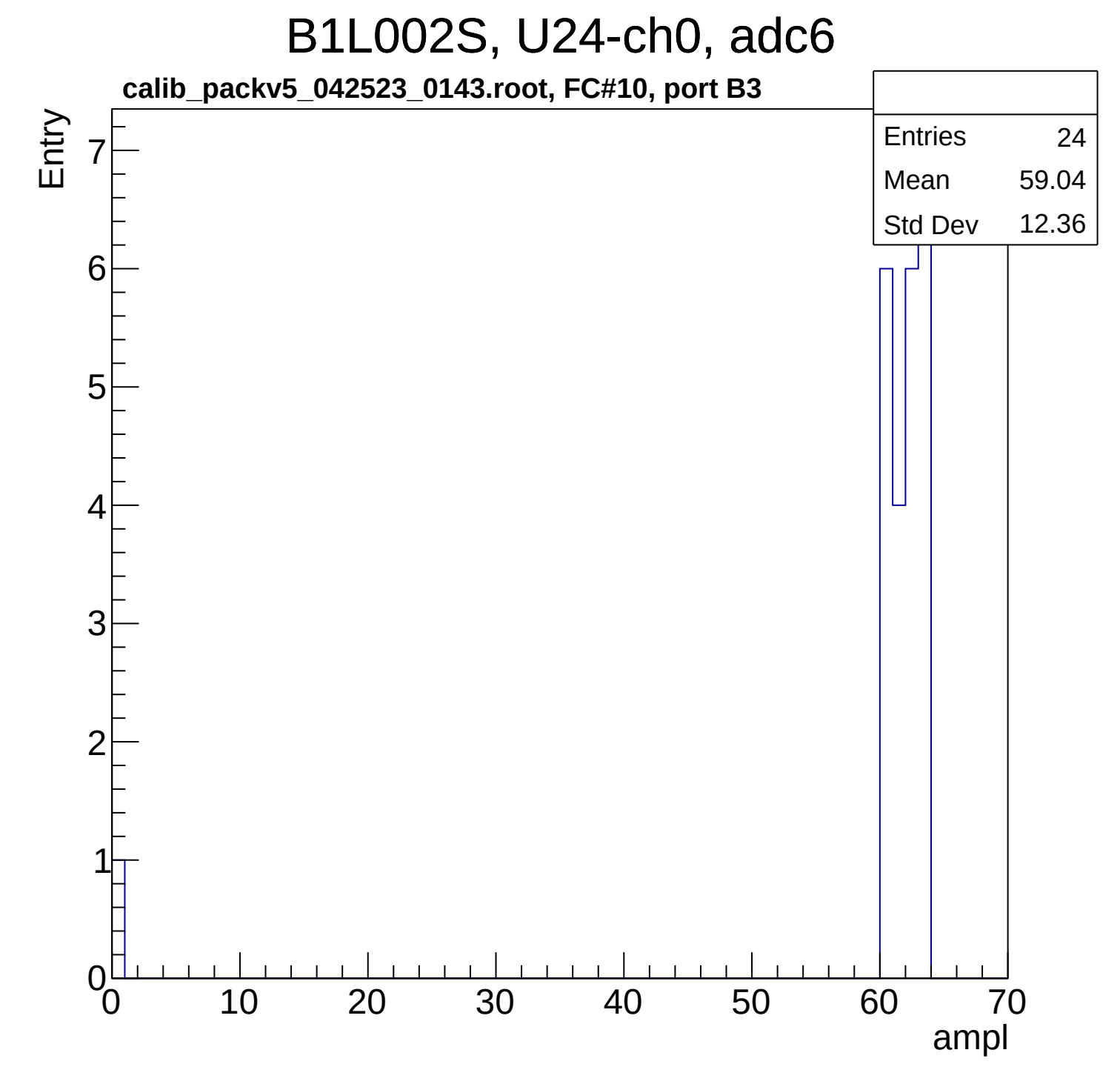
B1L002S, U24-ch0, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 59.04
Std Dev 12.36
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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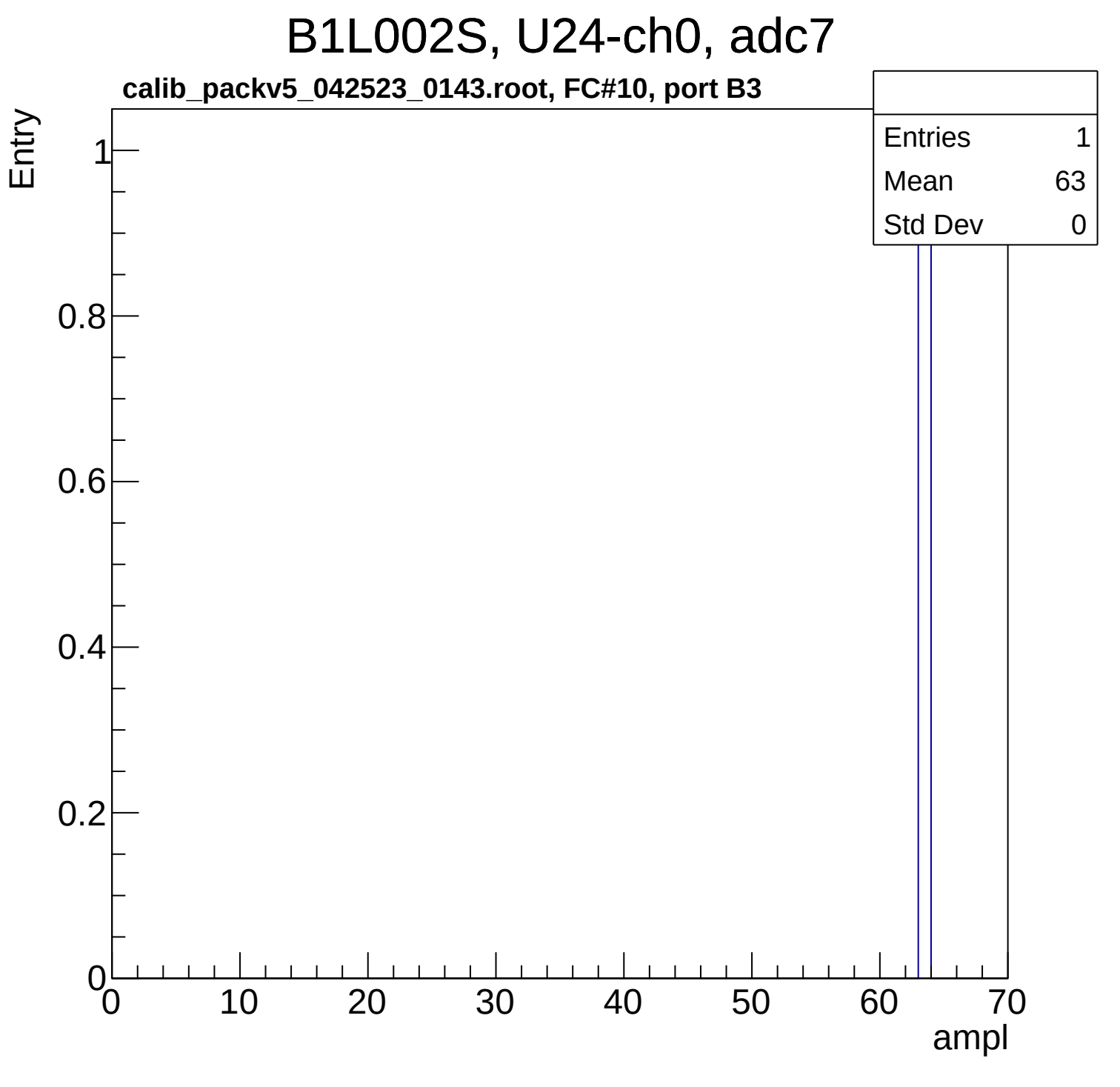
B1L002S, U24-ch0, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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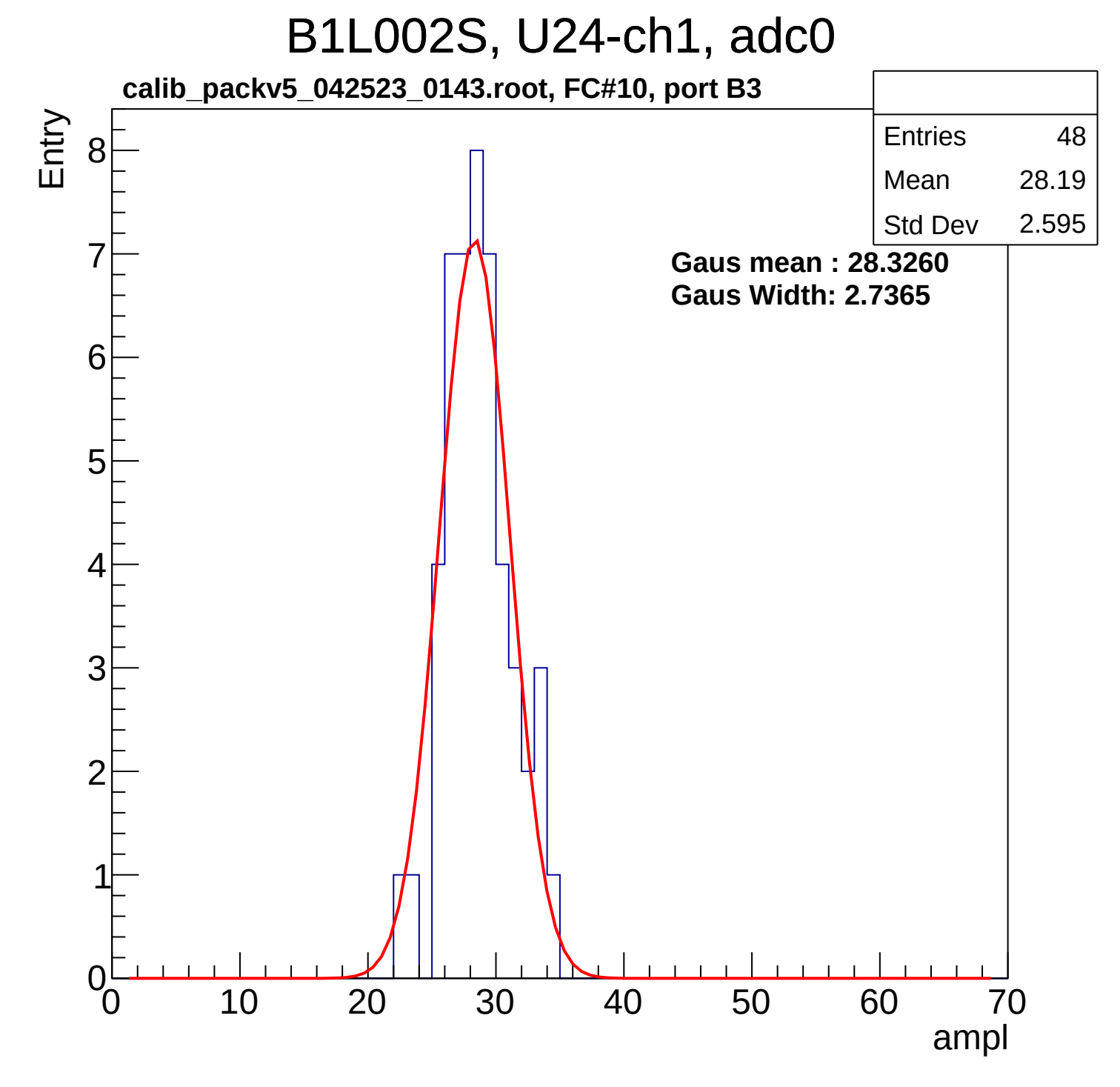
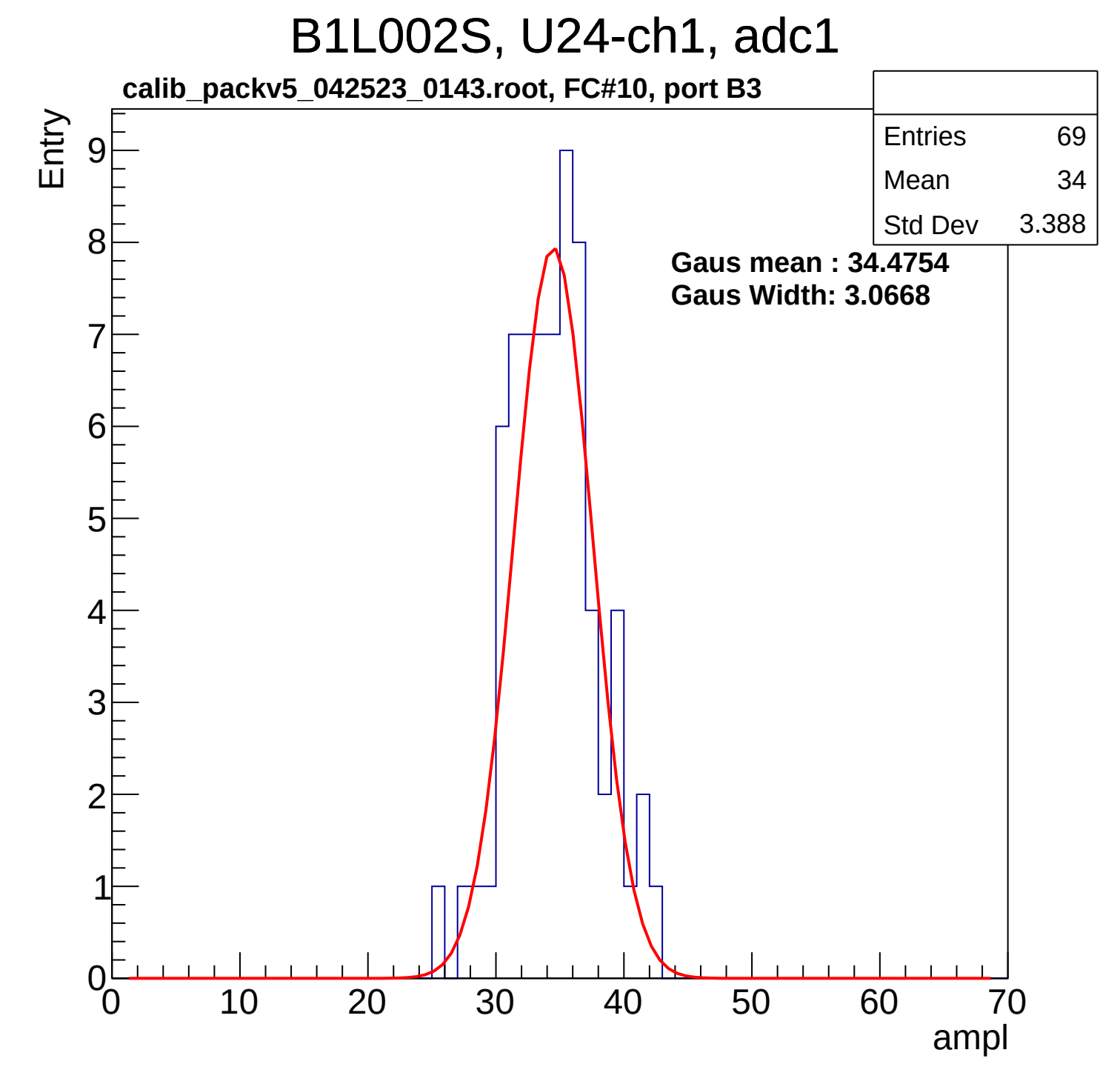
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch1, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.19
Std Dev 2.595
Gaus mean : 28.3260
Gaus Width: 2.7365
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch1, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34
Std Dev 3.388
Gaus mean : 34.4754
Gaus Width: 3.0668
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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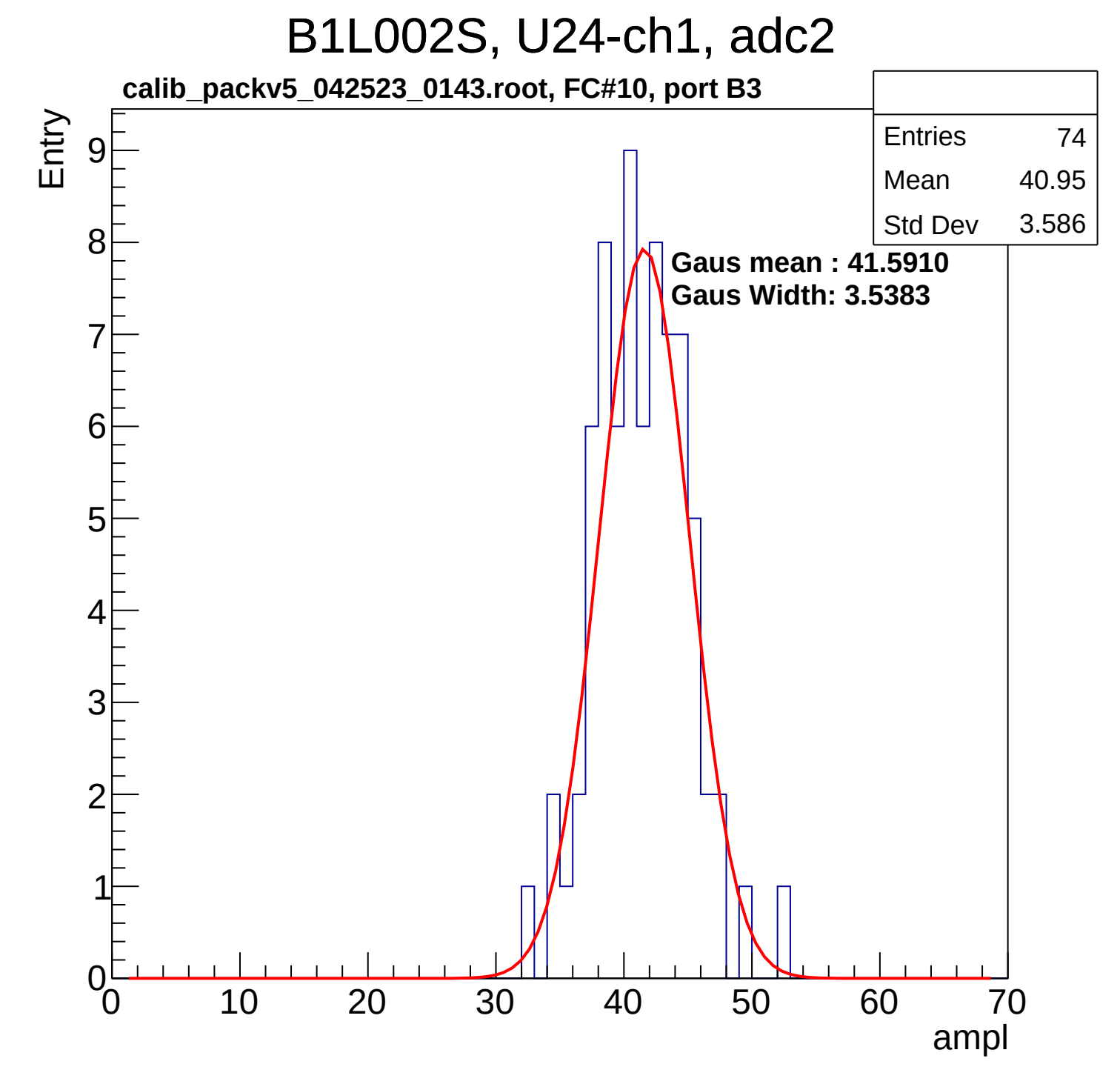
B1L002S, U24-ch1, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 40.95
Std Dev 3.586
Gaus mean : 41.5910
Gaus Width: 3.5383
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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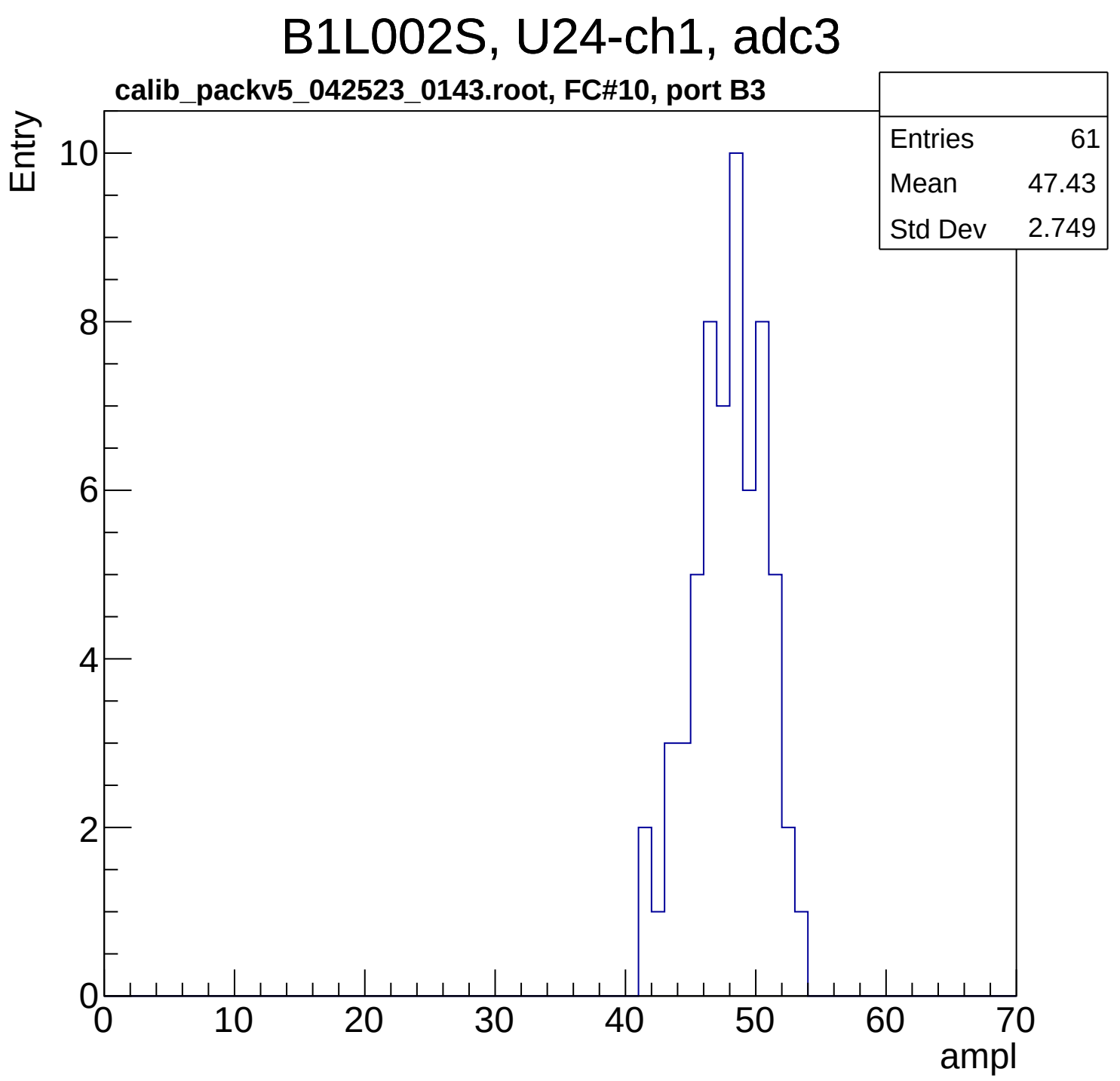
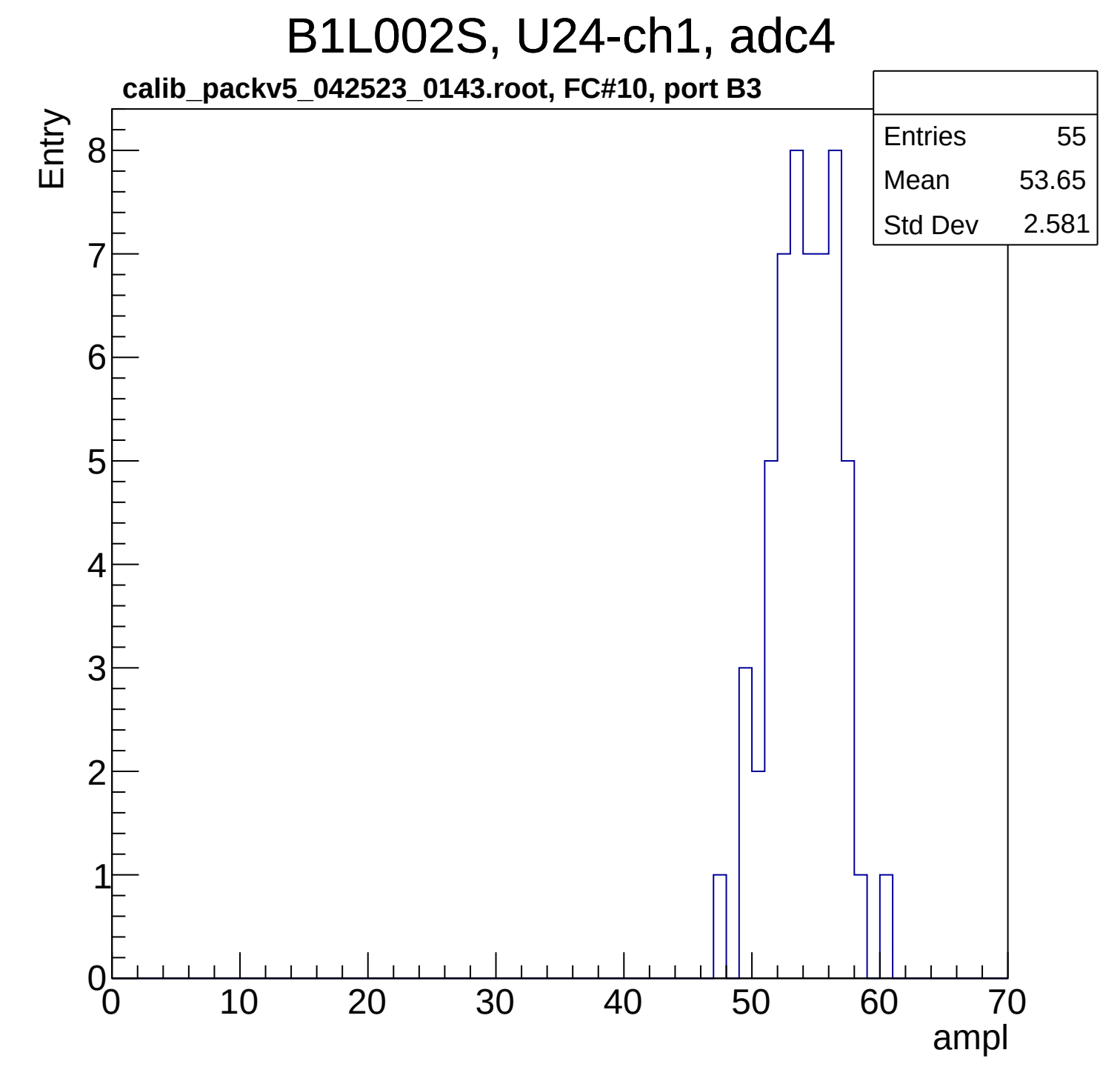

B1L002S, U24-ch1, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.43
Std Dev 2.749
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch1, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.65
Std Dev 2.581
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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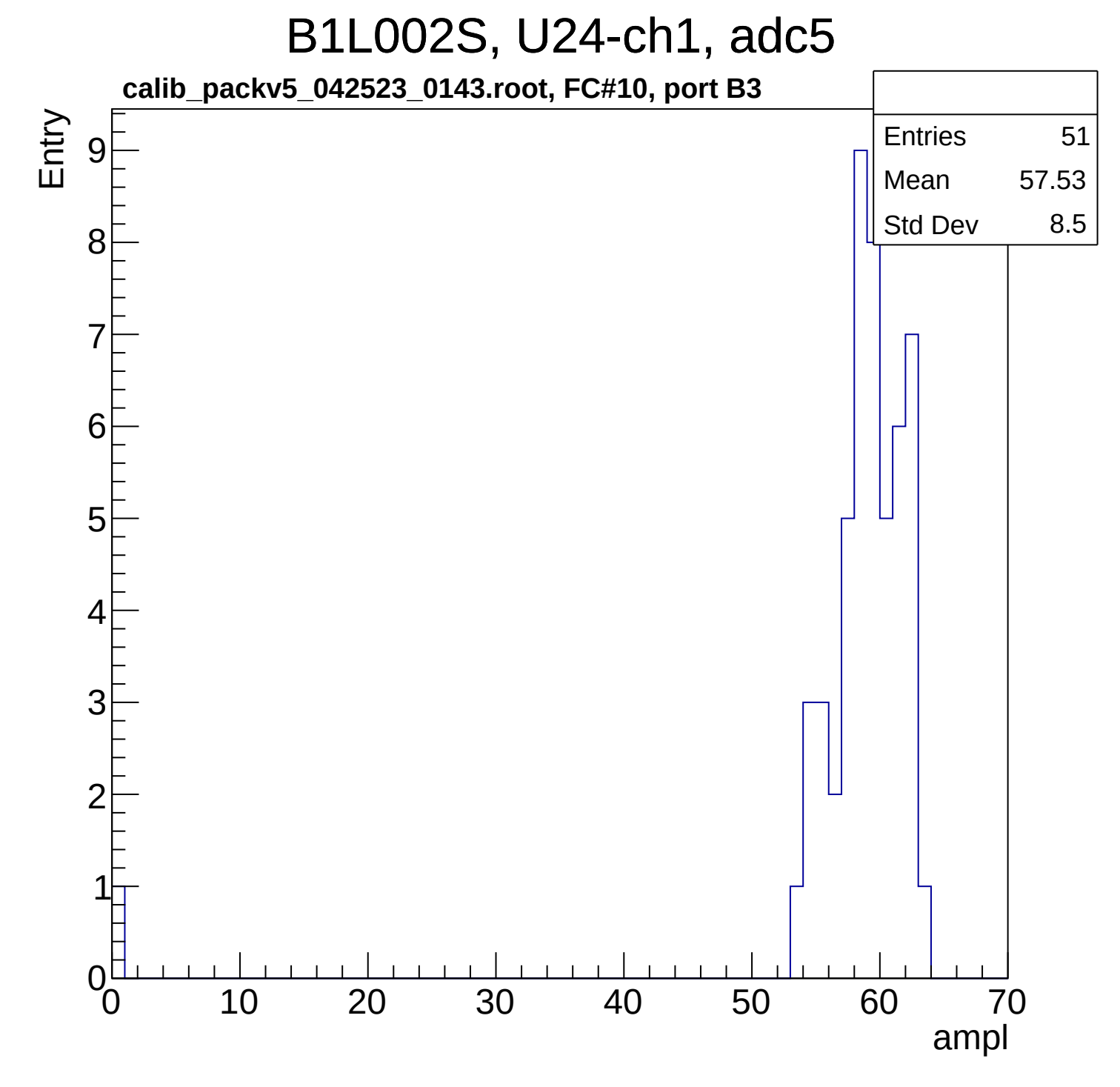
B1L002S, U24-ch1, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.53
Std Dev 8.5
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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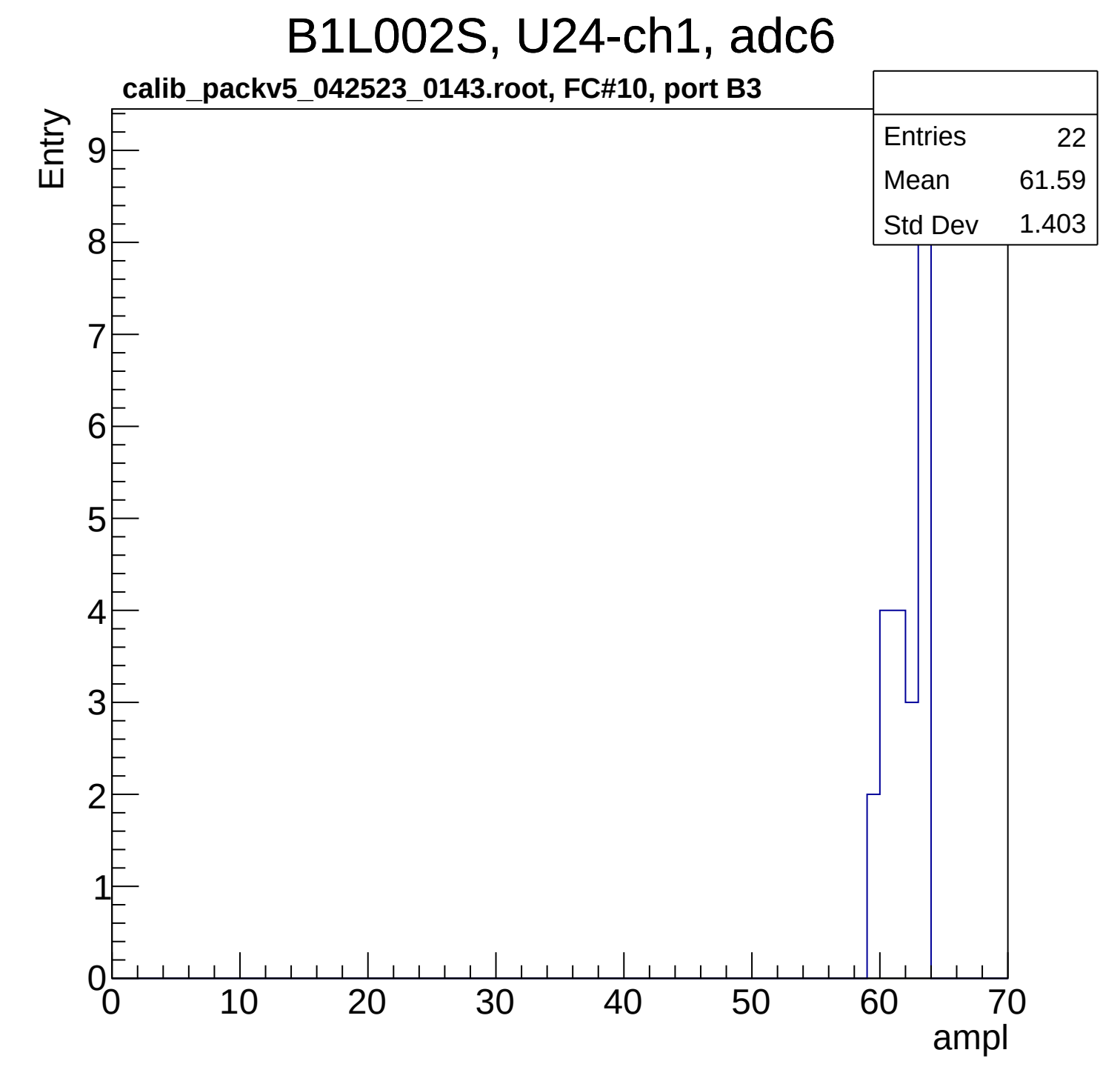
B1L002S, U24-ch1, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.59
Std Dev 1.403
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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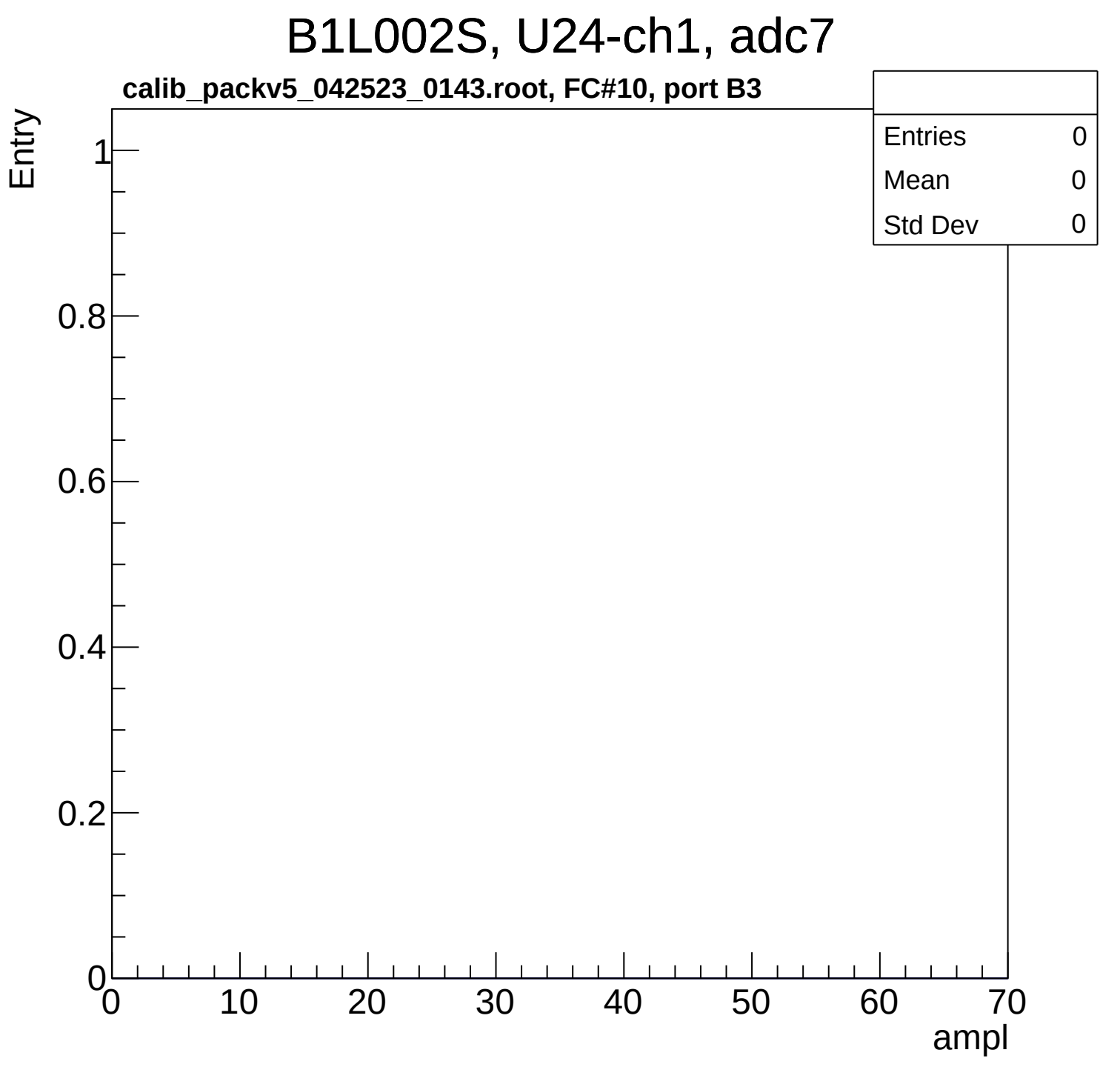
B1L002S, U24-ch1, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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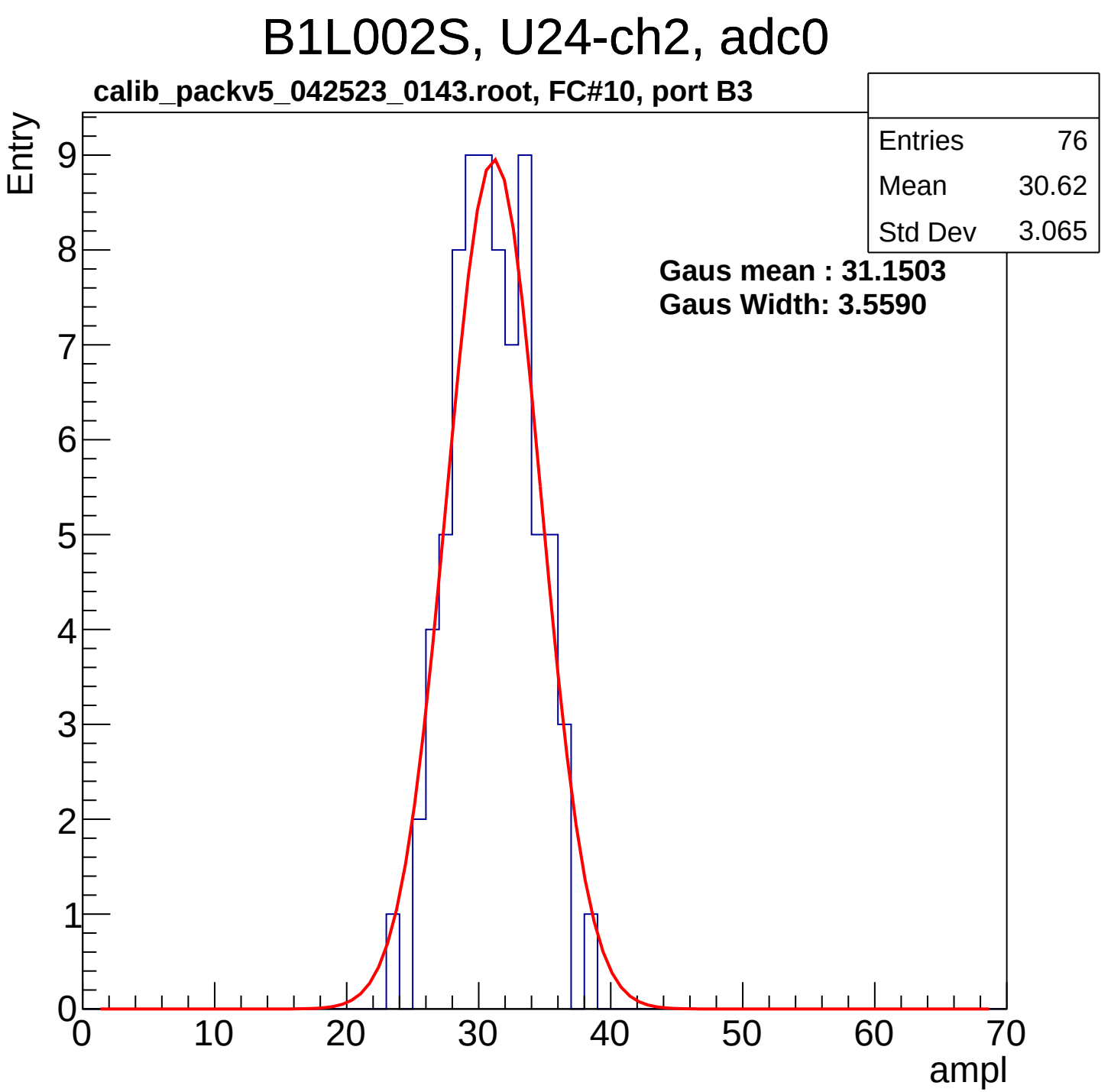
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch2, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 30.62
Std Dev 3.065
Gaus mean : 31.1503
Gaus Width: 3.5590
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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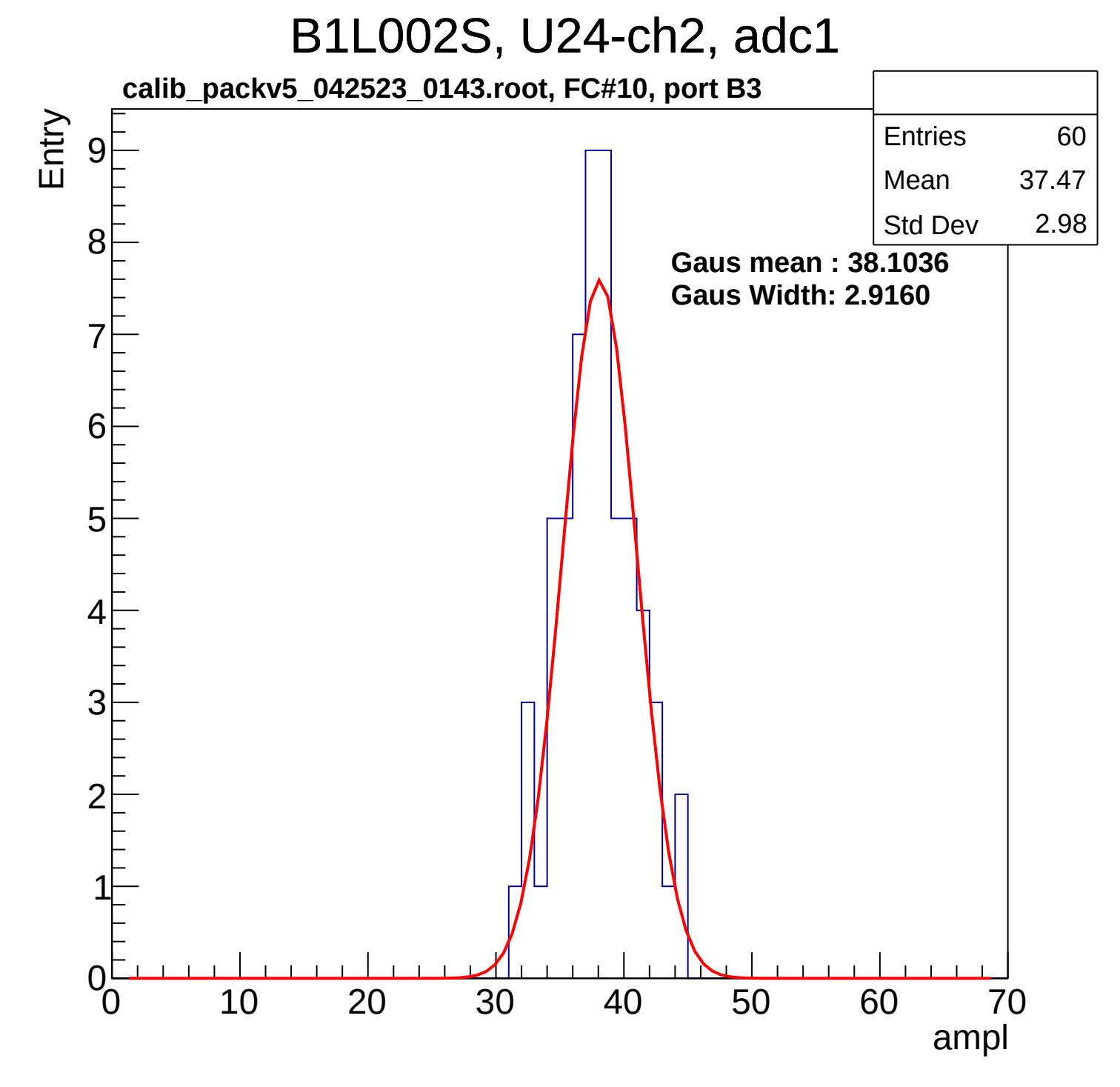
B1L002S, U24-ch2, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 37.47
Std Dev 2.98
Gaus mean : 38.1036
Gaus Width: 2.9160
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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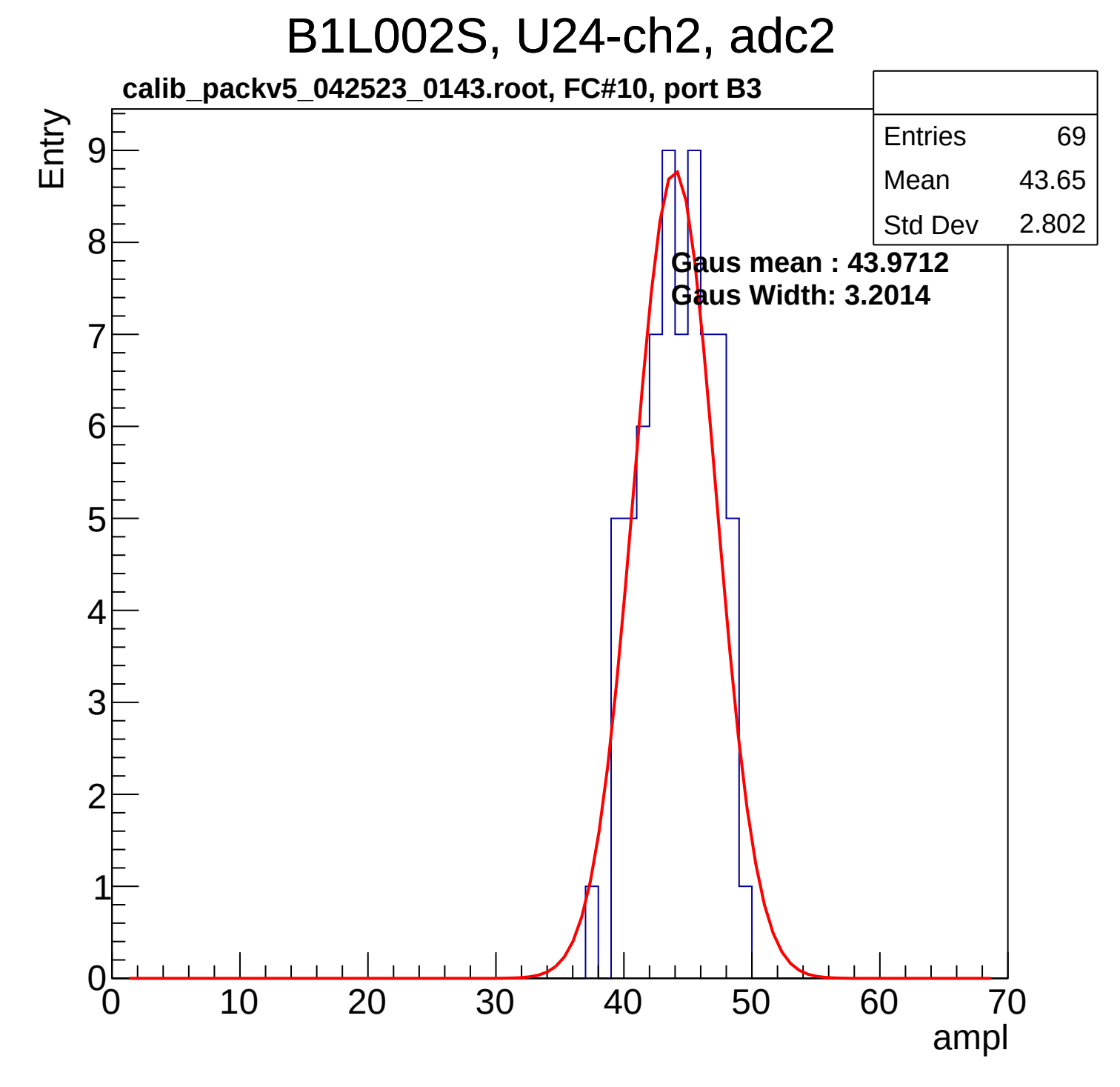
B1L002S, U24-ch2, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 43.65
Std Dev 2.802
Gaus mean : 43.9712
Gaus Width: 3.2014
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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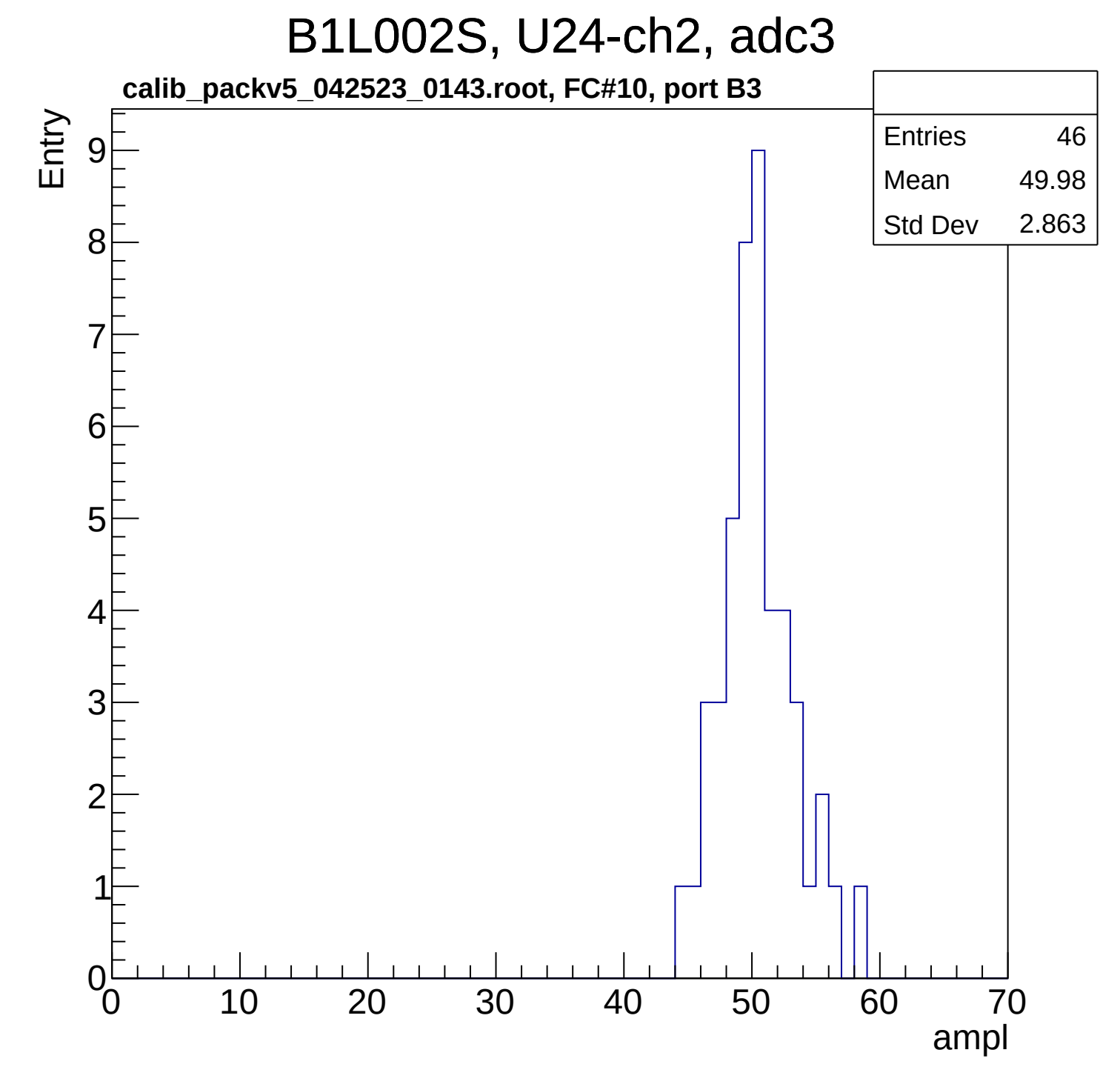
B1L002S, U24-ch2, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 49.98
Std Dev 2.863
Entry
ampl
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5
6
7
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0
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60
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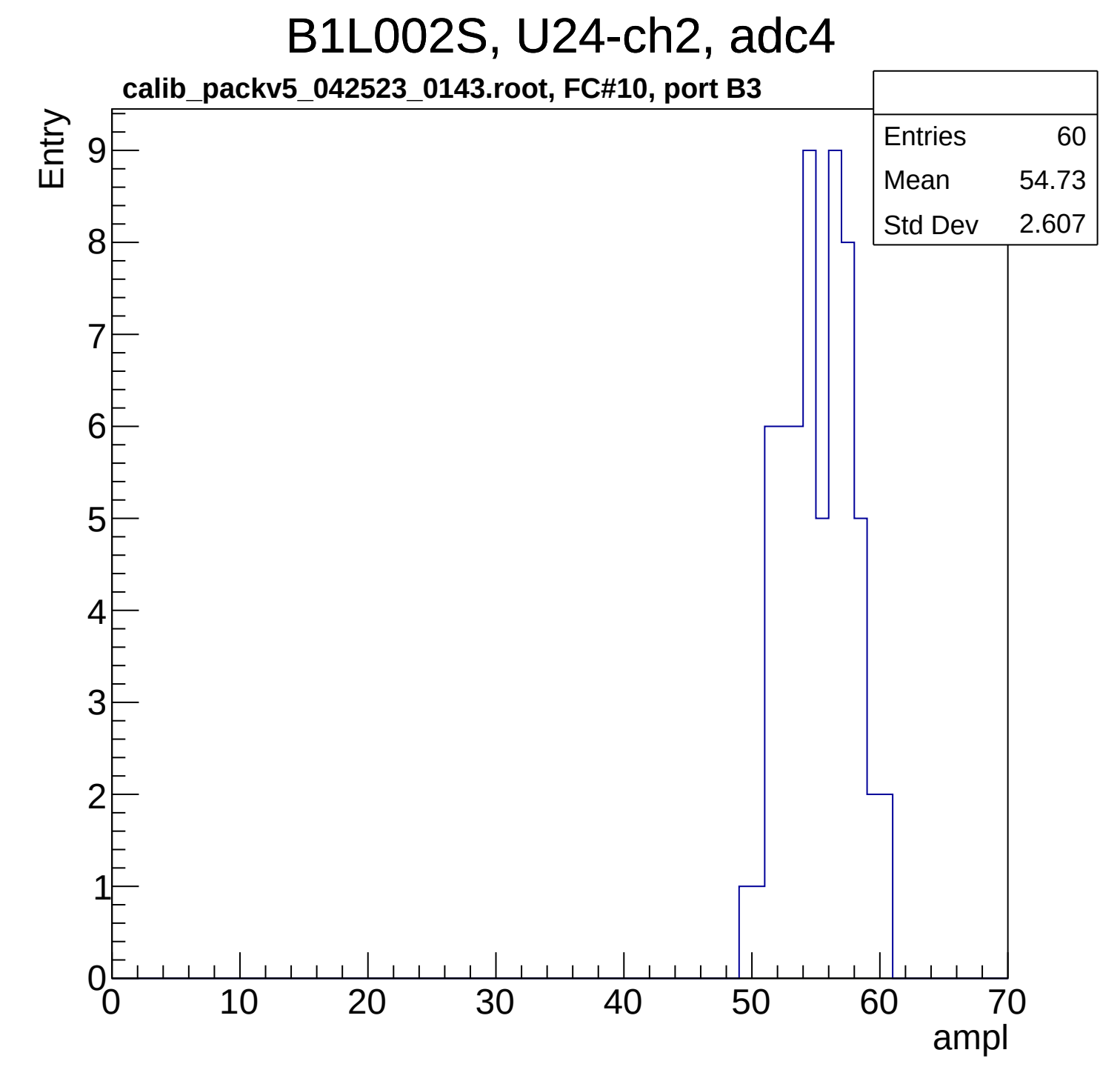
B1L002S, U24-ch2, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.73
Std Dev 2.607
Entry
ampl
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0
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60
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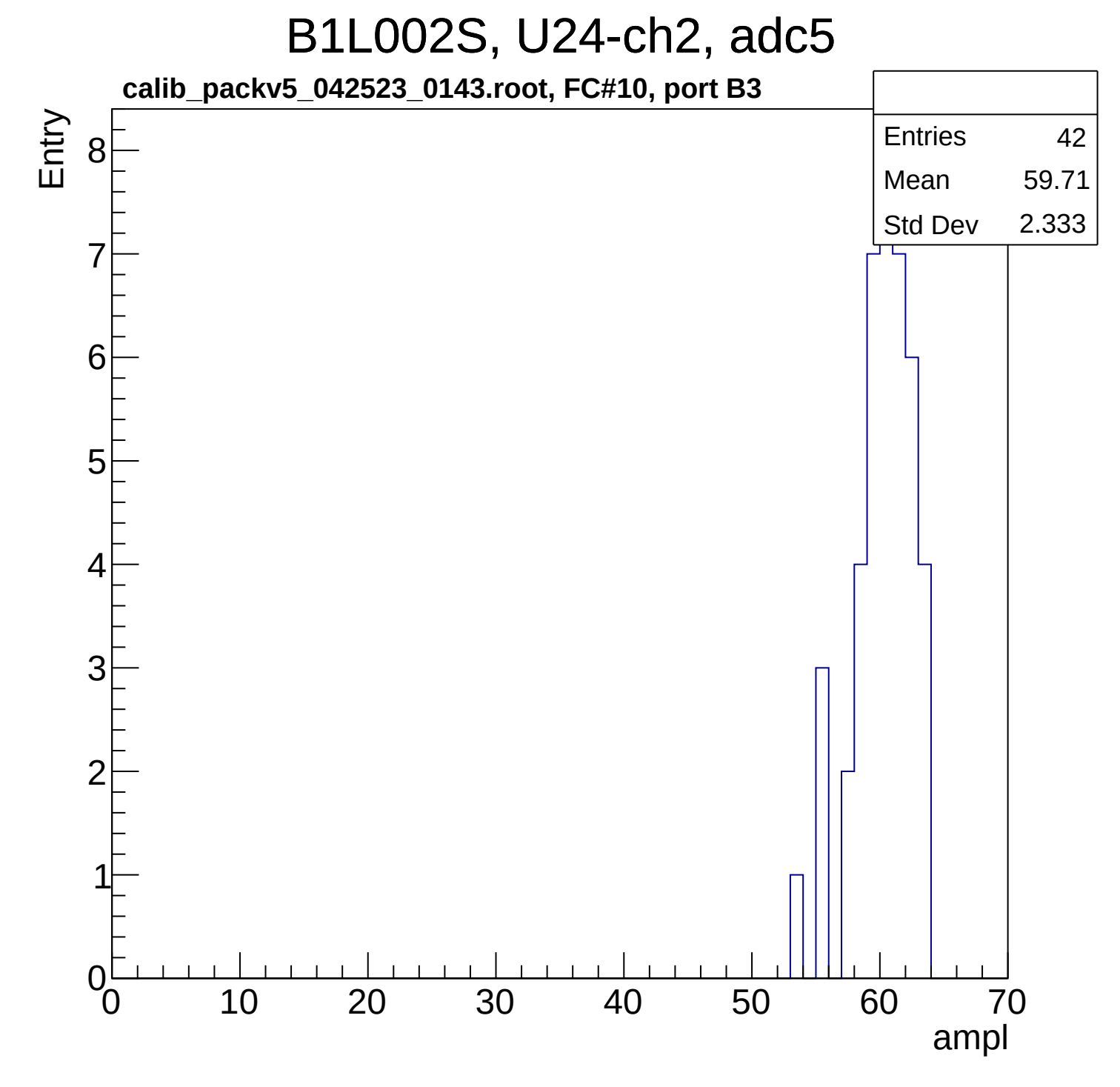
B1L002S, U24-ch2, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.71
Std Dev 2.333
Entry
ampl
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2
3
4
5
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7
8
0
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20
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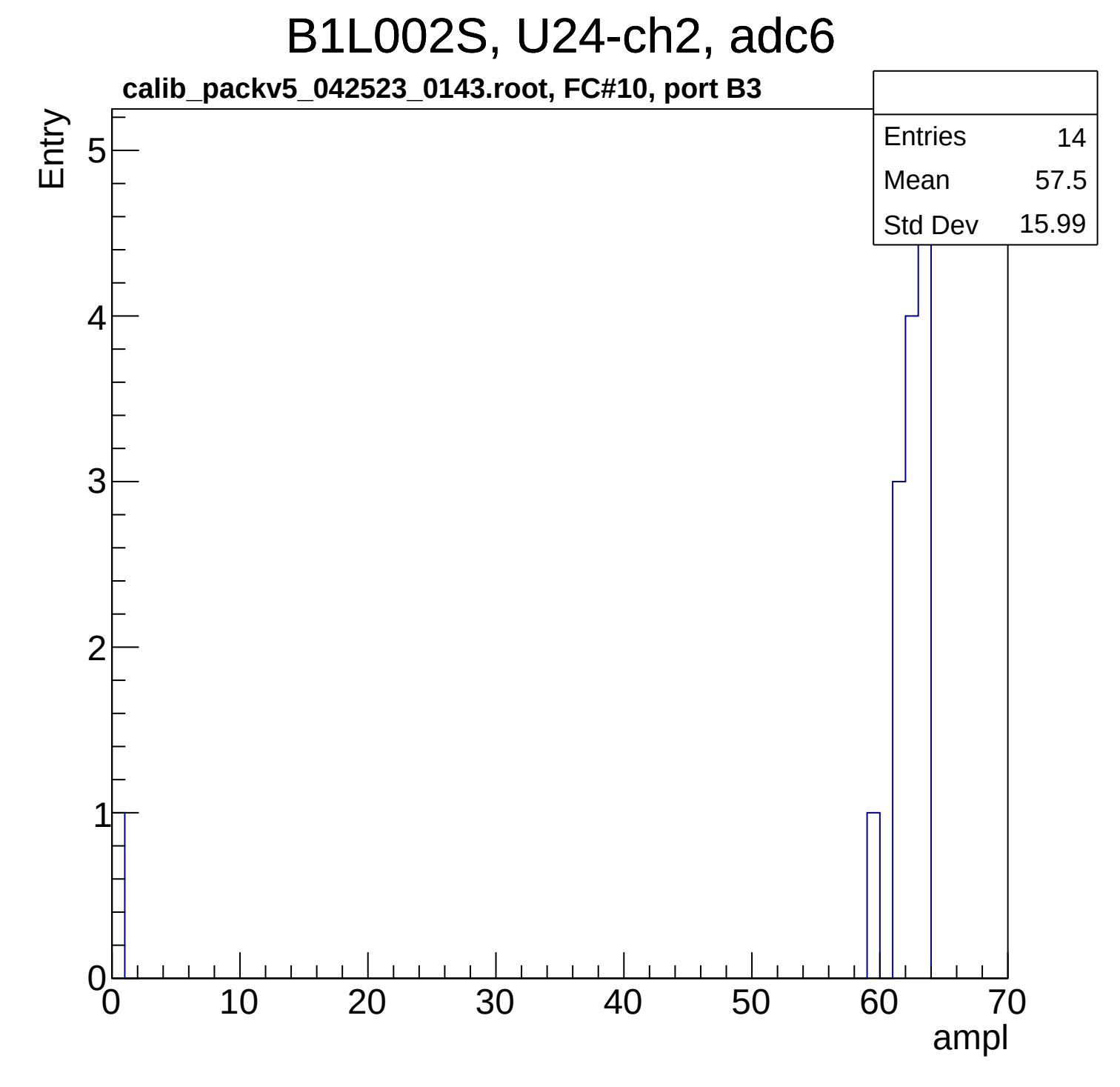
B1L002S, U24-ch2, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.5
Std Dev 15.99
Entry
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2
3
4
5
0
10
20
30
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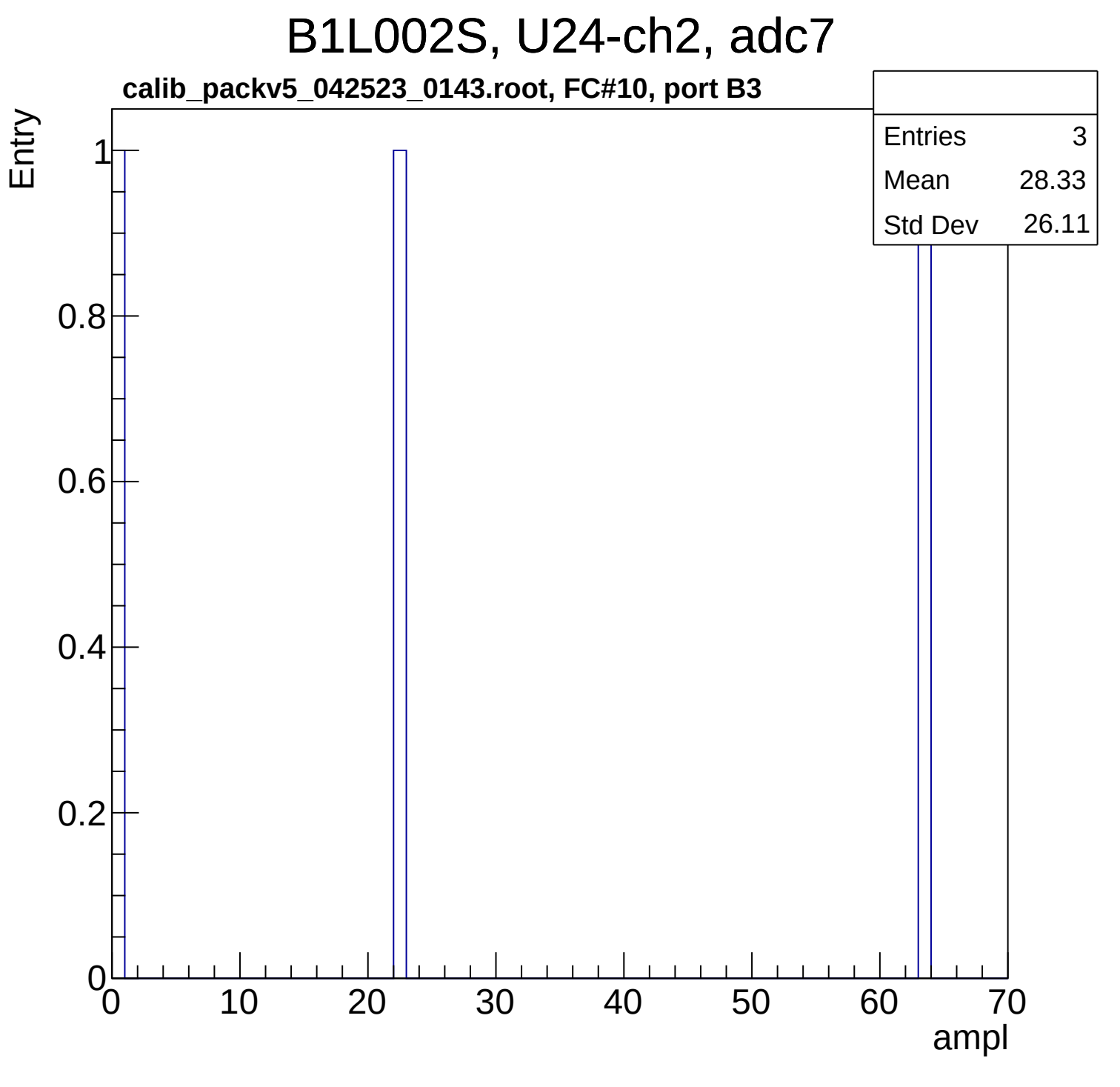
50
60
70
ampl


B1L002S, U24-ch2, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 28.33
Std Dev 26.11
Entry
ampl
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0.2
0.4
0.6
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0
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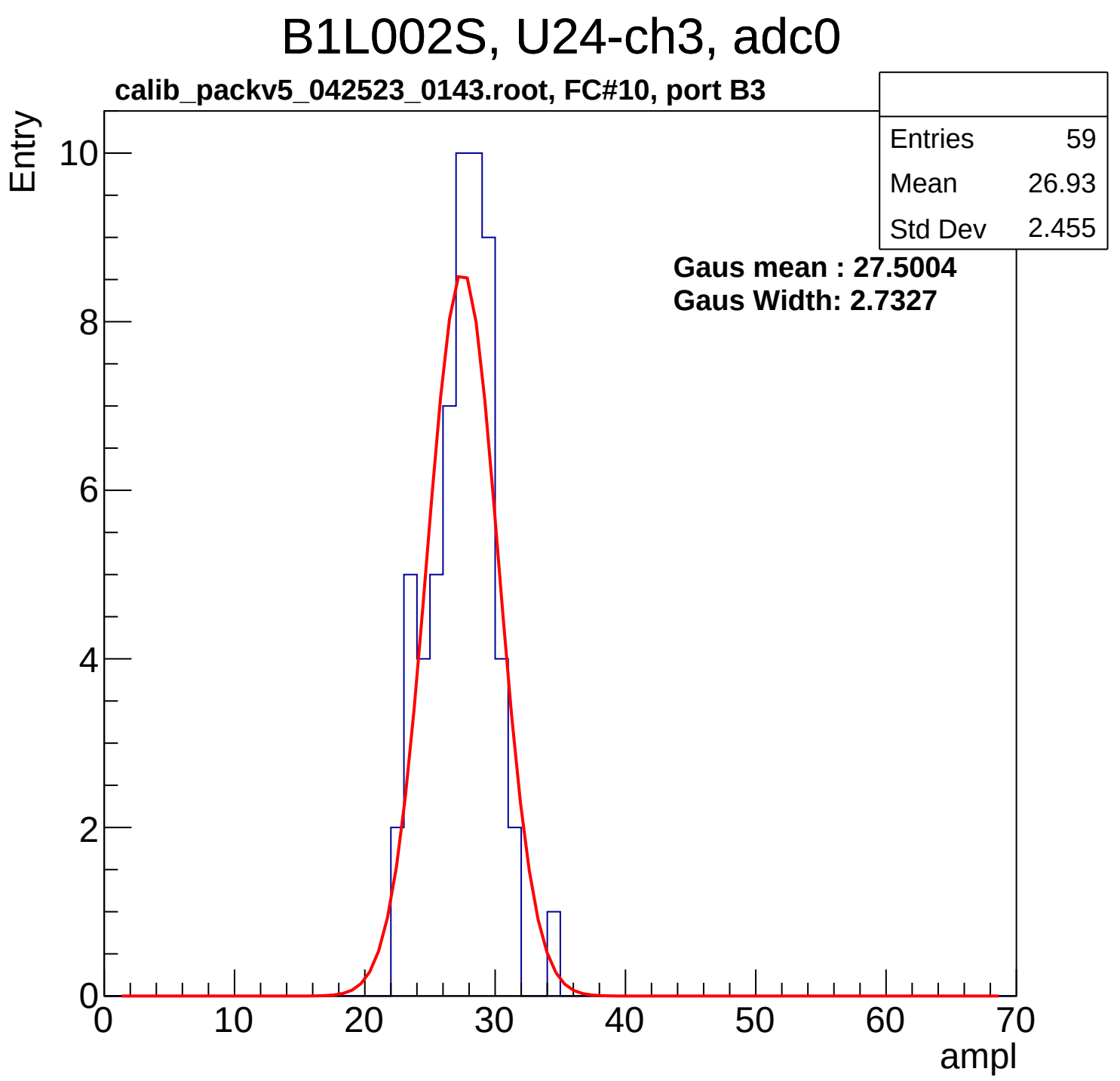
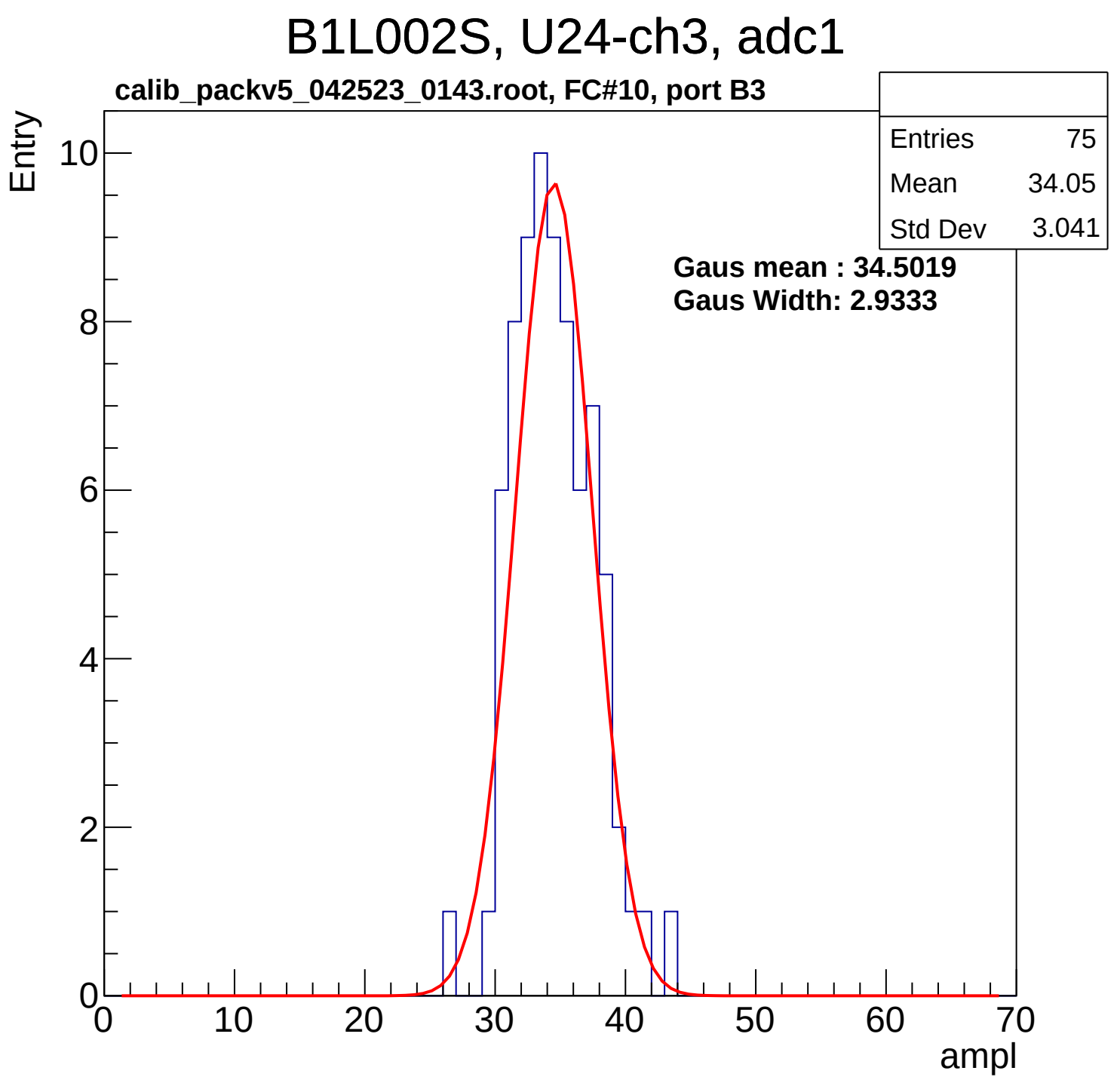
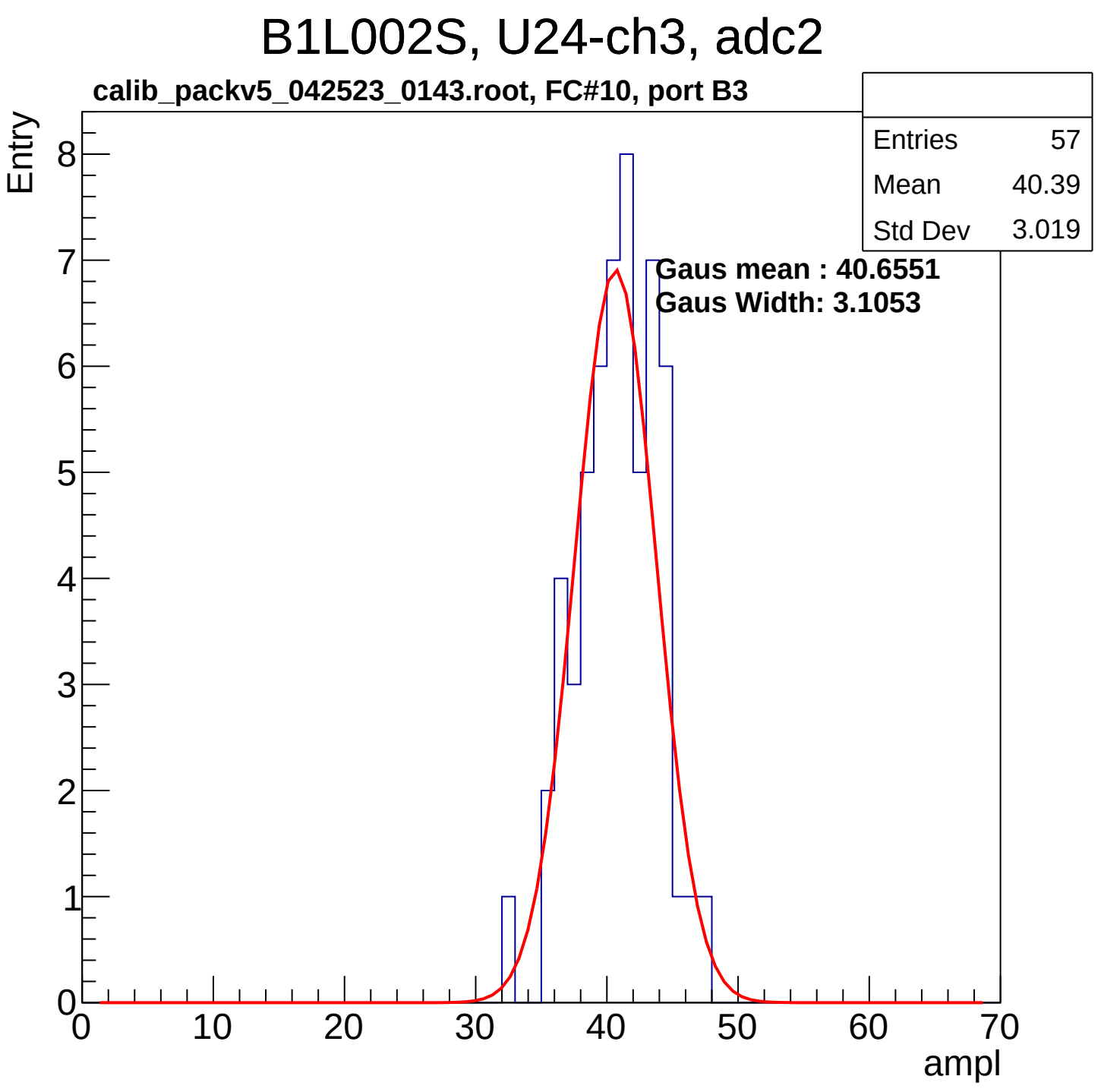

B1L002S, U24-ch3, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.93
Std Dev 2.455
Gaus mean : 27.5004
Gaus Width: 2.7327
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch3, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 34.05
Std Dev 3.041
Gaus mean : 34.5019
Gaus Width: 2.9333
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch3, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 40.39
Std Dev 3.019
Gaus mean : 40.6551
Gaus Width: 3.1053
Entry
ampl
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2
3
4
5
6
7
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0
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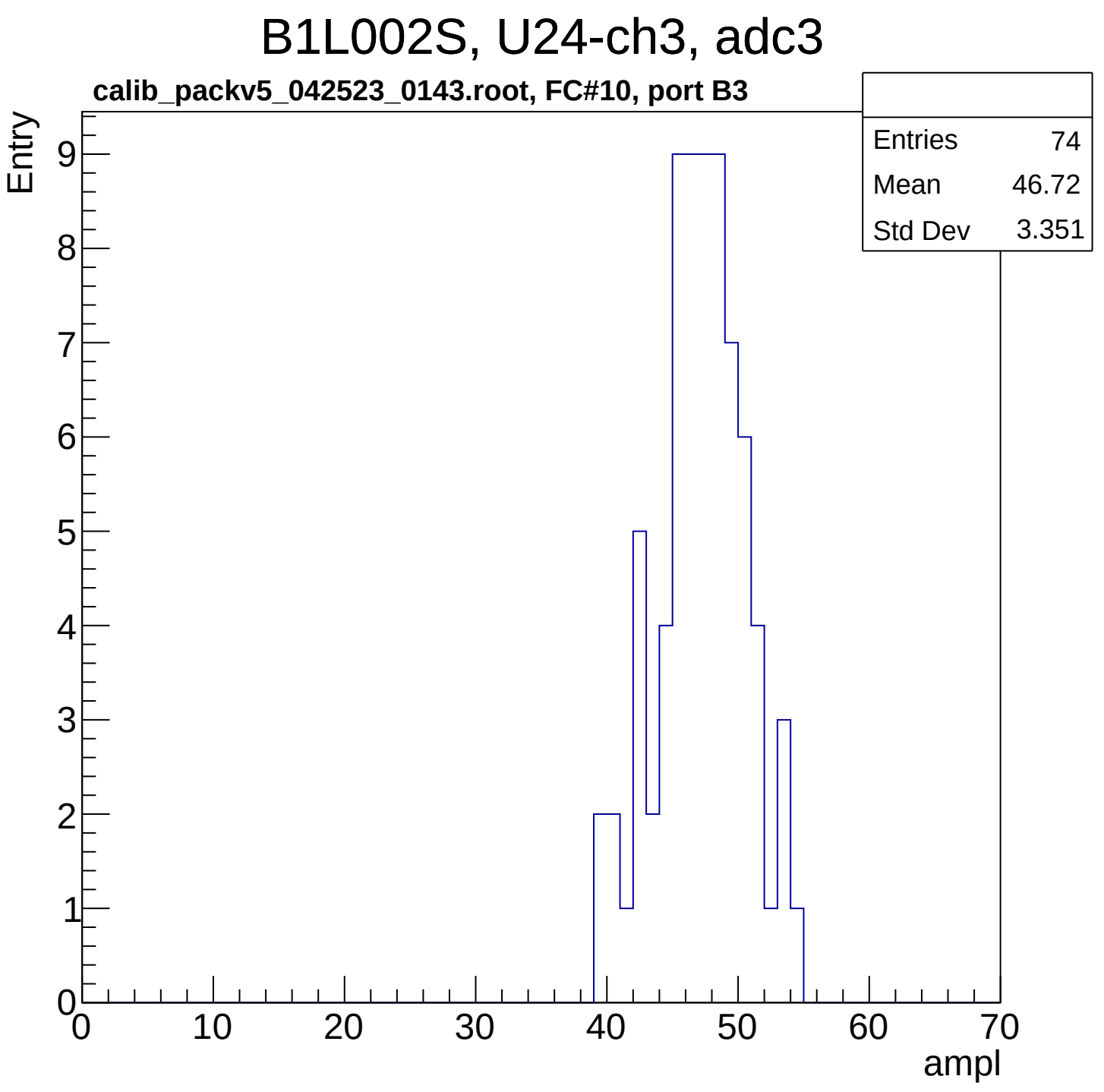
B1L002S, U24-ch3, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.72
Std Dev 3.351
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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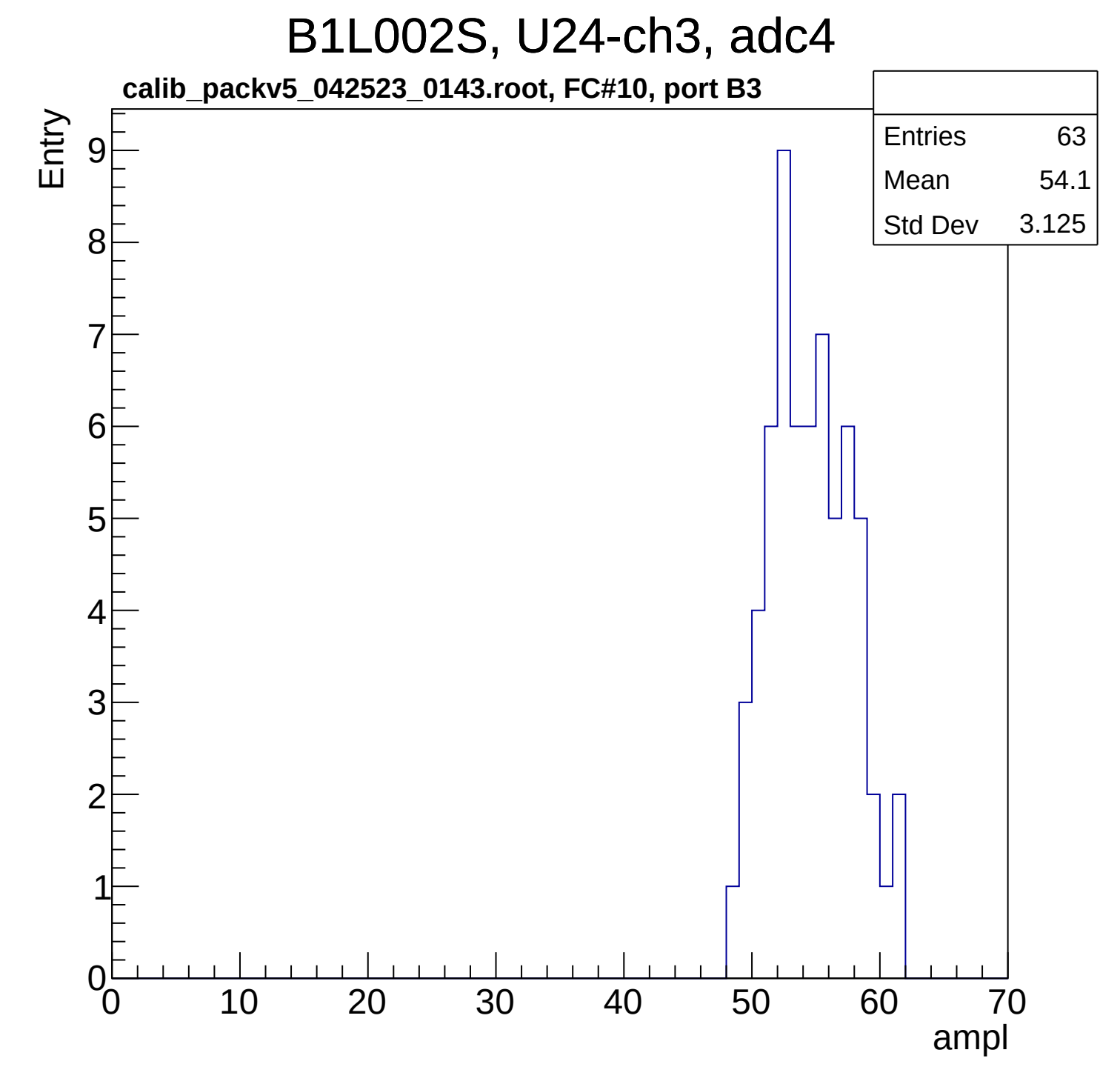
40
50
60
70


B1L002S, U24-ch3, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 54.1
Std Dev 3.125
Entry
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3
4
5
6
7
8
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10
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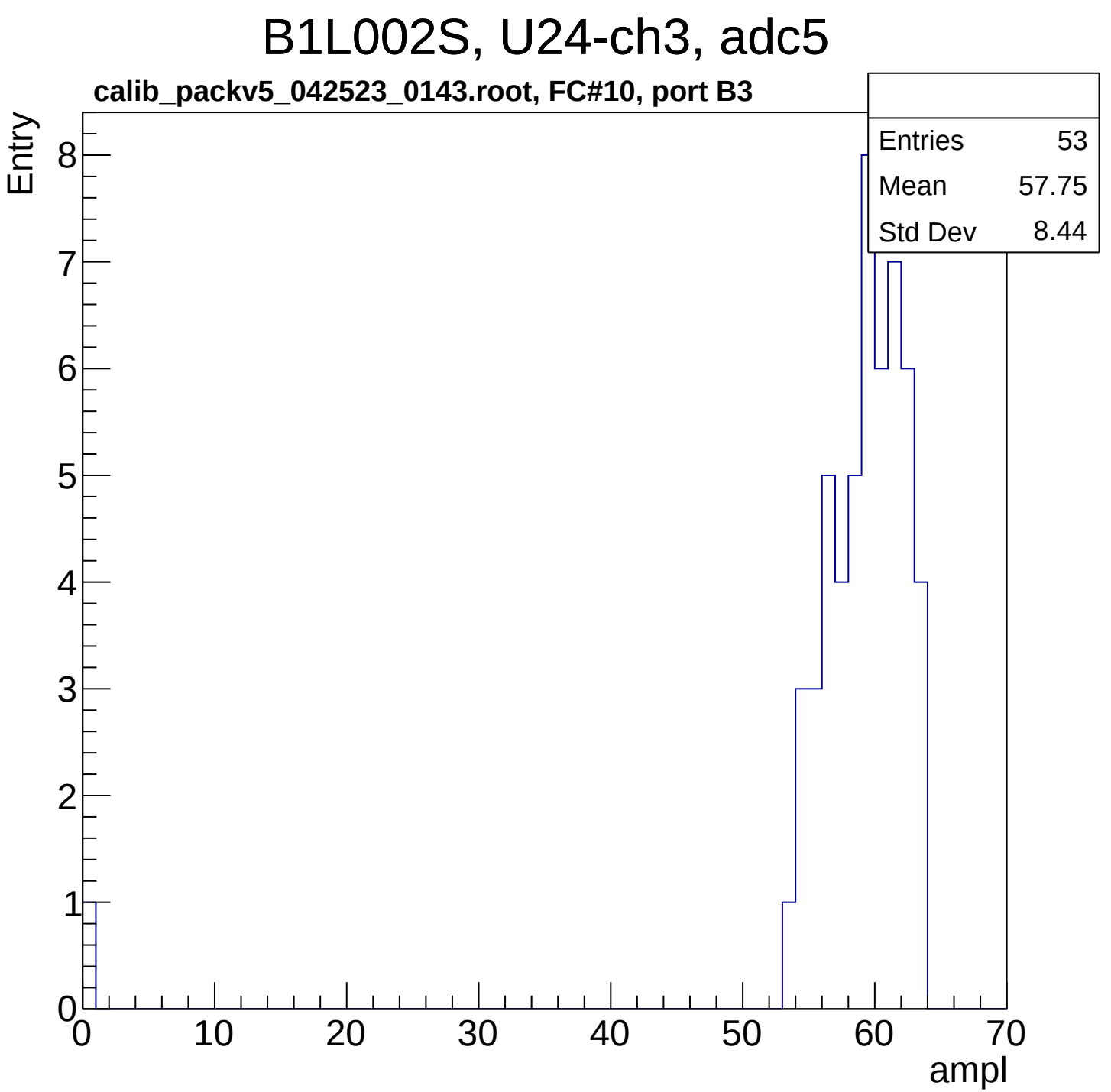
50
60
70
ampl


B1L002S, U24-ch3, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.75
Std Dev 8.44
Entry
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3
4
5
6
7
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20
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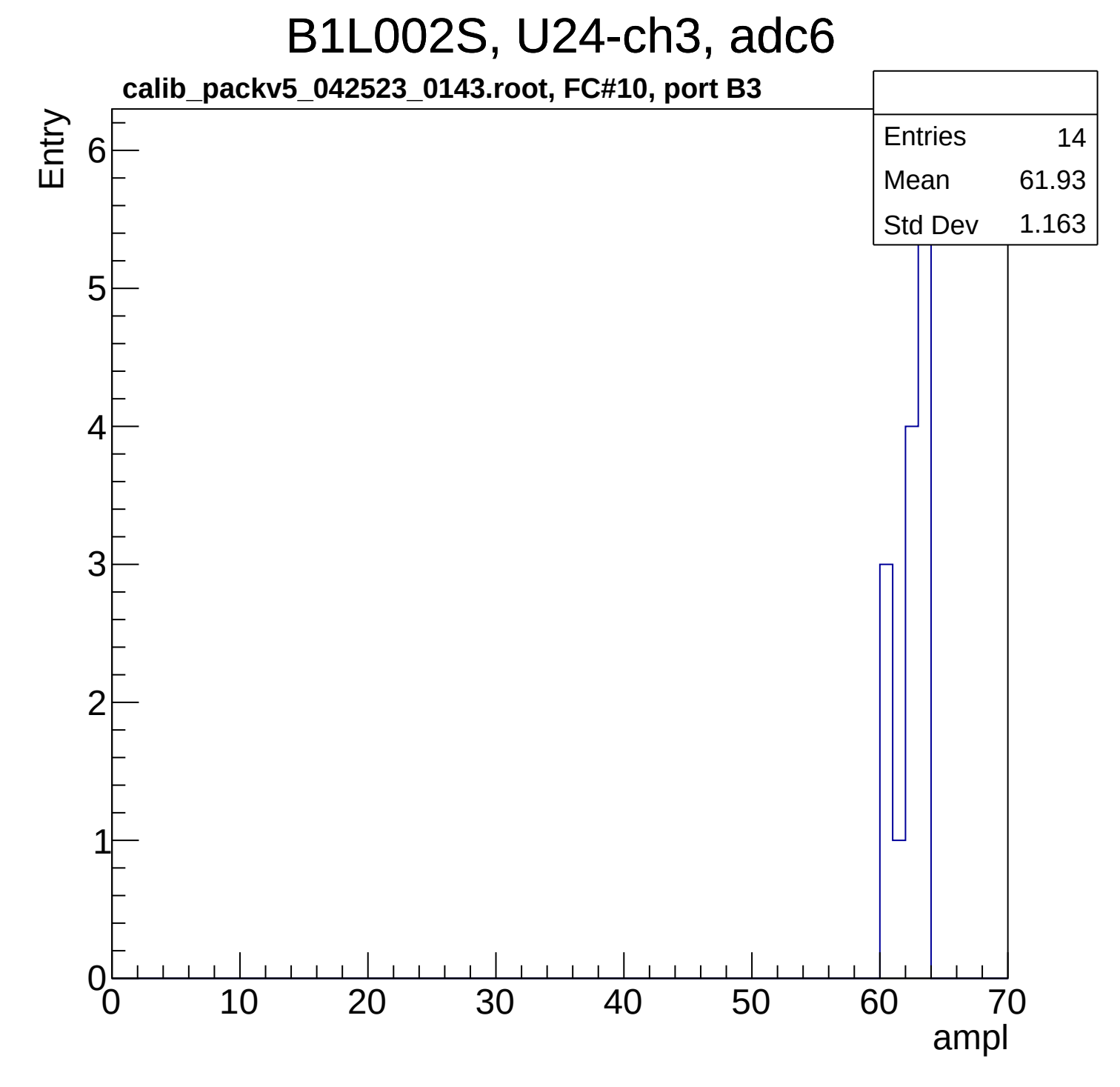
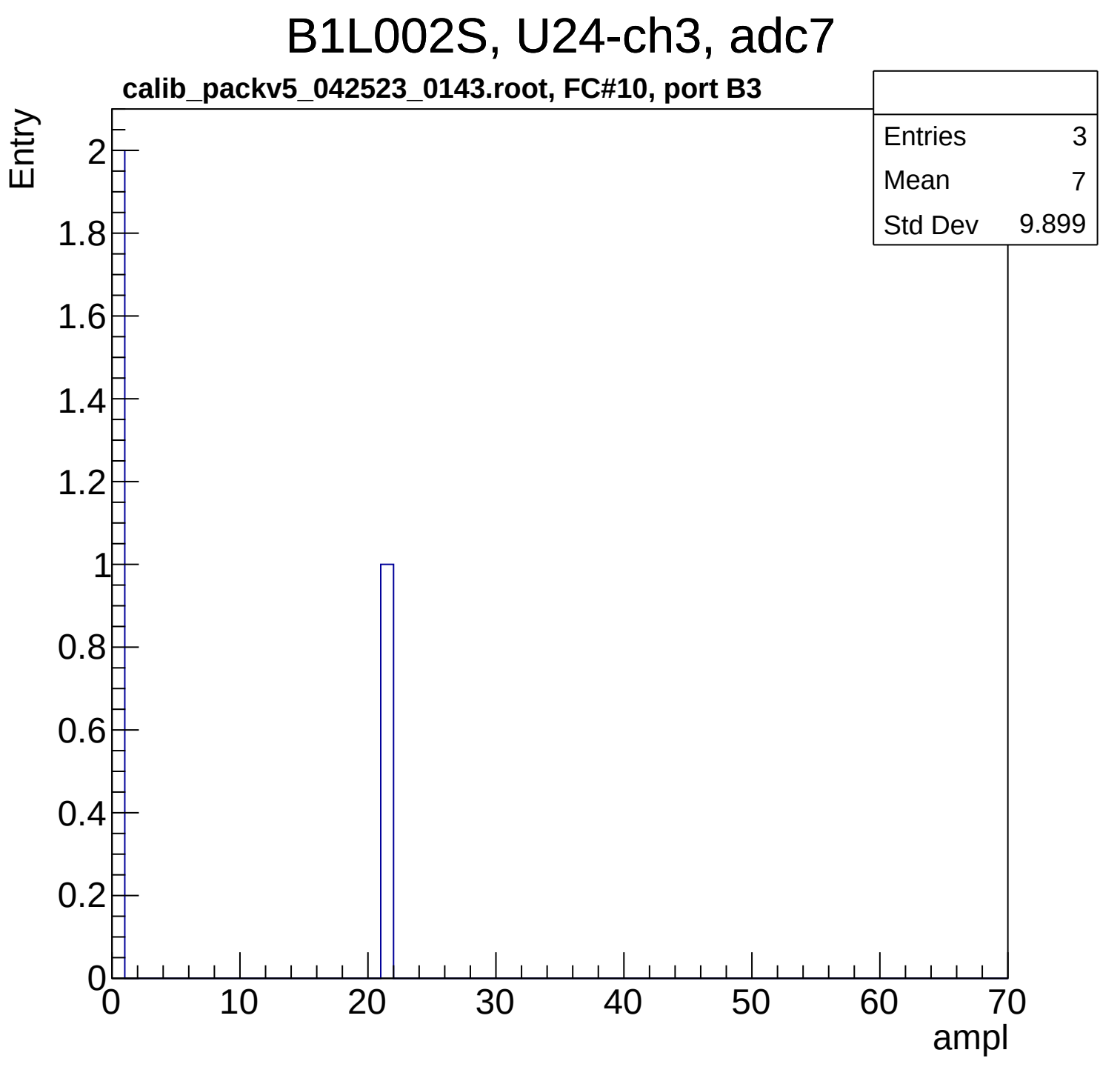
50
60
70
ampl


B1L002S, U24-ch3, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 61.93
Std Dev 1.163
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch3, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7
Std Dev 9.899
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
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60
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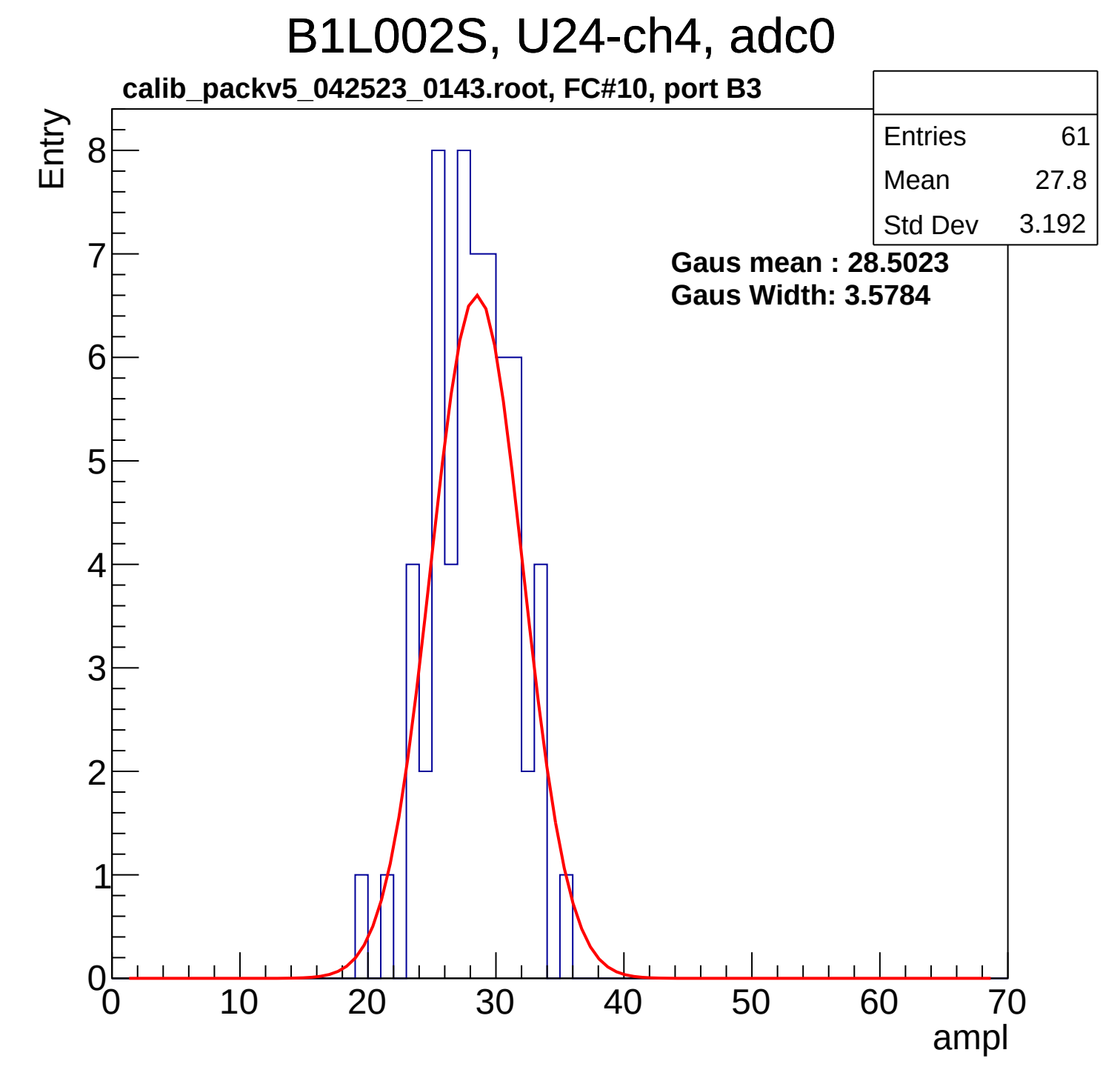
B1L002S, U24-ch4, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.8
Std Dev 3.192
Gaus mean : 28.5023
Gaus Width: 3.5784
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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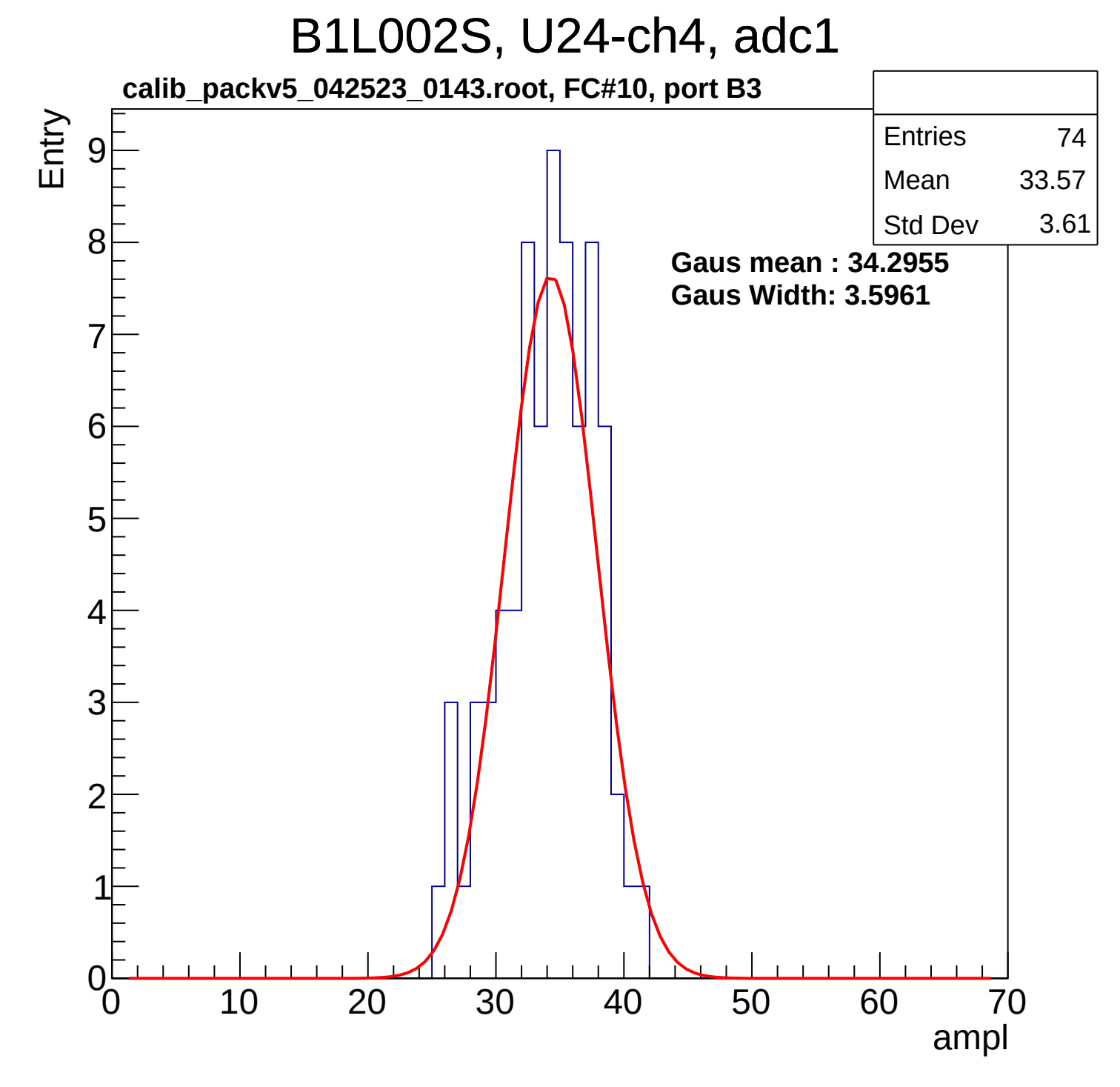
B1L002S, U24-ch4, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.57
Std Dev 3.61
Gaus mean : 34.2955
Gaus Width: 3.5961
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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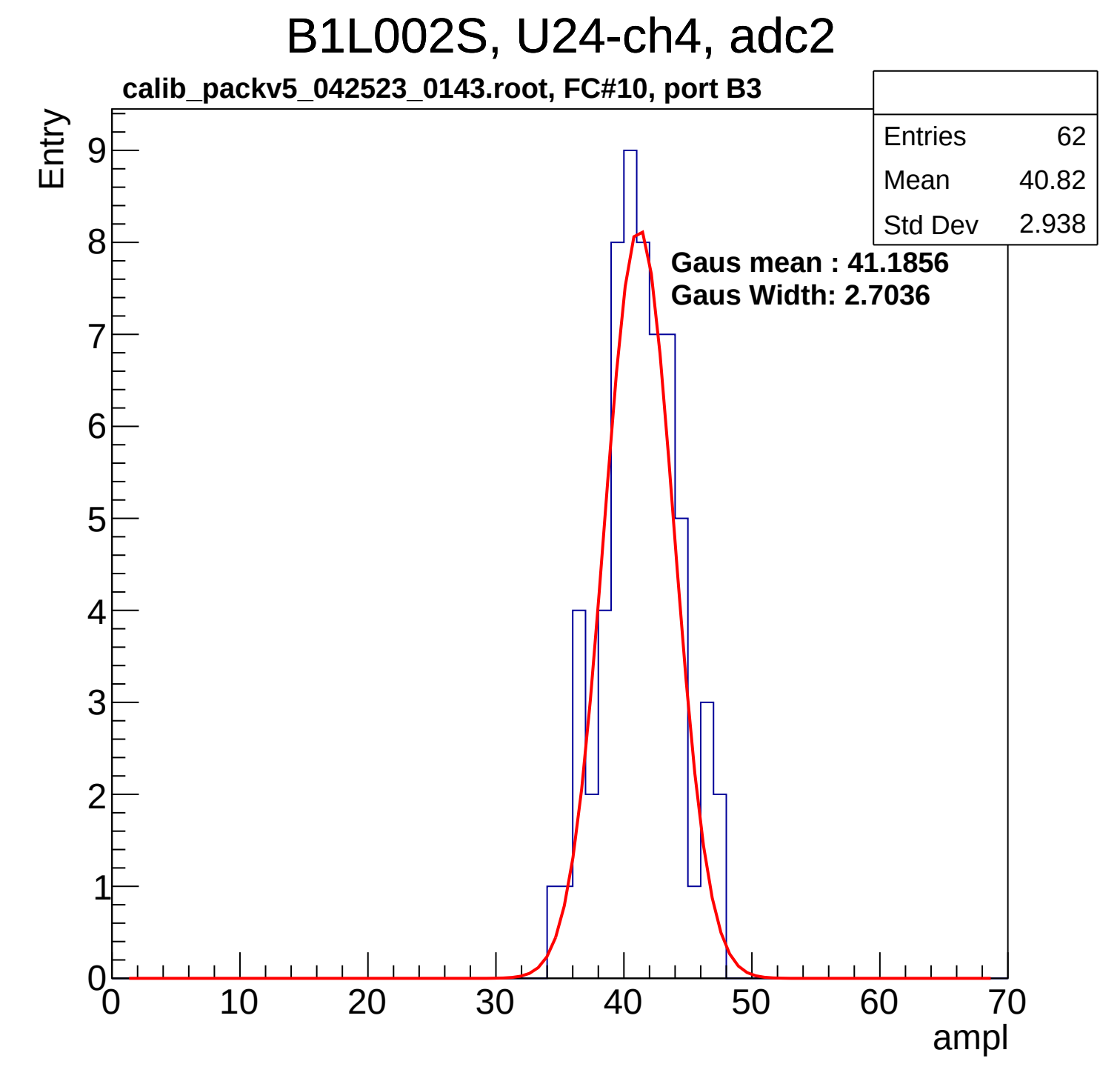
B1L002S, U24-ch4, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.82
Std Dev 2.938
Gaus mean : 41.1856
Gaus Width: 2.7036
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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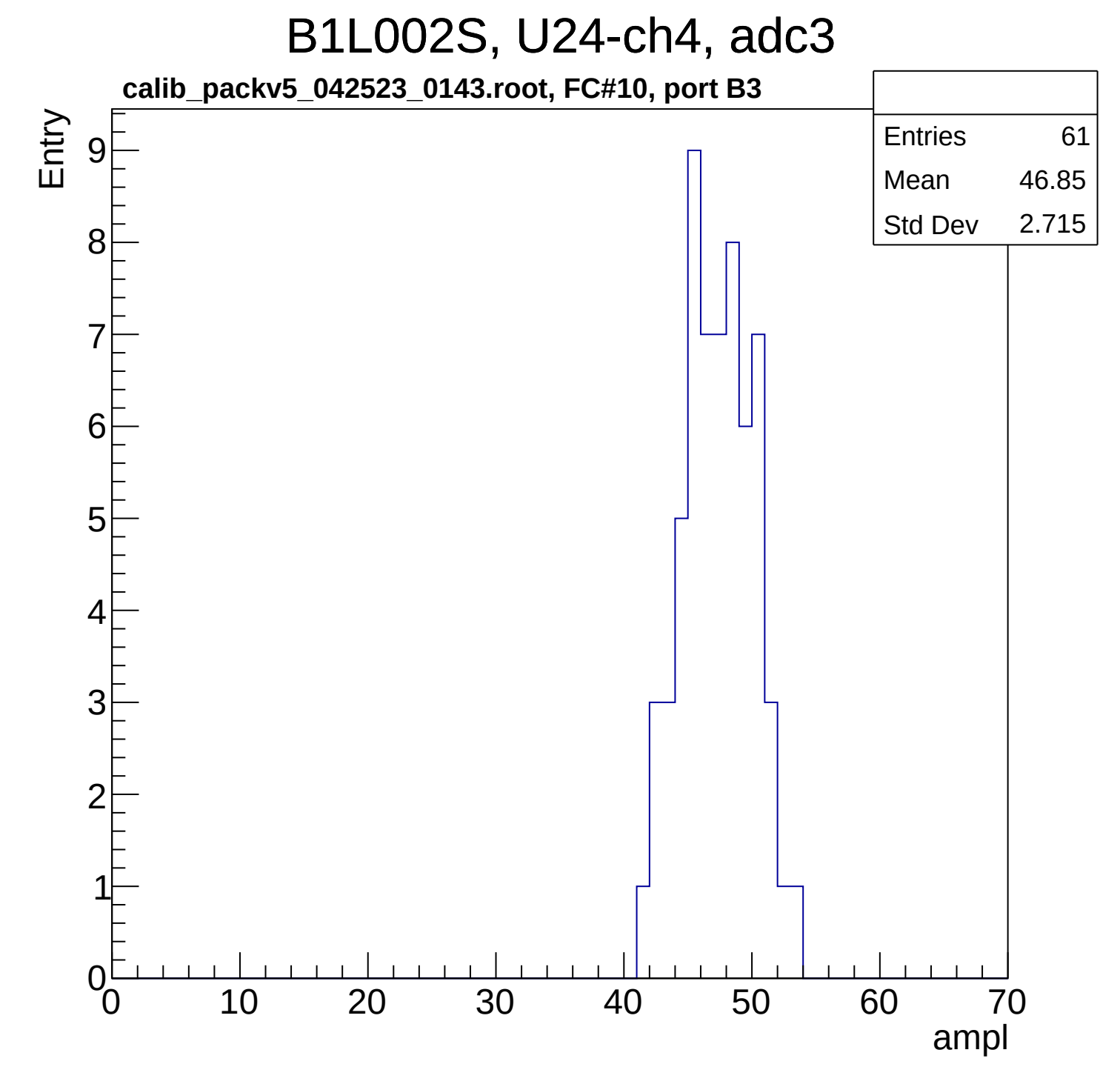
B1L002S, U24-ch4, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 46.85
Std Dev 2.715
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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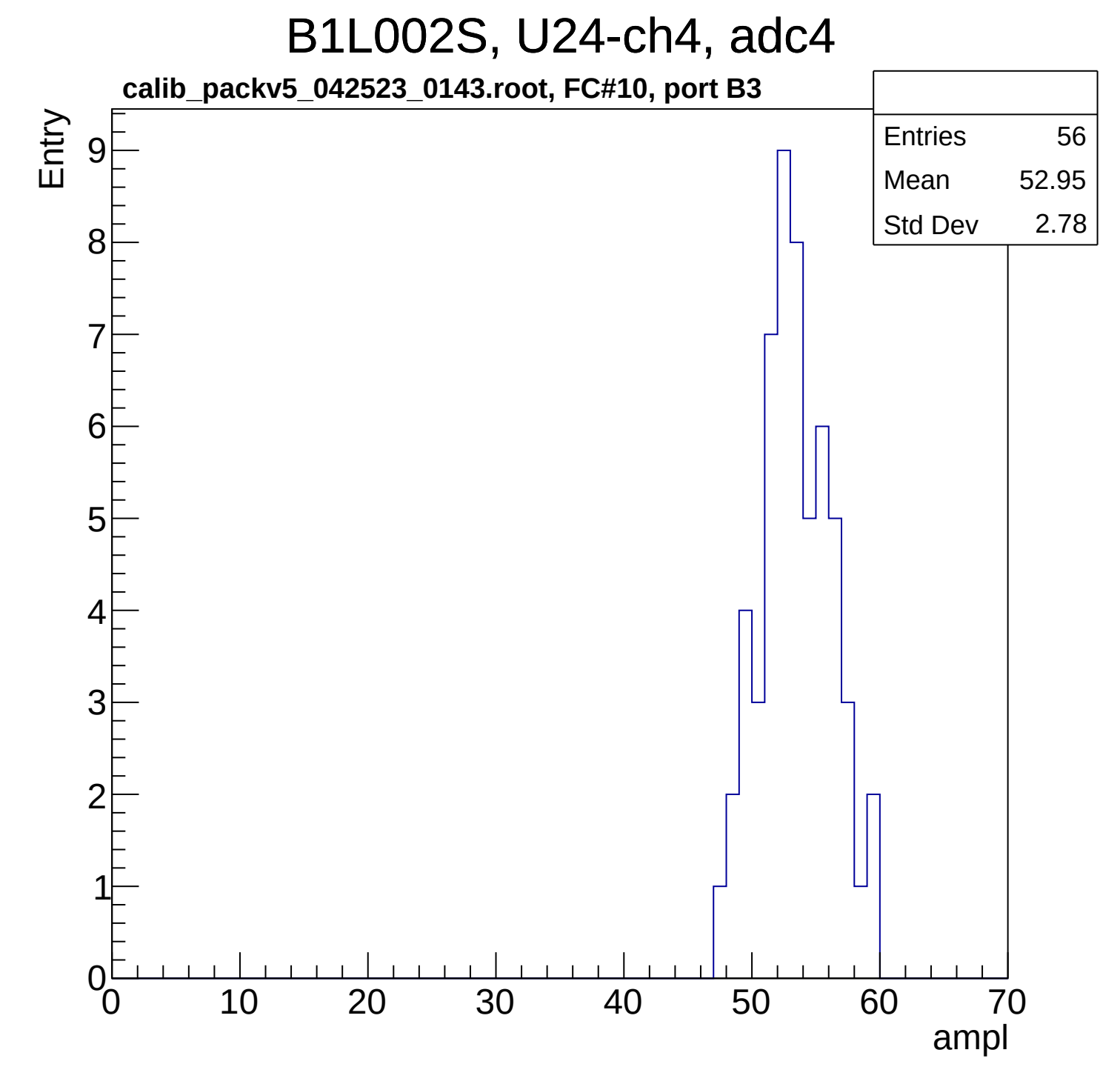
B1L002S, U24-ch4, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 52.95
Std Dev 2.78
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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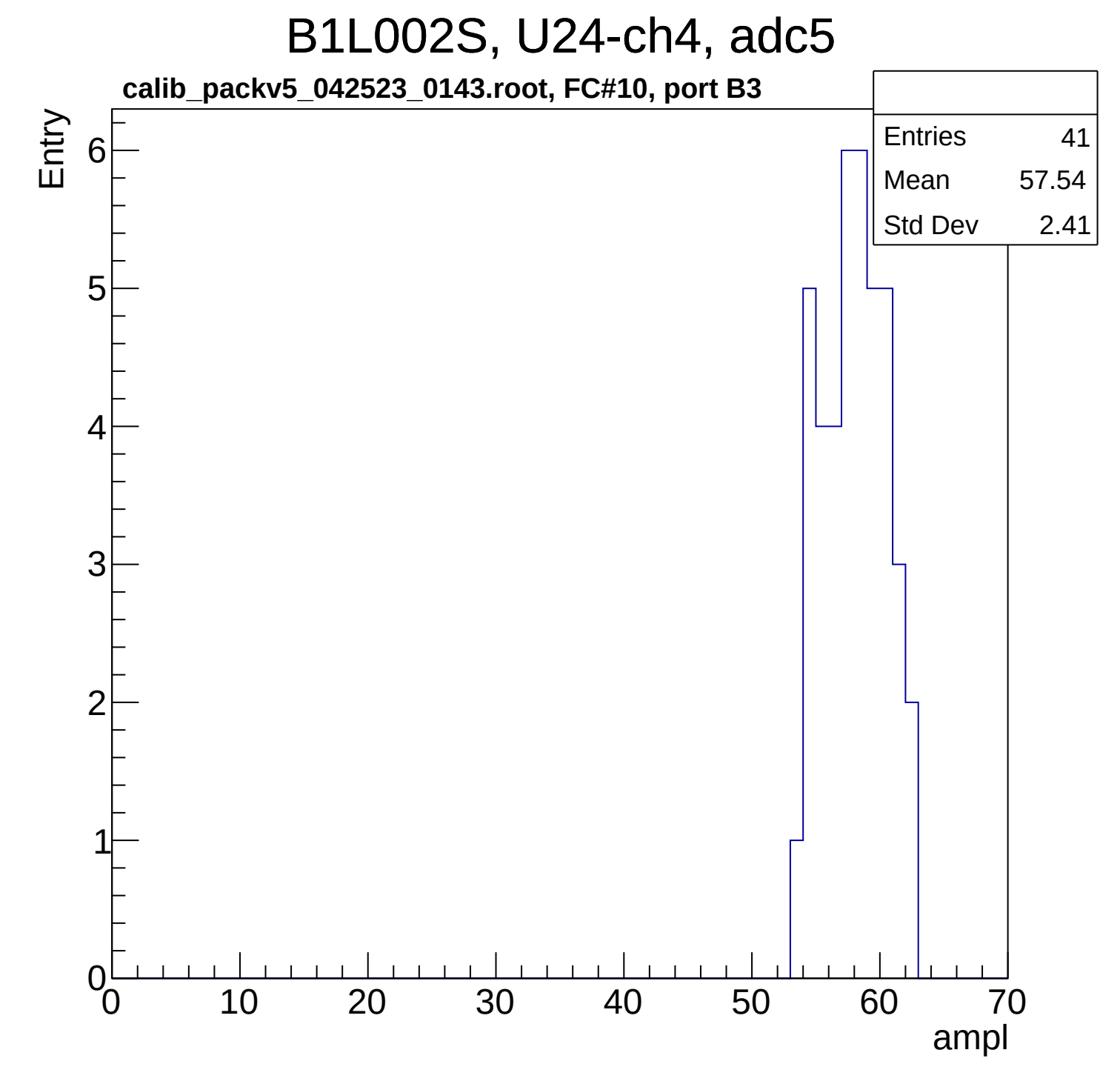
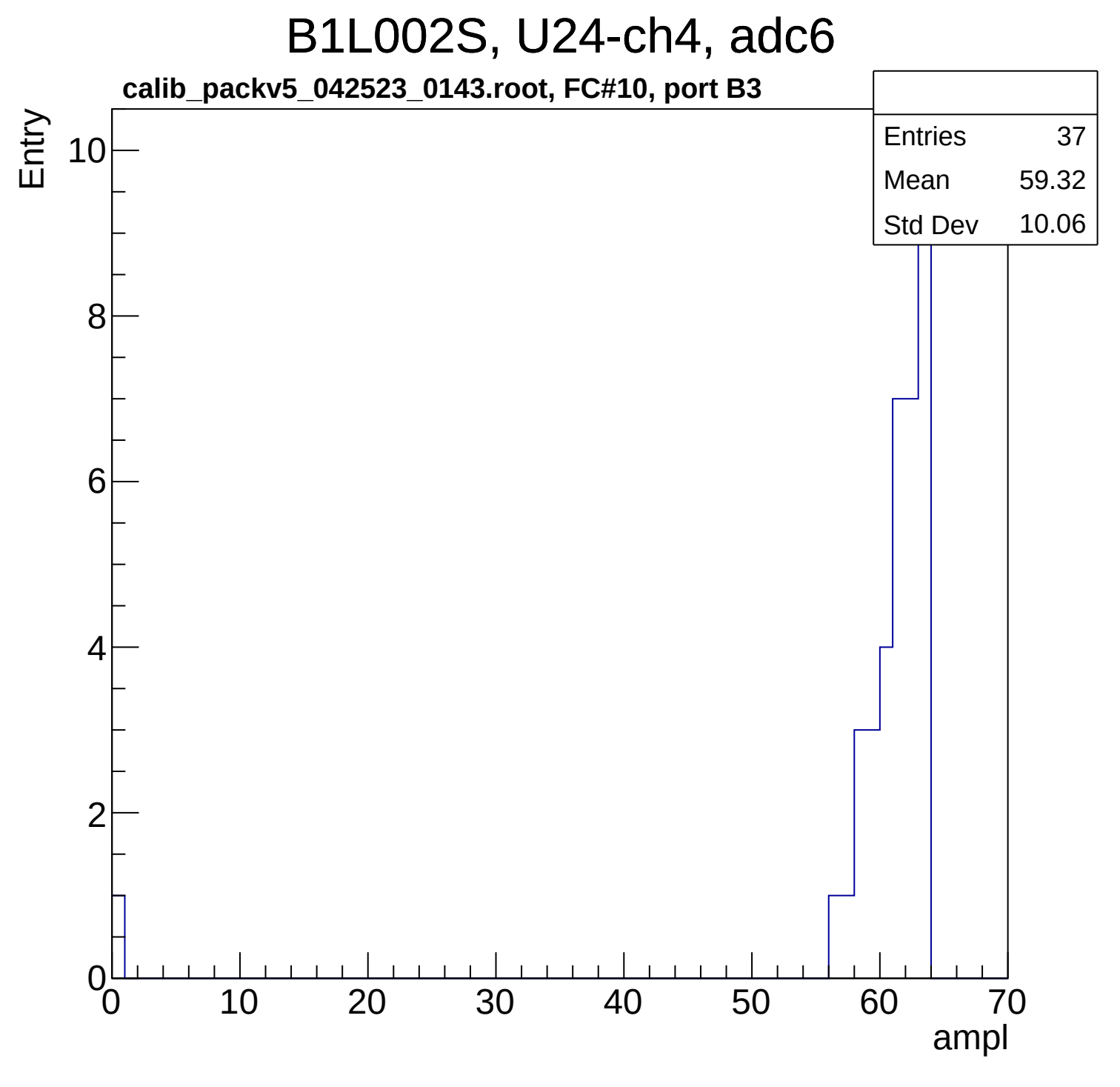
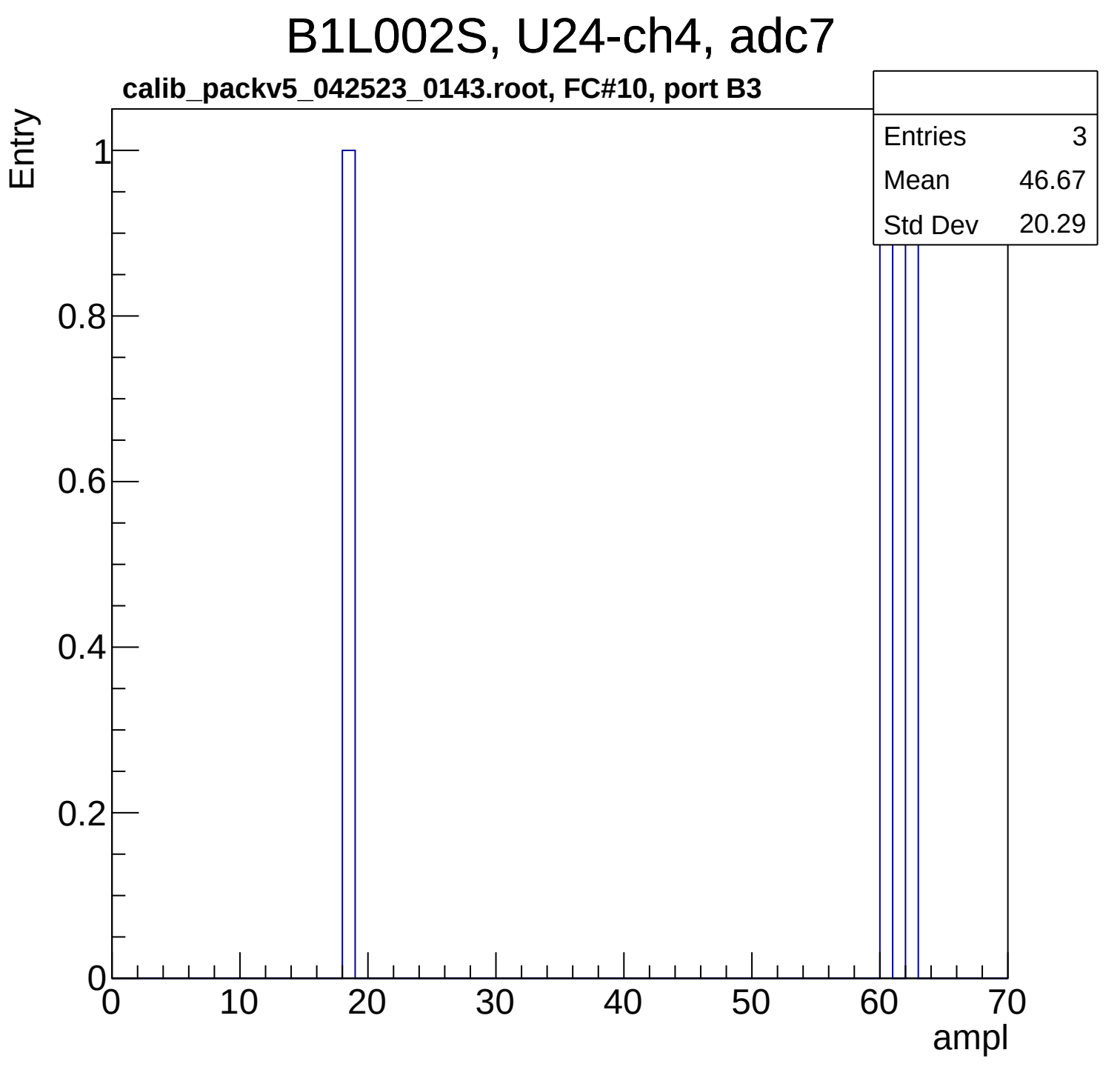
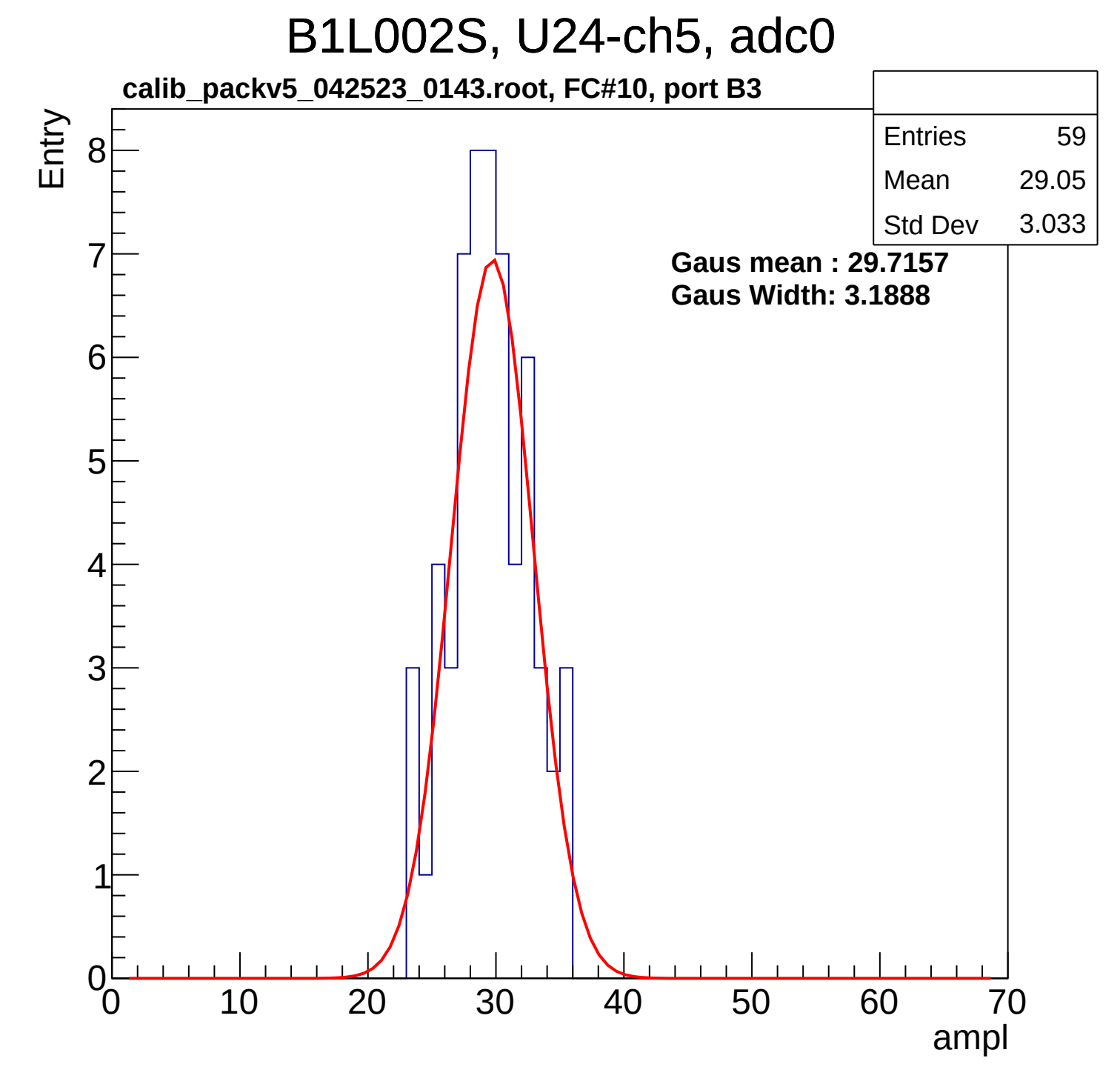
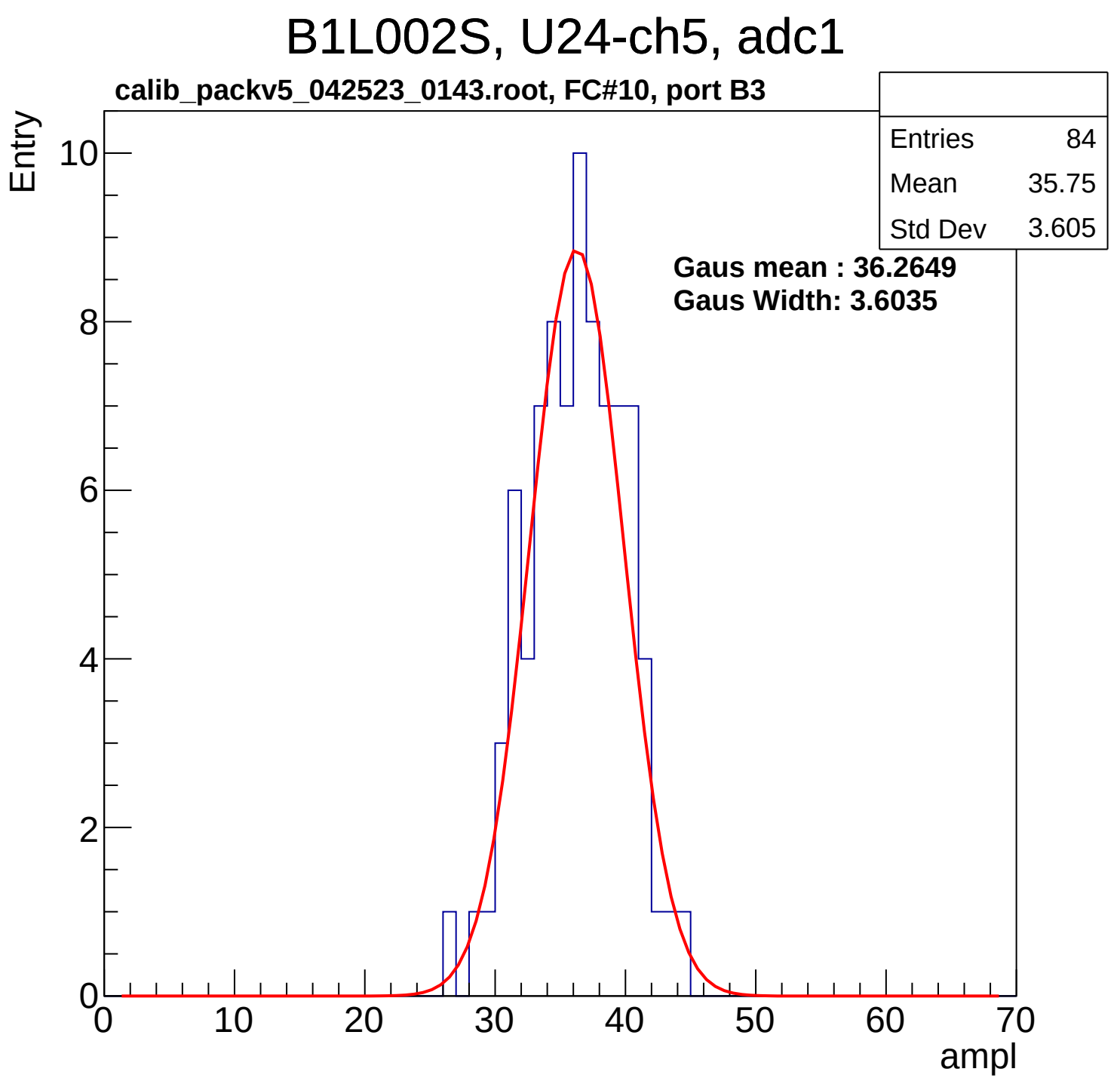
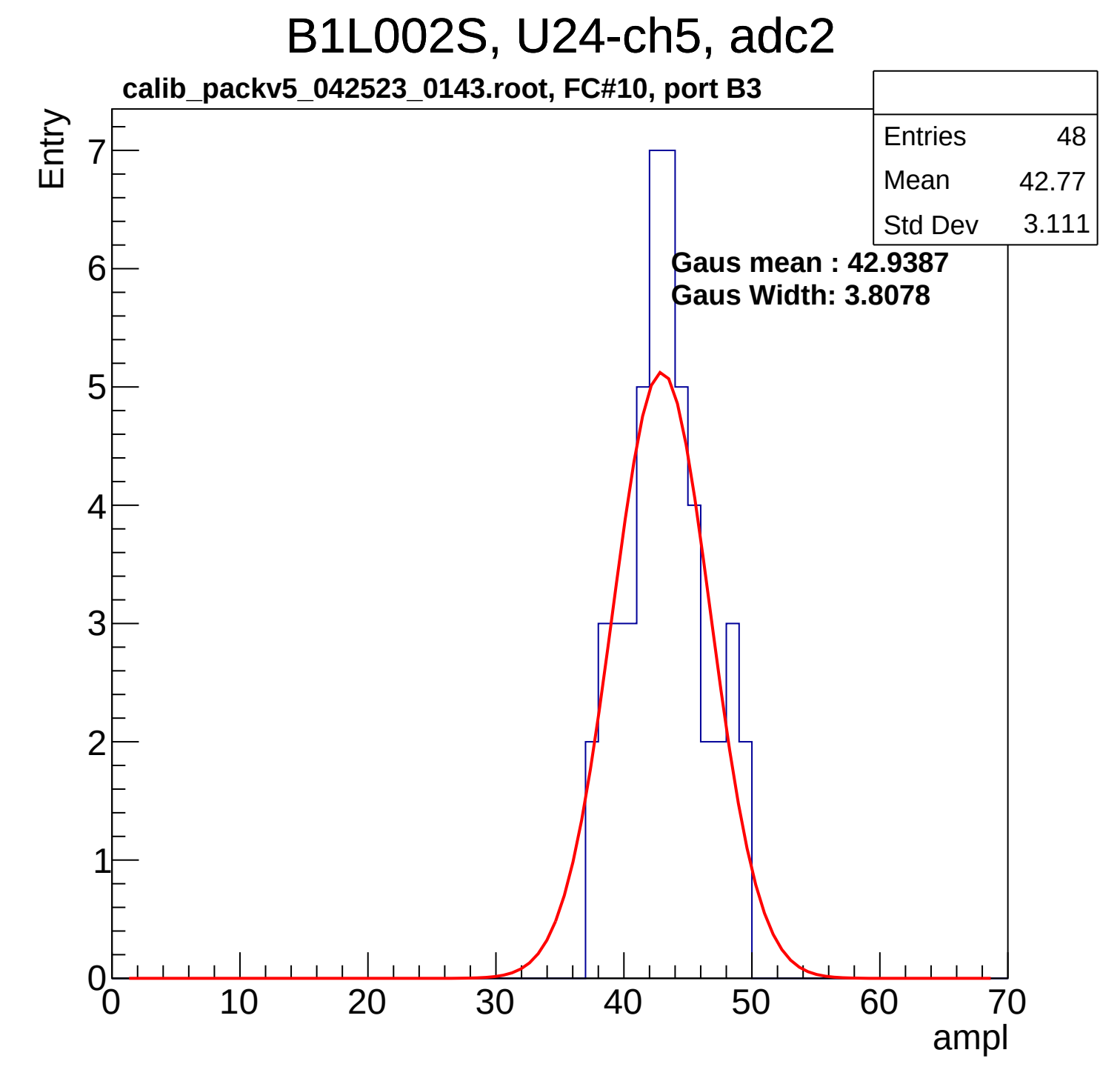

B1L002S, U24-ch4, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 57.54
Std Dev 2.41
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch4, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 59.32
Std Dev 10.06
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch4, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 46.67
Std Dev 20.29
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 29.05
Std Dev 3.033
Gaus mean : 29.7157
Gaus Width: 3.1888
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 35.75
Std Dev 3.605
Gaus mean : 36.2649
Gaus Width: 3.6035
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 42.77
Std Dev 3.111
Gaus mean : 42.9387
Gaus Width: 3.8078
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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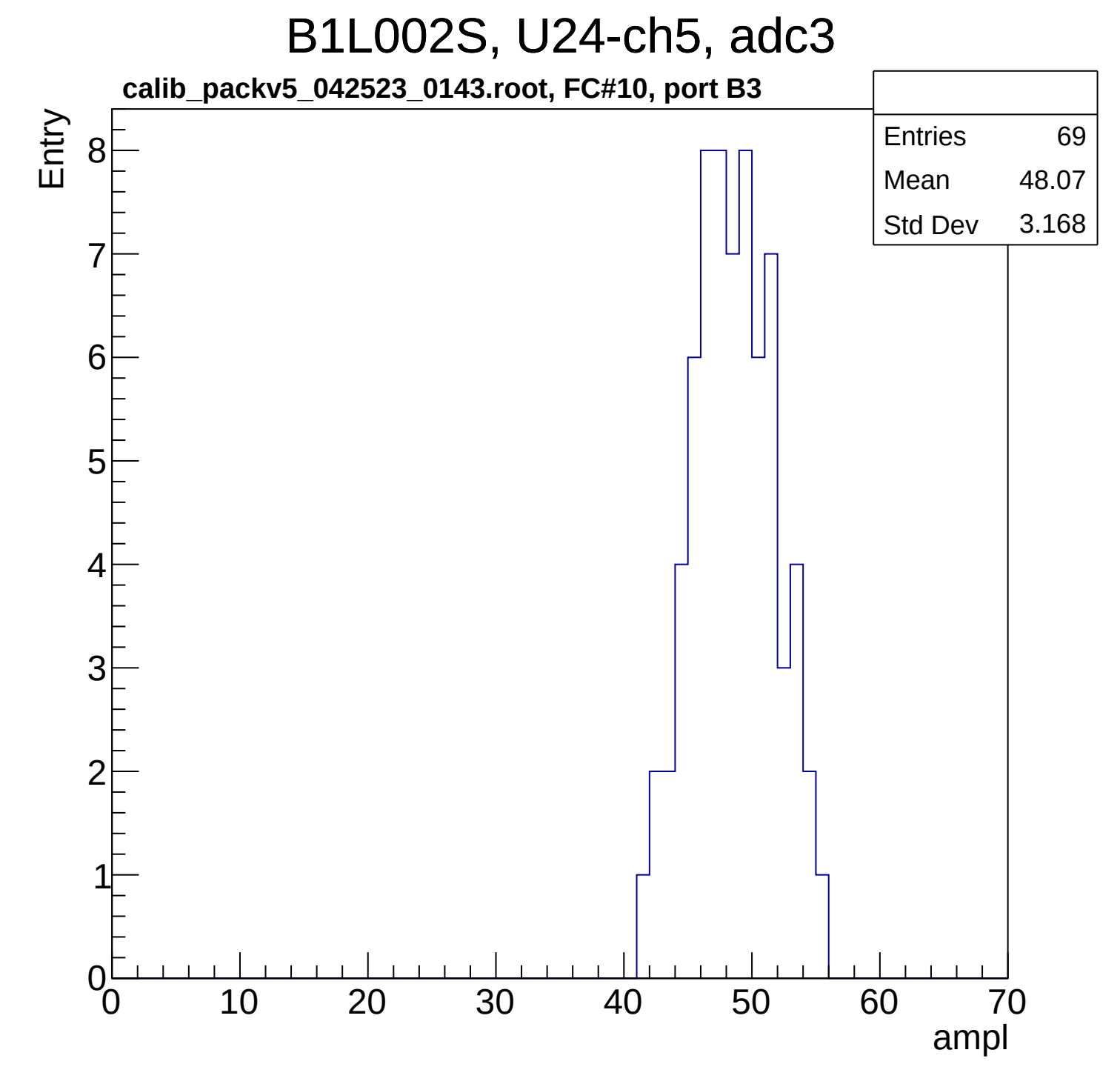
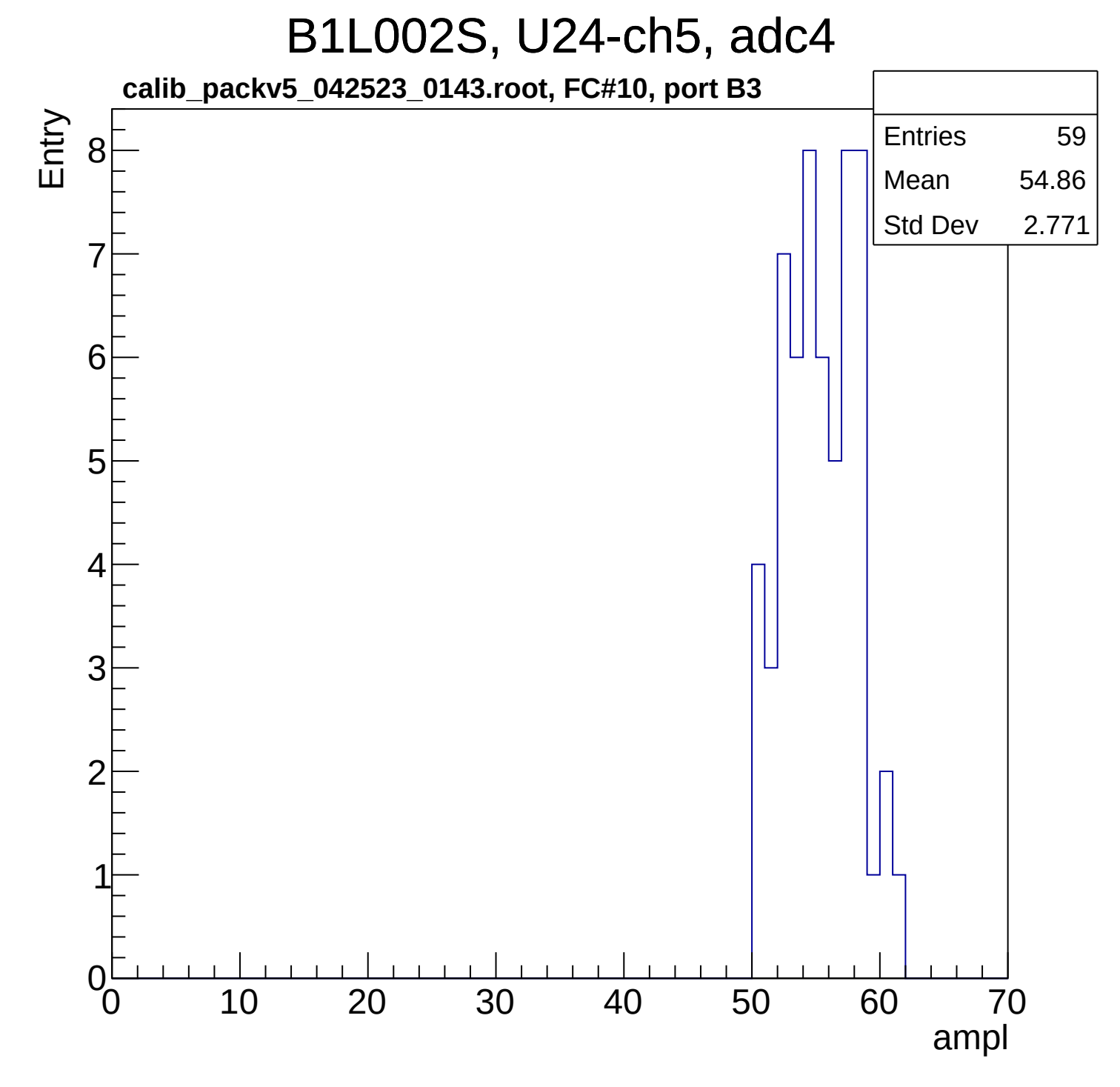
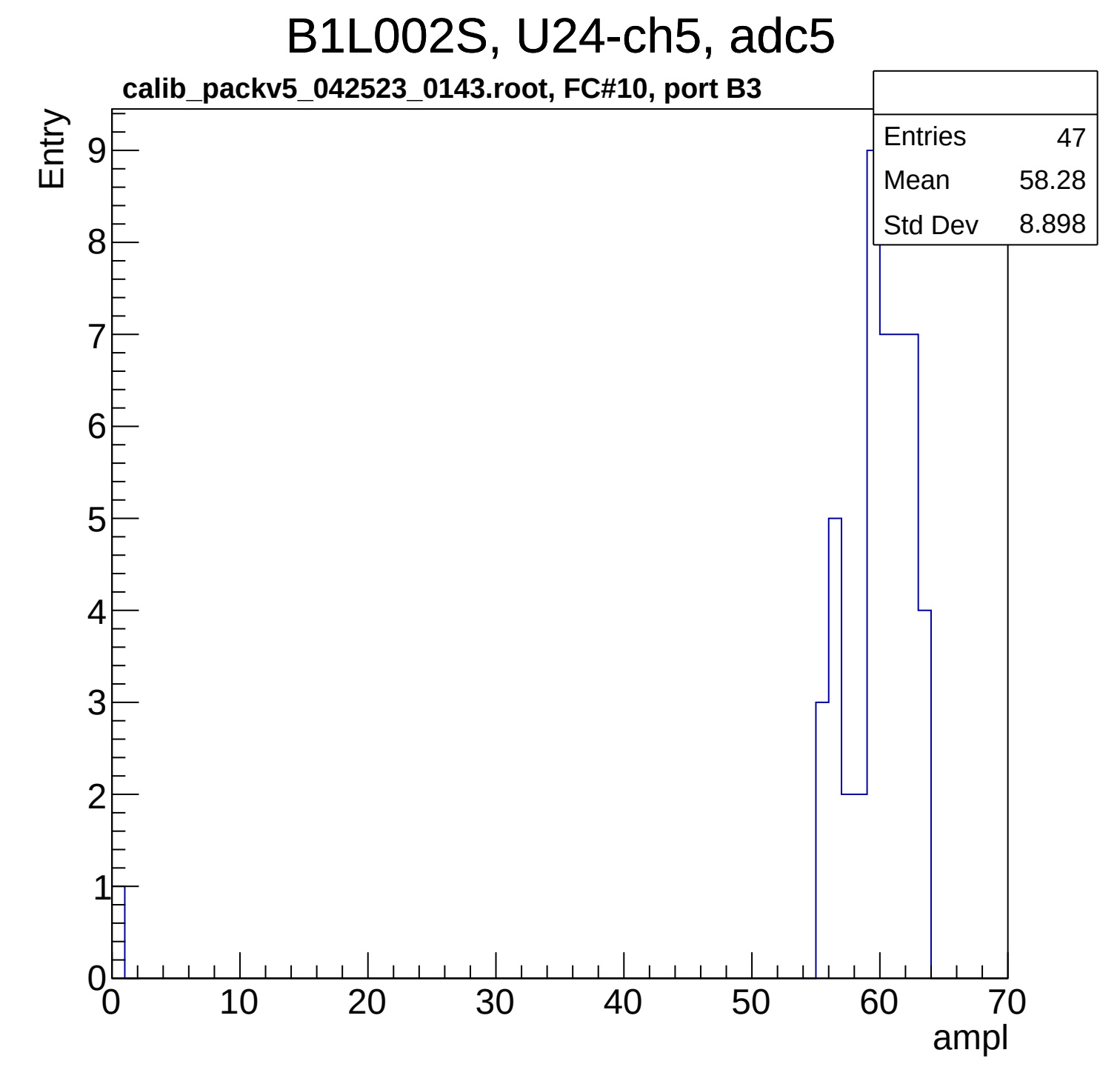
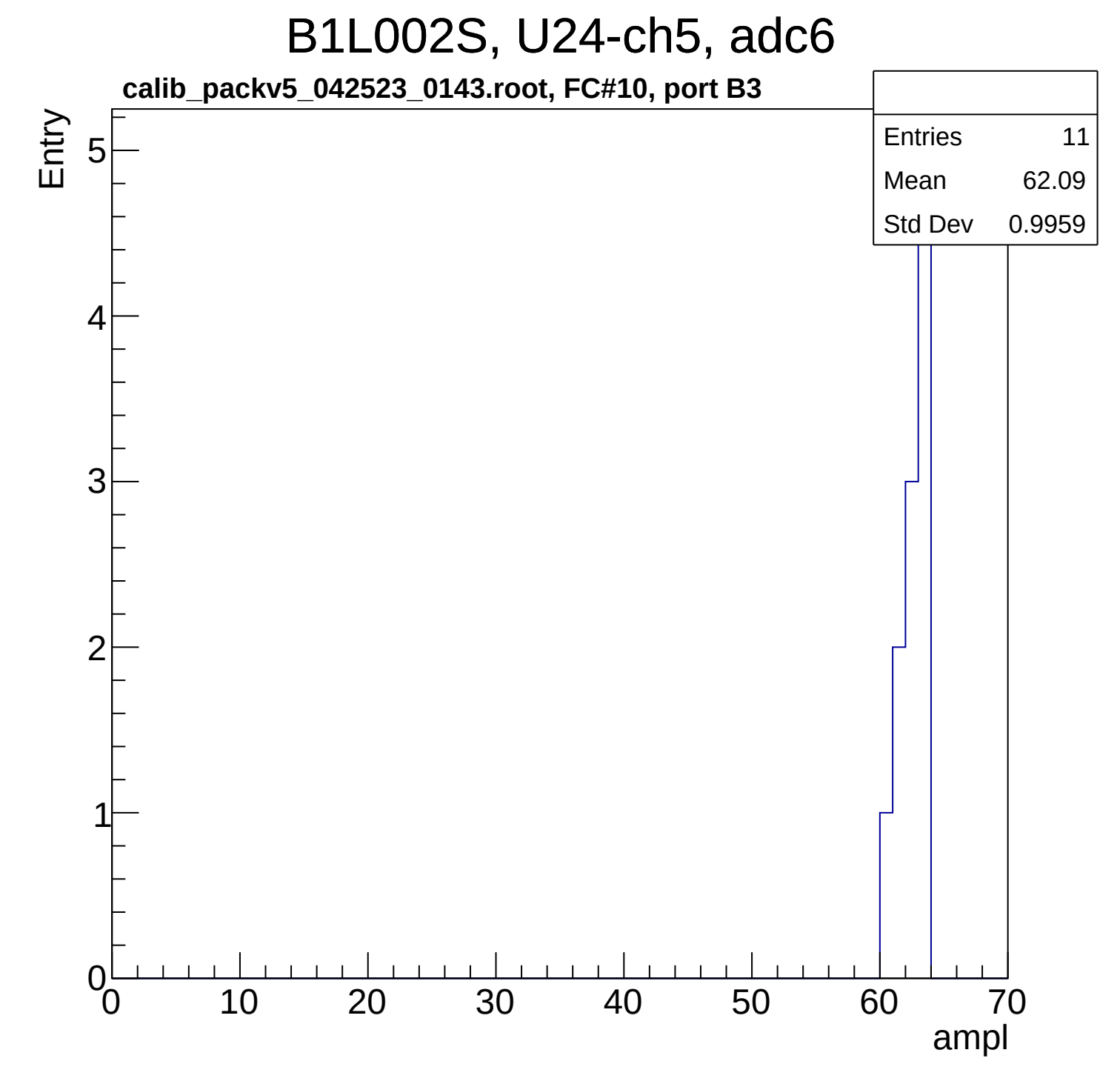
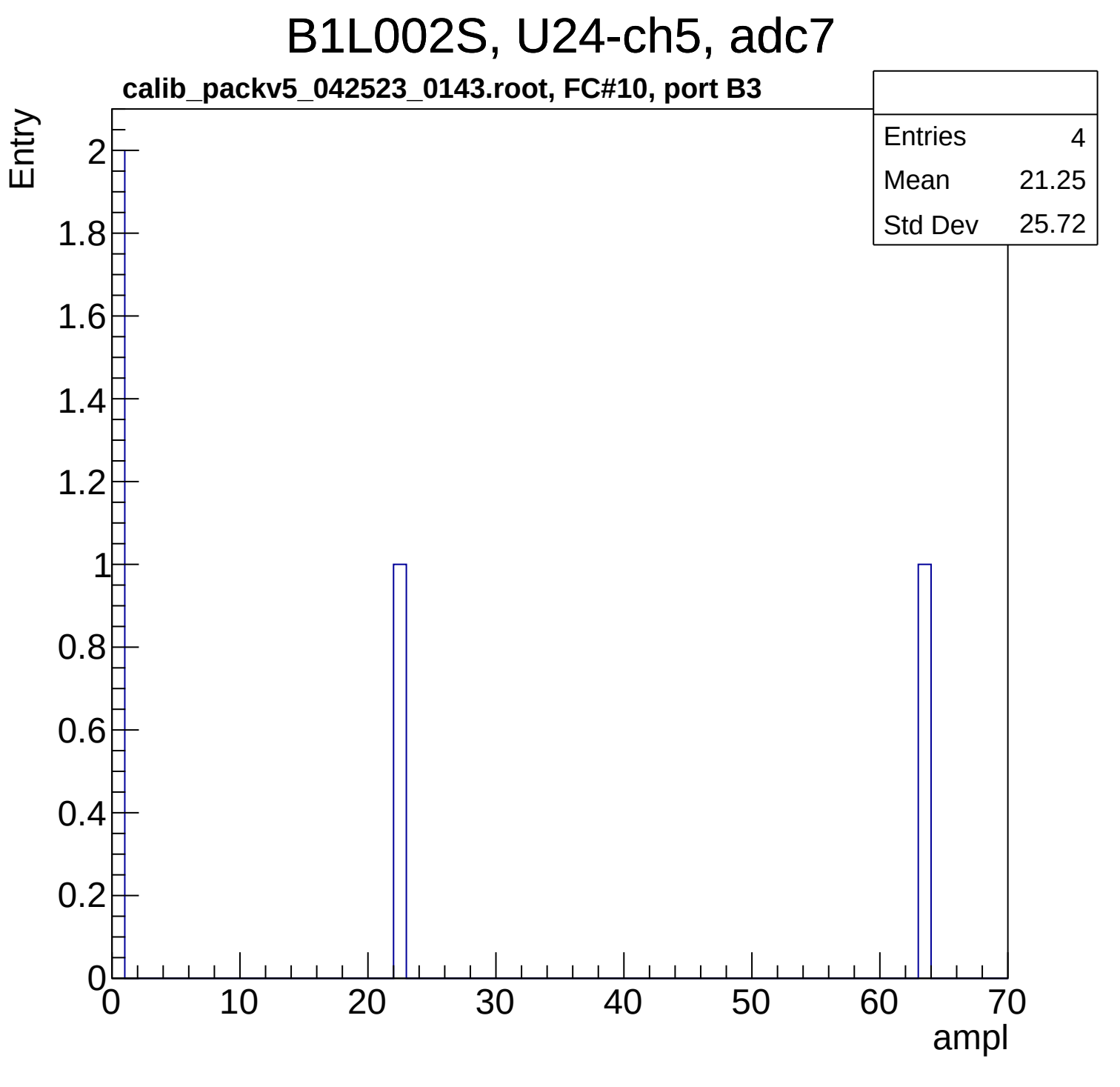
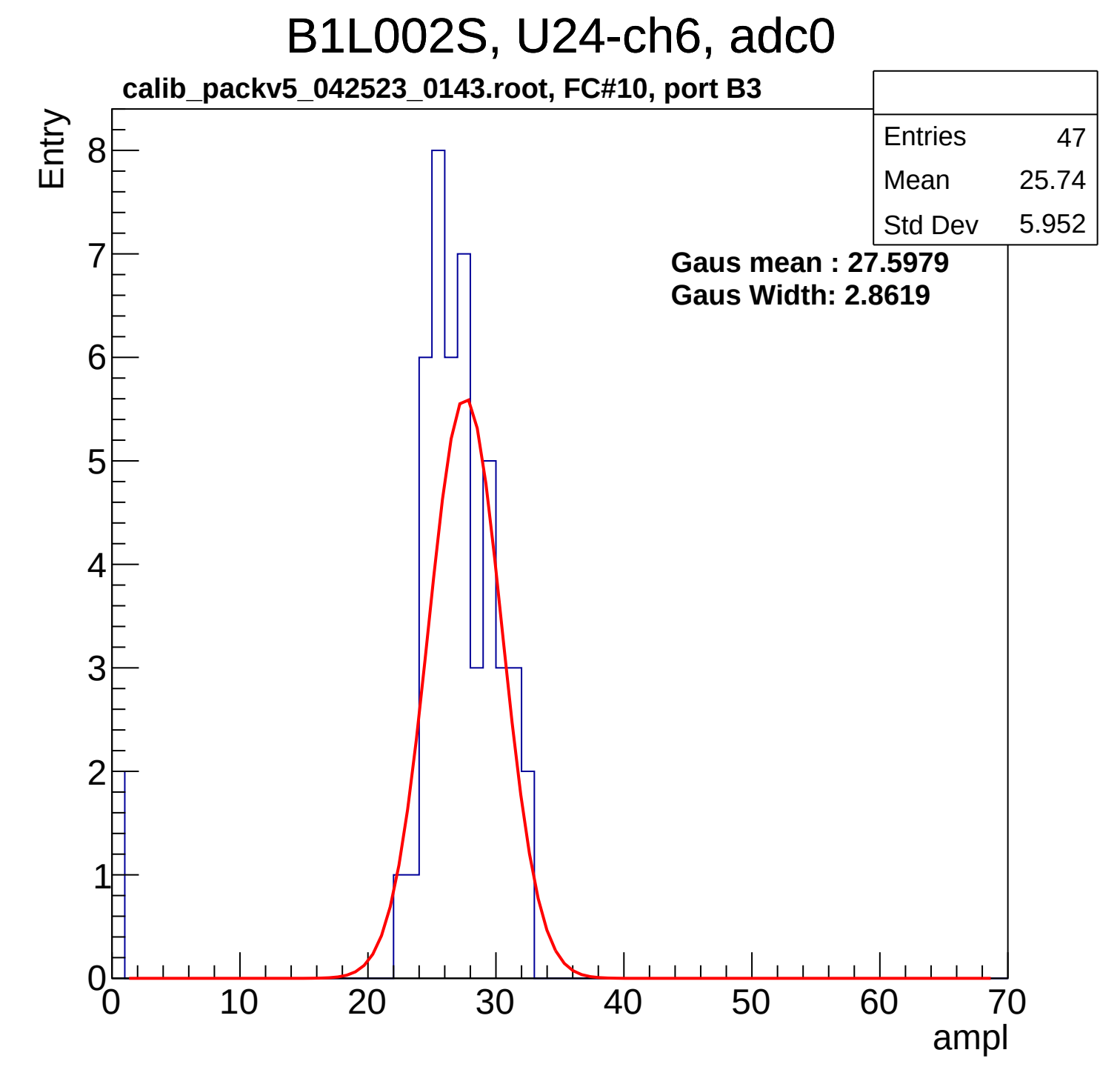

B1L002S, U24-ch5, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.07
Std Dev 3.168
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 54.86
Std Dev 2.771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.28
Std Dev 8.898
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch5, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.09
Std Dev 0.9959
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch5, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21.25
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch6, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 25.74
Std Dev 5.952
Gaus mean : 27.5979
Gaus Width: 2.8619
Entry
ampl
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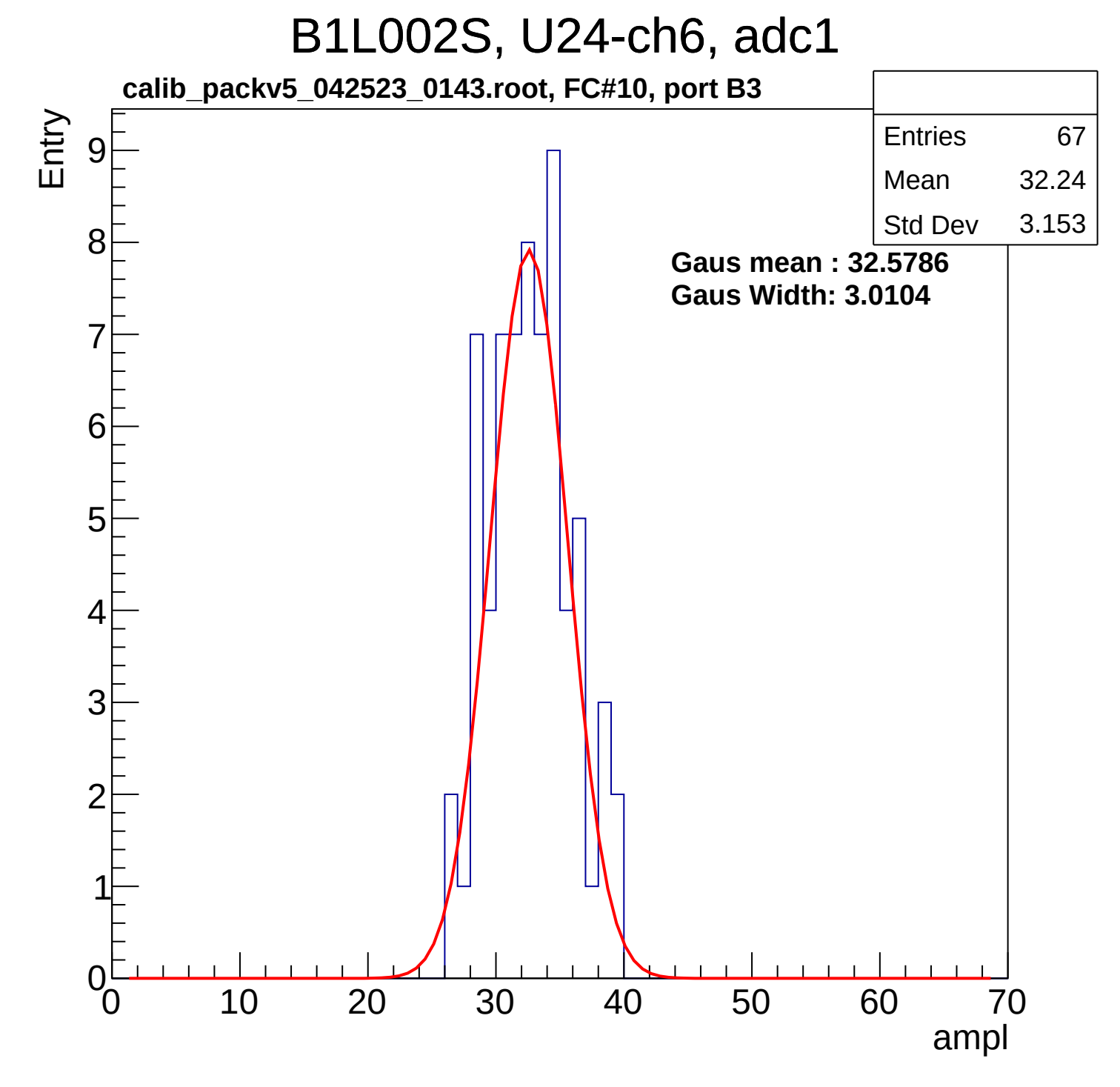
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch6, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 32.24
Std Dev 3.153
Gaus mean : 32.5786
Gaus Width: 3.0104
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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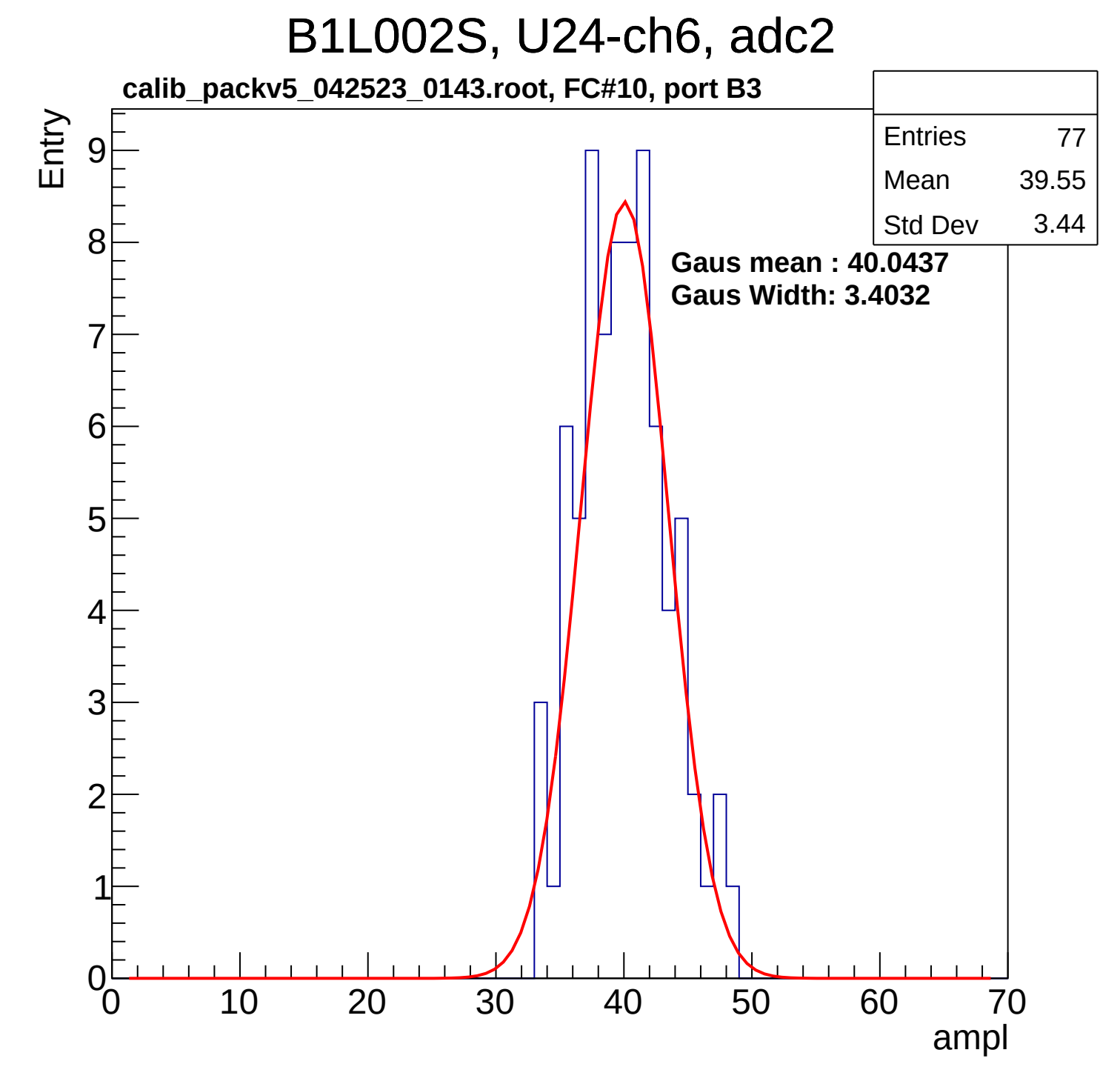
20
30
40
50
60
70


B1L002S, U24-ch6, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 39.55
Std Dev 3.44
Gaus mean : 40.0437
Gaus Width: 3.4032
Entry
ampl
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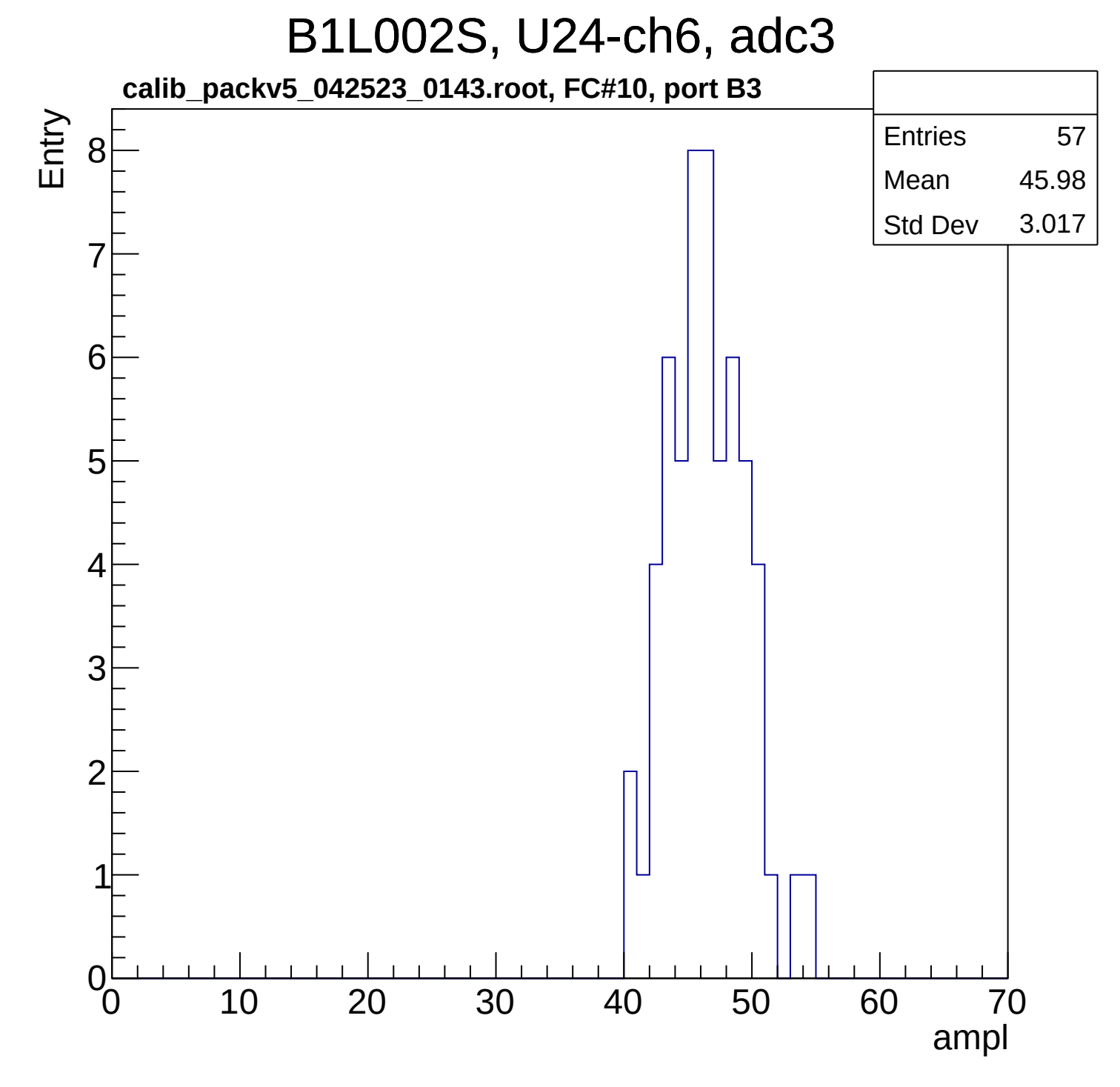
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch6, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 45.98
Std Dev 3.017
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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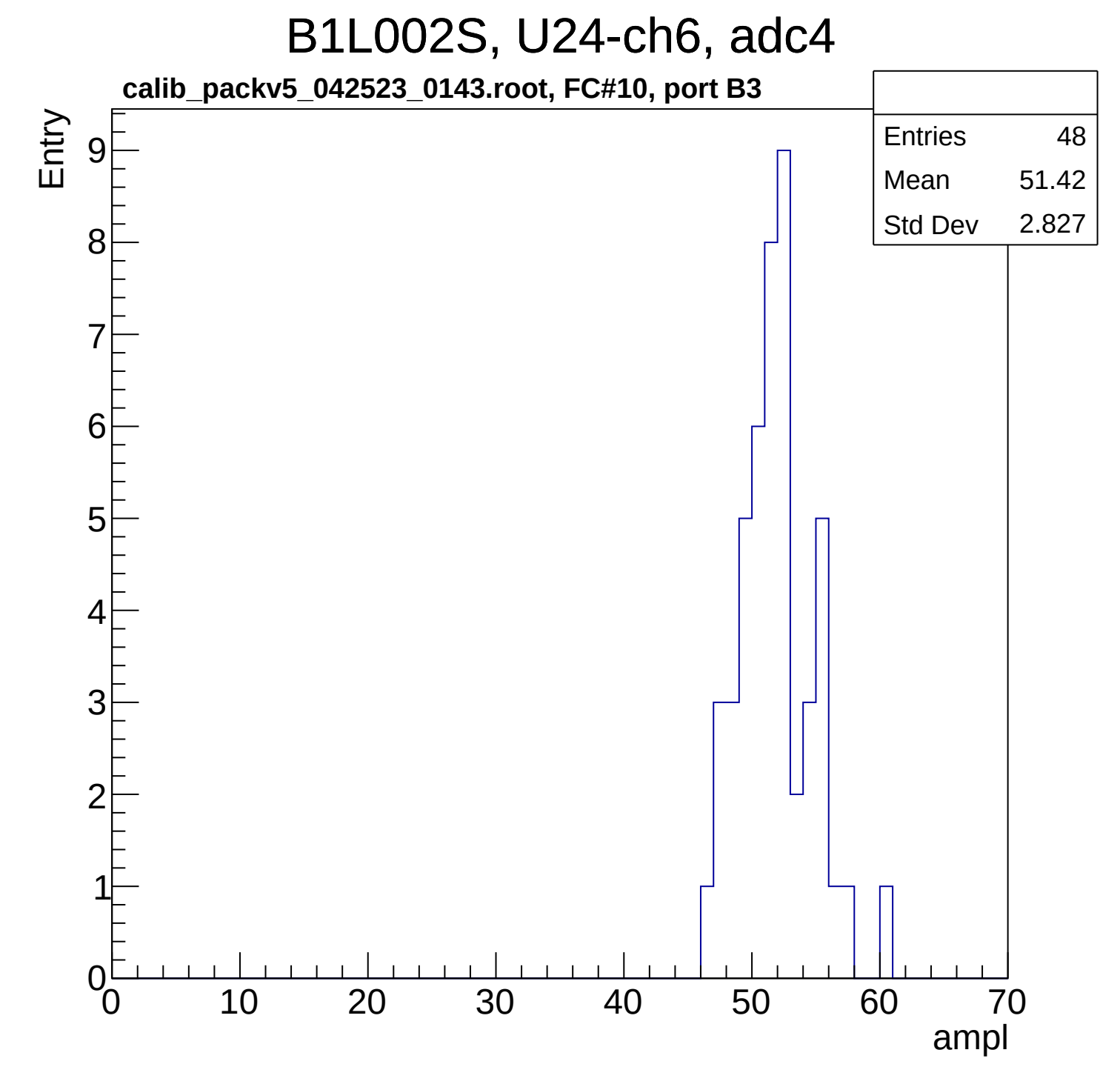
60
70


B1L002S, U24-ch6, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 51.42
Std Dev 2.827
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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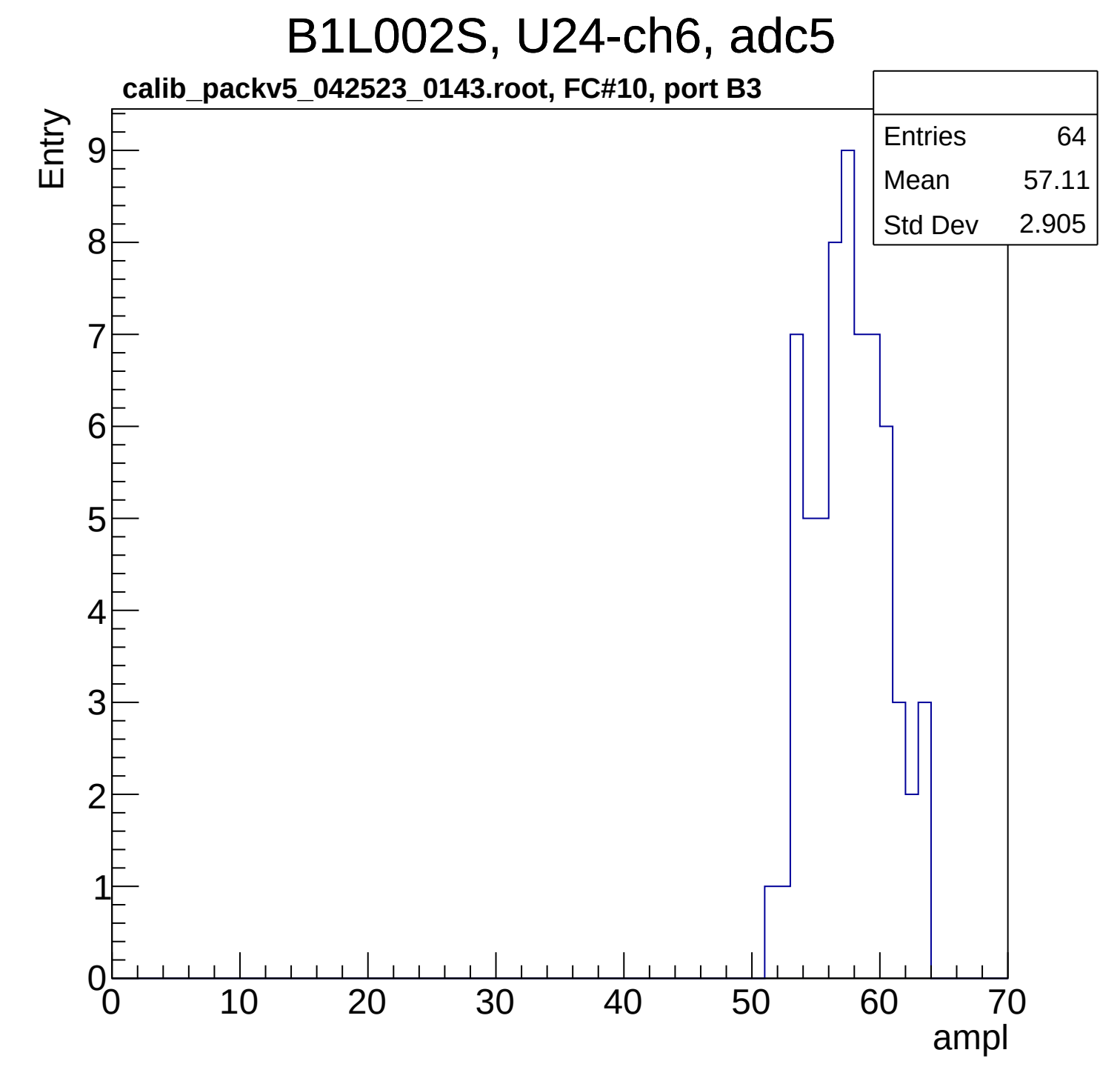
B1L002S, U24-ch6, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 57.11
Std Dev 2.905
Entry
ampl
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6
7
8
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0
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40
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60
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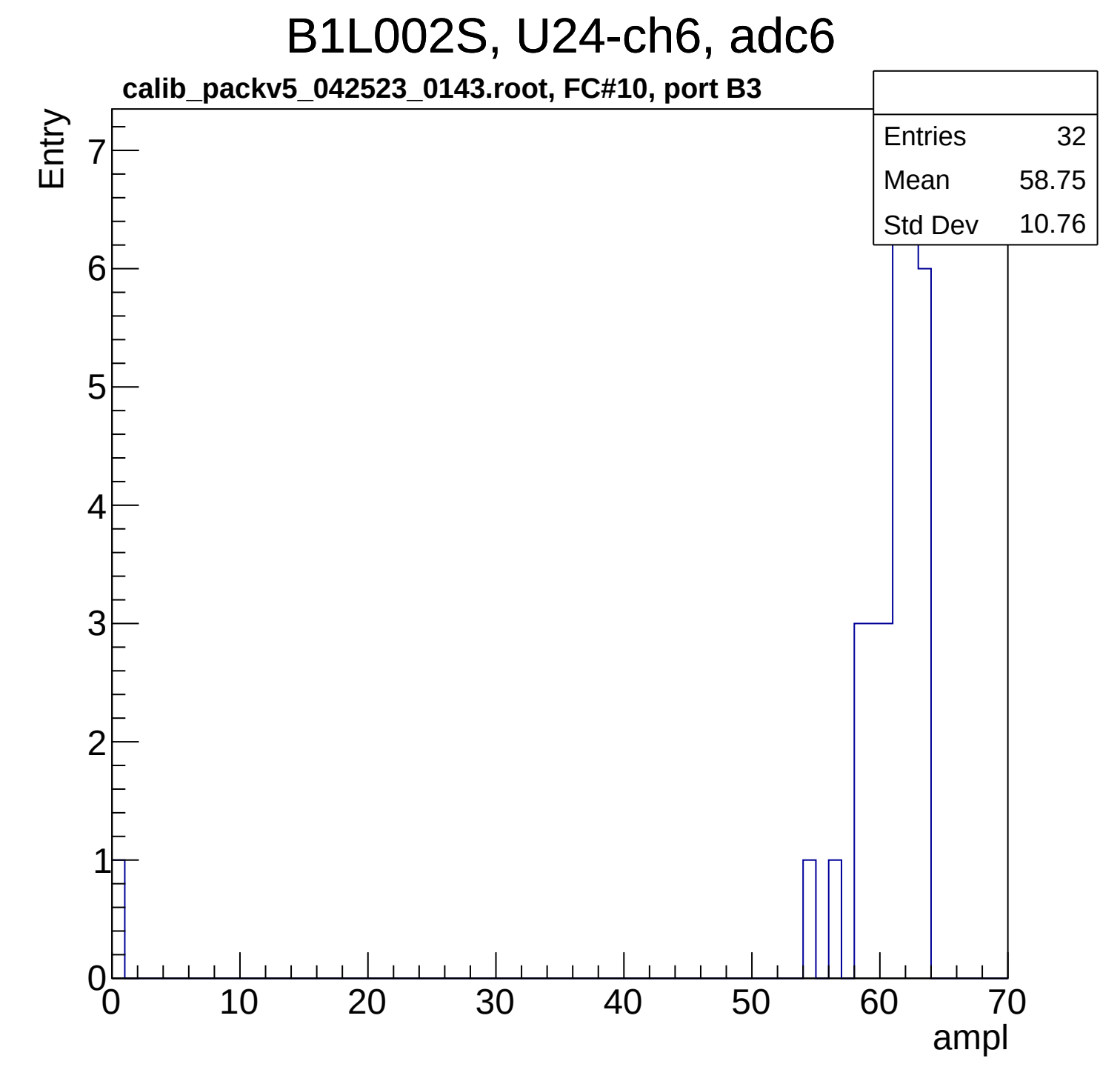
B1L002S, U24-ch6, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 58.75
Std Dev 10.76
Entry
ampl
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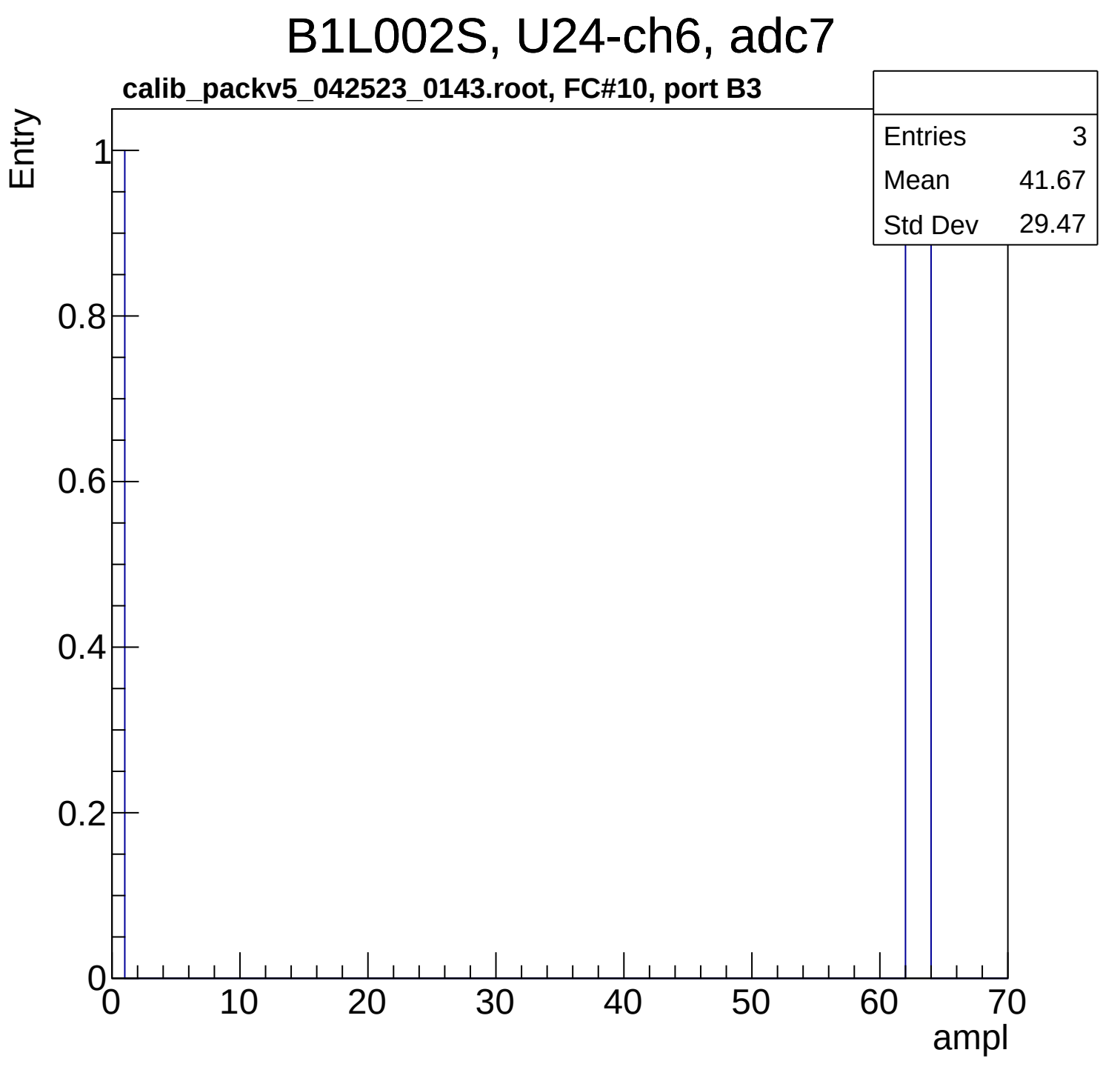
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch6, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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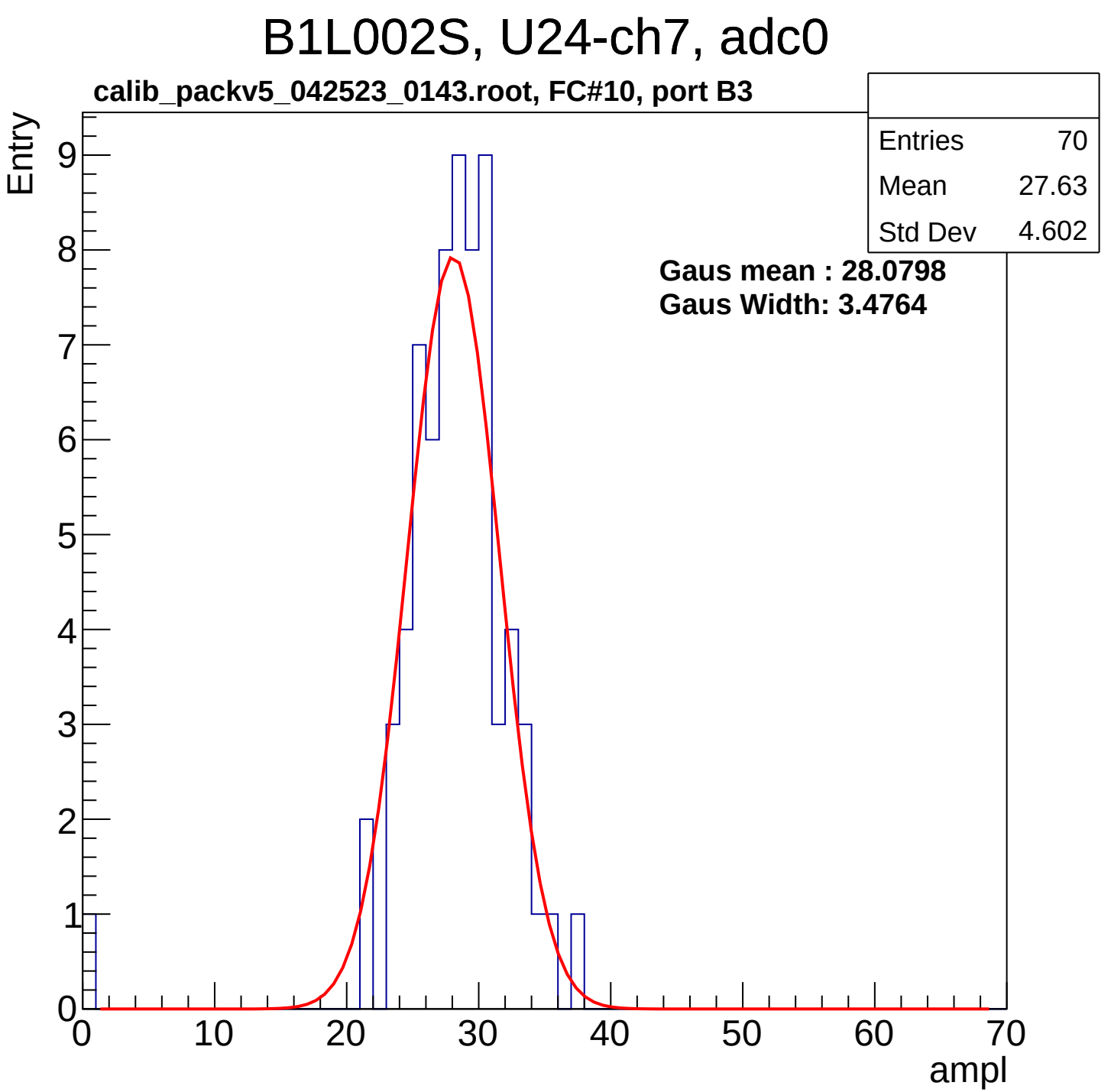
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch7, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 27.63
Std Dev 4.602
Gaus mean : 28.0798
Gaus Width: 3.4764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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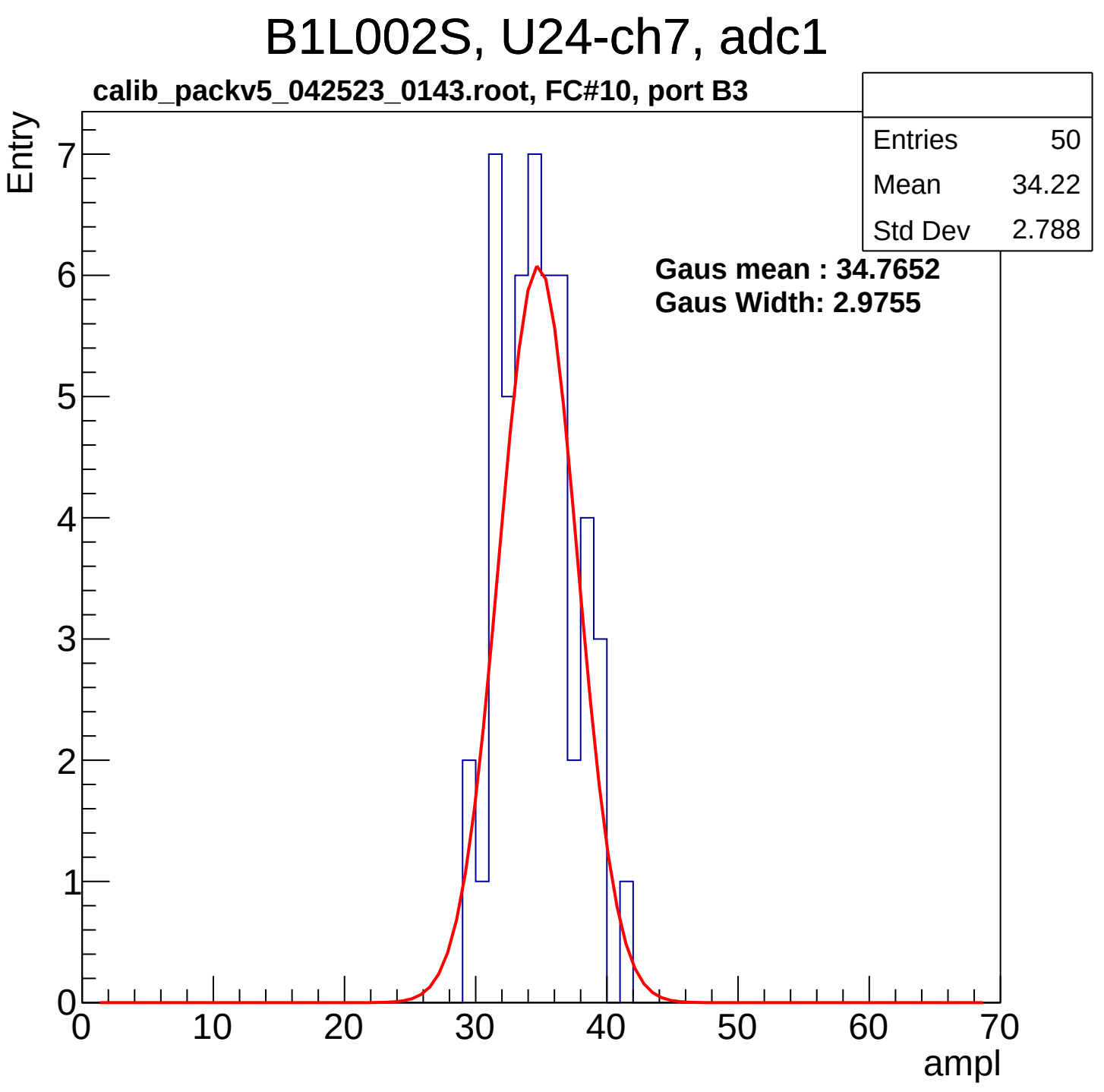
B1L002S, U24-ch7, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 34.22
Std Dev 2.788
Gaus mean : 34.7652
Gaus Width: 2.9755
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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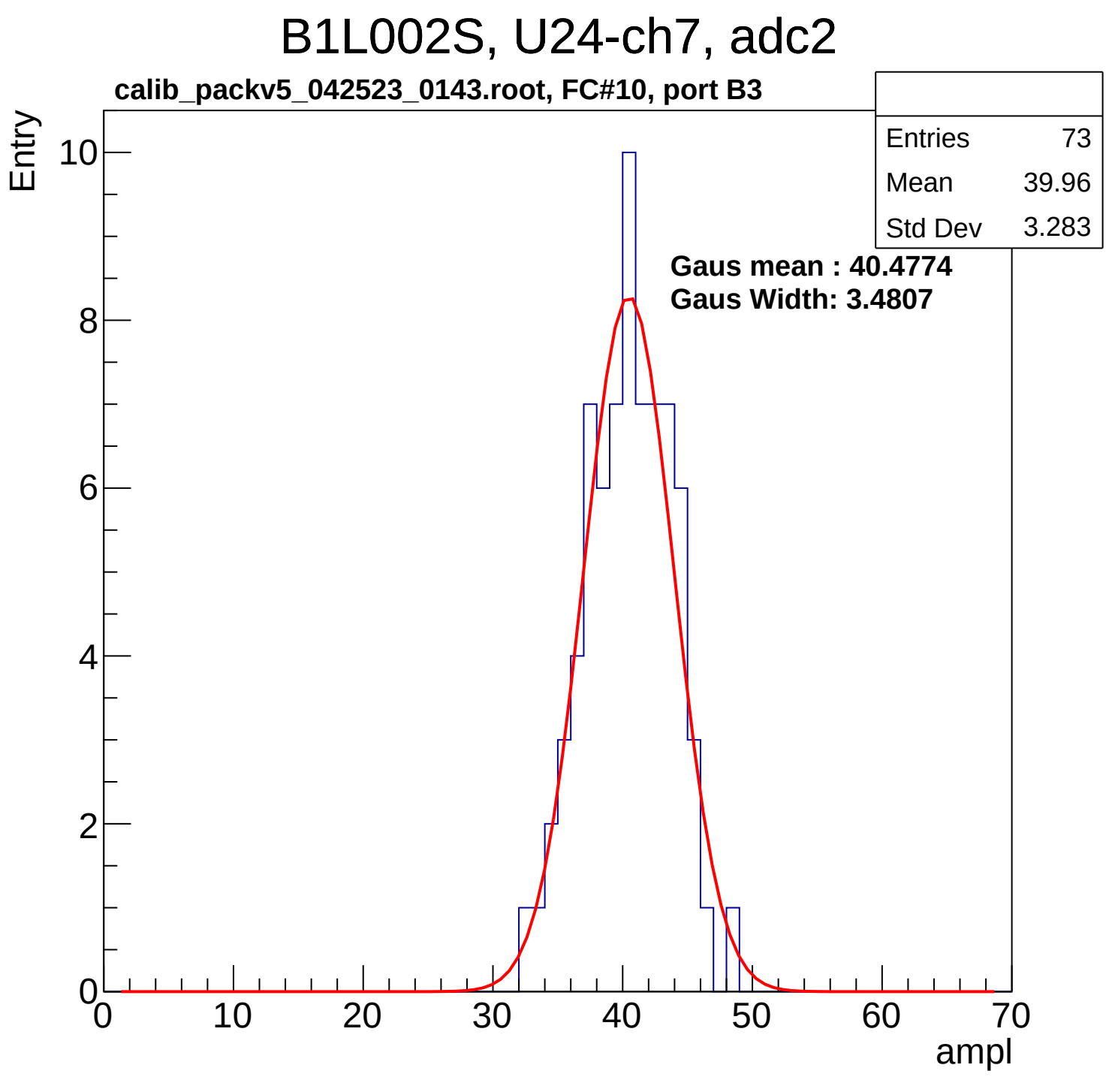
B1L002S, U24-ch7, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 39.96
Std Dev 3.283
Gaus mean : 40.4774
Gaus Width: 3.4807
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
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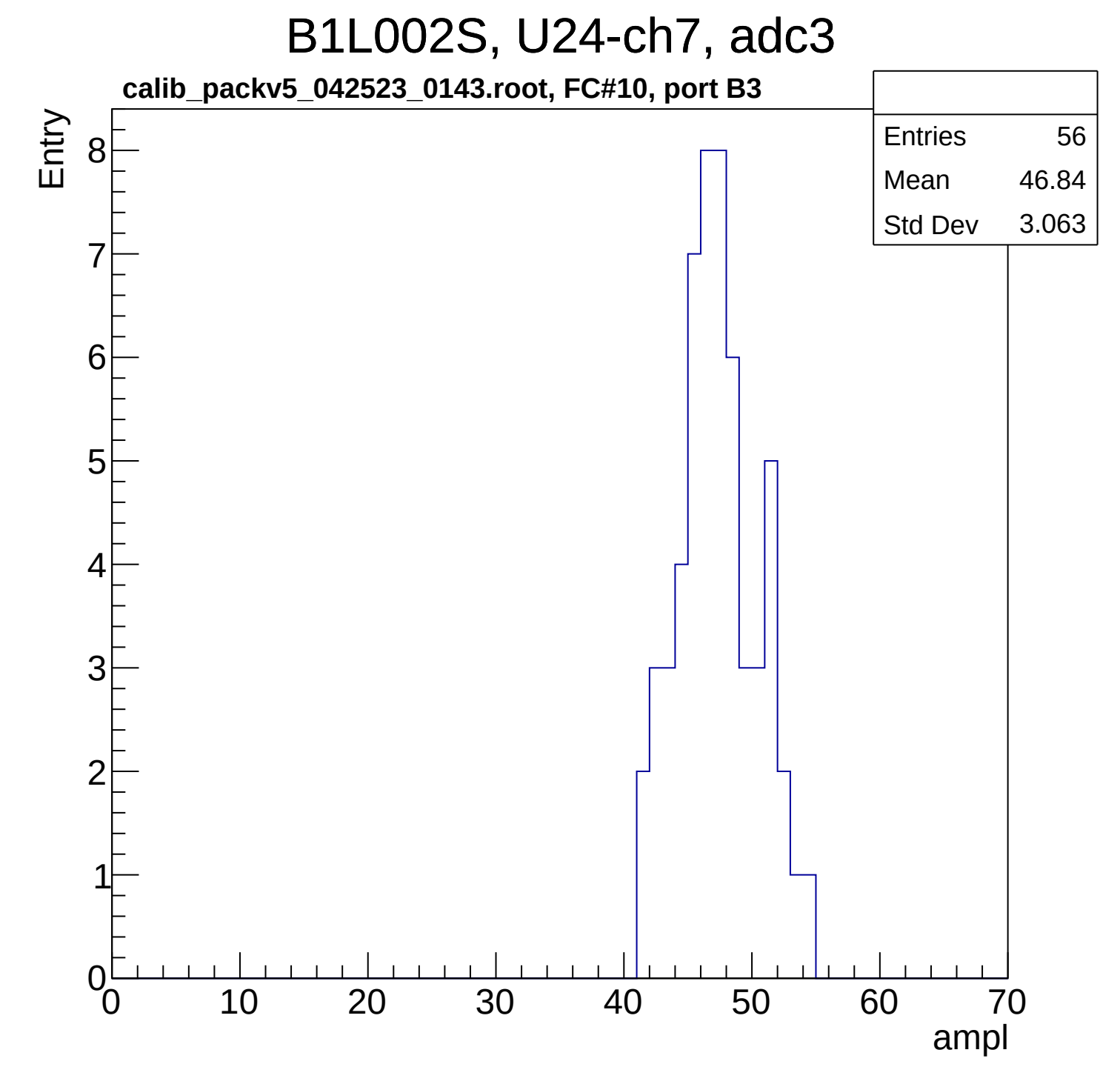
B1L002S, U24-ch7, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.84
Std Dev 3.063
Entry
ampl
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1
2
3
4
5
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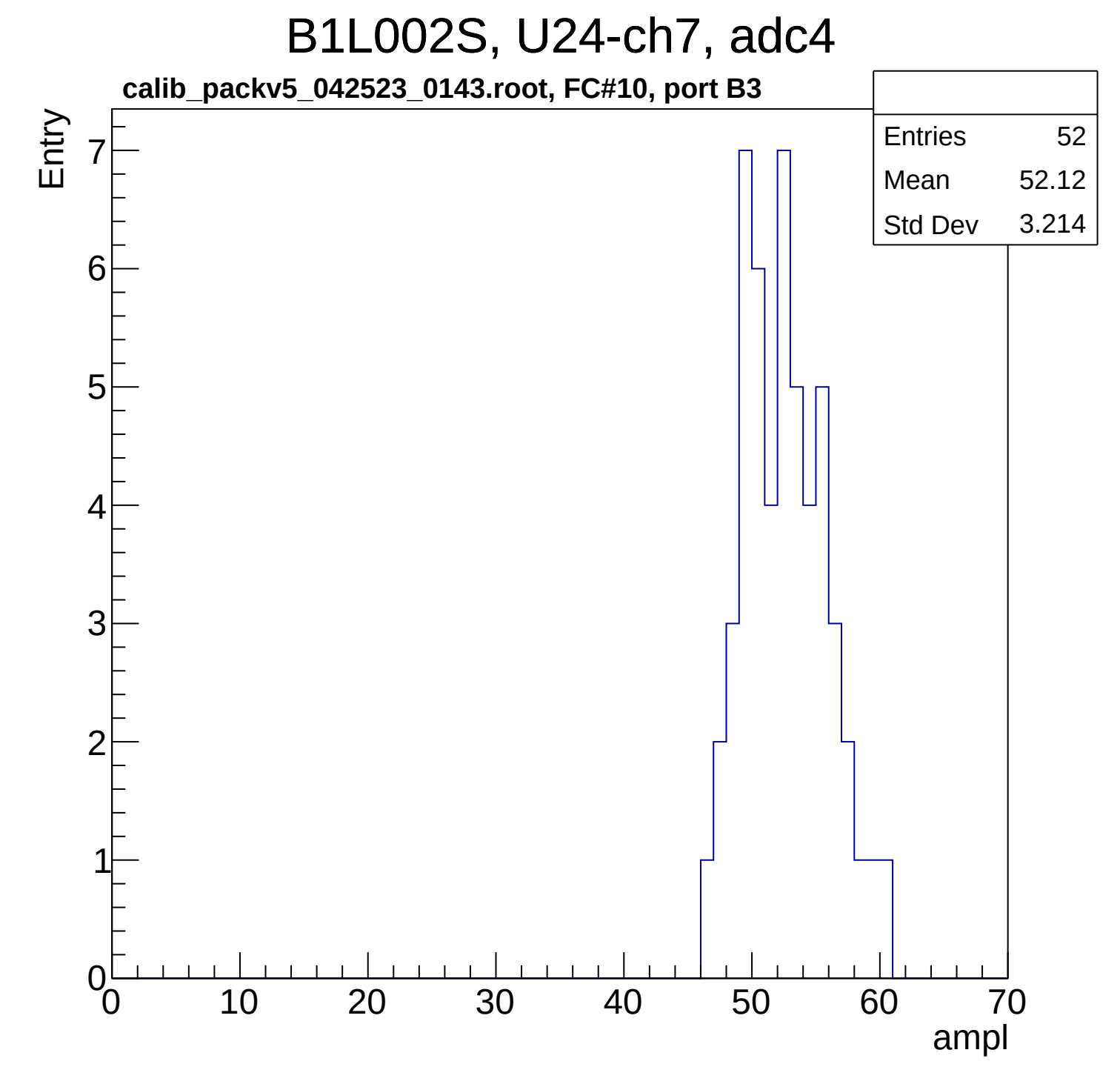
B1L002S, U24-ch7, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.12
Std Dev 3.214
Entry
ampl
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4
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0
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40
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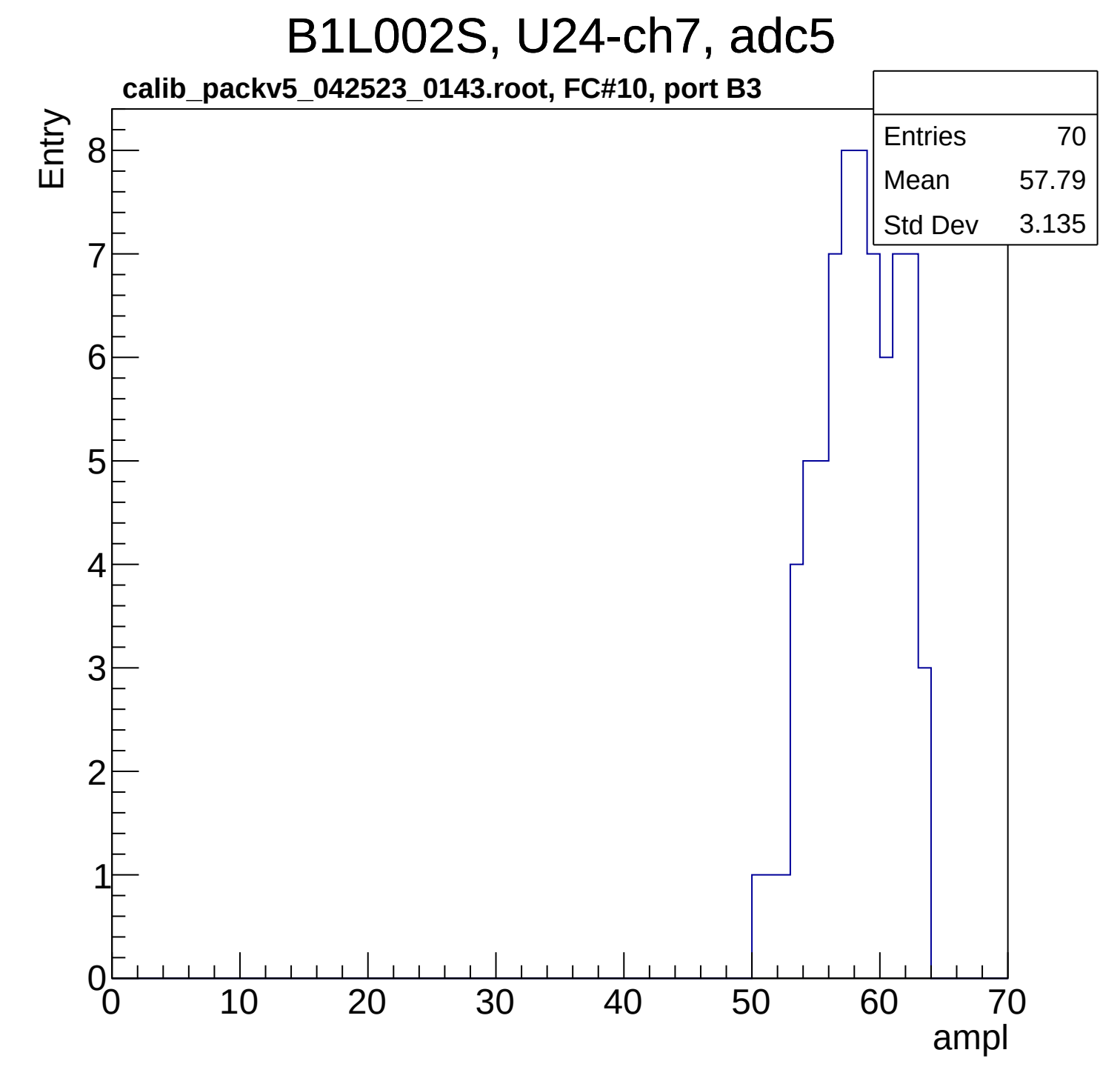
60
70


B1L002S, U24-ch7, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 57.79
Std Dev 3.135
Entry
0
1
2
3
4
5
6
7
8
0
10
20
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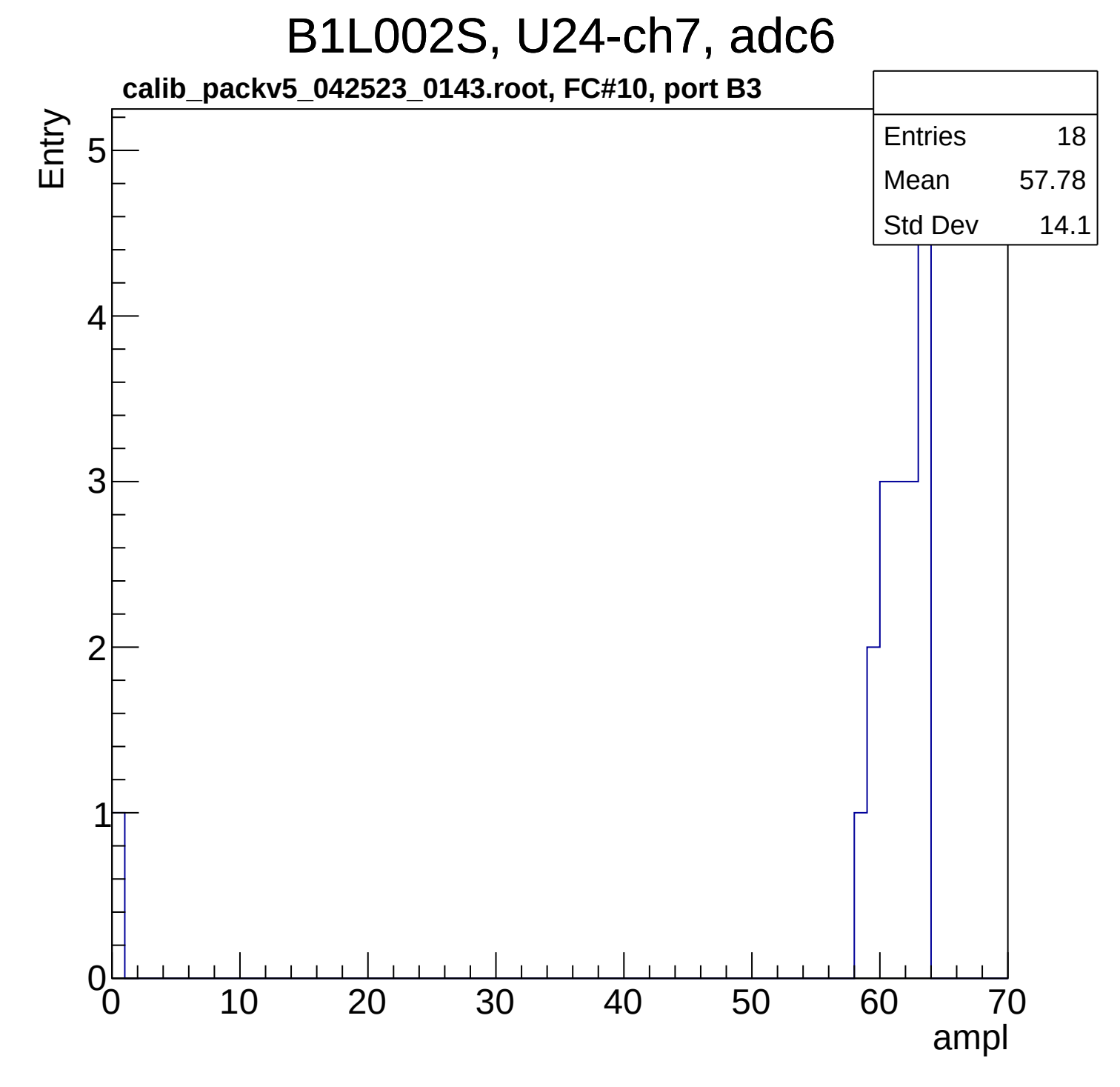
50
60
70
ampl


B1L002S, U24-ch7, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 57.78
Std Dev 14.1
Entry
0
1
2
3
4
5
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10
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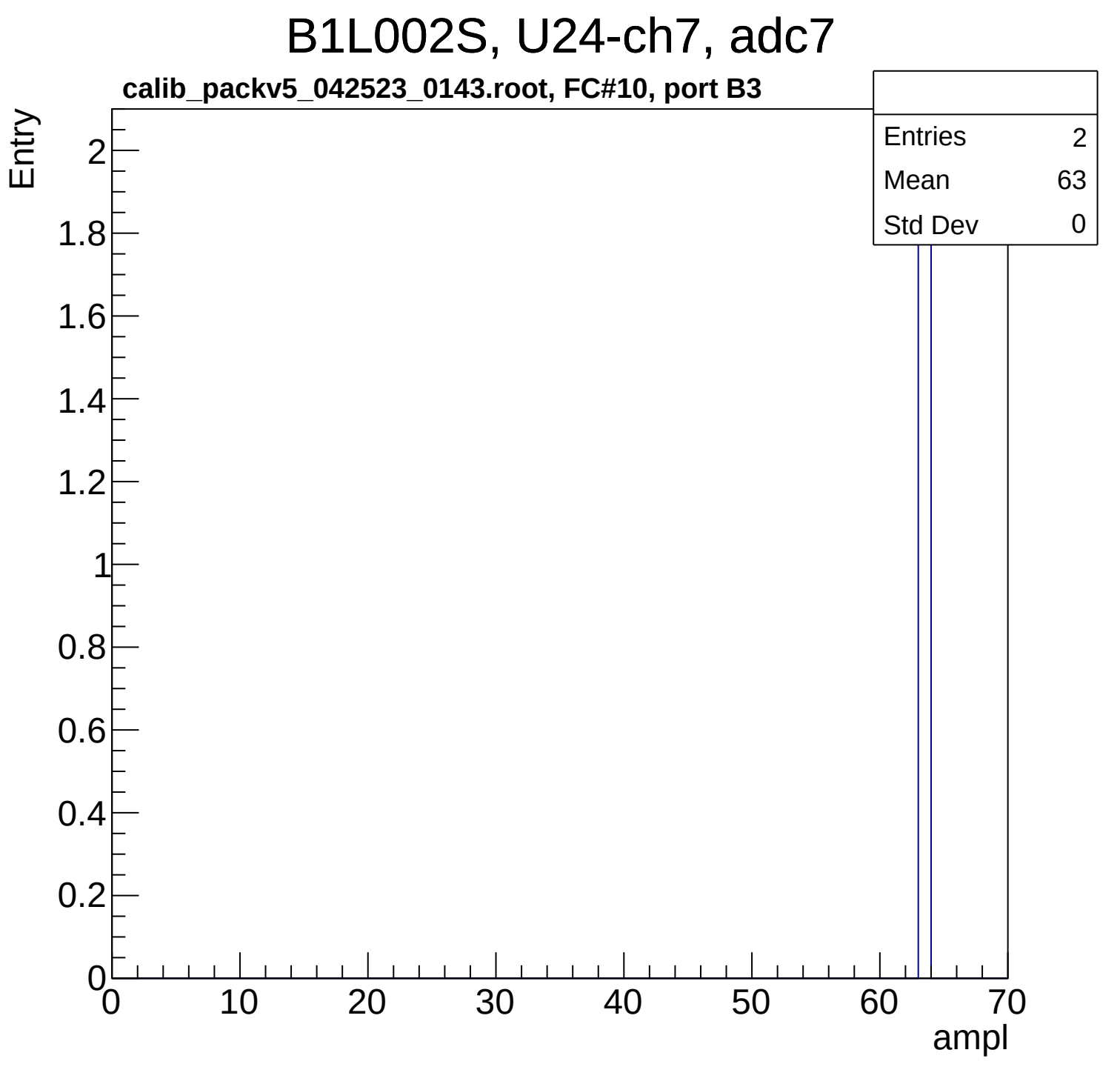
70
ampl


B1L002S, U24-ch7, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
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1.2
1.4
1.6
1.8
2
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40
50
60
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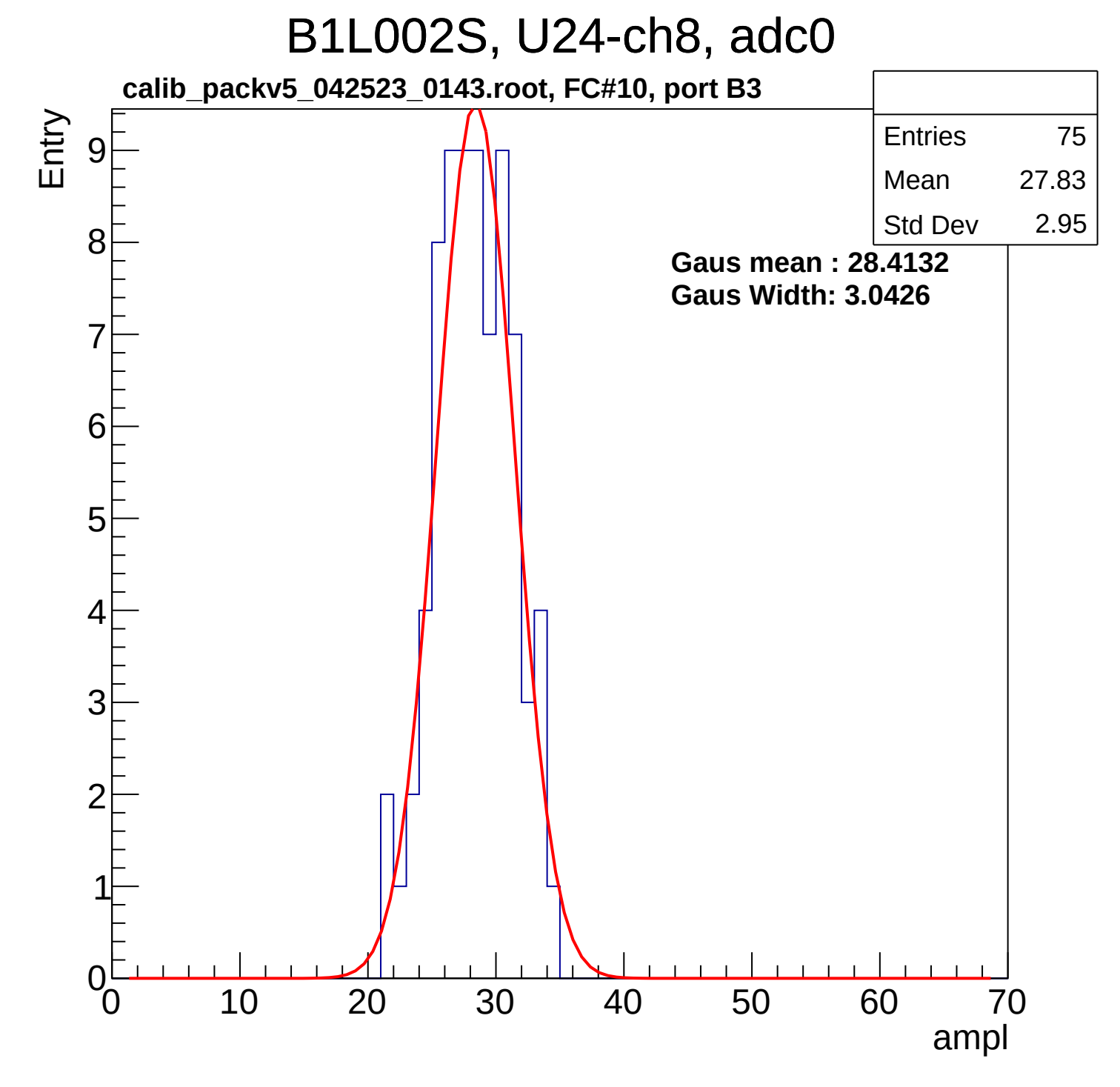
B1L002S, U24-ch8, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.83
Std Dev 2.95
Gaus mean : 28.4132
Gaus Width: 3.0426
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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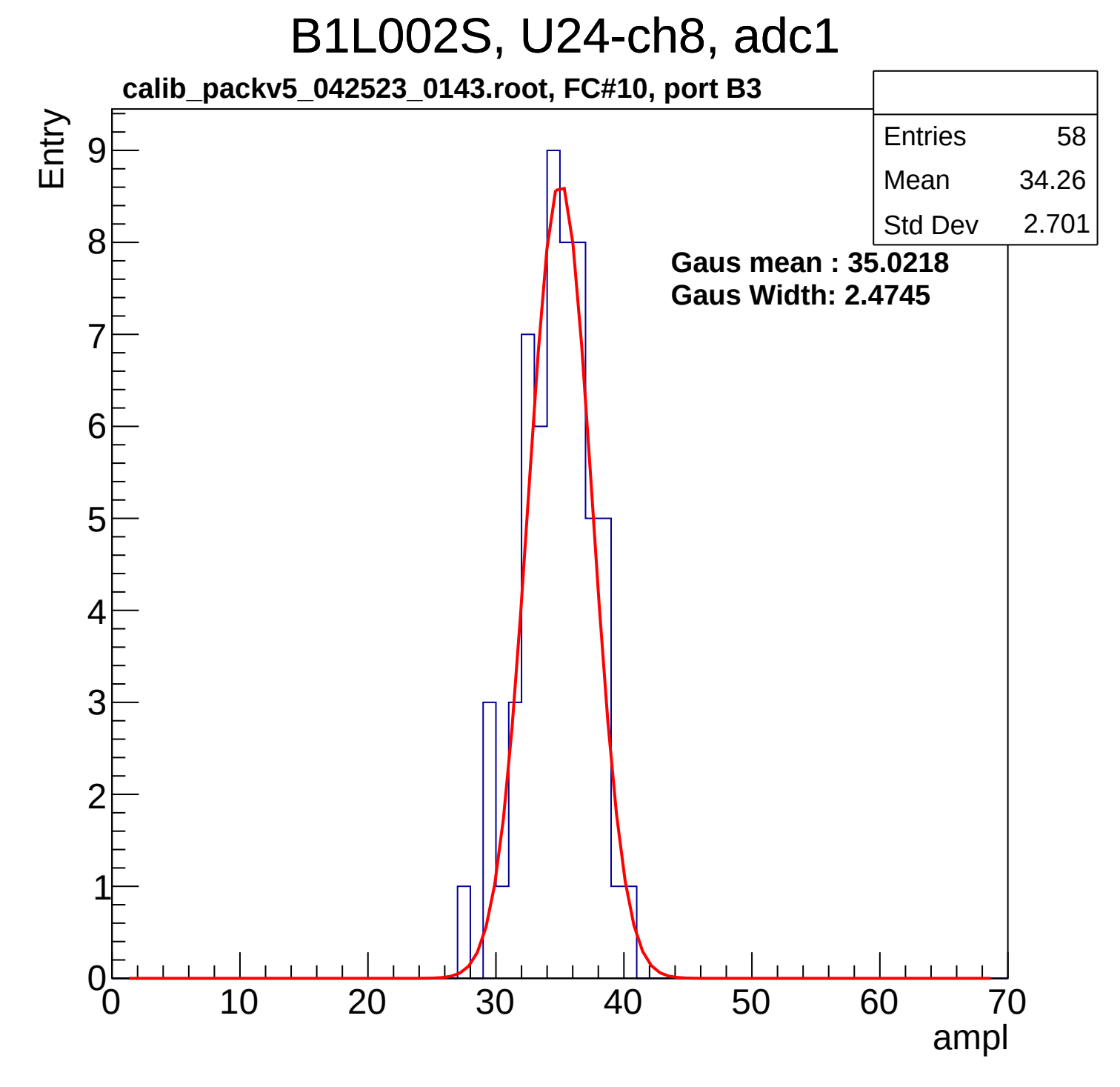
B1L002S, U24-ch8, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.26
Std Dev 2.701
Gaus mean : 35.0218
Gaus Width: 2.4745
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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40
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60
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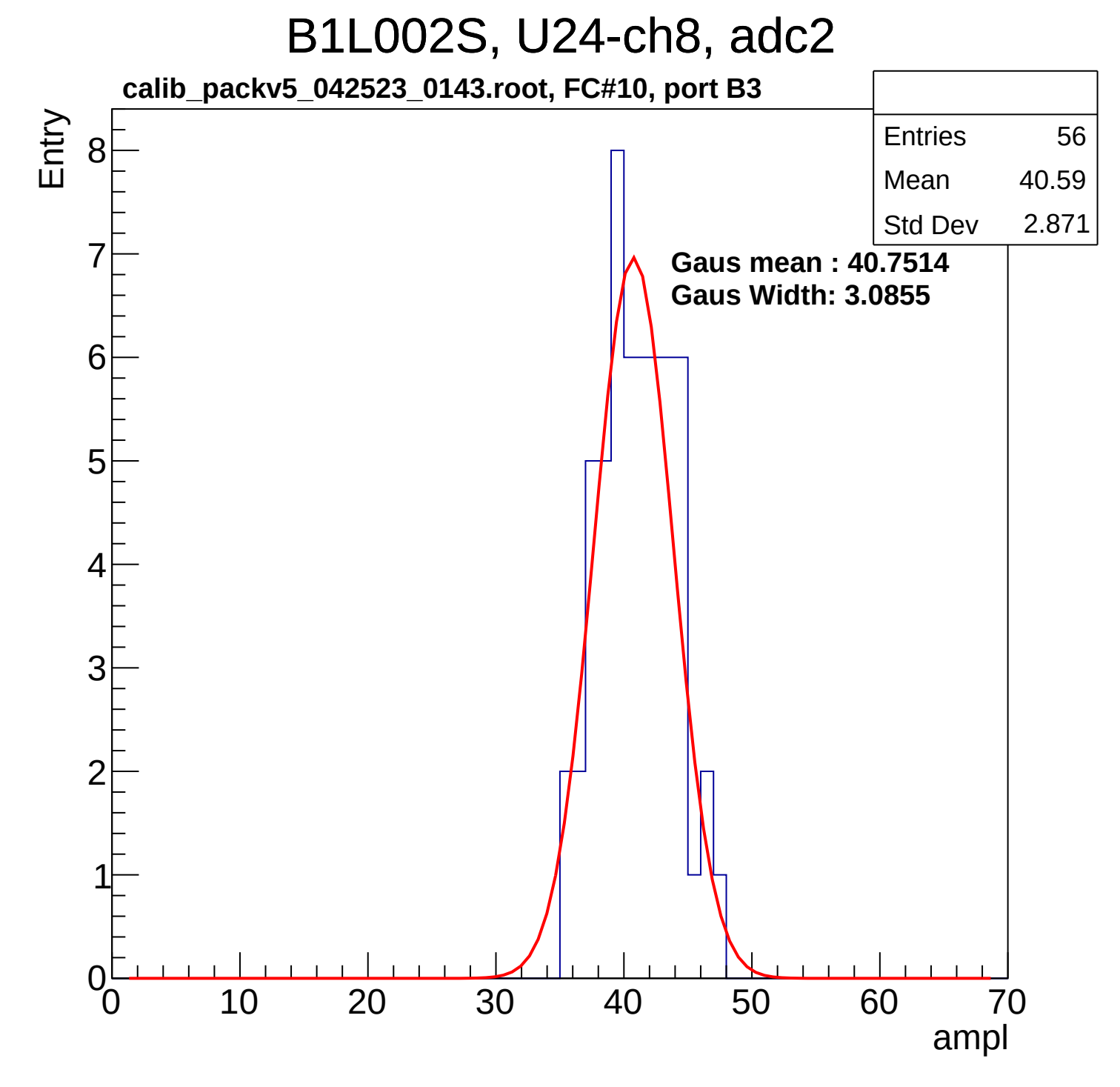
B1L002S, U24-ch8, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.59
Std Dev 2.871
Gaus mean : 40.7514
Gaus Width: 3.0855
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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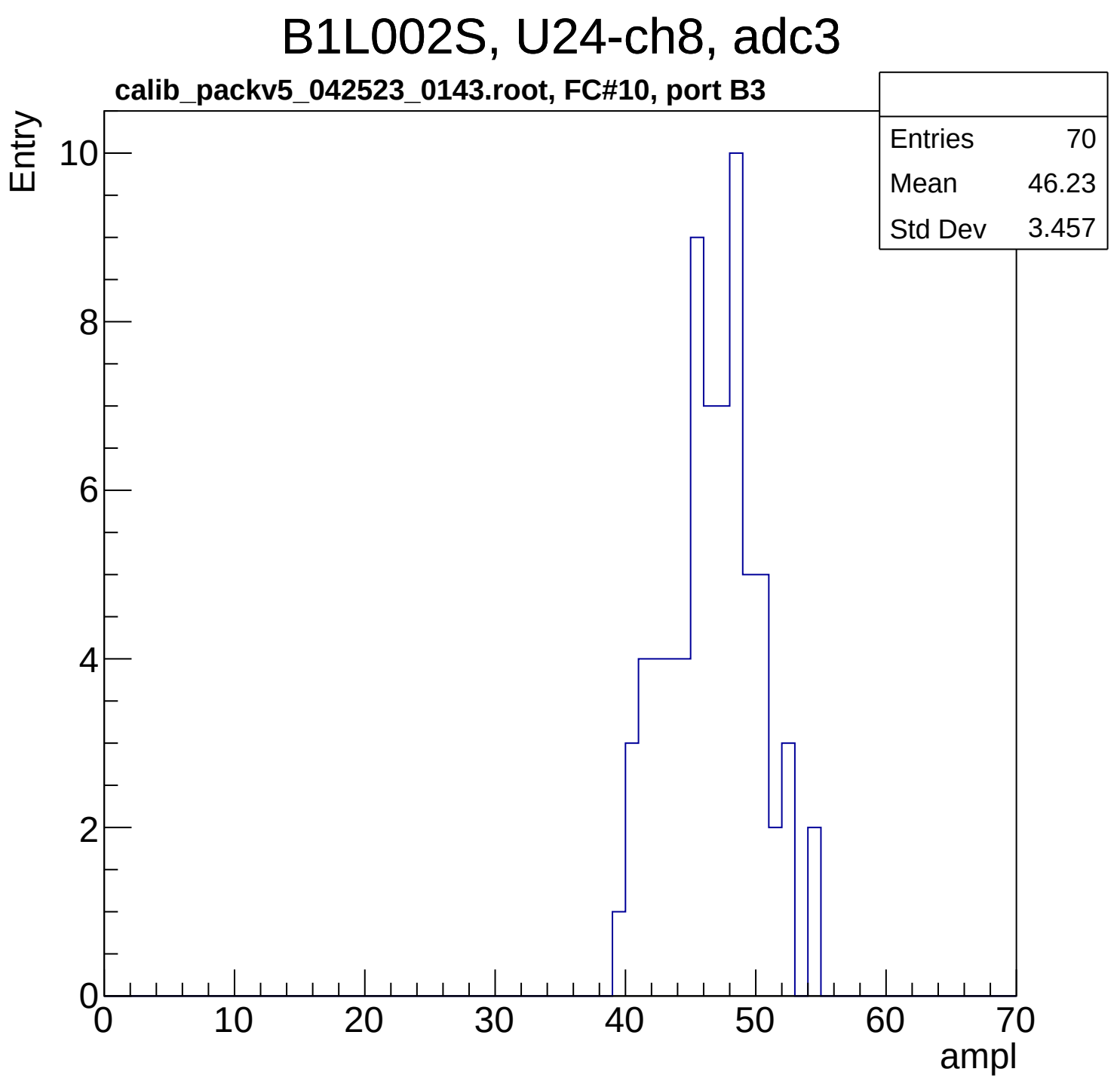
B1L002S, U24-ch8, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.23
Std Dev 3.457
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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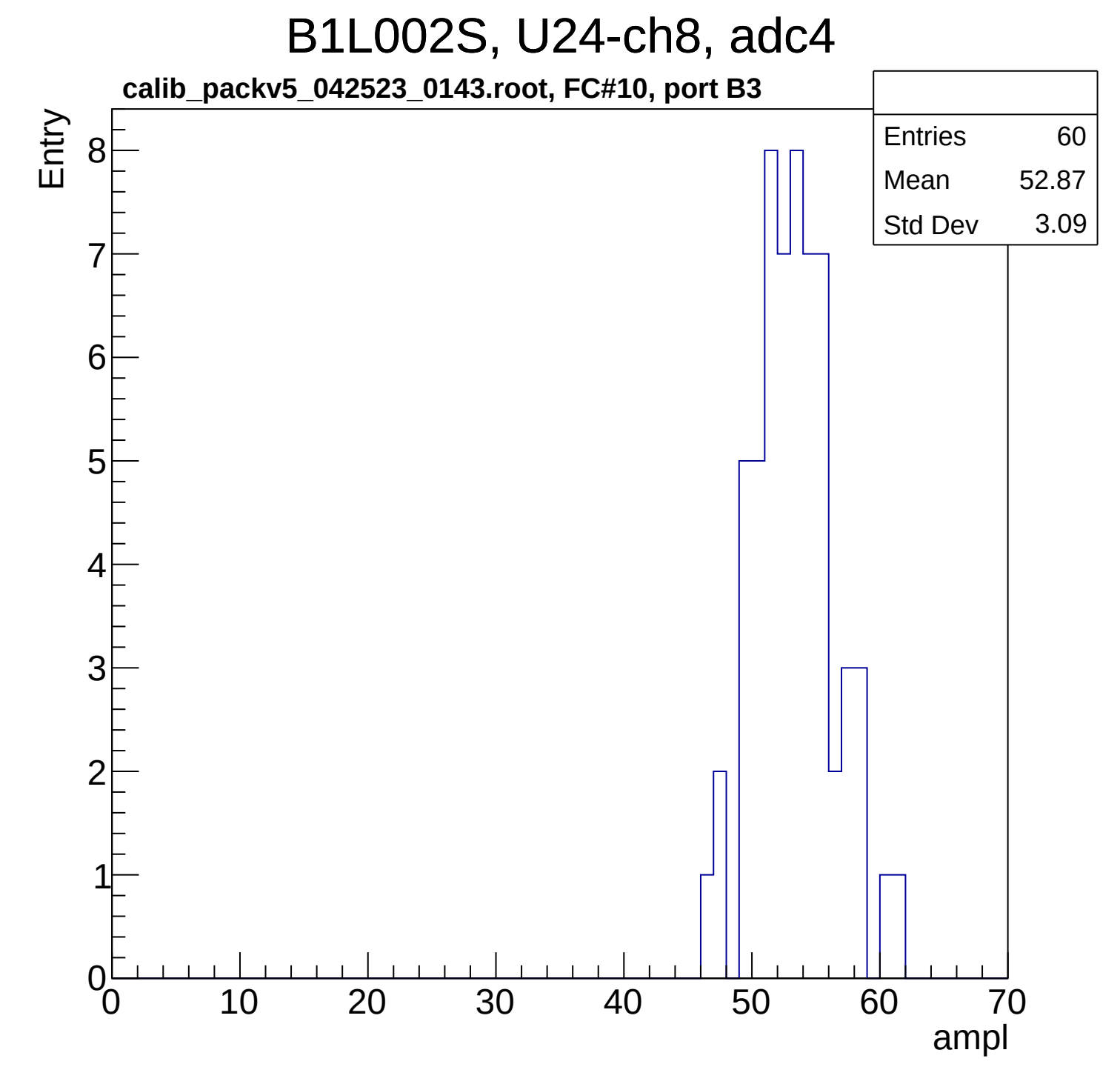
B1L002S, U24-ch8, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.87
Std Dev 3.09
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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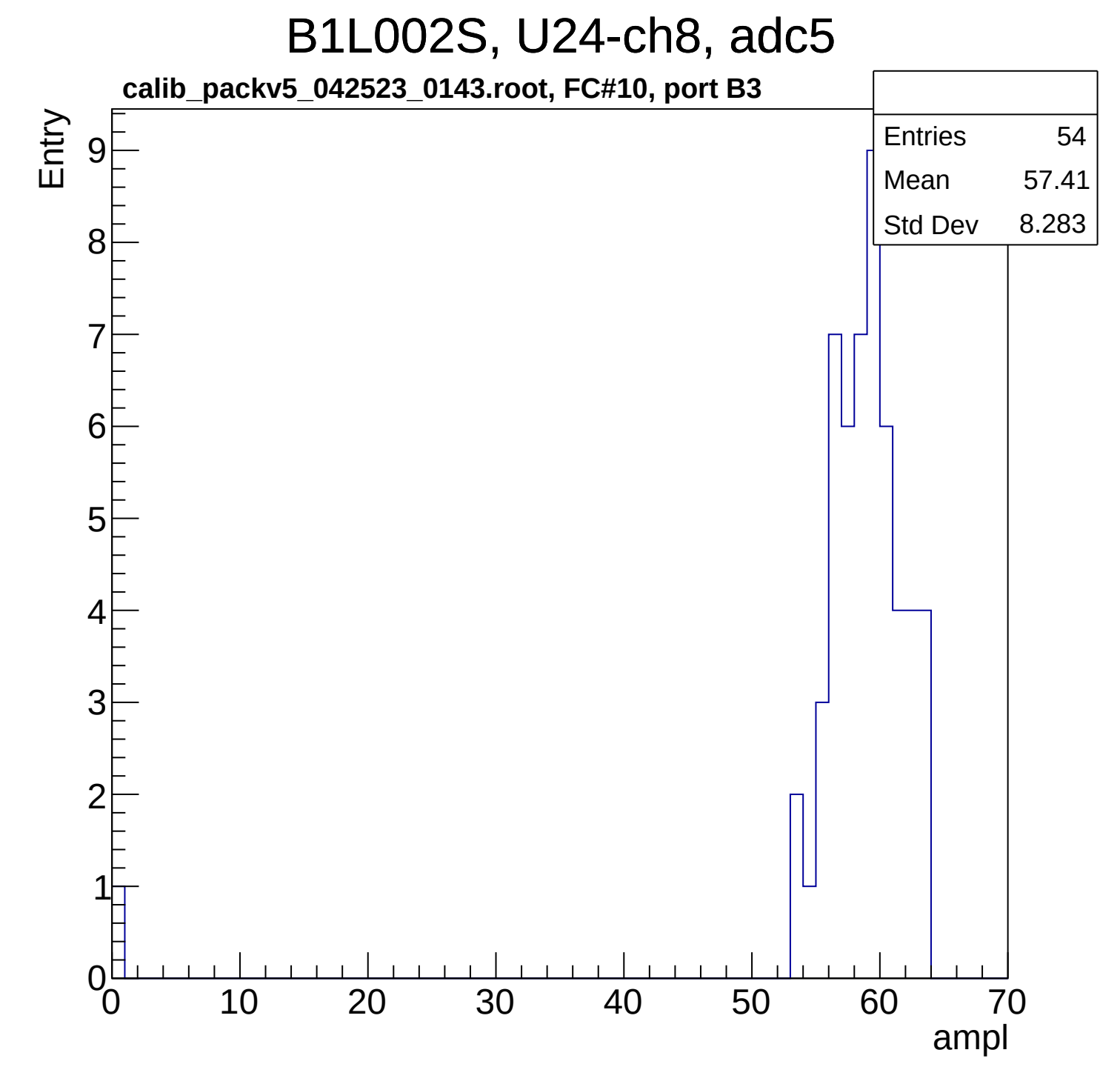
B1L002S, U24-ch8, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.41
Std Dev 8.283
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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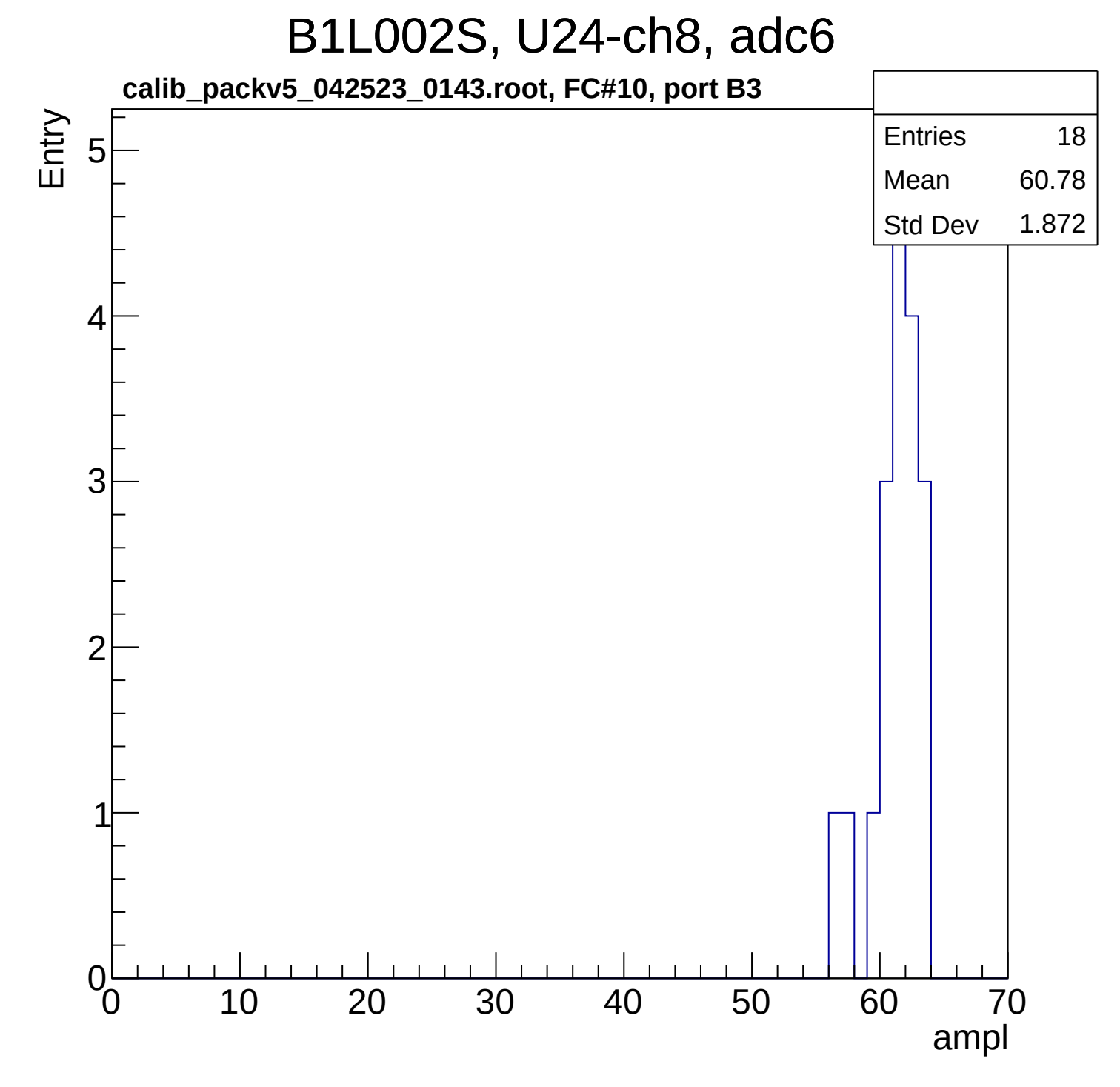
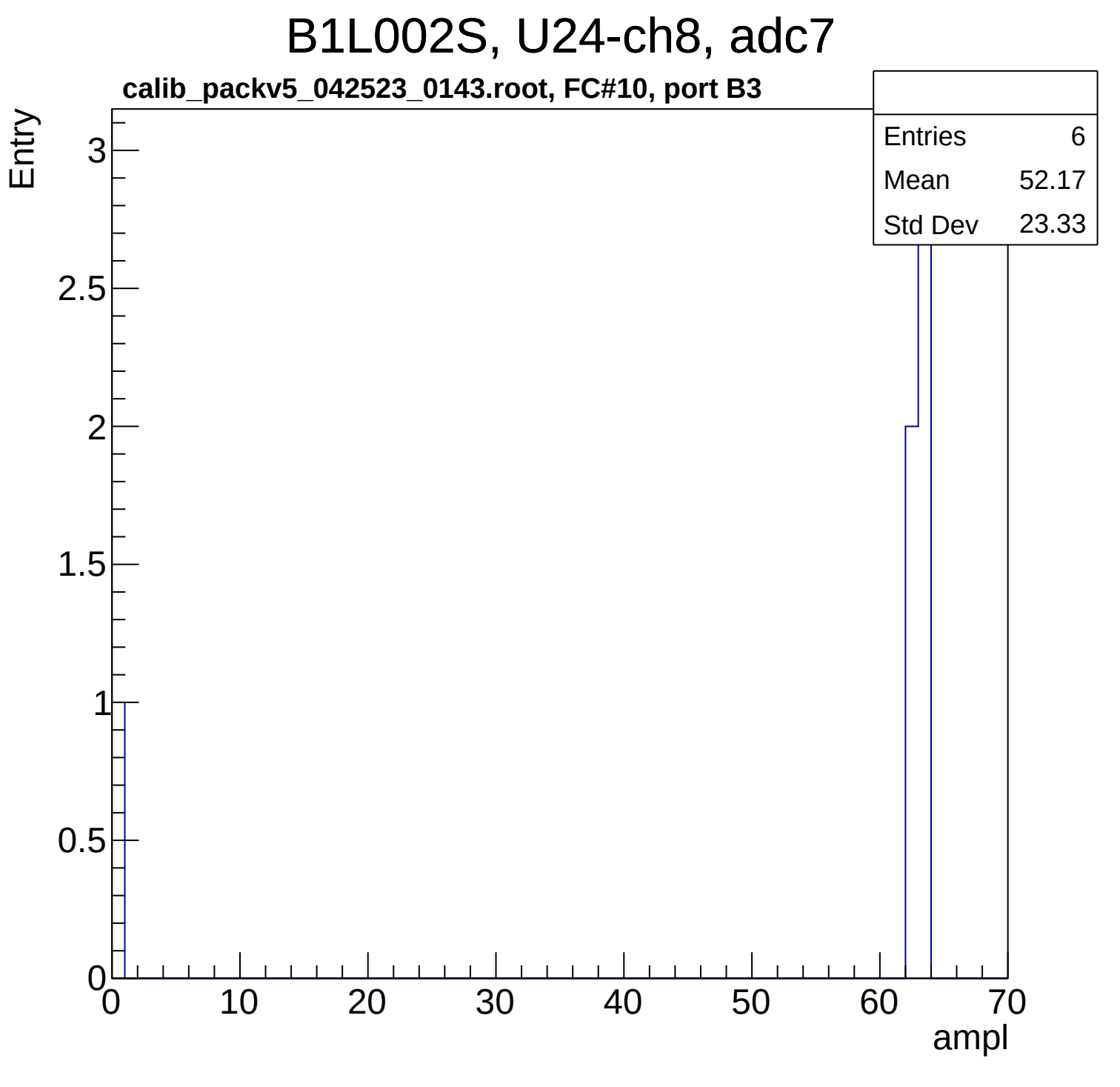

B1L002S, U24-ch8, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 60.78
Std Dev 1.872
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch8, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 52.17
Std Dev 23.33
Entry
ampl
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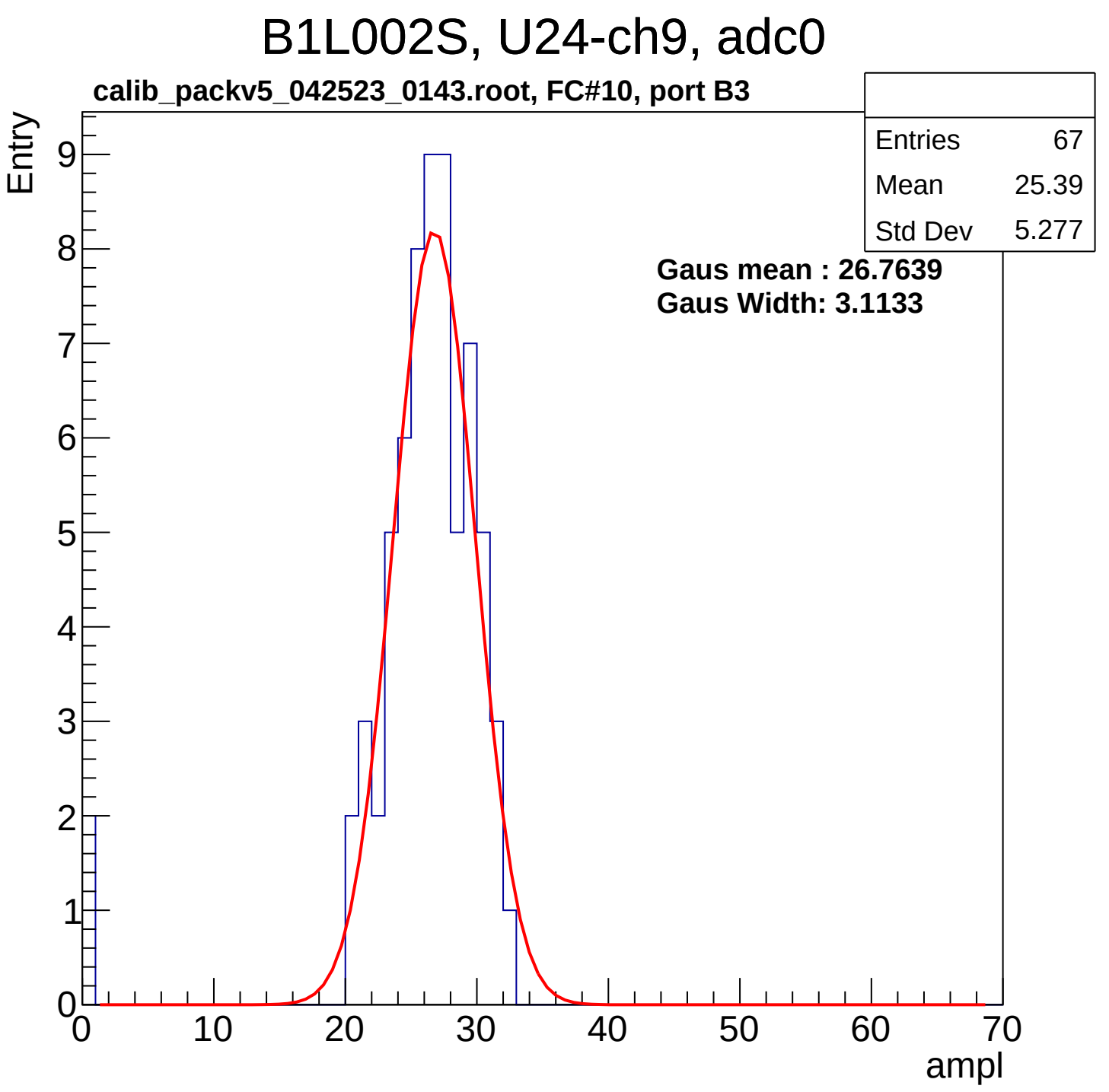
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch9, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 25.39
Std Dev 5.277
Gaus mean : 26.7639
Gaus Width: 3.1133
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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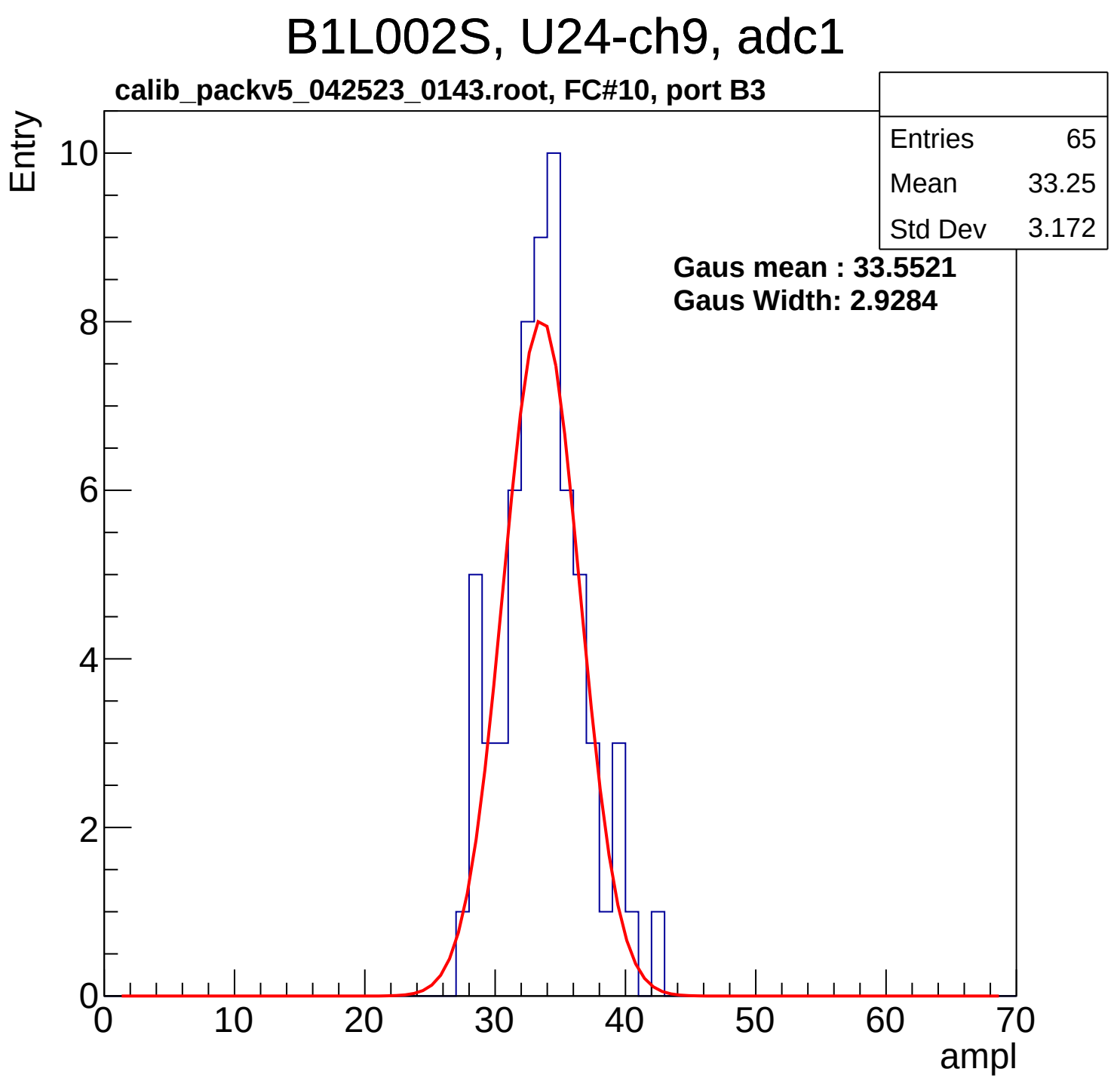
B1L002S, U24-ch9, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.25
Std Dev 3.172
Gaus mean : 33.5521
Gaus Width: 2.9284
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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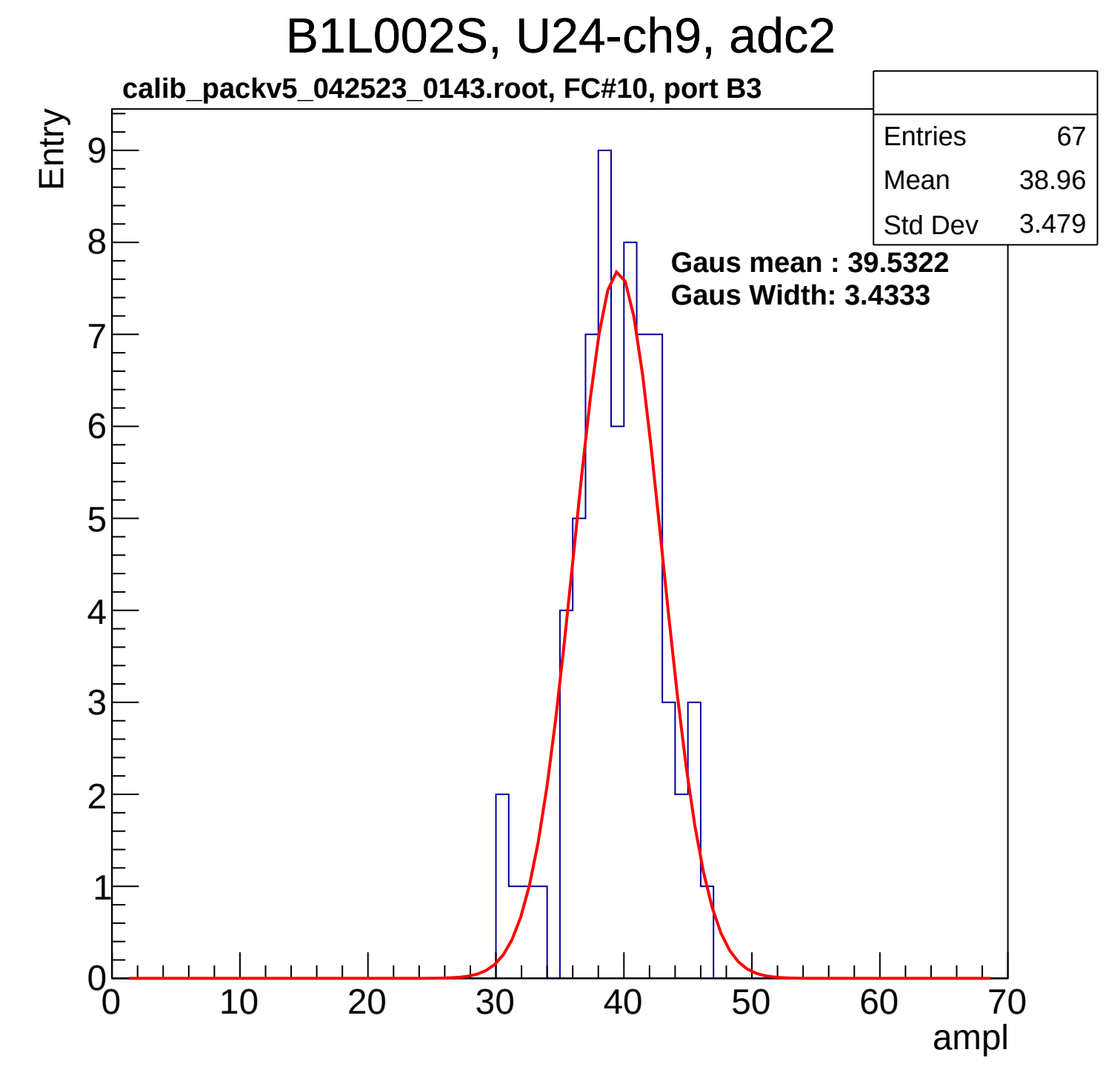
B1L002S, U24-ch9, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 38.96
Std Dev 3.479
Gaus mean : 39.5322
Gaus Width: 3.4333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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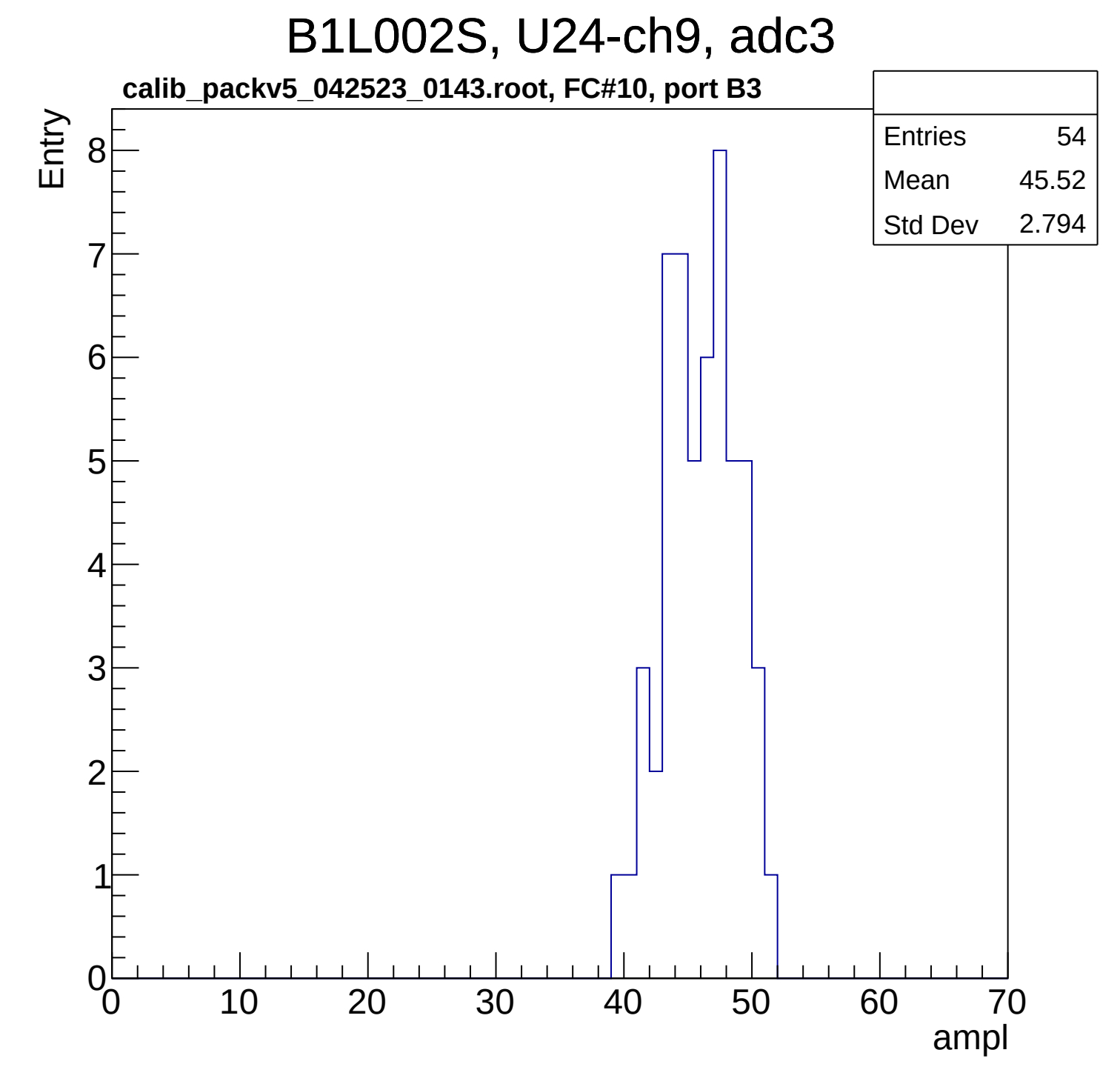
B1L002S, U24-ch9, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 45.52
Std Dev 2.794
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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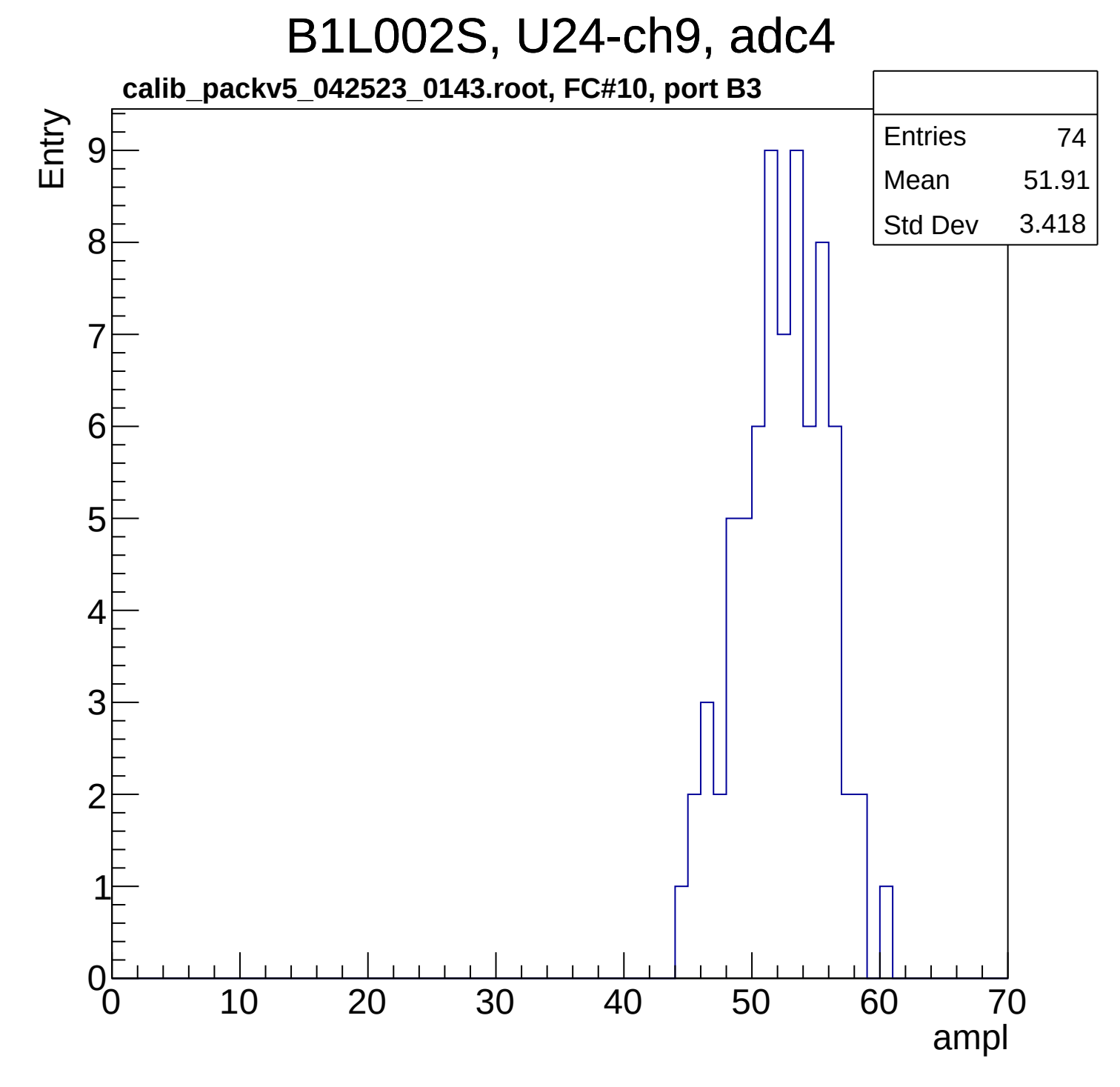
B1L002S, U24-ch9, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 51.91
Std Dev 3.418
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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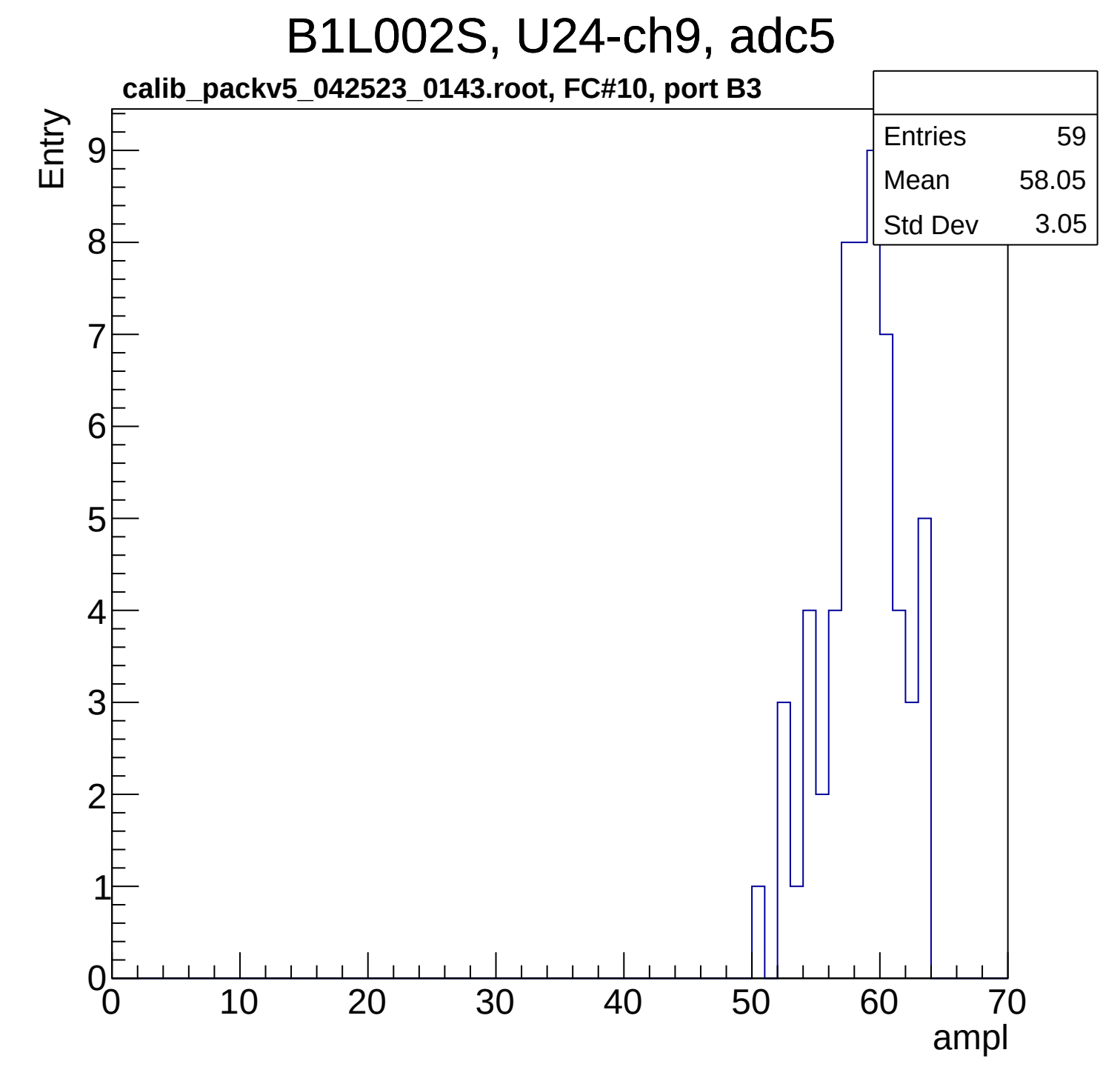
B1L002S, U24-ch9, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.05
Std Dev 3.05
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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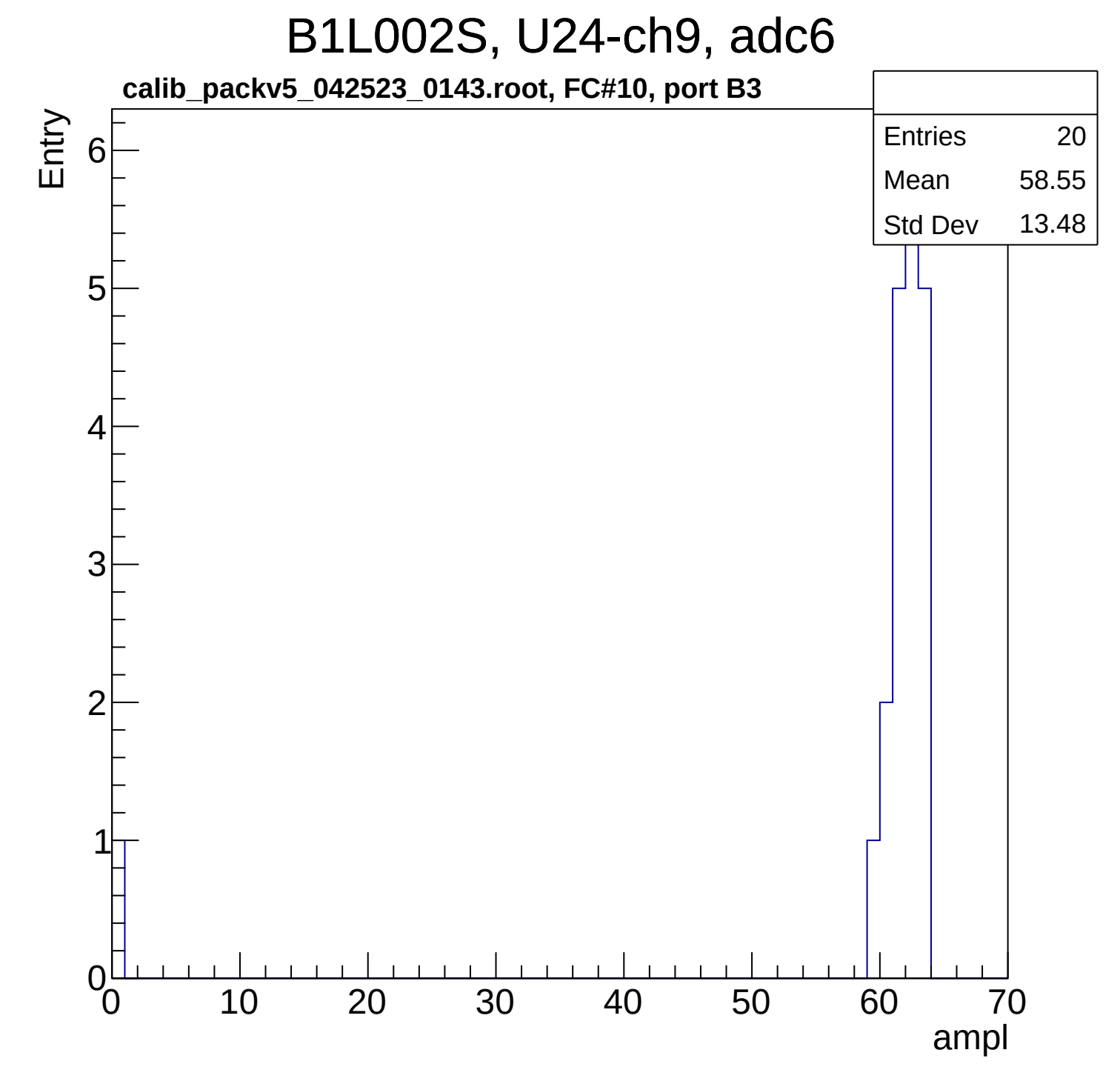
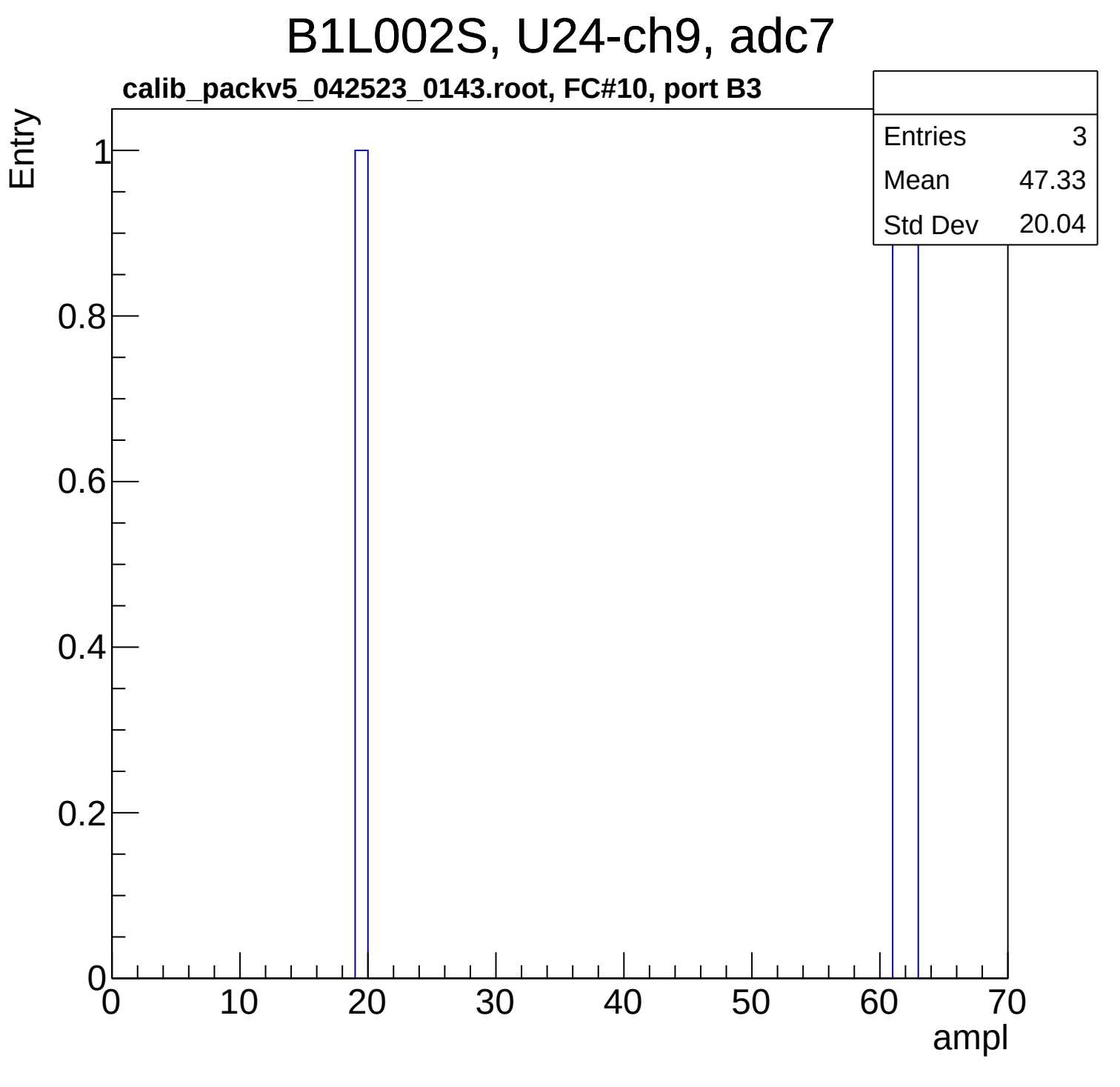

B1L002S, U24-ch9, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.55
Std Dev 13.48
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch9, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 47.33
Std Dev 20.04
Entry
ampl
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0.2
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0.6
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0
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20
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40
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60
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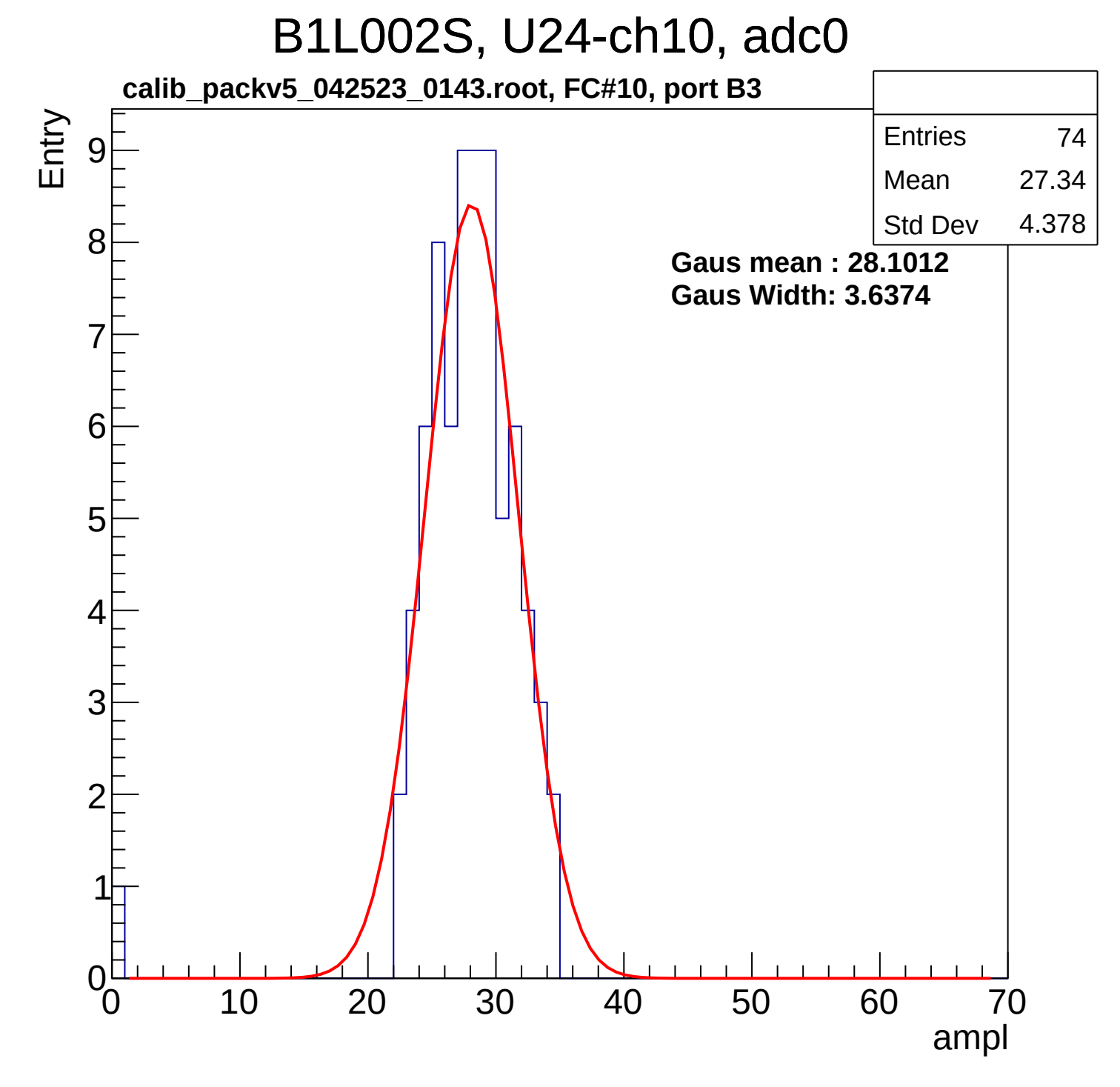
B1L002S, U24-ch10, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.34
Std Dev 4.378
Gaus mean : 28.1012
Gaus Width: 3.6374
Entry
ampl
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2
3
4
5
6
7
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0
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60
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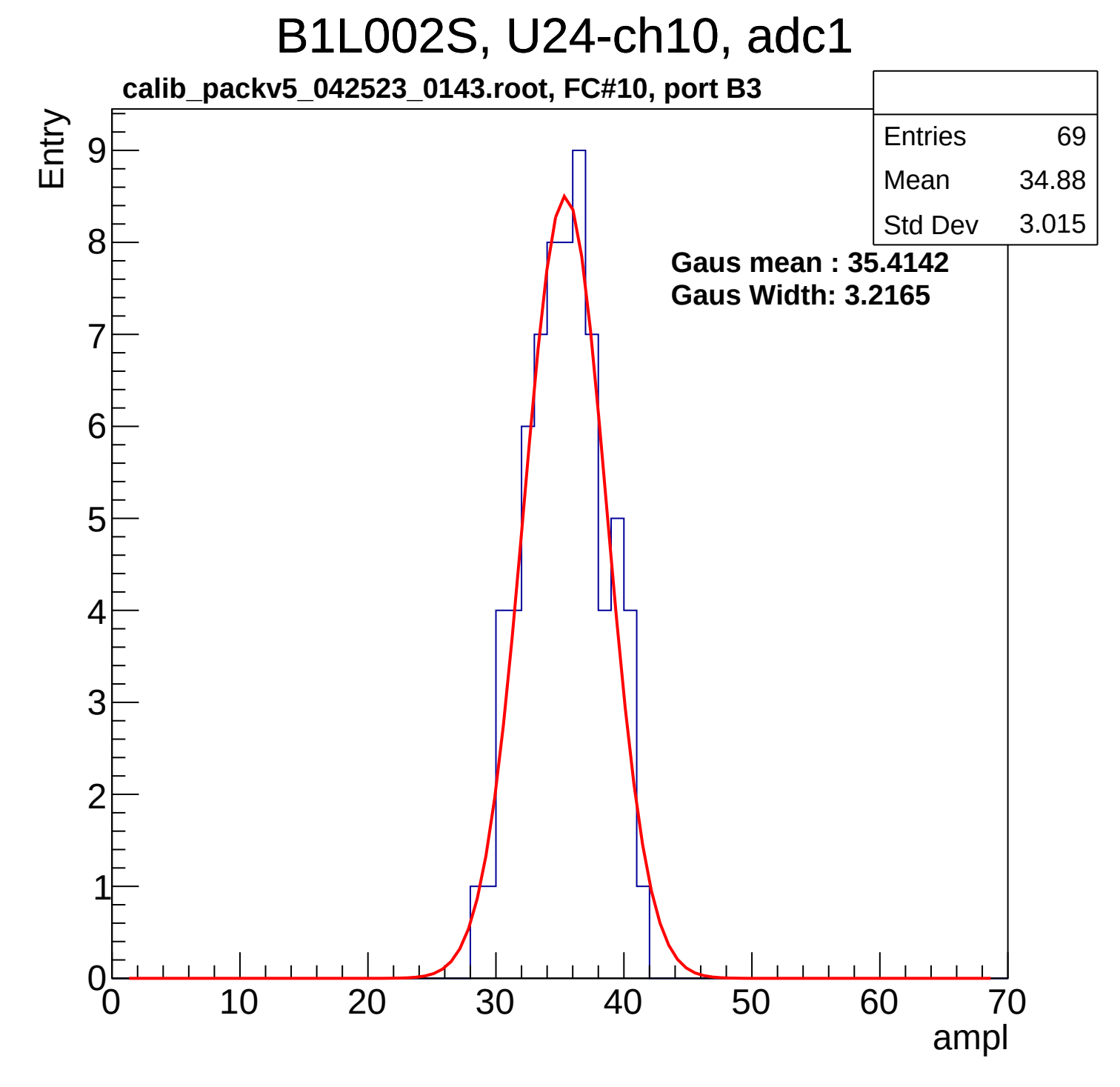
B1L002S, U24-ch10, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.88
Std Dev 3.015
Gaus mean : 35.4142
Gaus Width: 3.2165
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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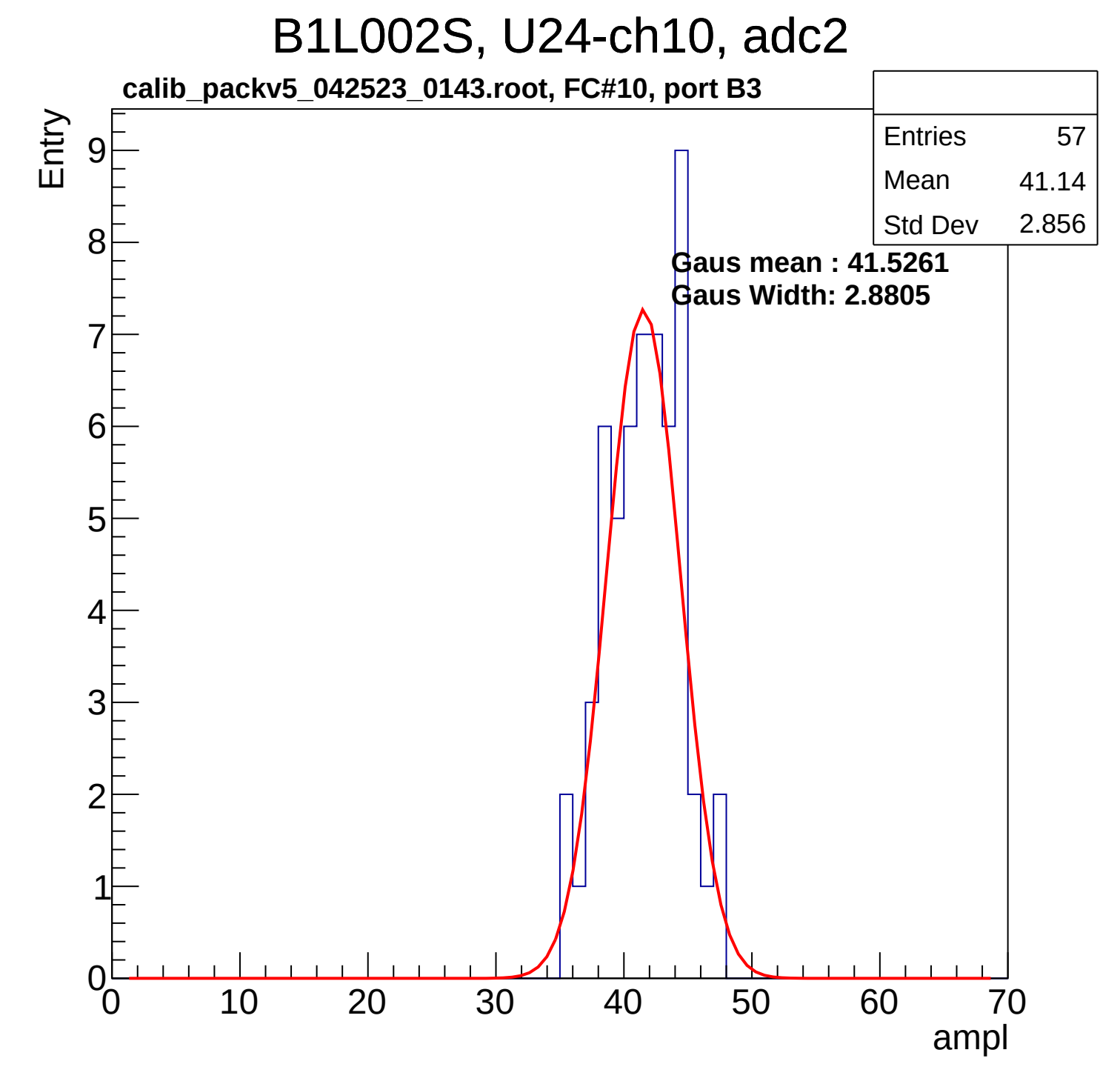
B1L002S, U24-ch10, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.14
Std Dev 2.856
Gaus mean : 41.5261
Gaus Width: 2.8805
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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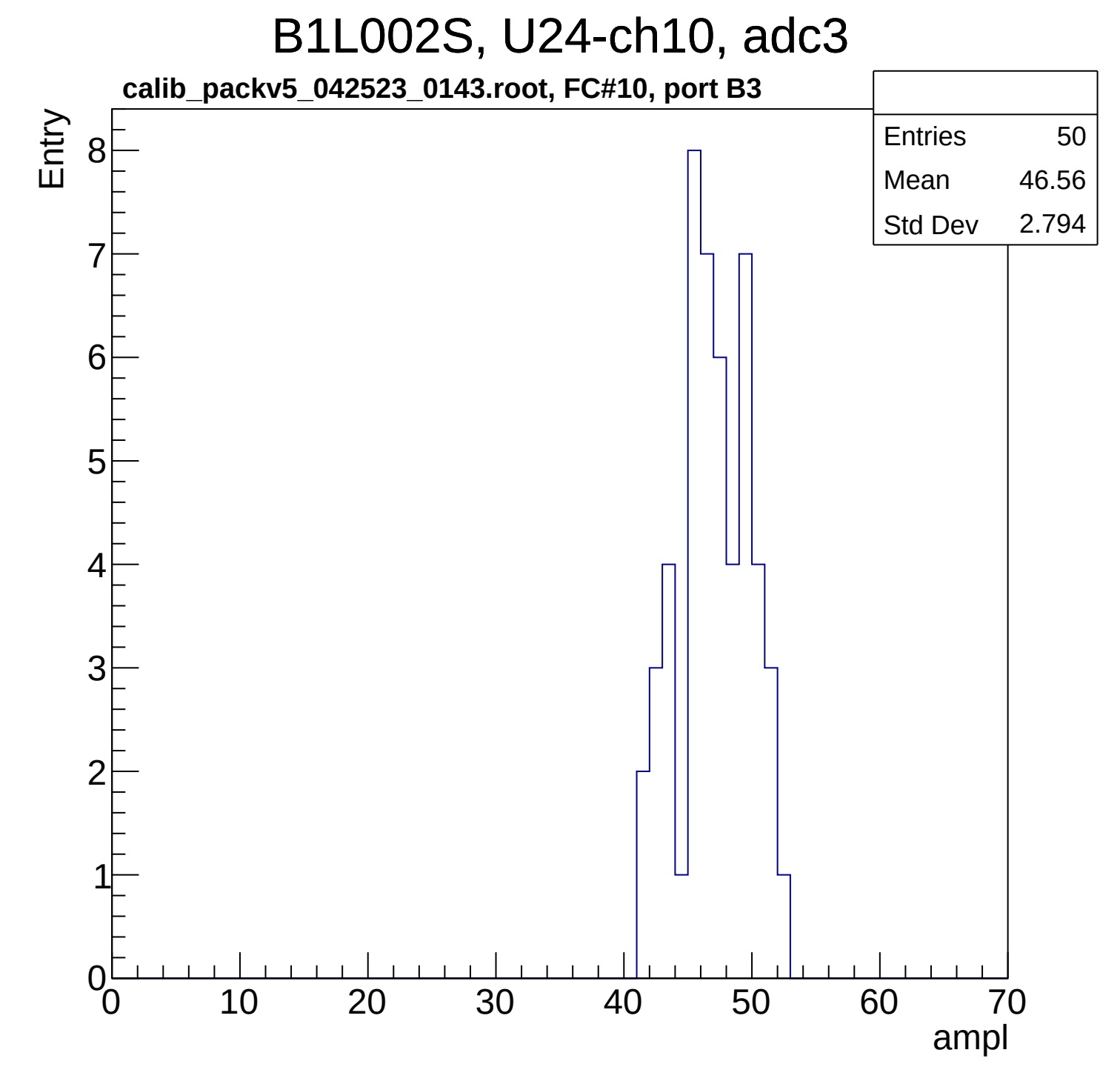
B1L002S, U24-ch10, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 46.56
Std Dev 2.794
Entry
ampl
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1
2
3
4
5
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7
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0
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20
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60
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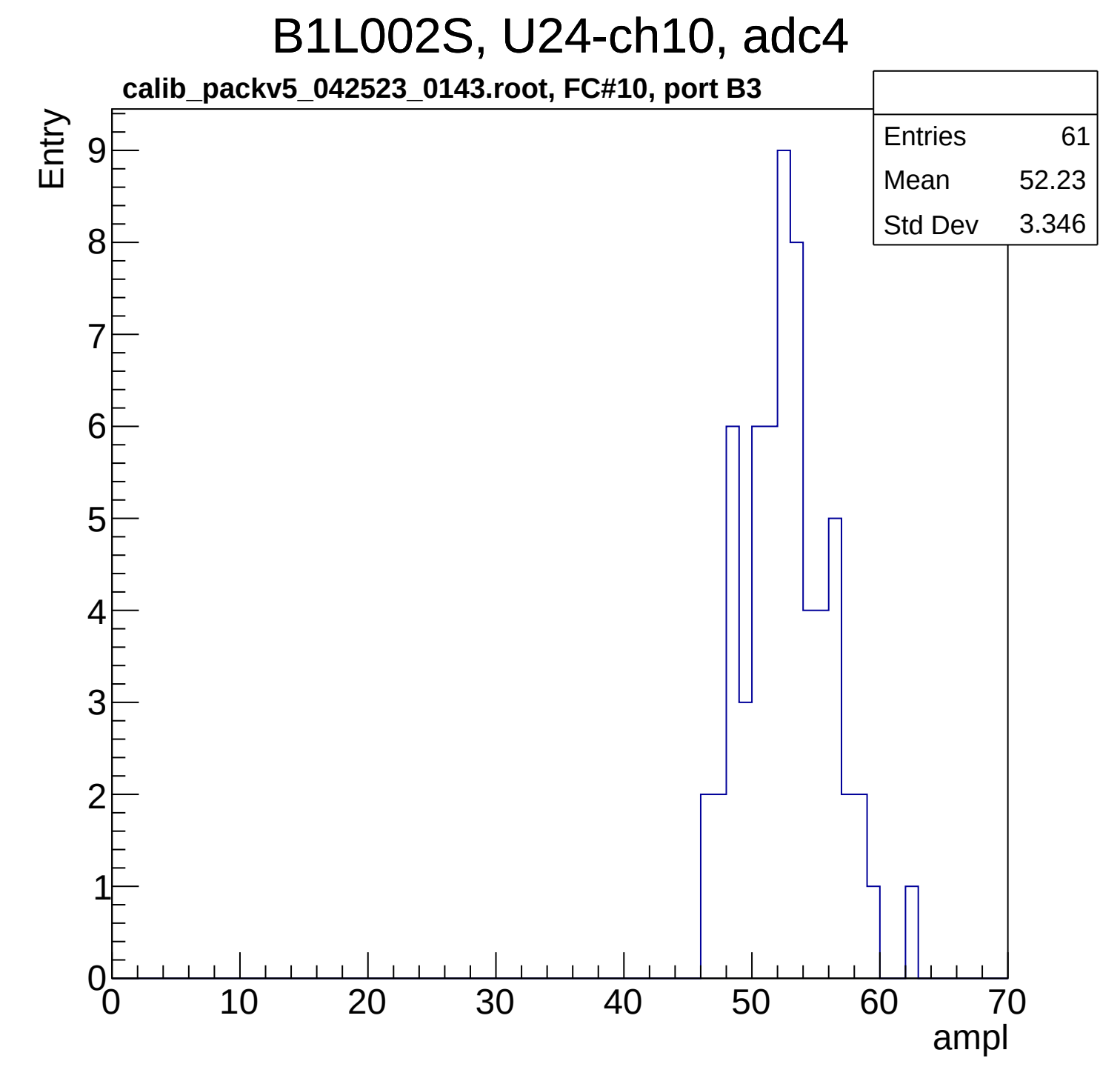
B1L002S, U24-ch10, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.23
Std Dev 3.346
Entry
ampl
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5
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8
9
0
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60
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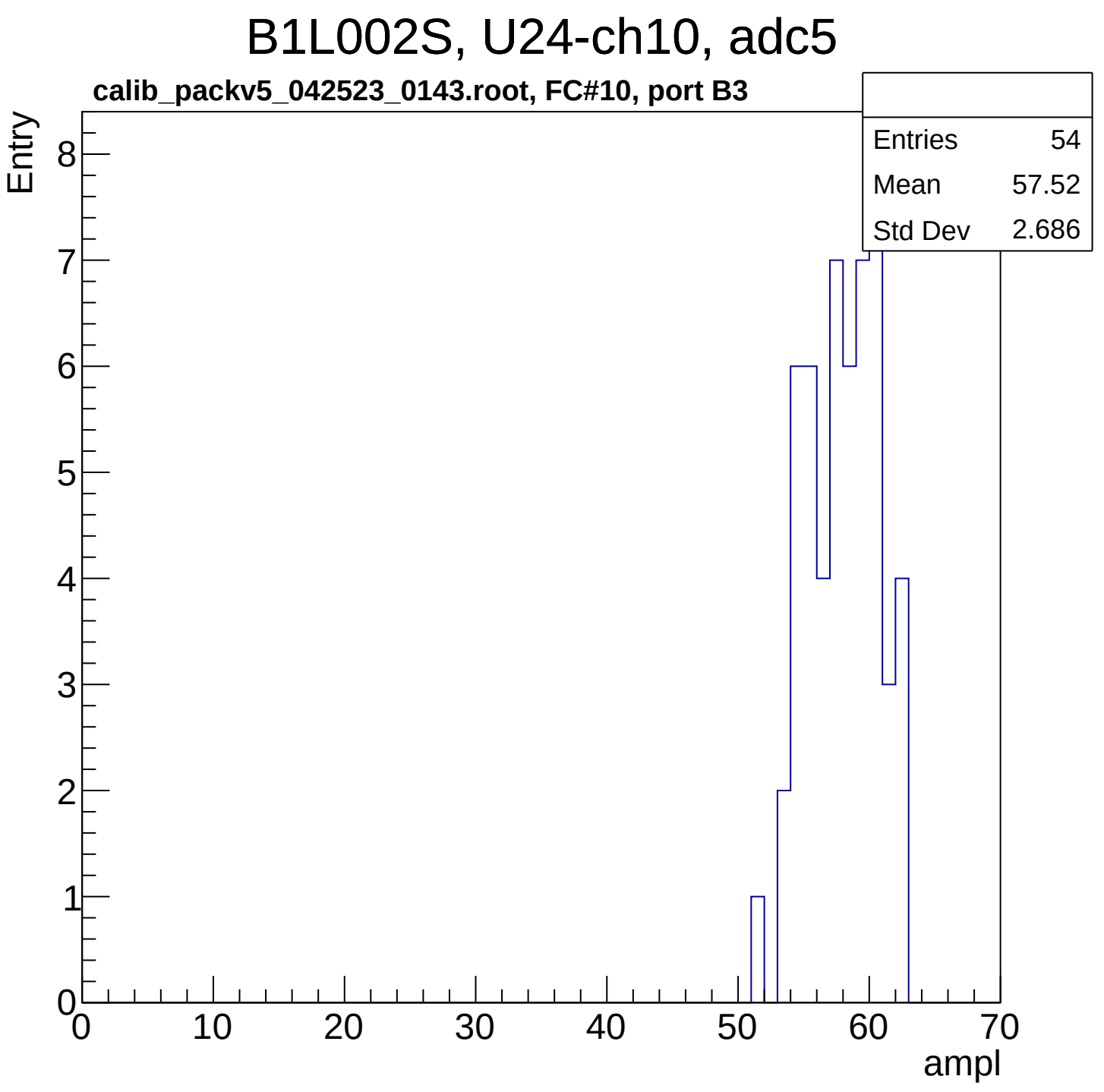
B1L002S, U24-ch10, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.52
Std Dev 2.686
Entry
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2
3
4
5
6
7
8
0
10
20
30
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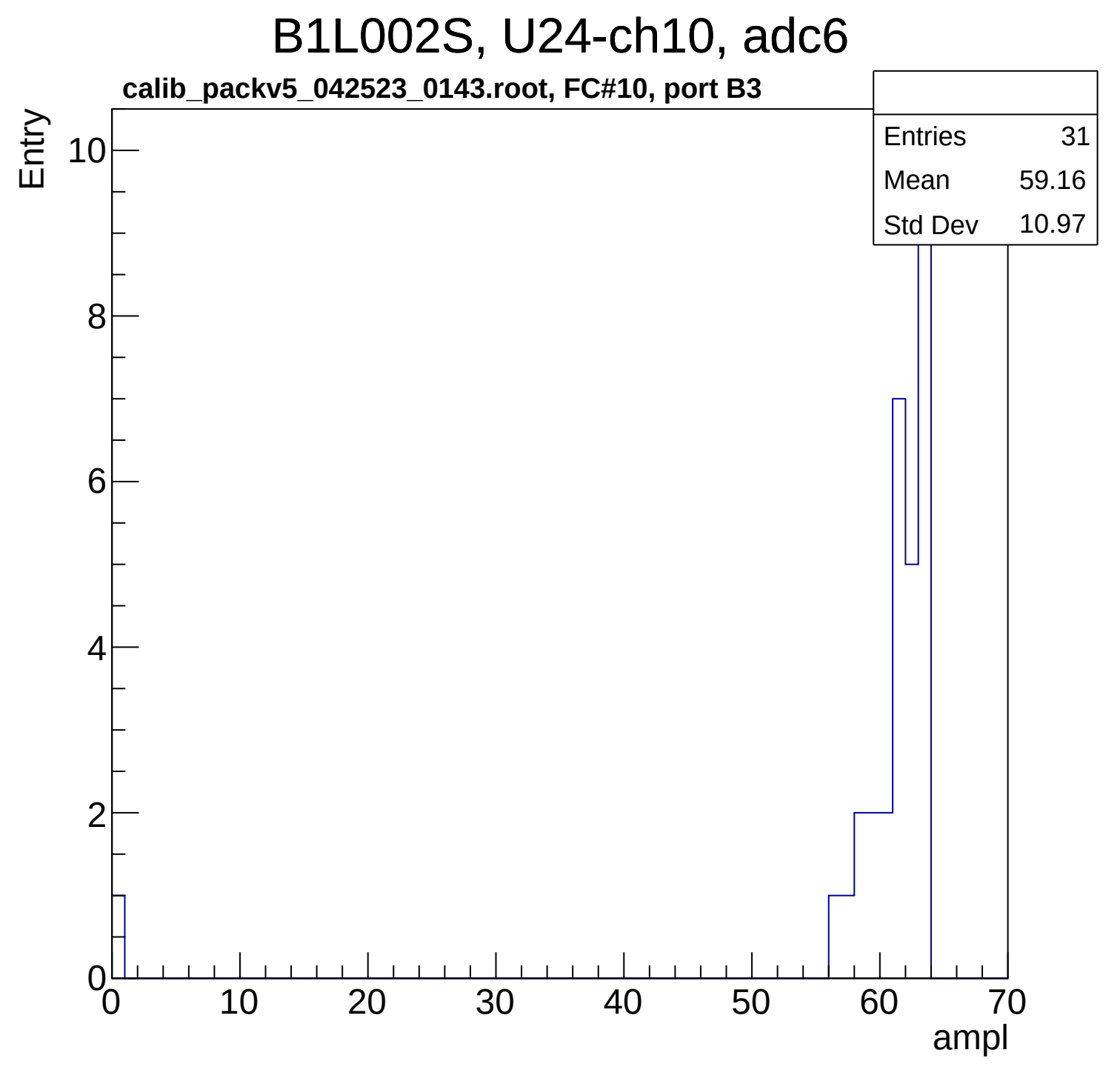
50
60
70
ampl


B1L002S, U24-ch10, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 31
Mean 59.16
Std Dev 10.97
Entry
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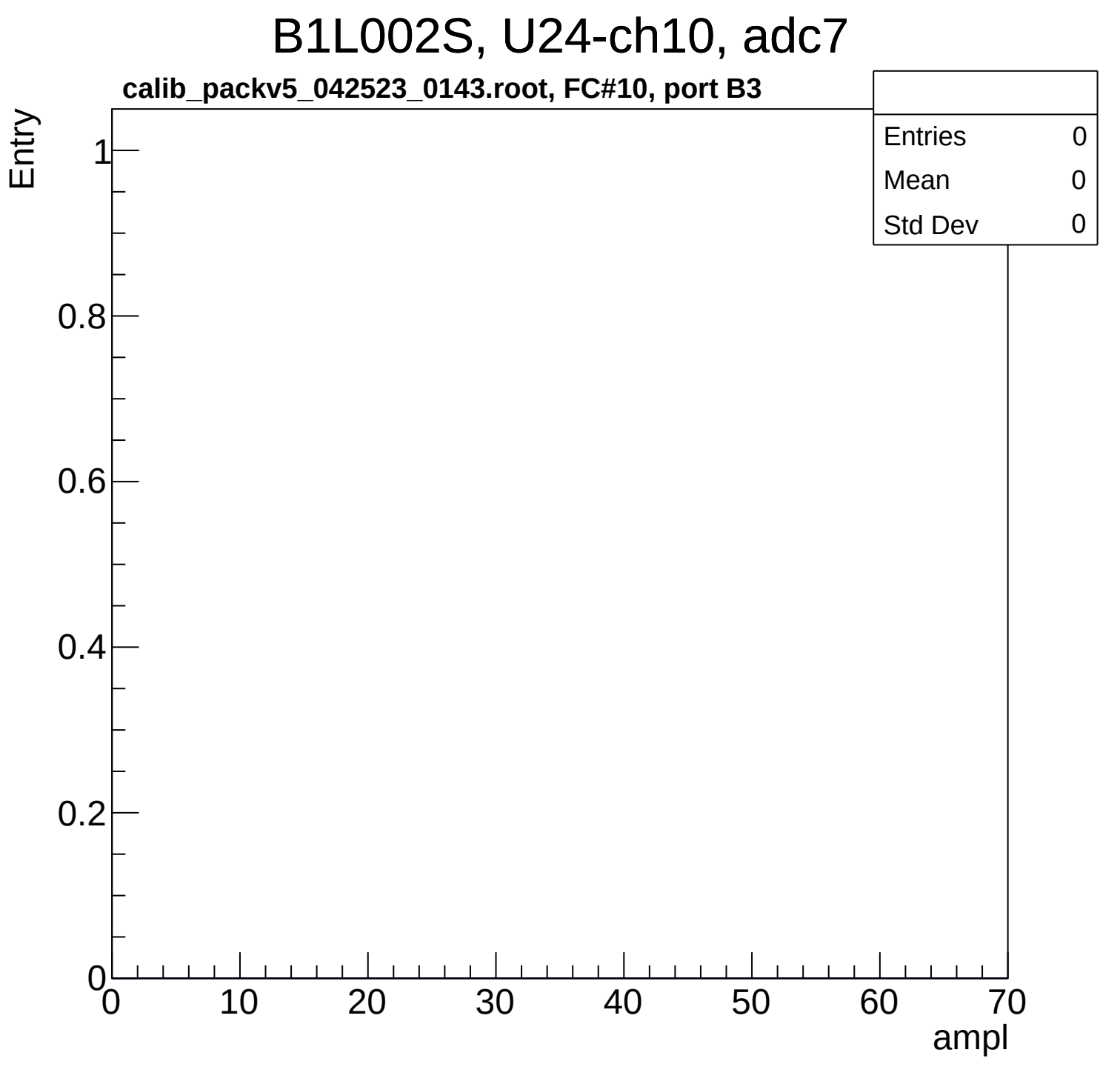
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch10, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.4
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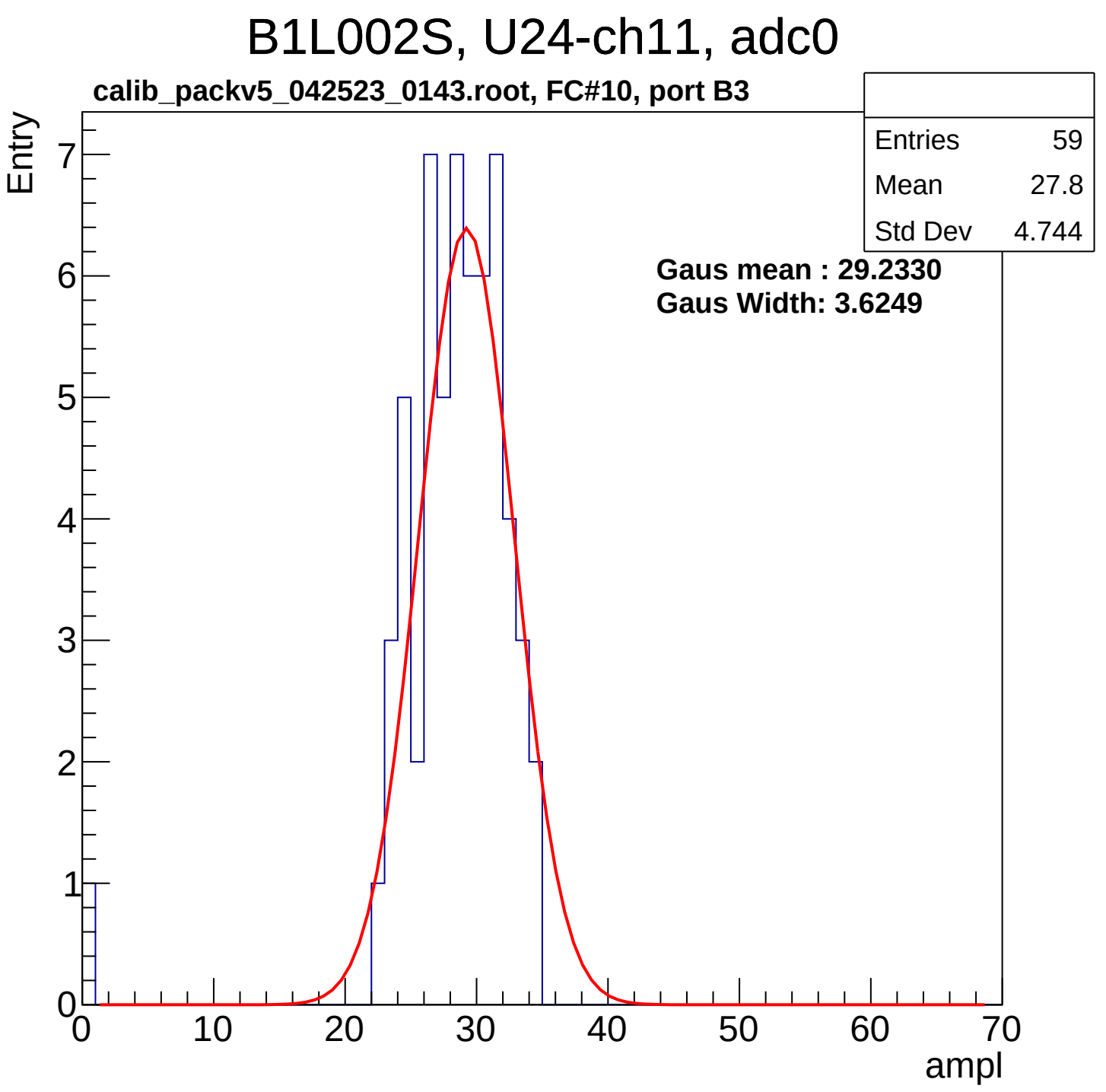
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch11, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.8
Std Dev 4.744
Gaus mean : 29.2330
Gaus Width: 3.6249
Entry
ampl
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3
4
5
6
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0
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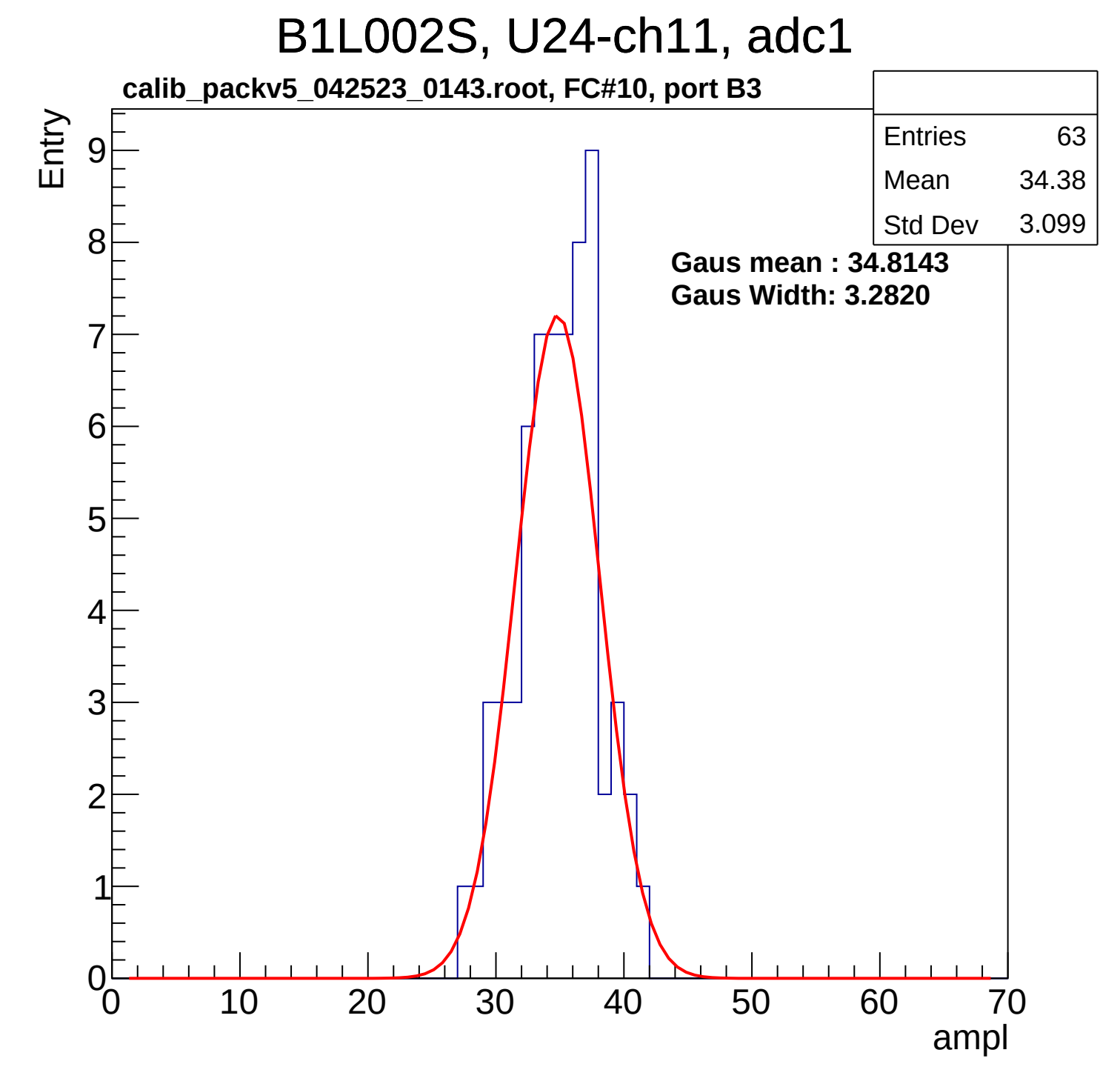
B1L002S, U24-ch11, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.38
Std Dev 3.099
Gaus mean : 34.8143
Gaus Width: 3.2820
Entry
ampl
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7
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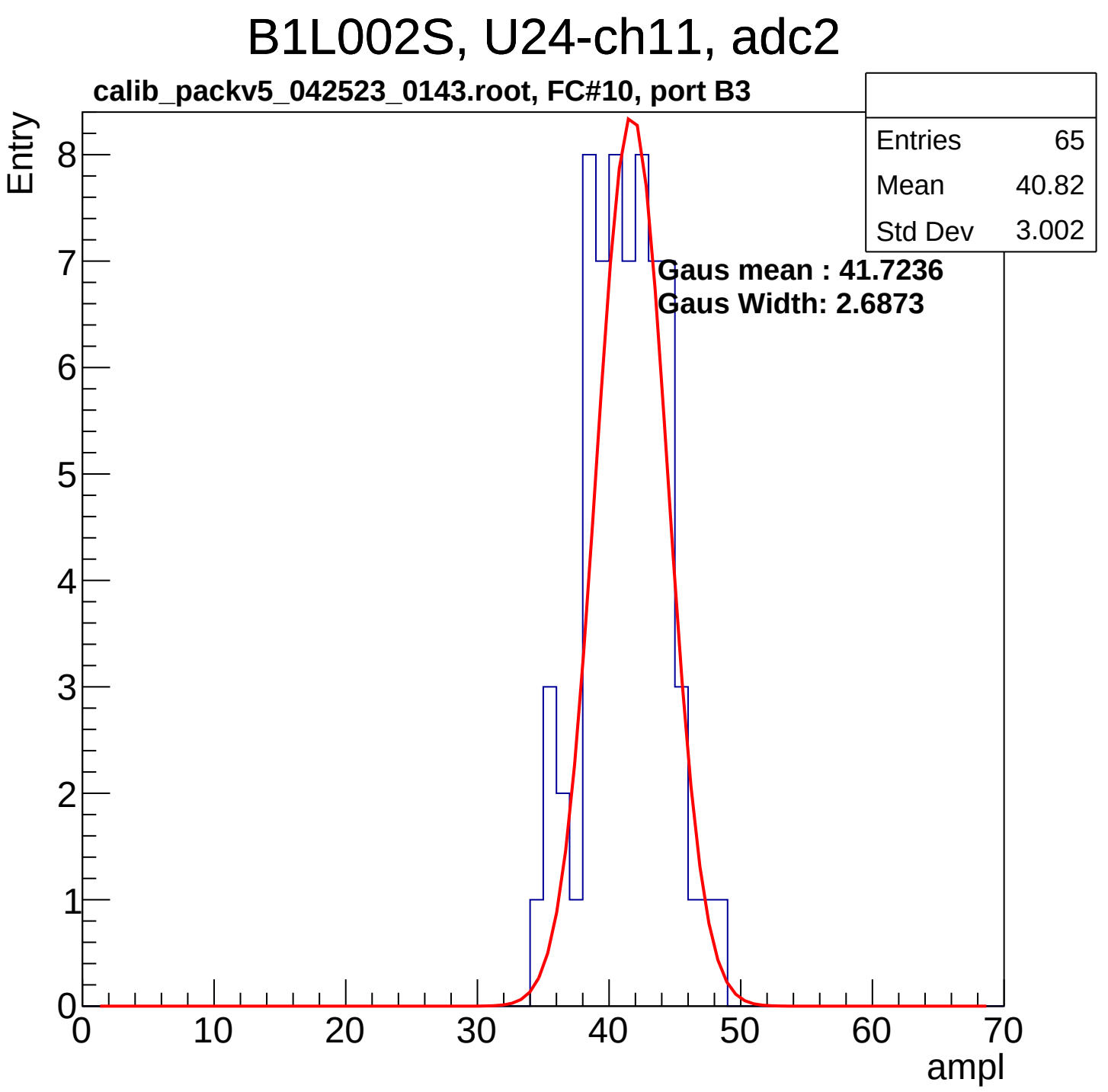
B1L002S, U24-ch11, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.82
Std Dev 3.002
Gaus mean : 41.7236
Gaus Width: 2.6873
Entry
ampl
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1
2
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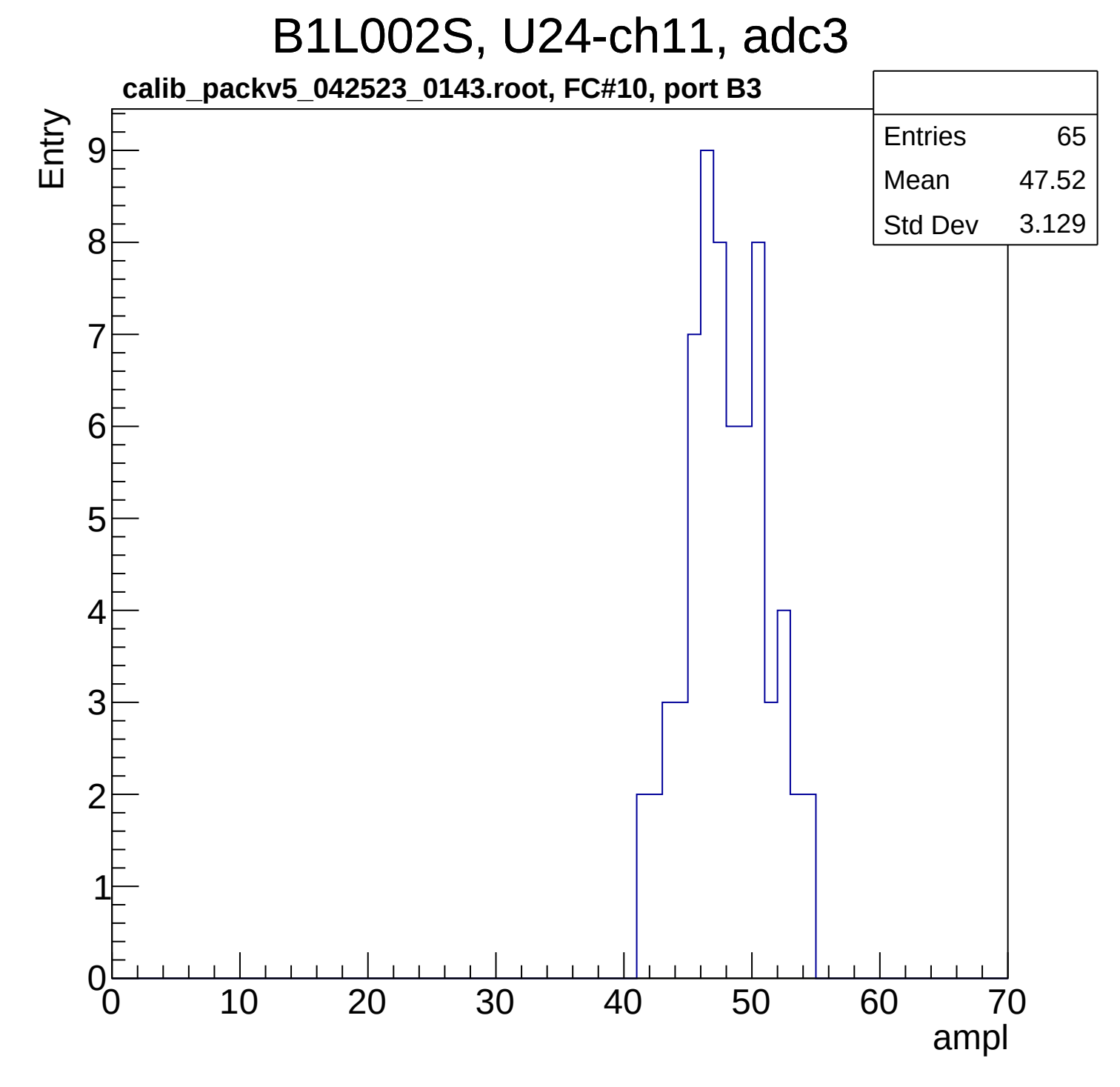
B1L002S, U24-ch11, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.52
Std Dev 3.129
Entry
ampl
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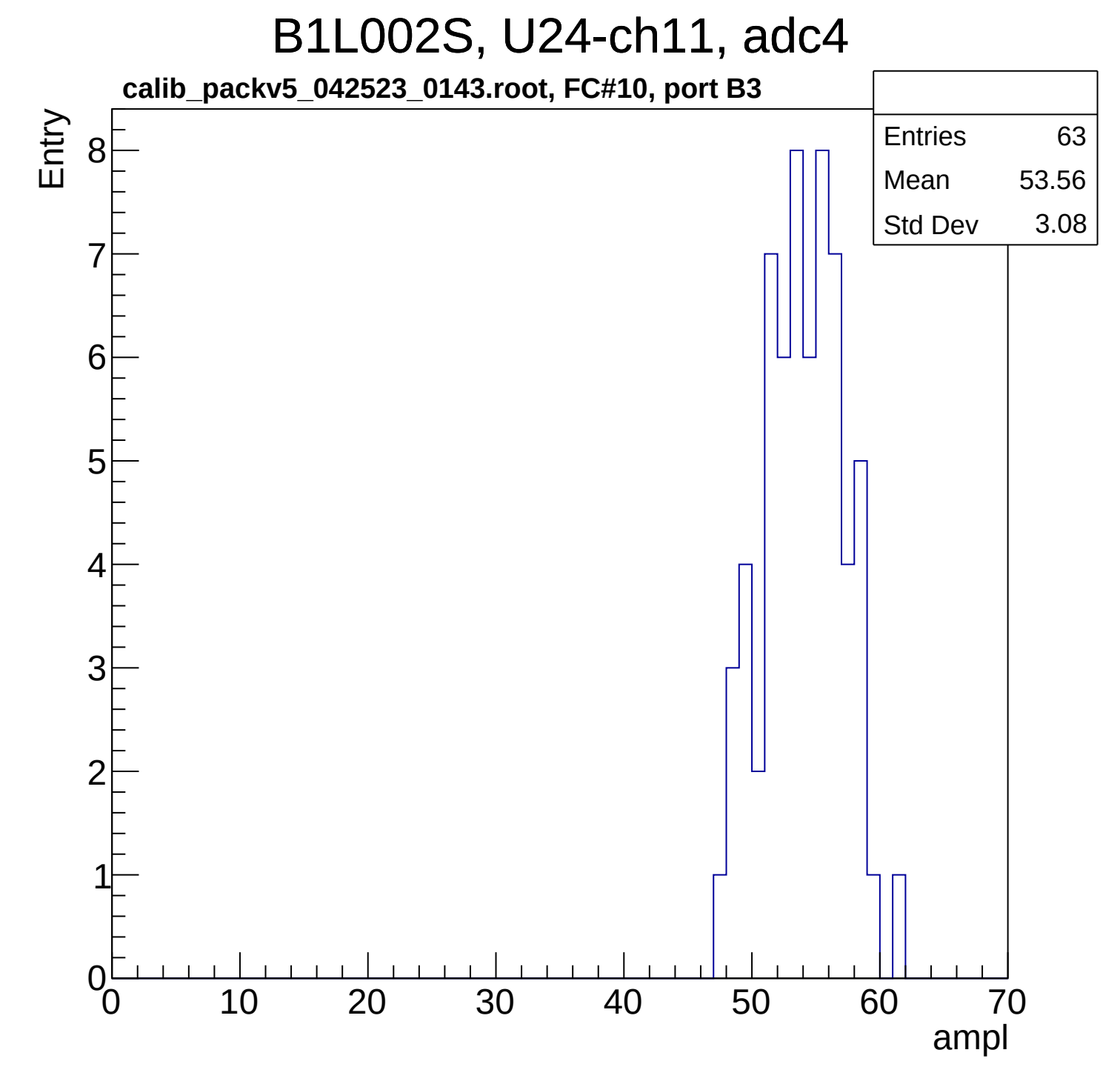
B1L002S, U24-ch11, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.56
Std Dev 3.08
Entry
ampl
0
1
2
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5
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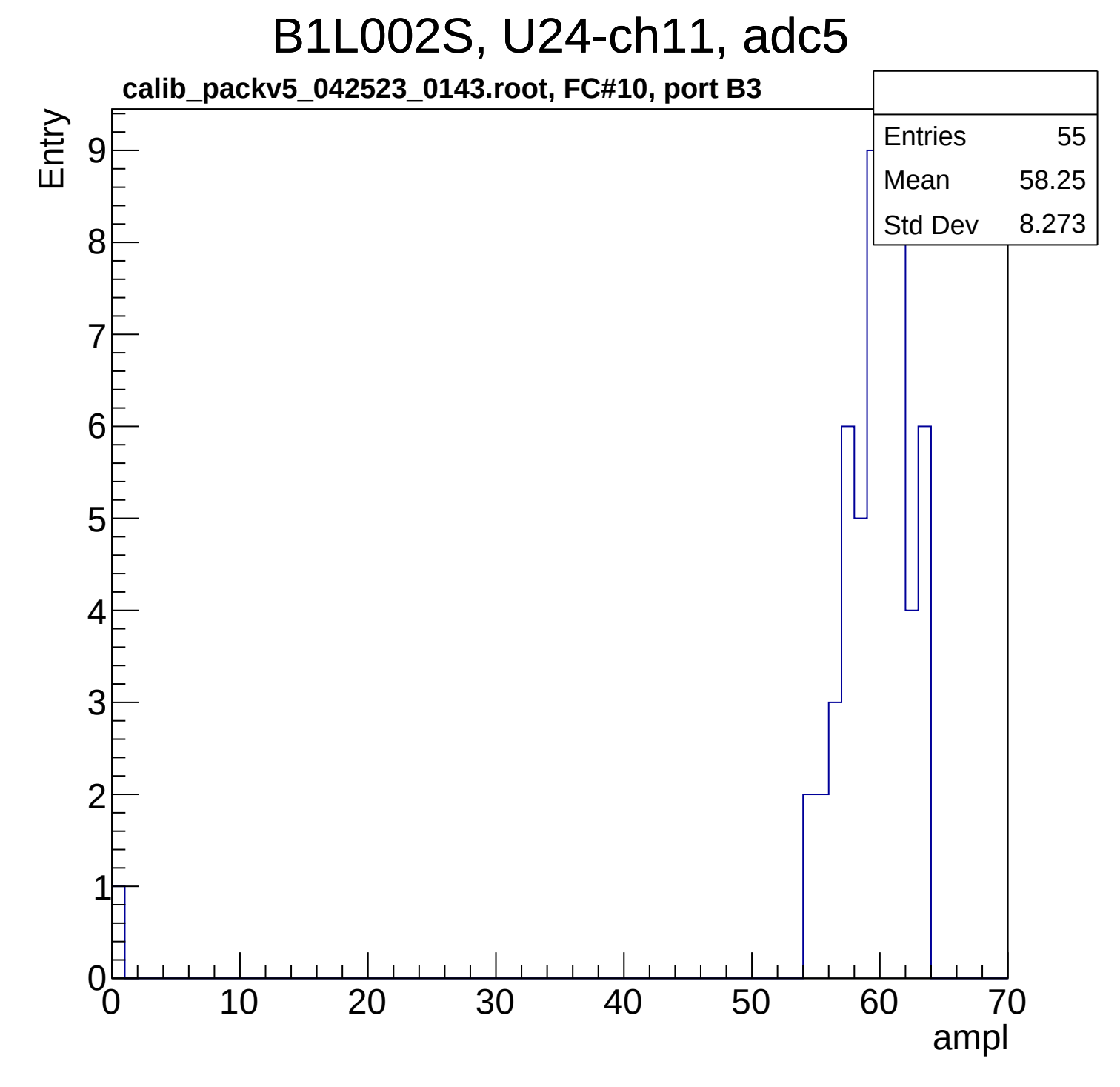
B1L002S, U24-ch11, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.25
Std Dev 8.273
Entry
ampl
0
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3
4
5
6
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8
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0
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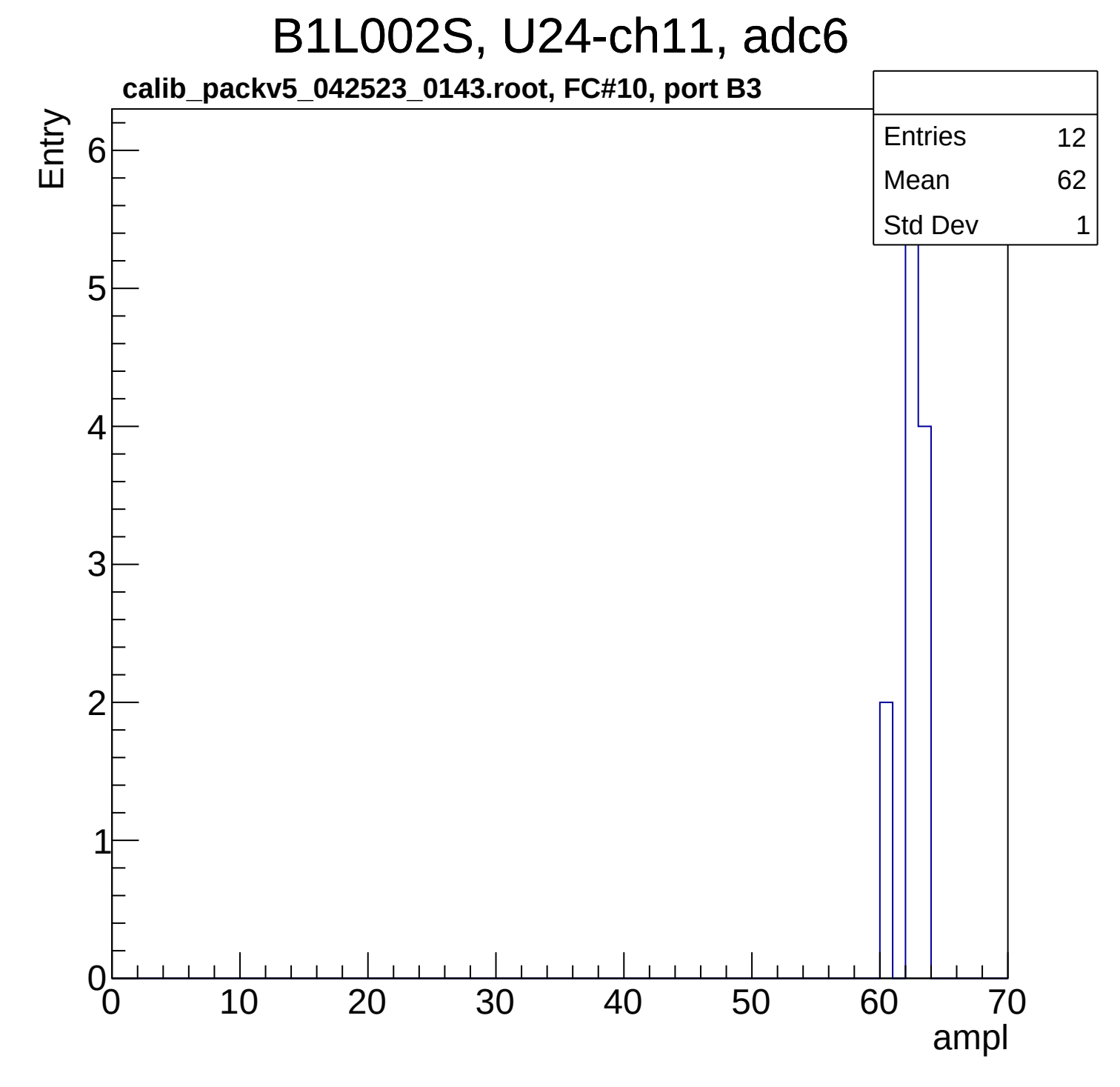
B1L002S, U24-ch11, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 62
Std Dev 1
Entry
0
1
2
3
4
5
6
0
10
20
30
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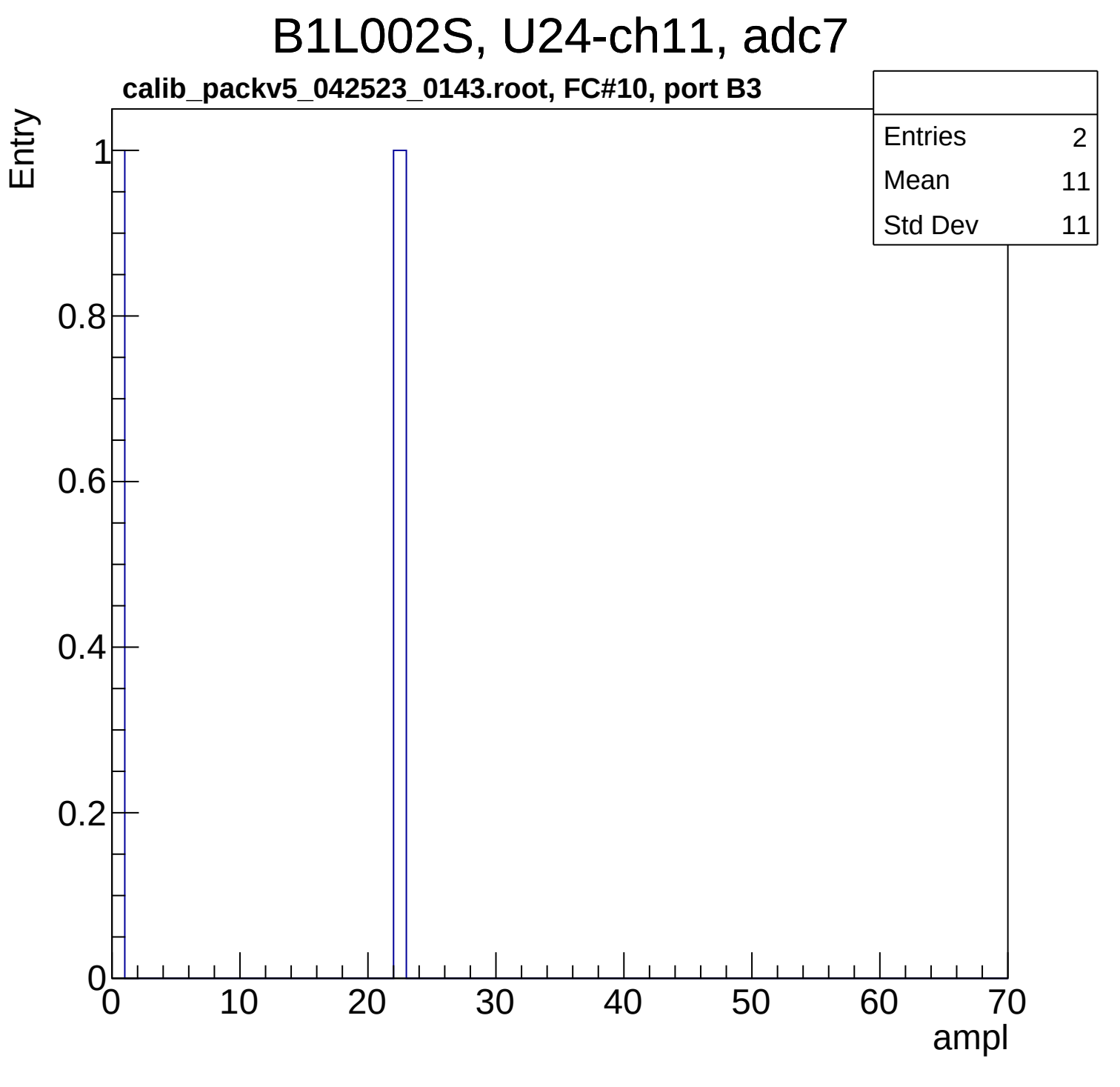
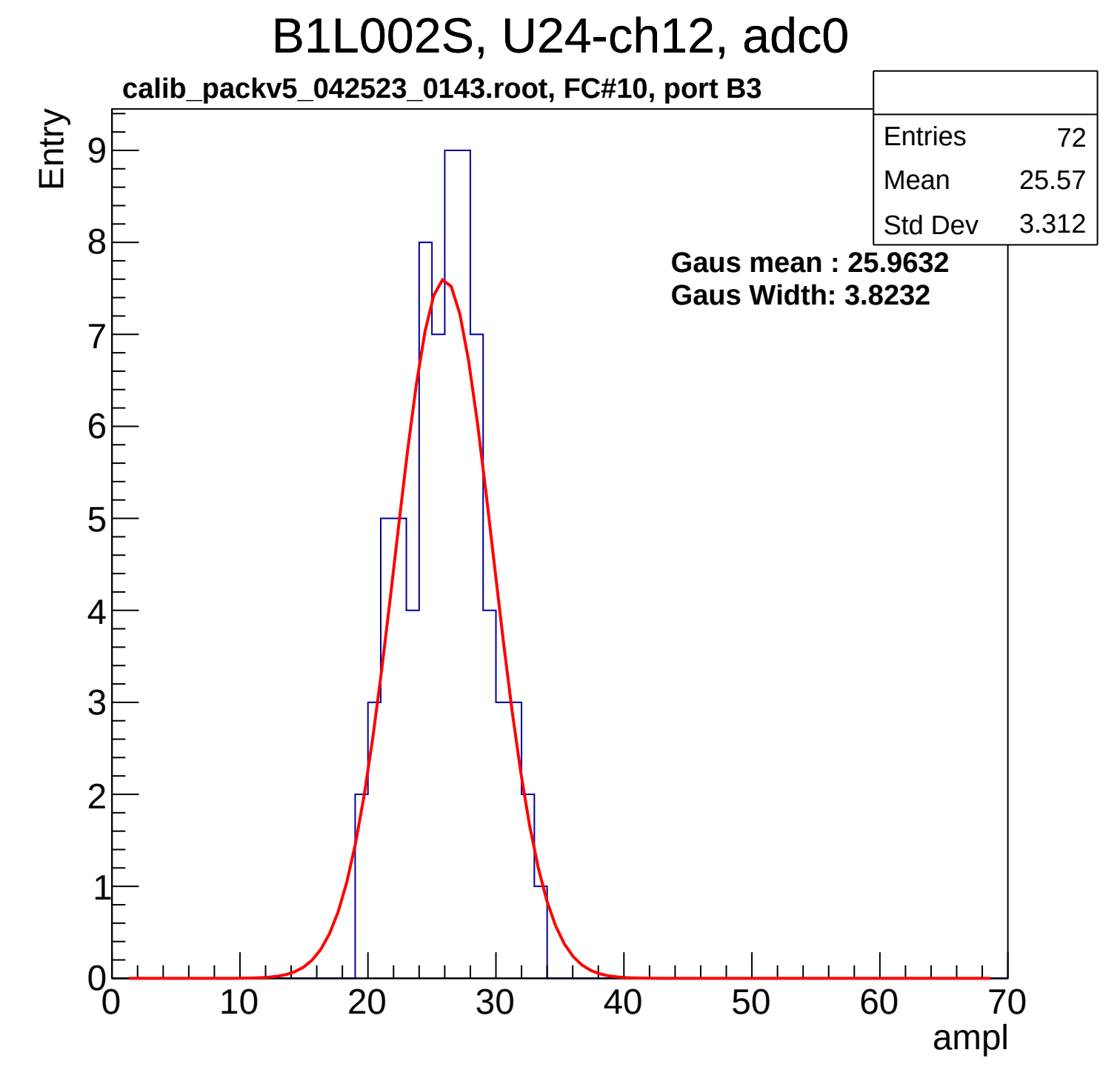
50
60
70
ampl


B1L002S, U24-ch11, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch12, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 25.57
Std Dev 3.312
Gaus mean : 25.9632
Gaus Width: 3.8232
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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40
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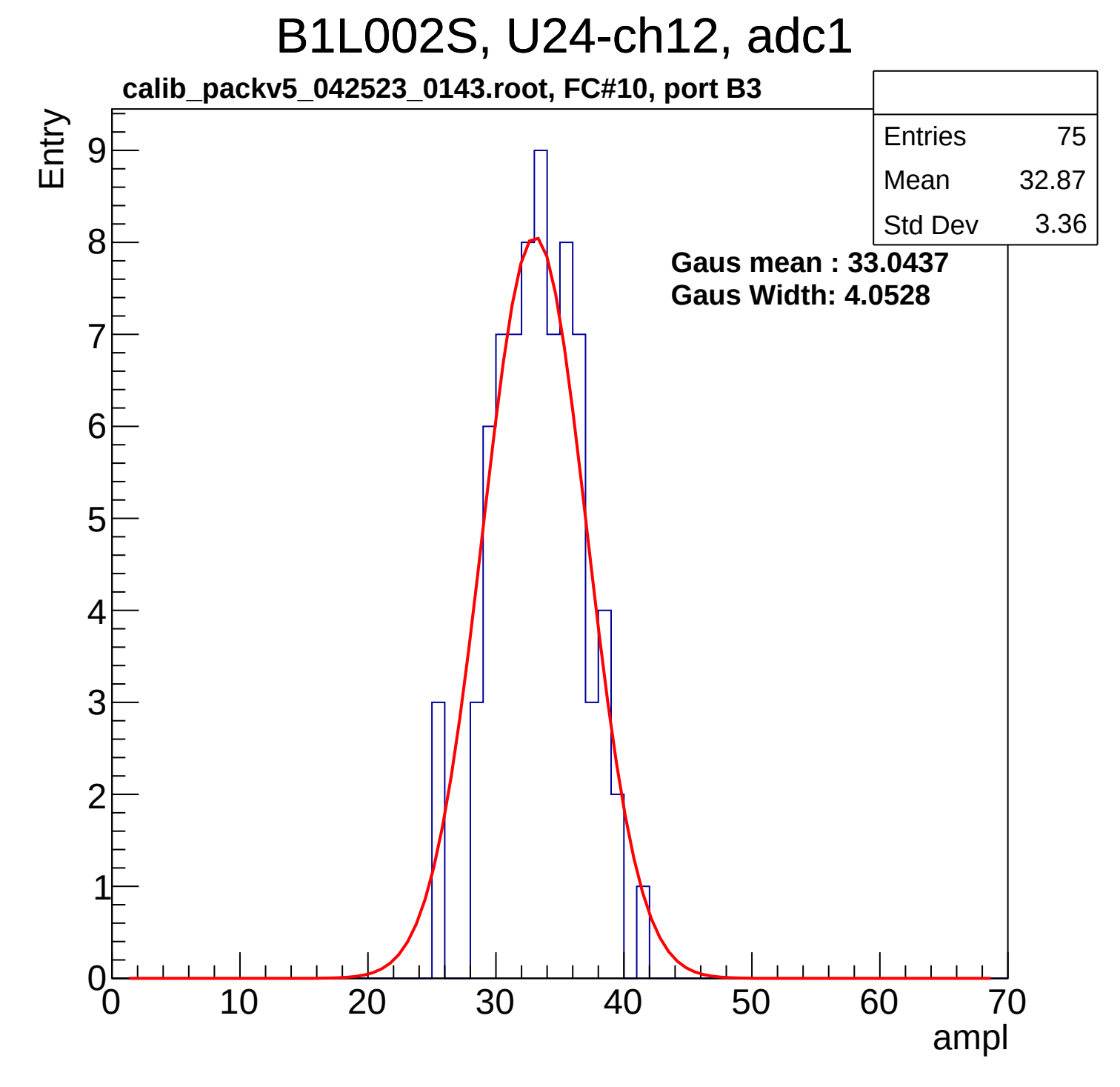
B1L002S, U24-ch12, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 32.87
Std Dev 3.36
Gaus mean : 33.0437
Gaus Width: 4.0528
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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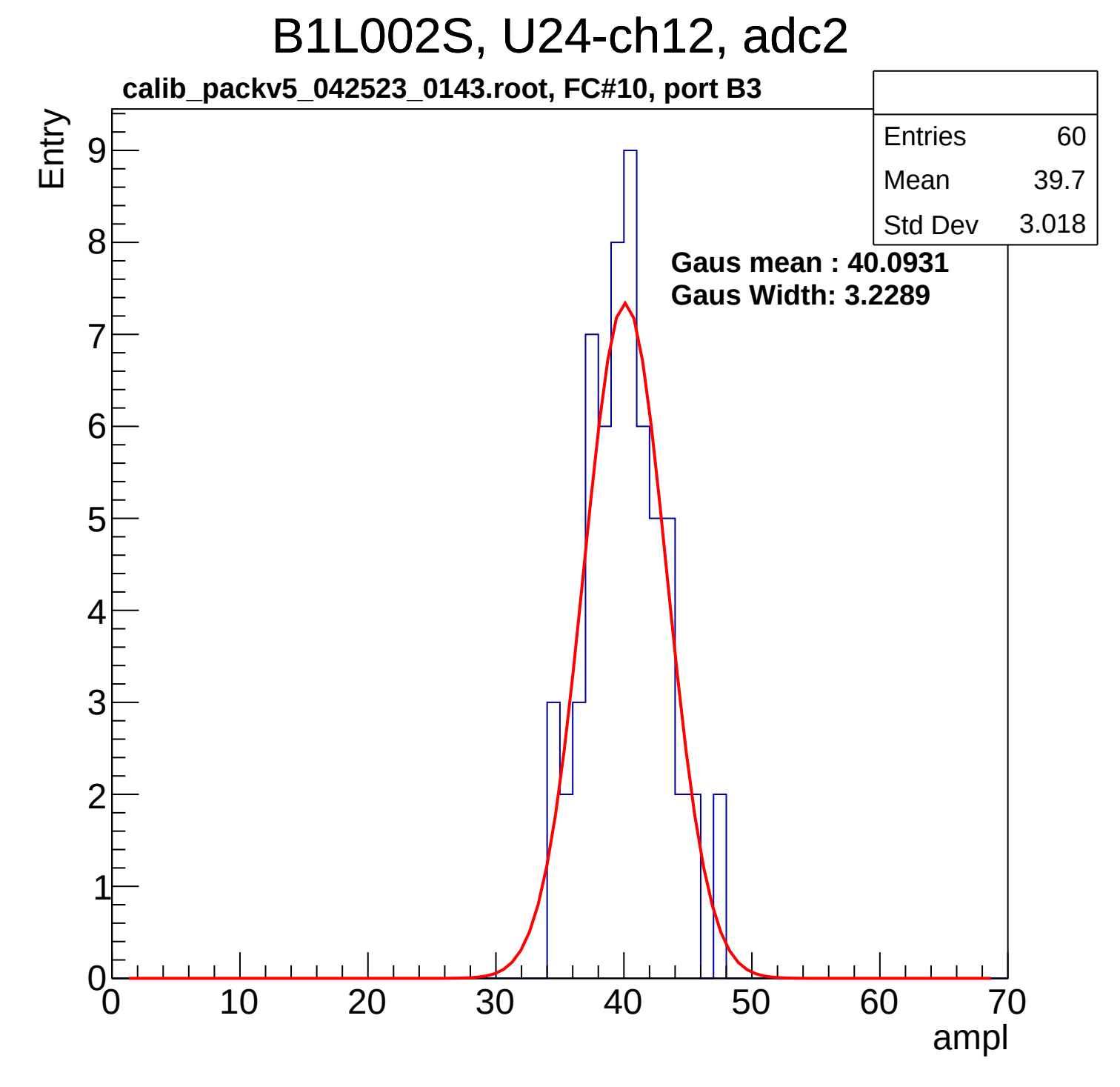
B1L002S, U24-ch12, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 39.7
Std Dev 3.018
Gaus mean : 40.0931
Gaus Width: 3.2289
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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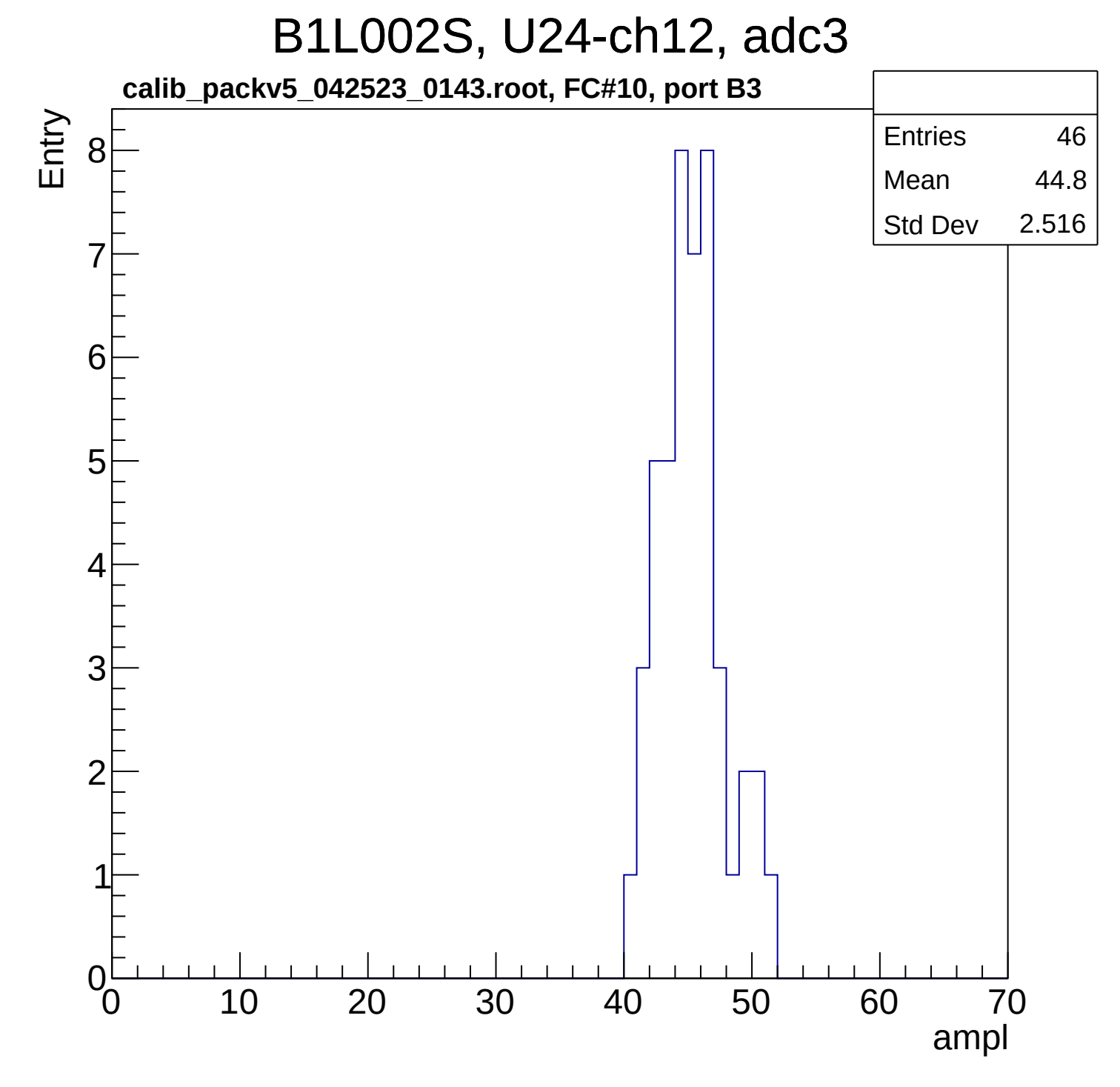
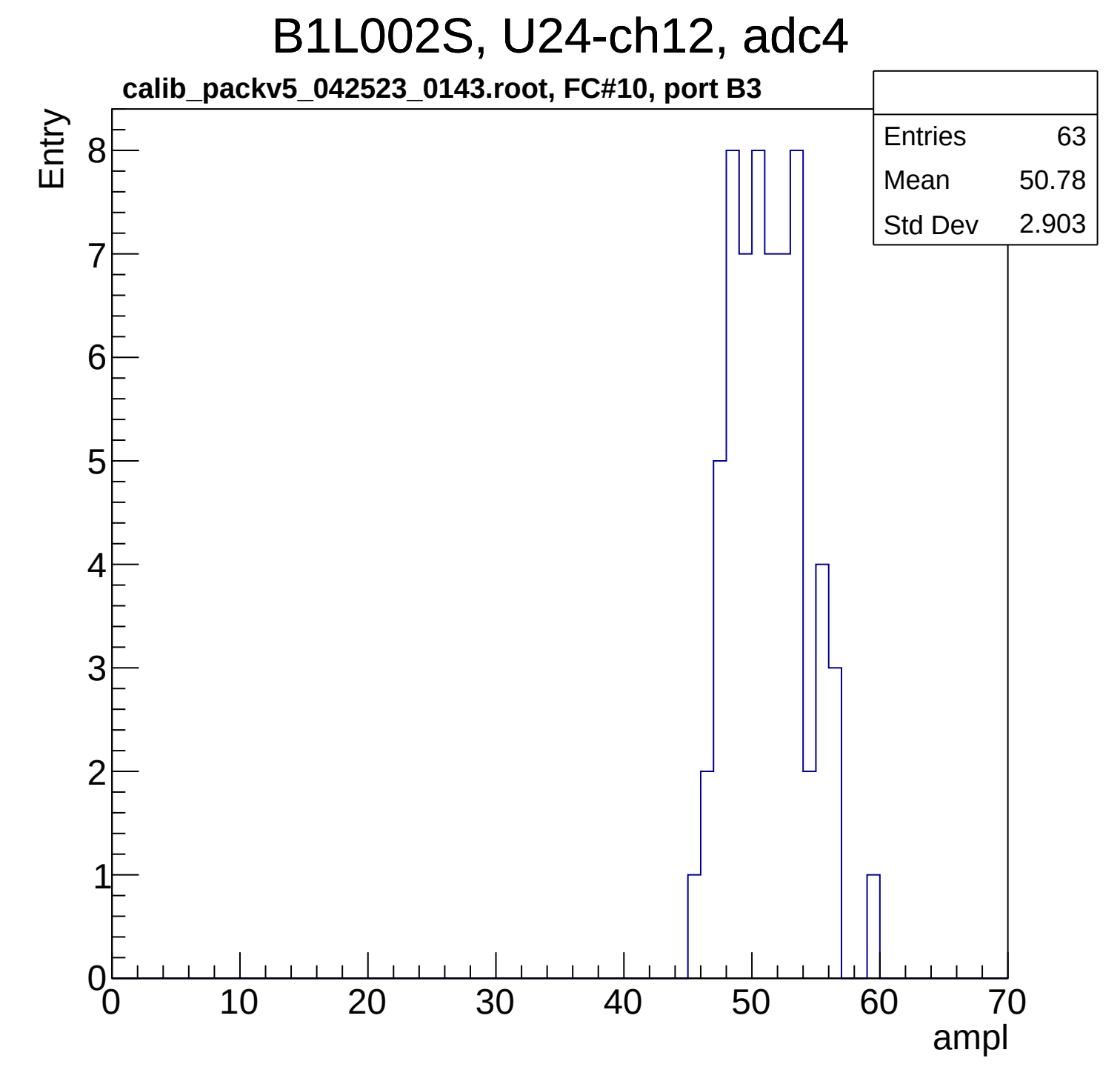

B1L002S, U24-ch12, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 44.8
Std Dev 2.516
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch12, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 50.78
Std Dev 2.903
Entry
ampl
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4
5
6
7
8
0
10
20
30
40
50
60
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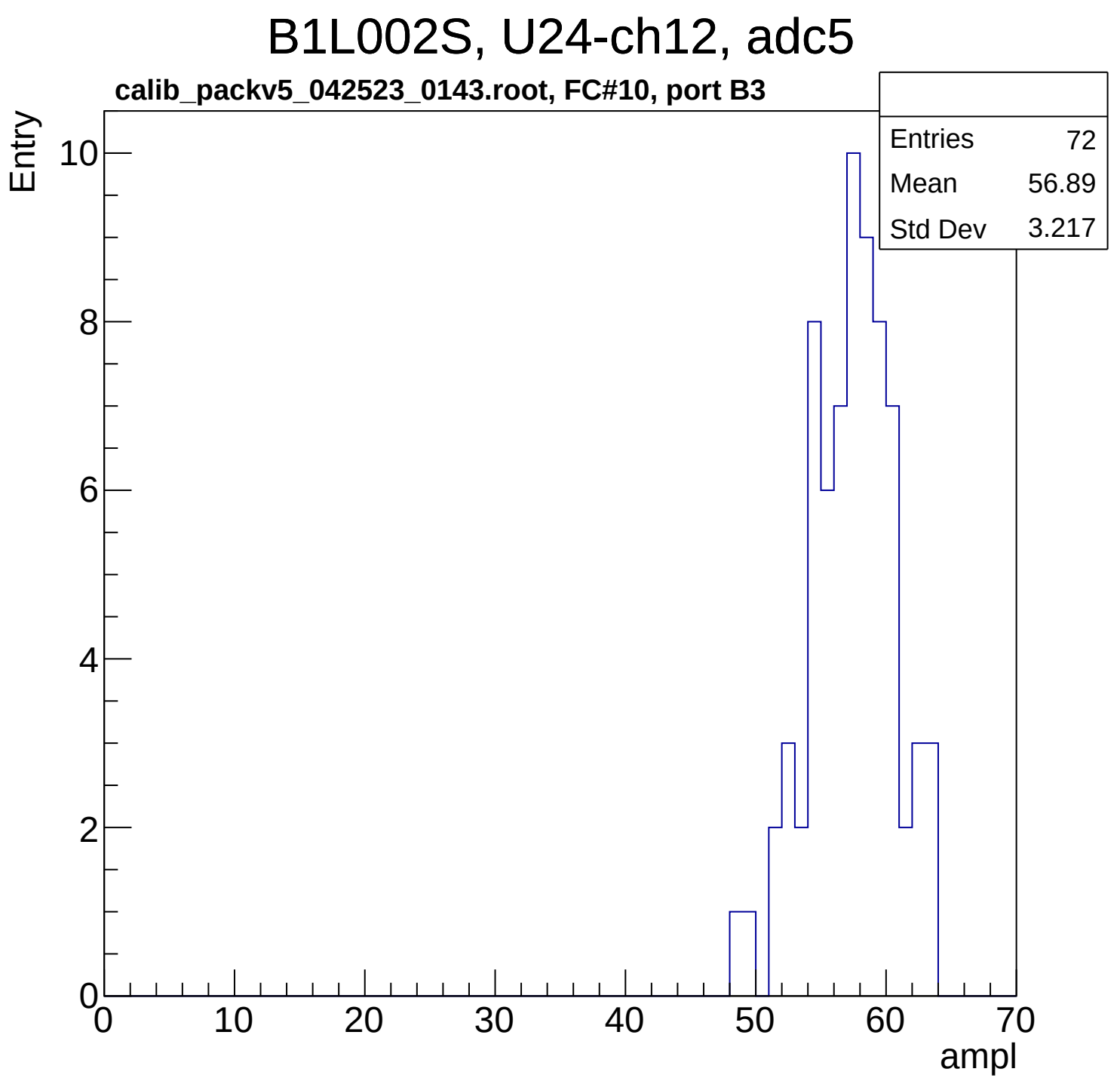
B1L002S, U24-ch12, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 56.89
Std Dev 3.217
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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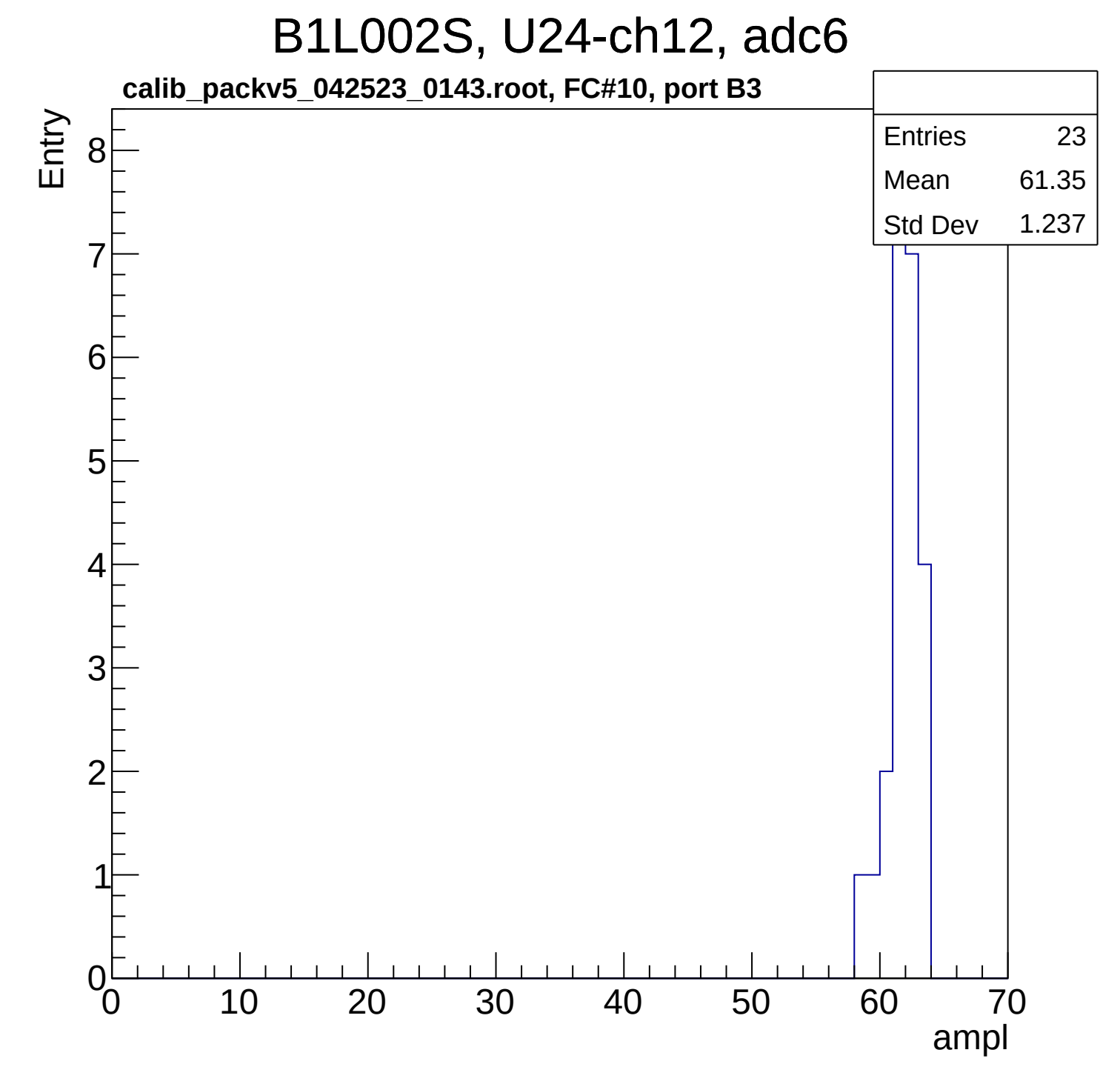
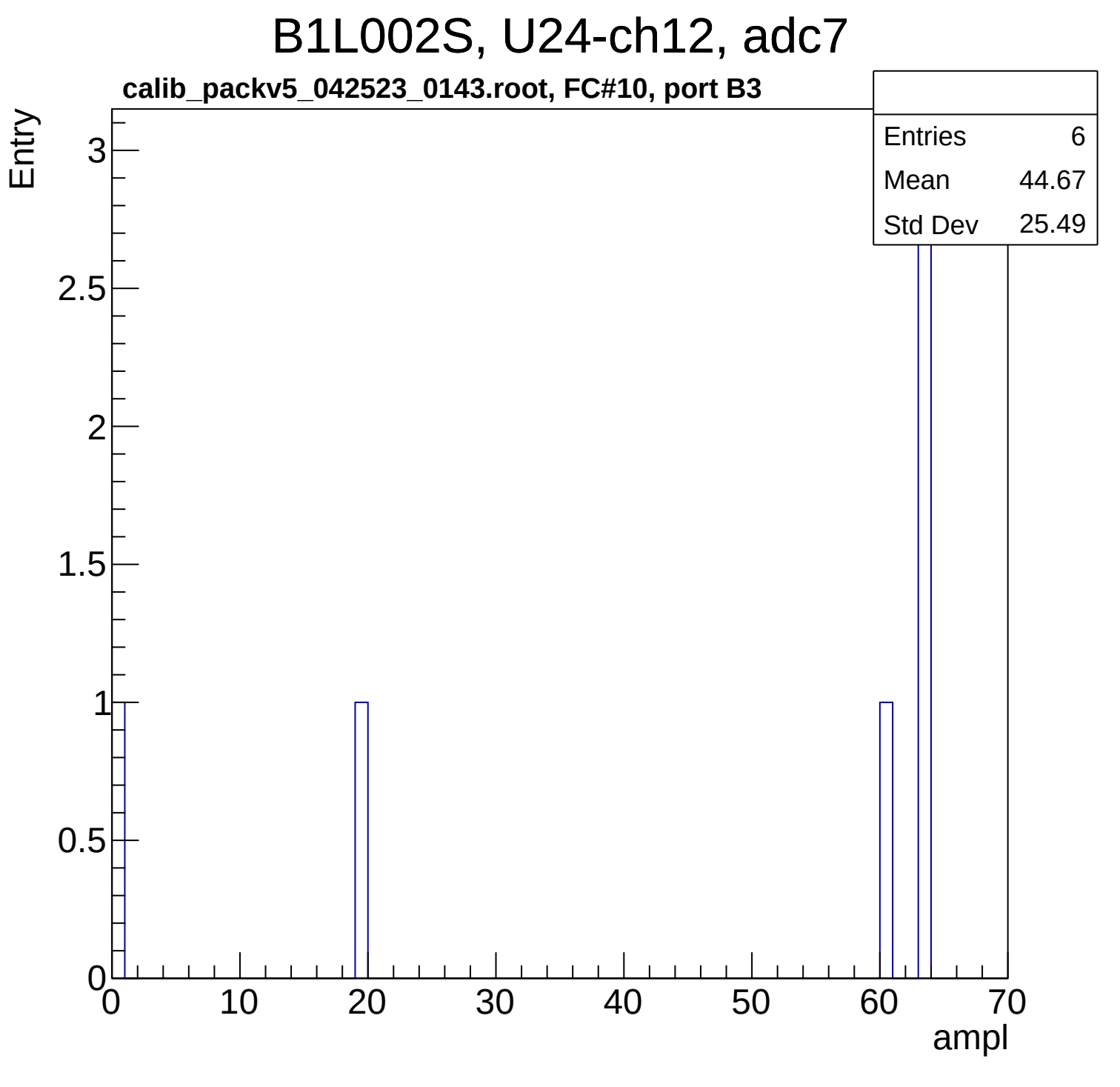
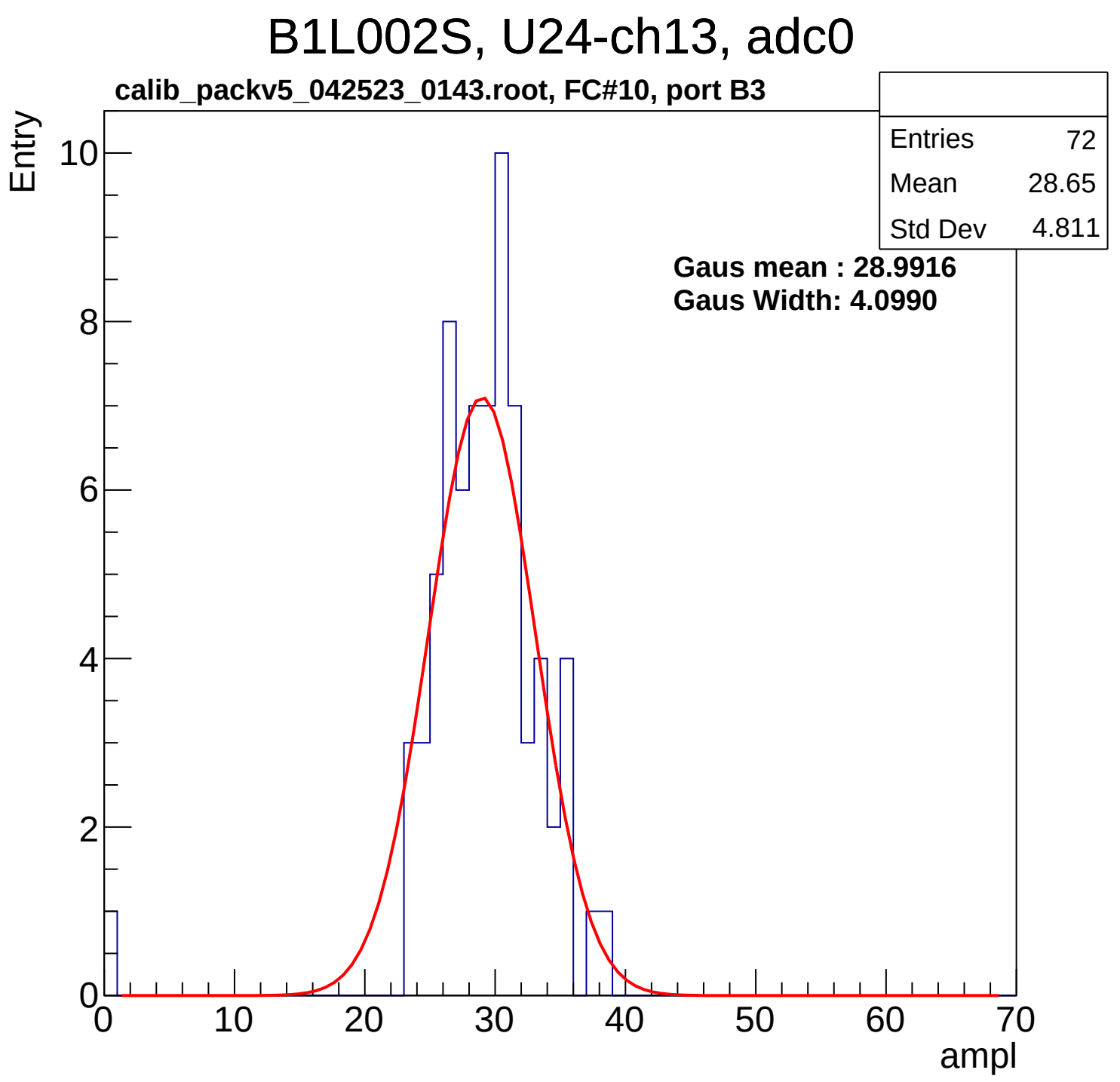
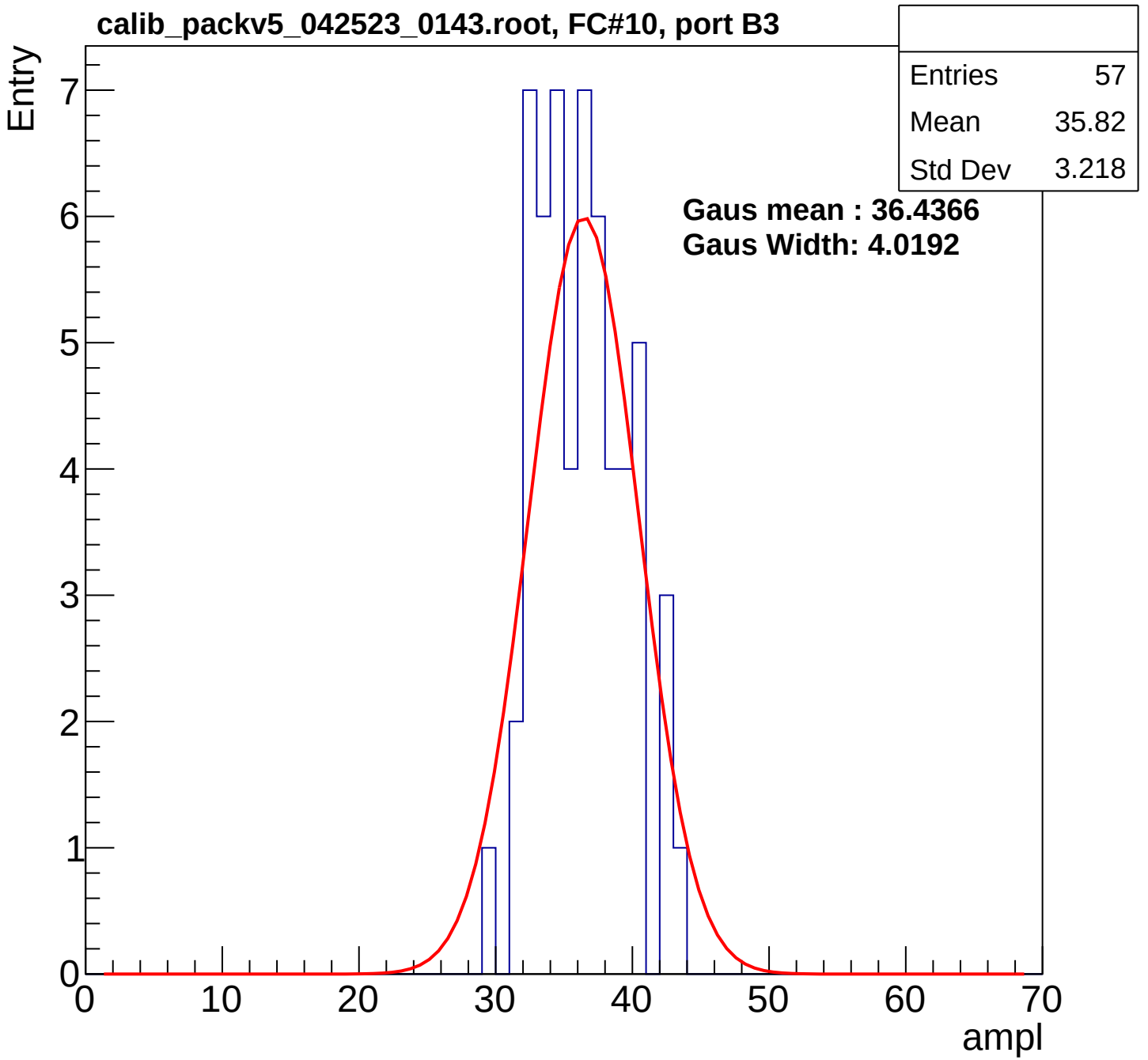

B1L002S, U24-ch12, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.35
Std Dev 1.237
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch12, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.67
Std Dev 25.49
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch13, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.65
Std Dev 4.811
Gaus mean : 28.9916
Gaus Width: 4.0990
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch13, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.82
Std Dev 3.218
Gaus mean : 36.4366
Gaus Width: 4.0192
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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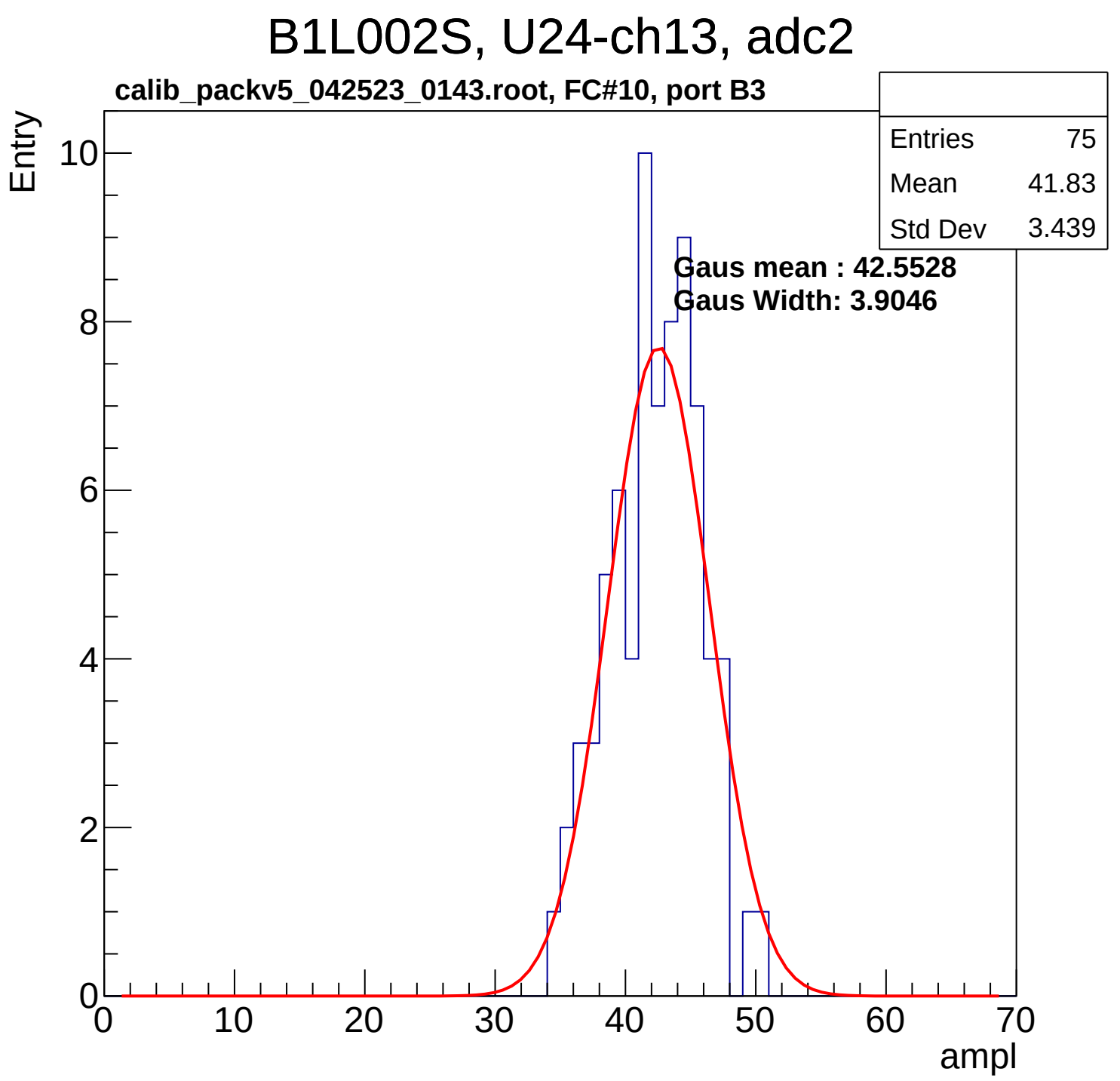
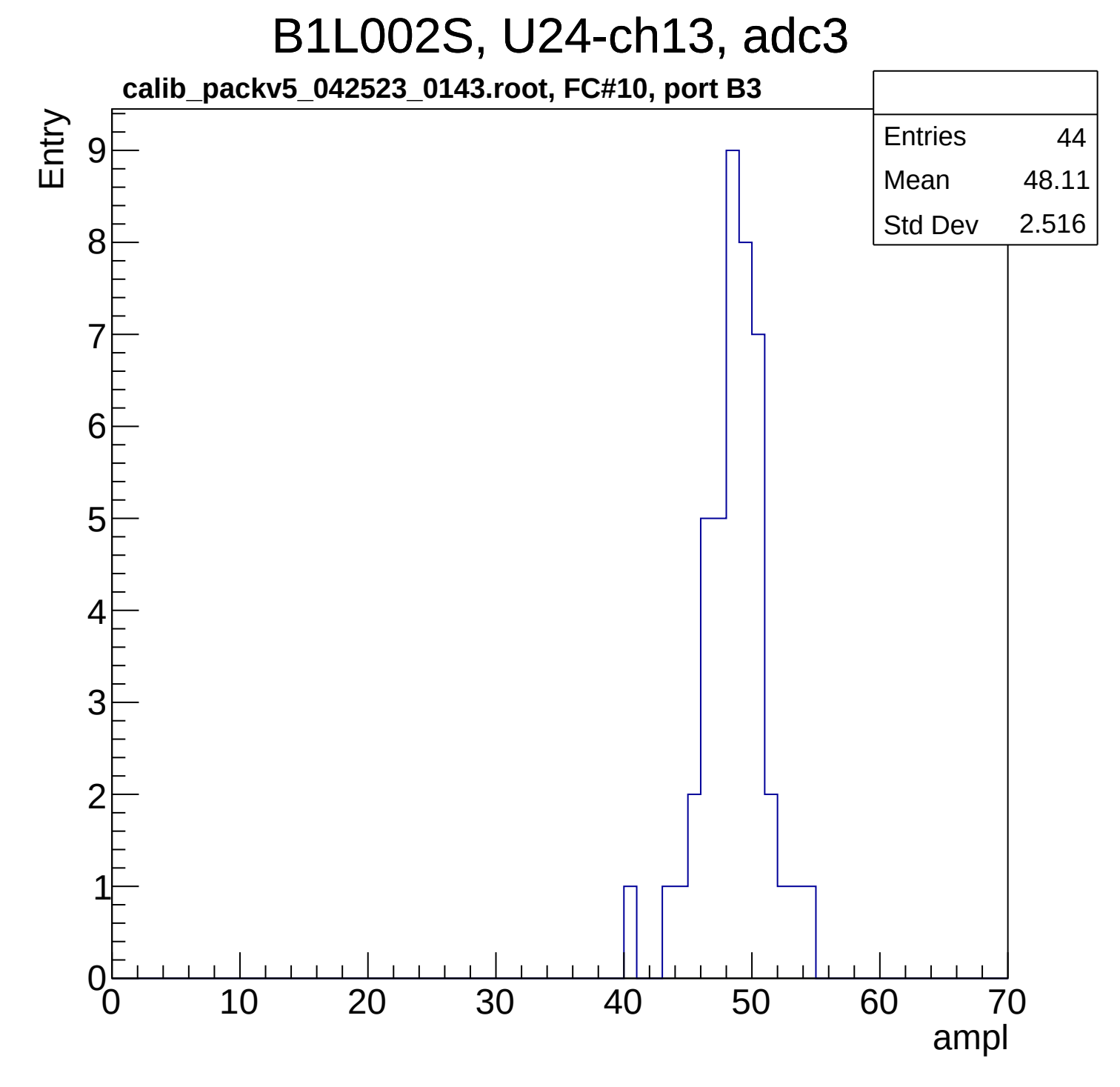

B1L002S, U24-ch13, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 41.83
Std Dev 3.439
Gaus mean : 42.5528
Gaus Width: 3.9046
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch13, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 48.11
Std Dev 2.516
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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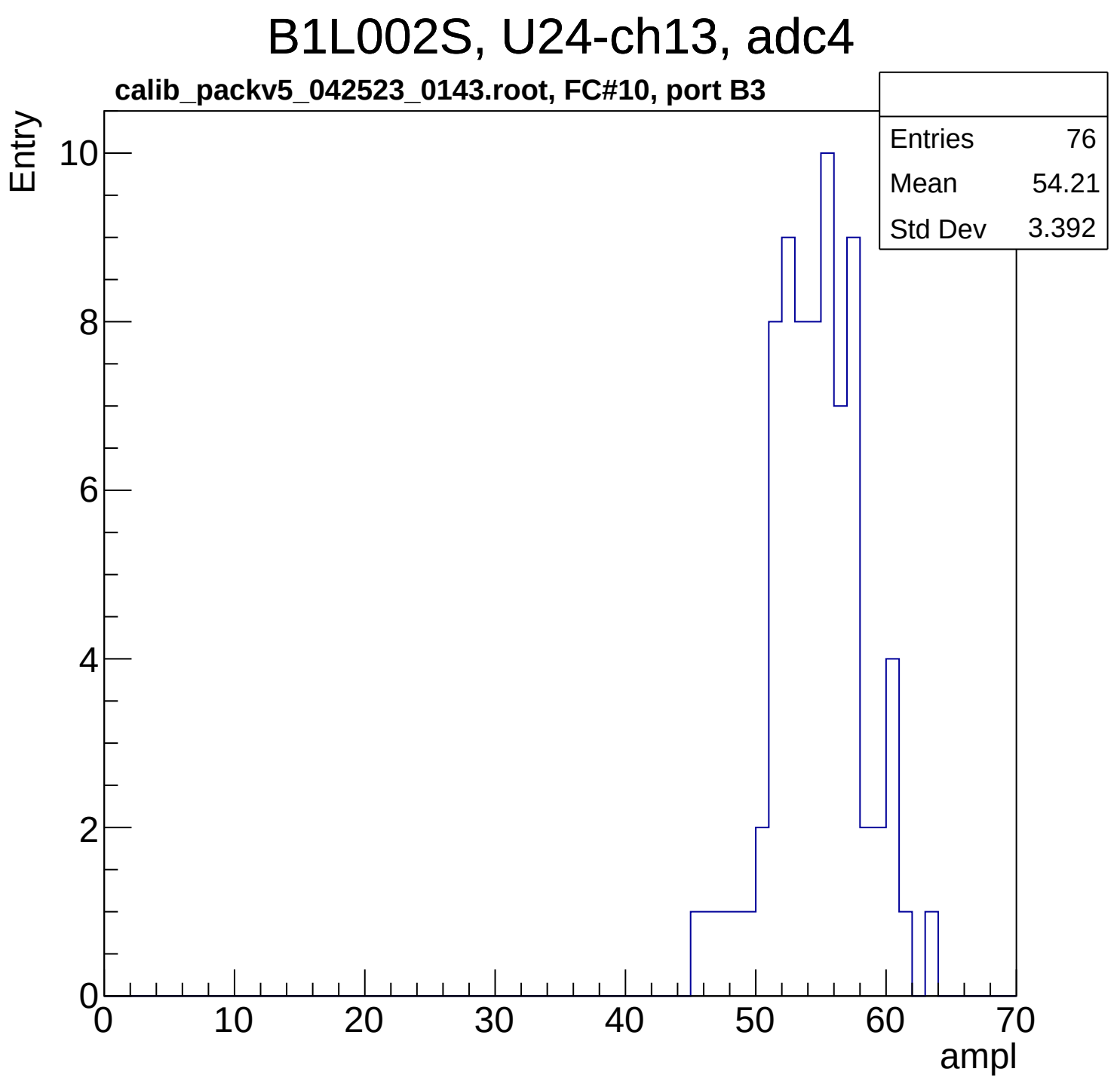
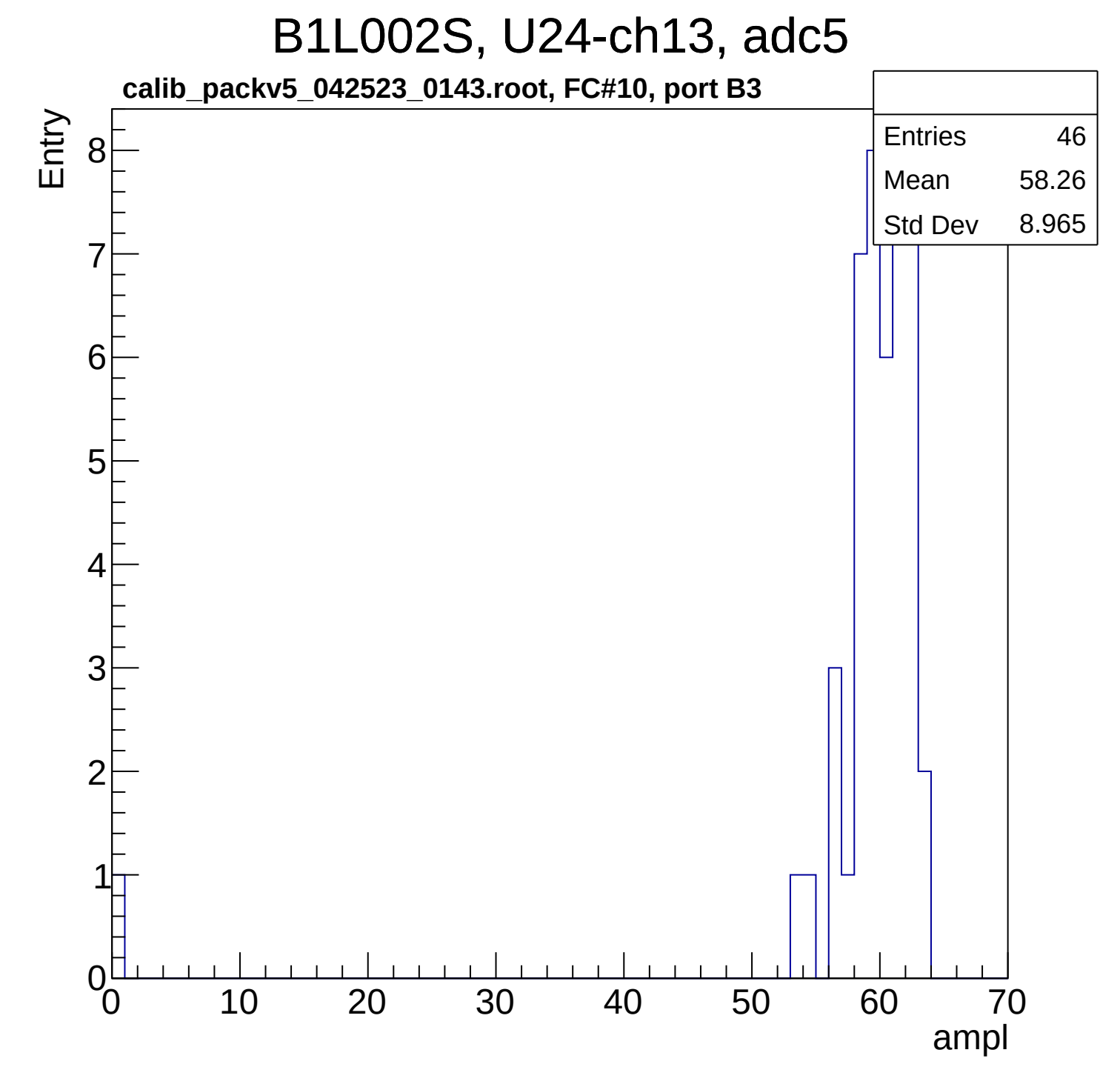

B1L002S, U24-ch13, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 54.21
Std Dev 3.392
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch13, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.26
Std Dev 8.965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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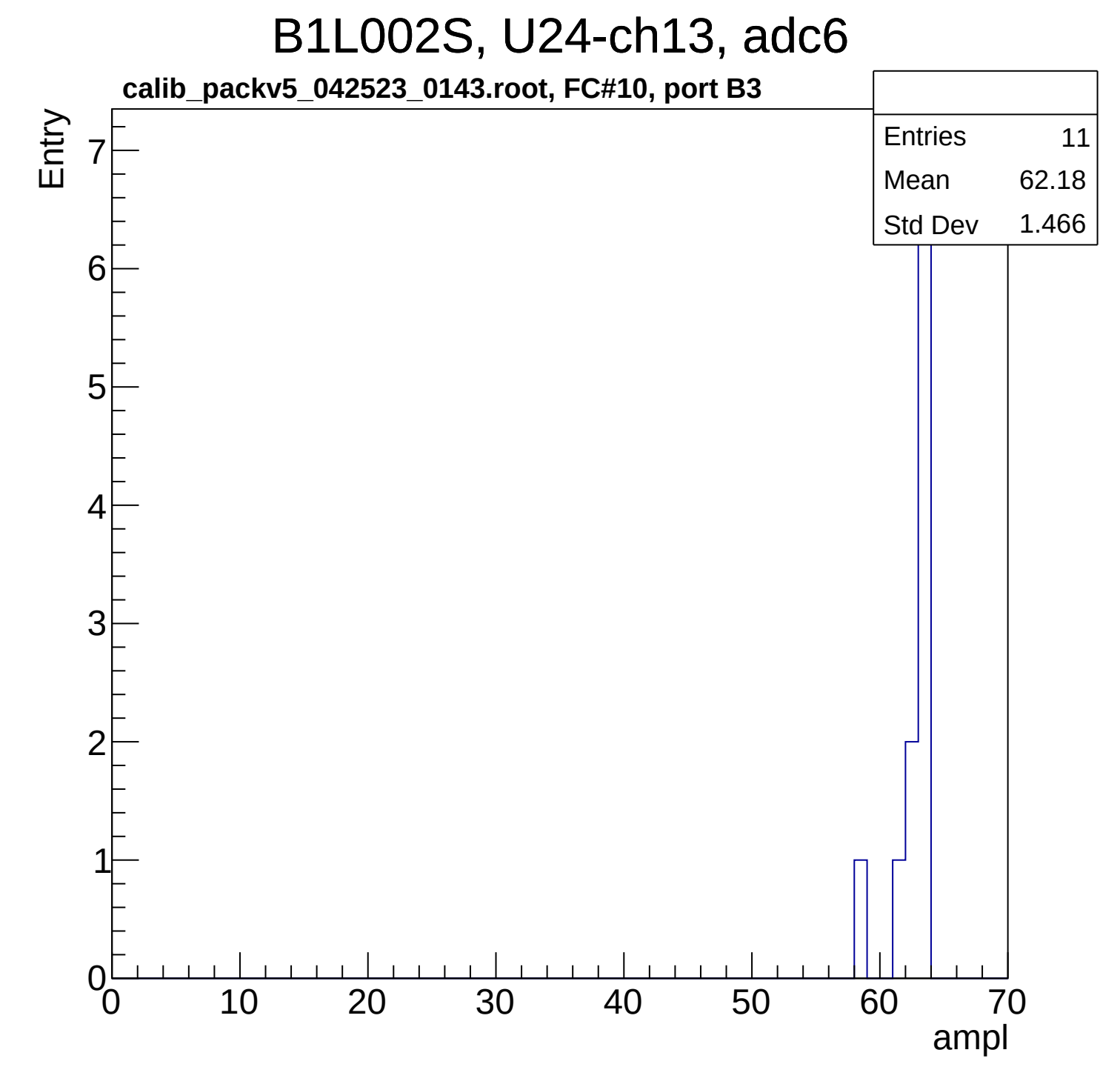
B1L002S, U24-ch13, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 62.18
Std Dev 1.466
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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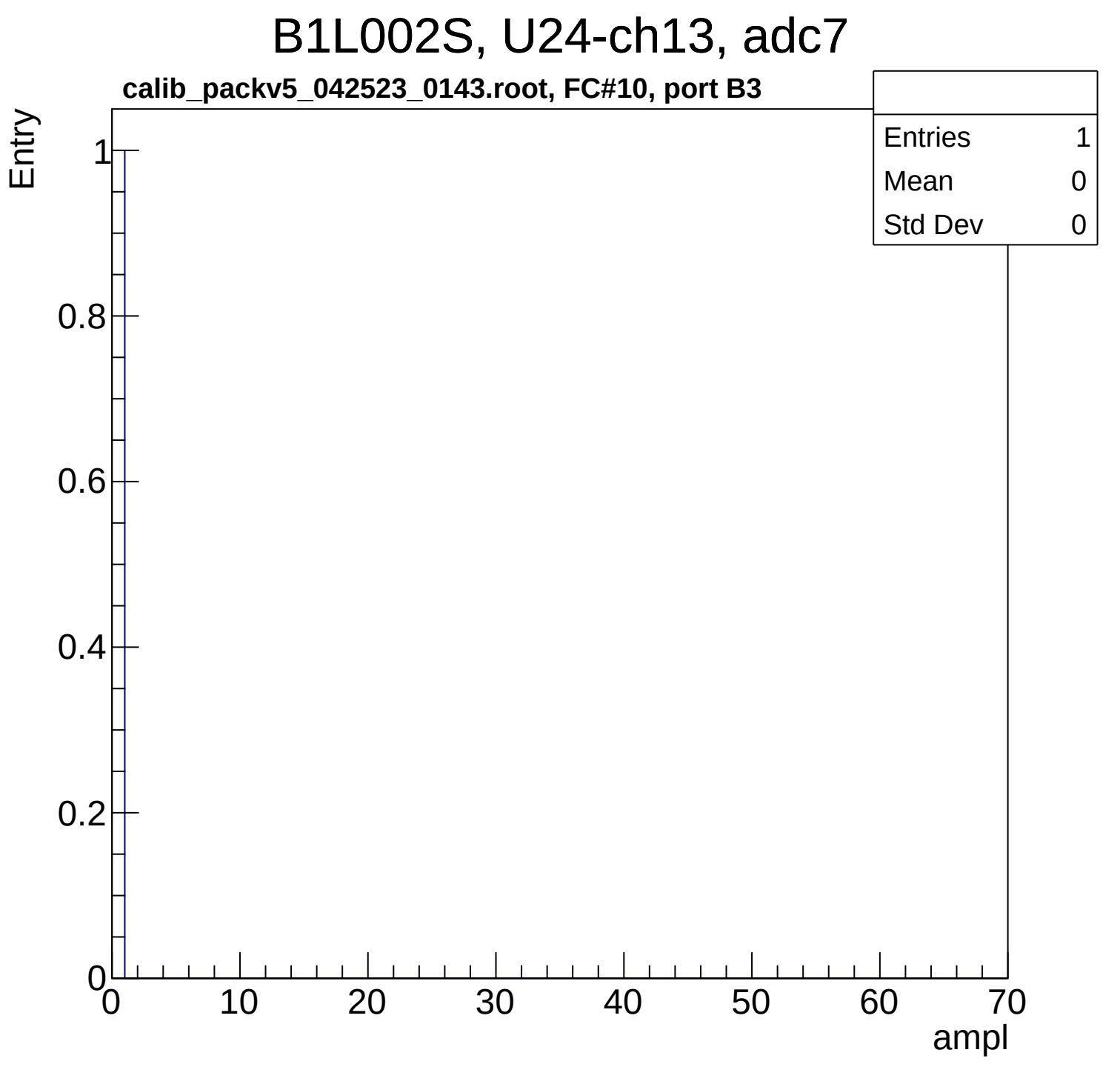
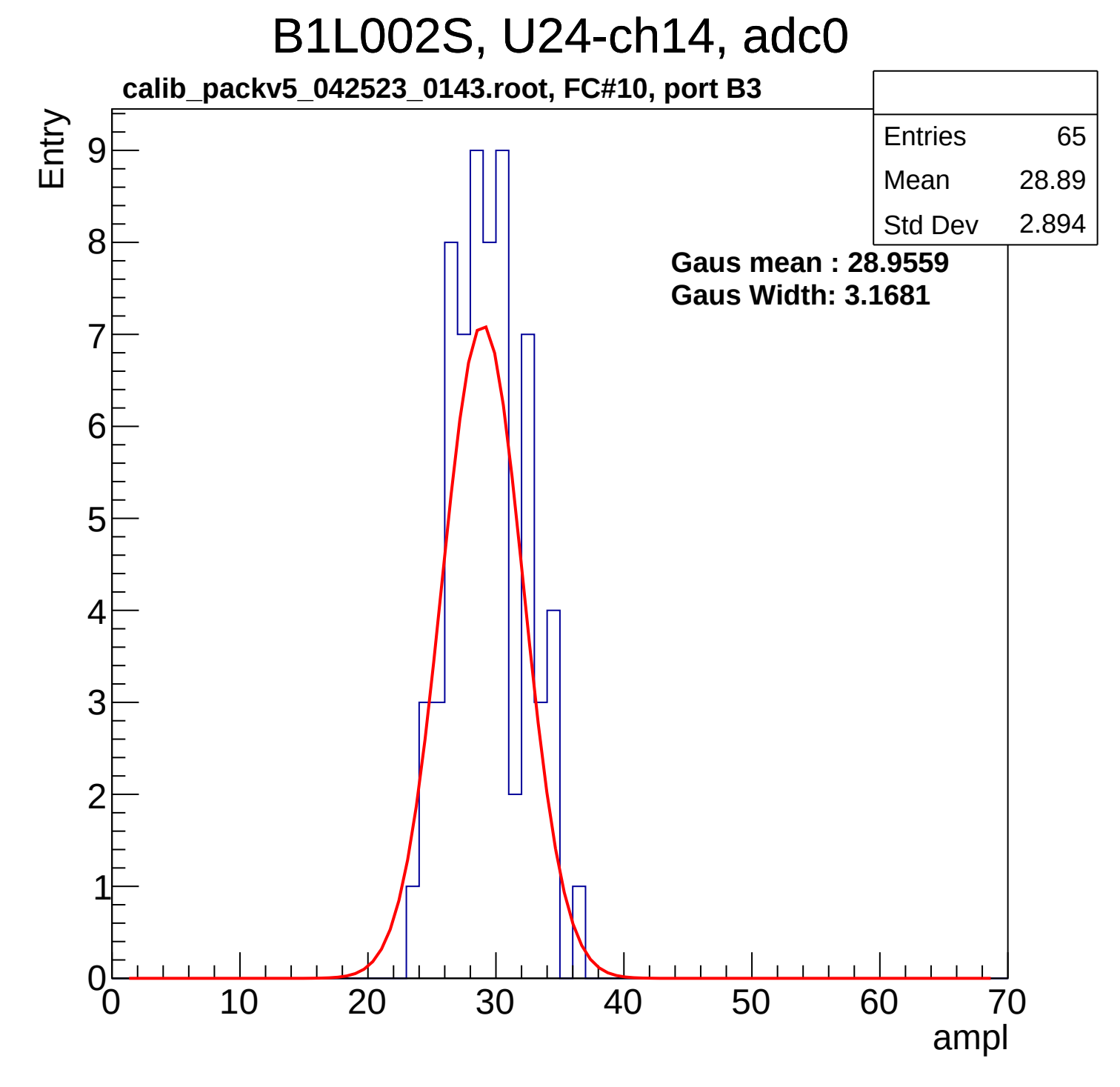
40
50
60
70


B1L002S, U24-ch13, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch14, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.89
Std Dev 2.894
Gaus mean : 28.9559
Gaus Width: 3.1681
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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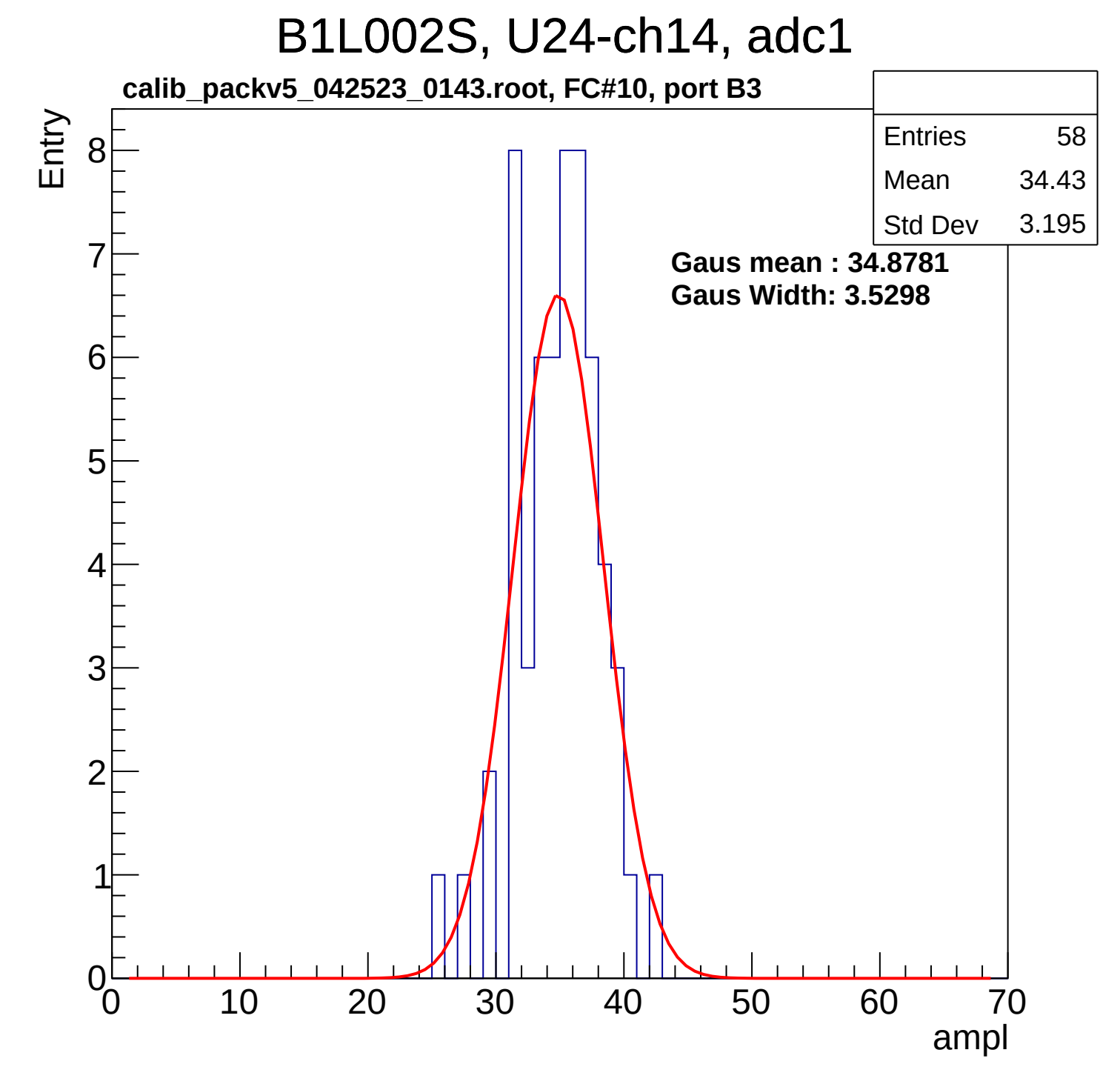
B1L002S, U24-ch14, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 34.43
Std Dev 3.195
Gaus mean : 34.8781
Gaus Width: 3.5298
Entry
ampl
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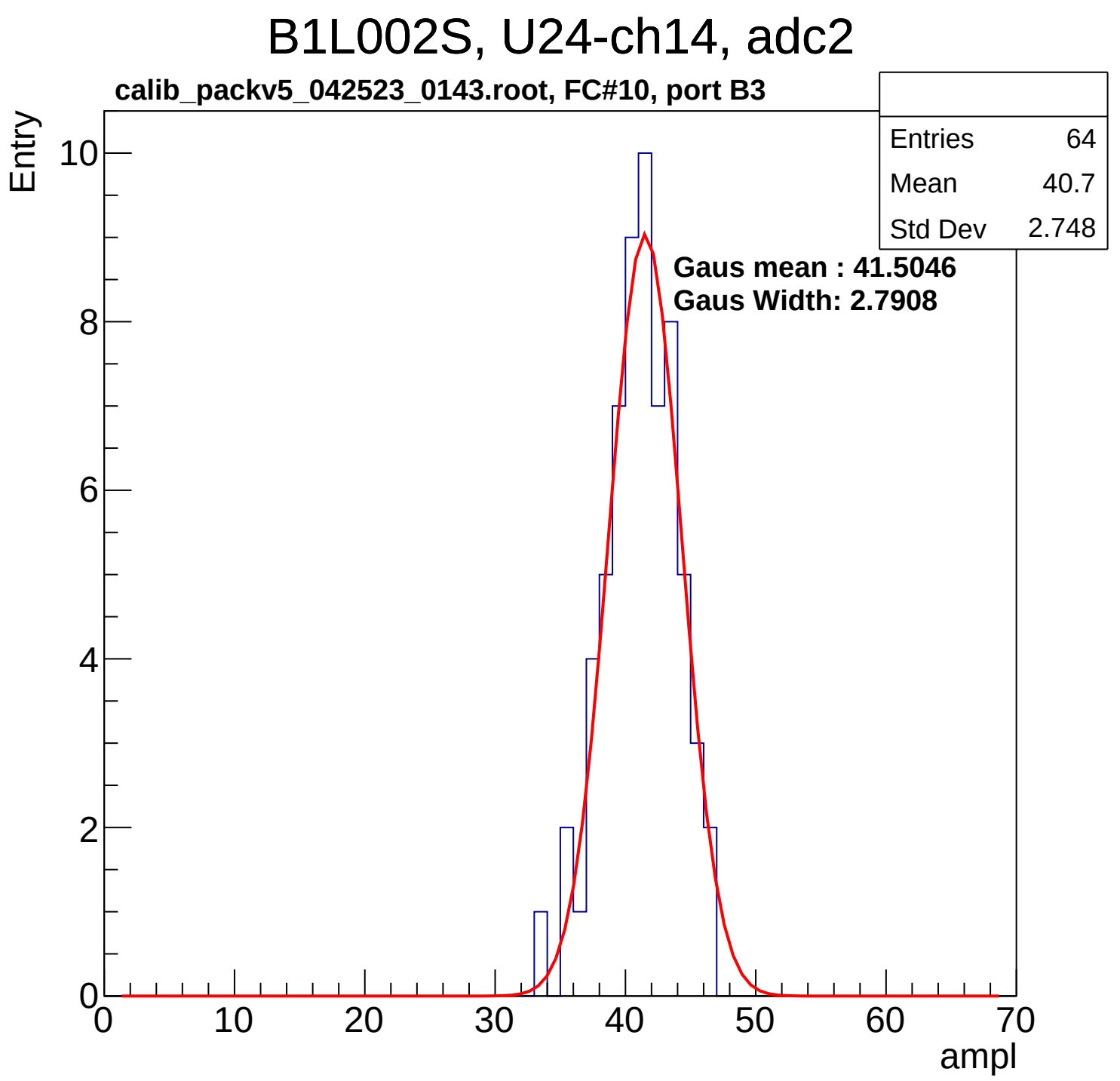
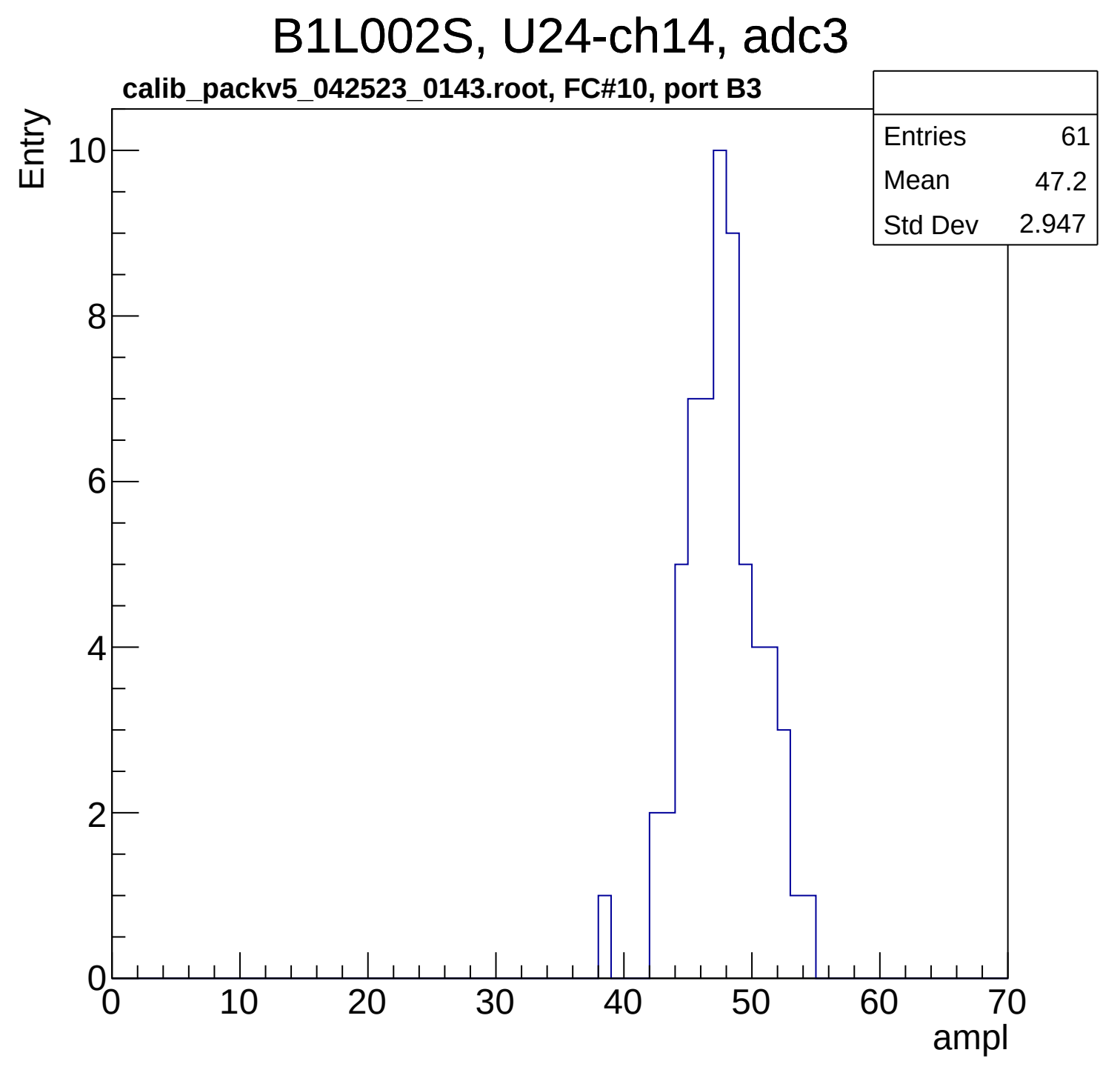
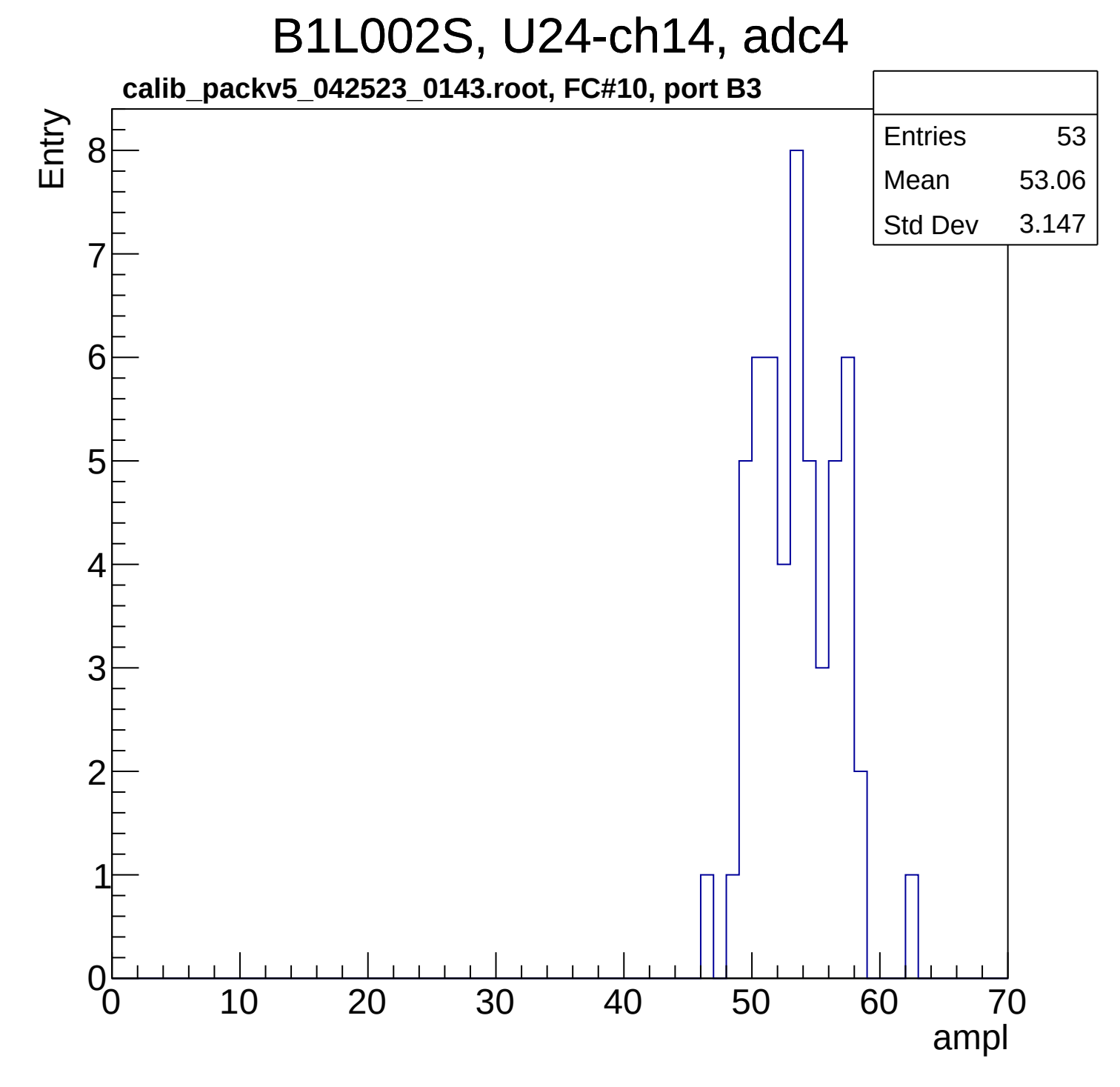
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch14, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.7
Std Dev 2.748
Gaus mean : 41.5046
Gaus Width: 2.7908
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch14, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.2
Std Dev 2.947
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch14, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.06
Std Dev 3.147
Entry
ampl
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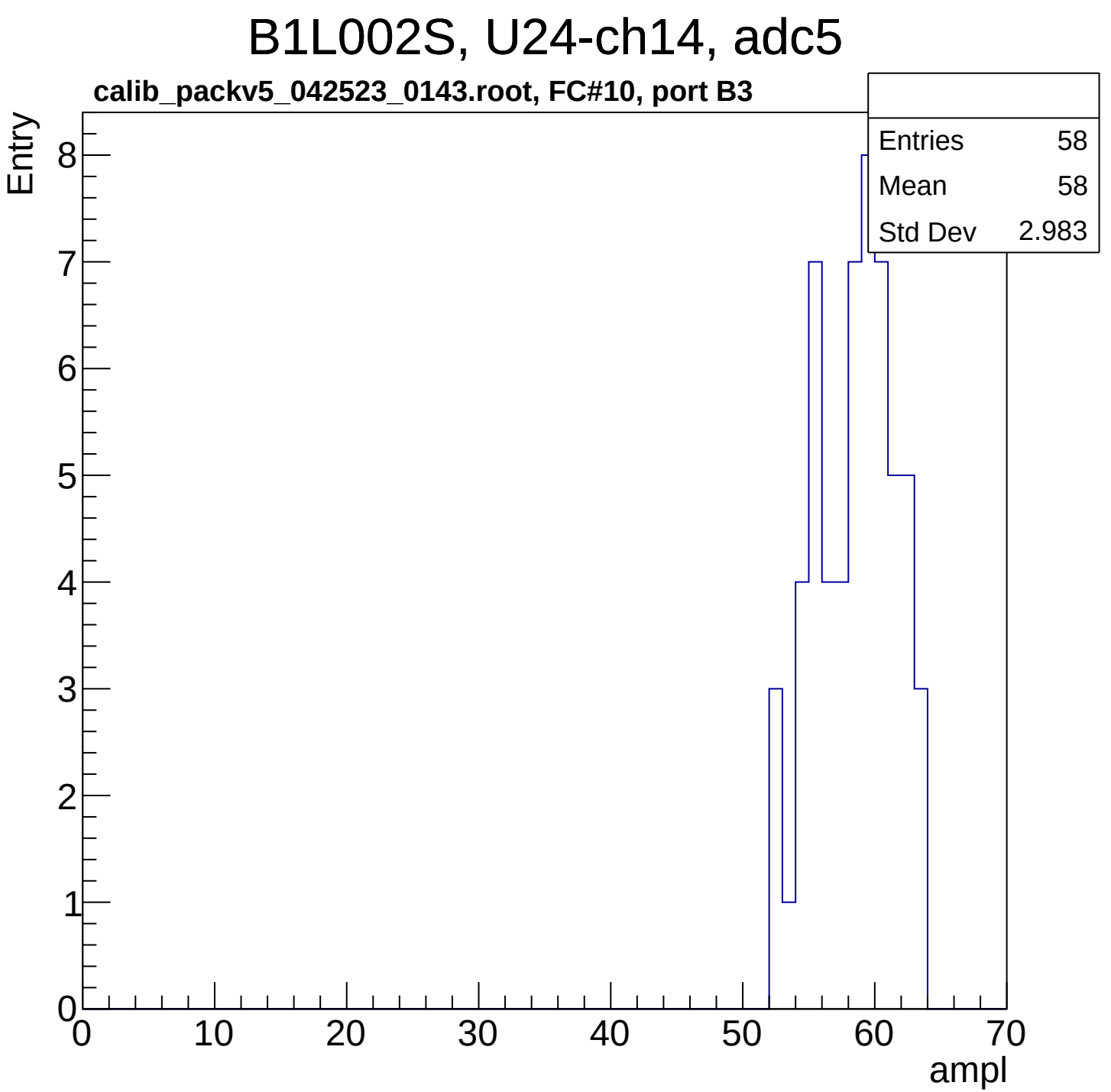
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch14, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58
Std Dev 2.983
Entry
ampl
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4
5
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7
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0
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40
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60
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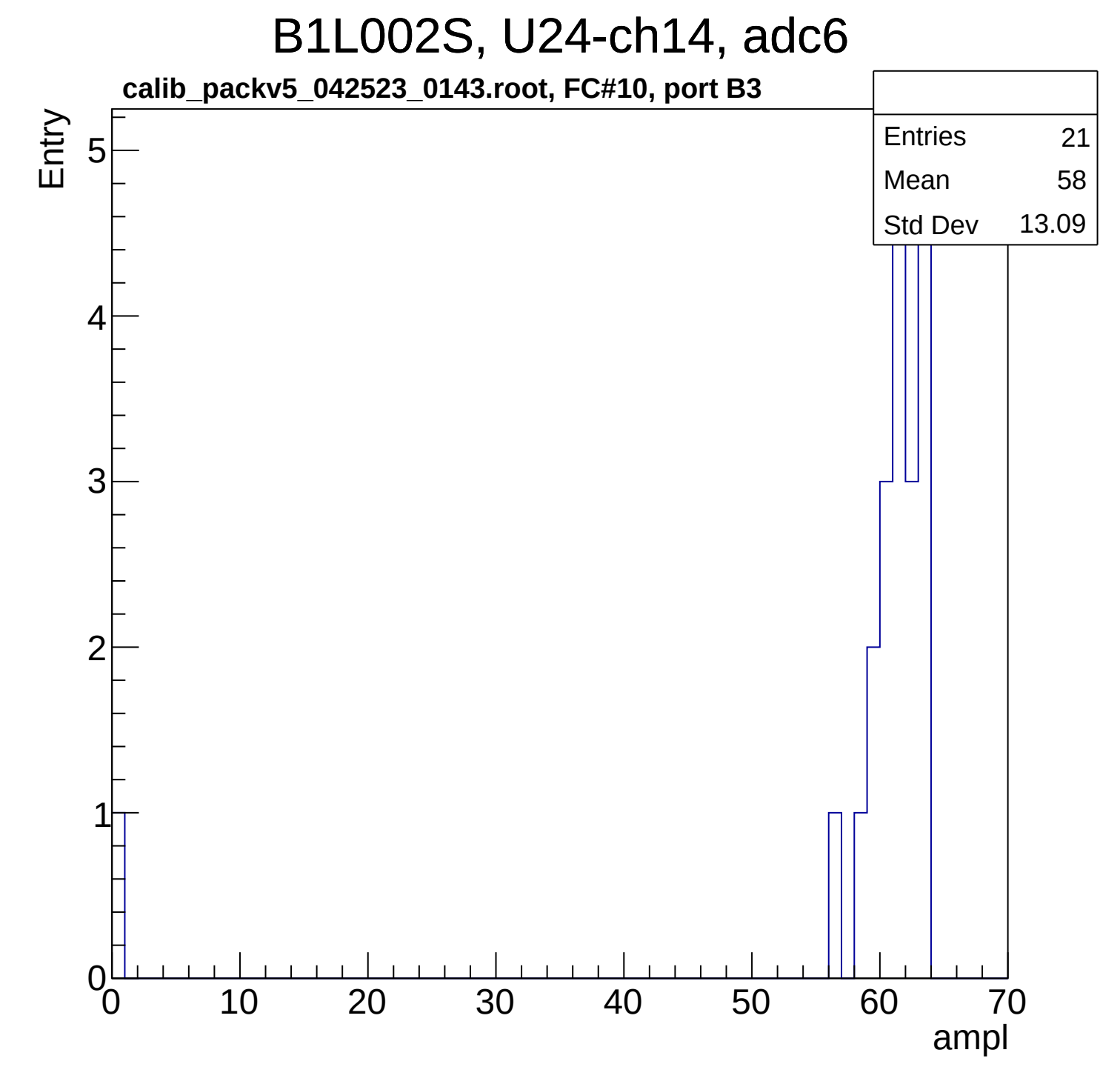
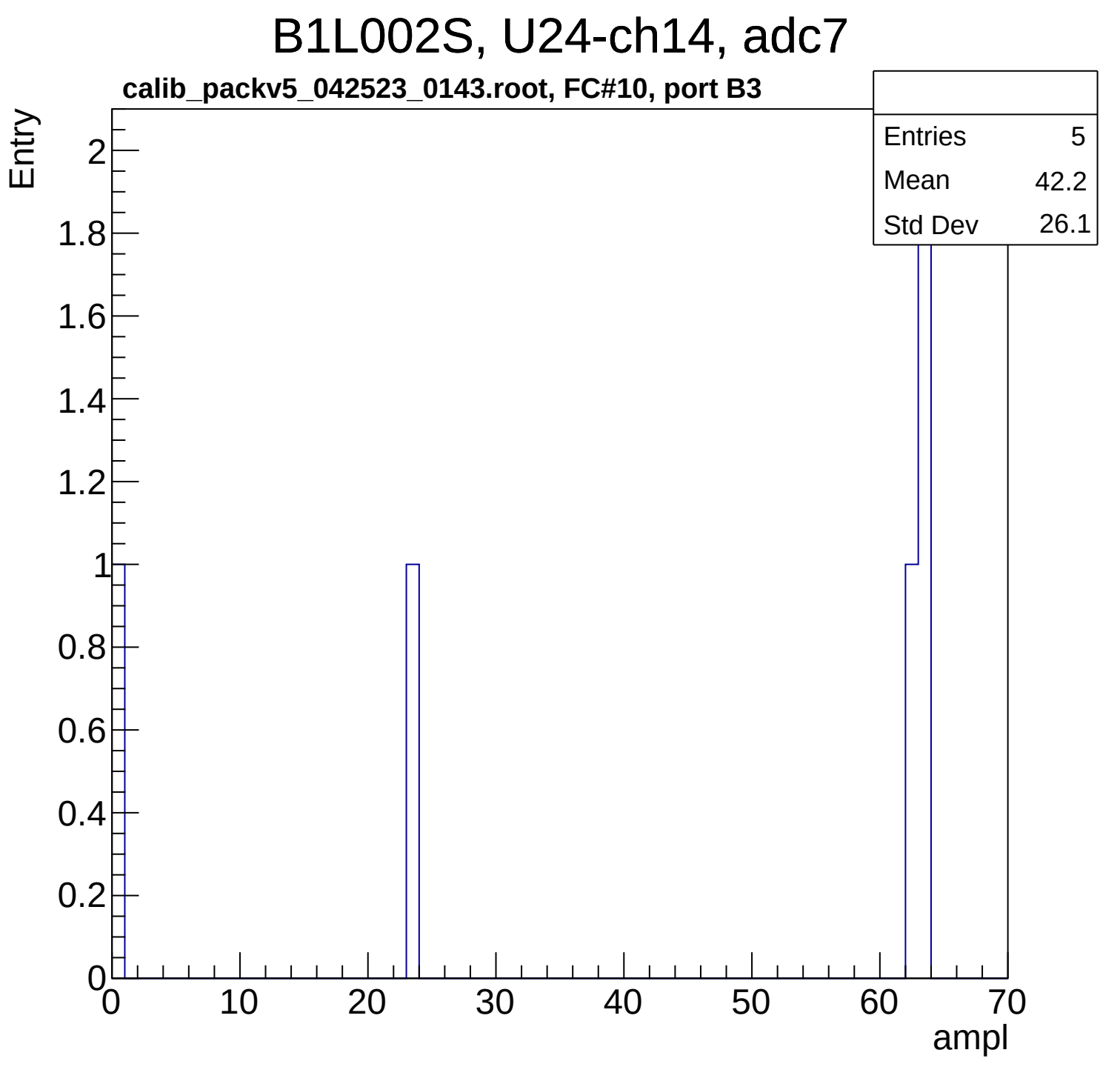
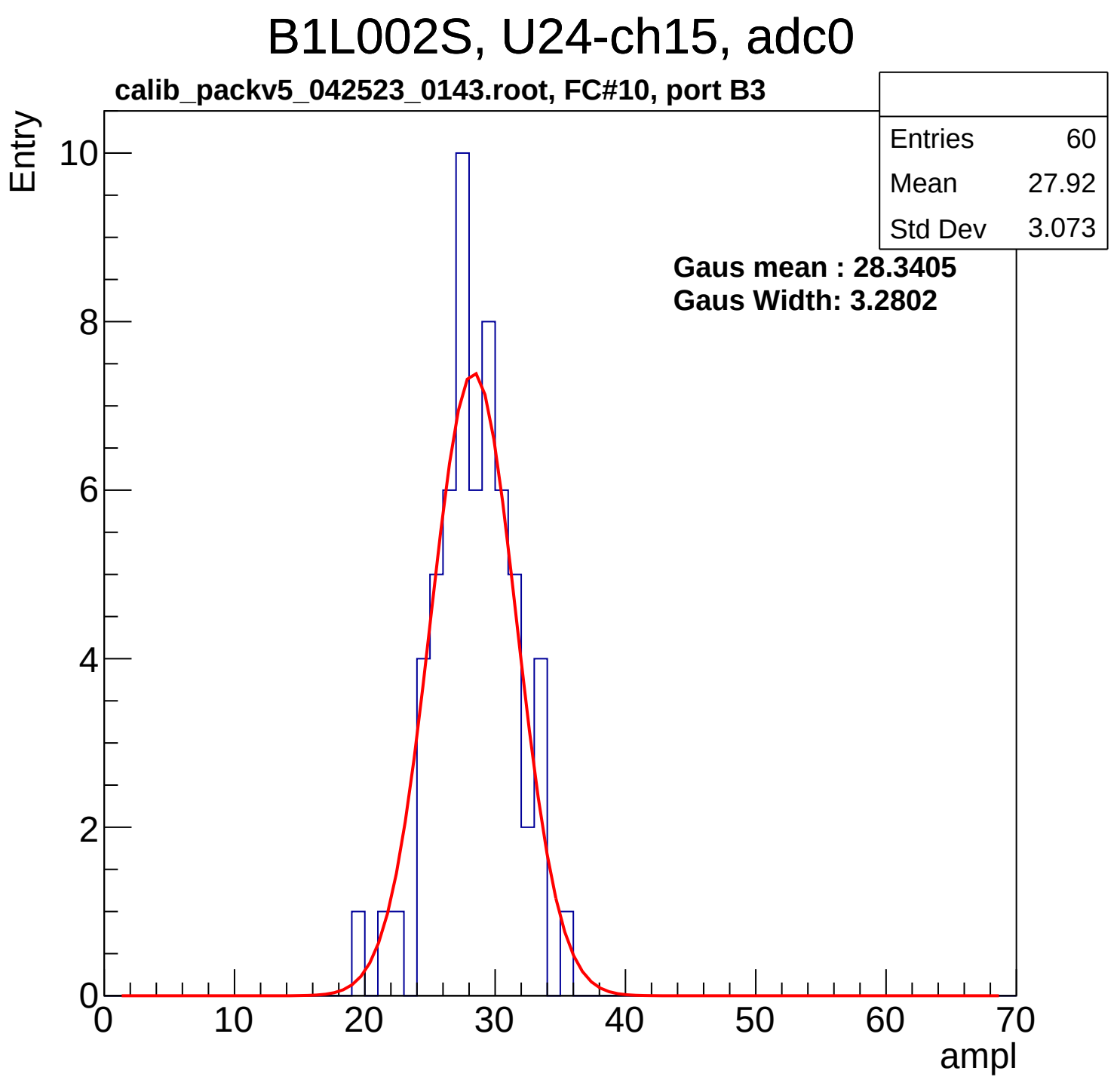
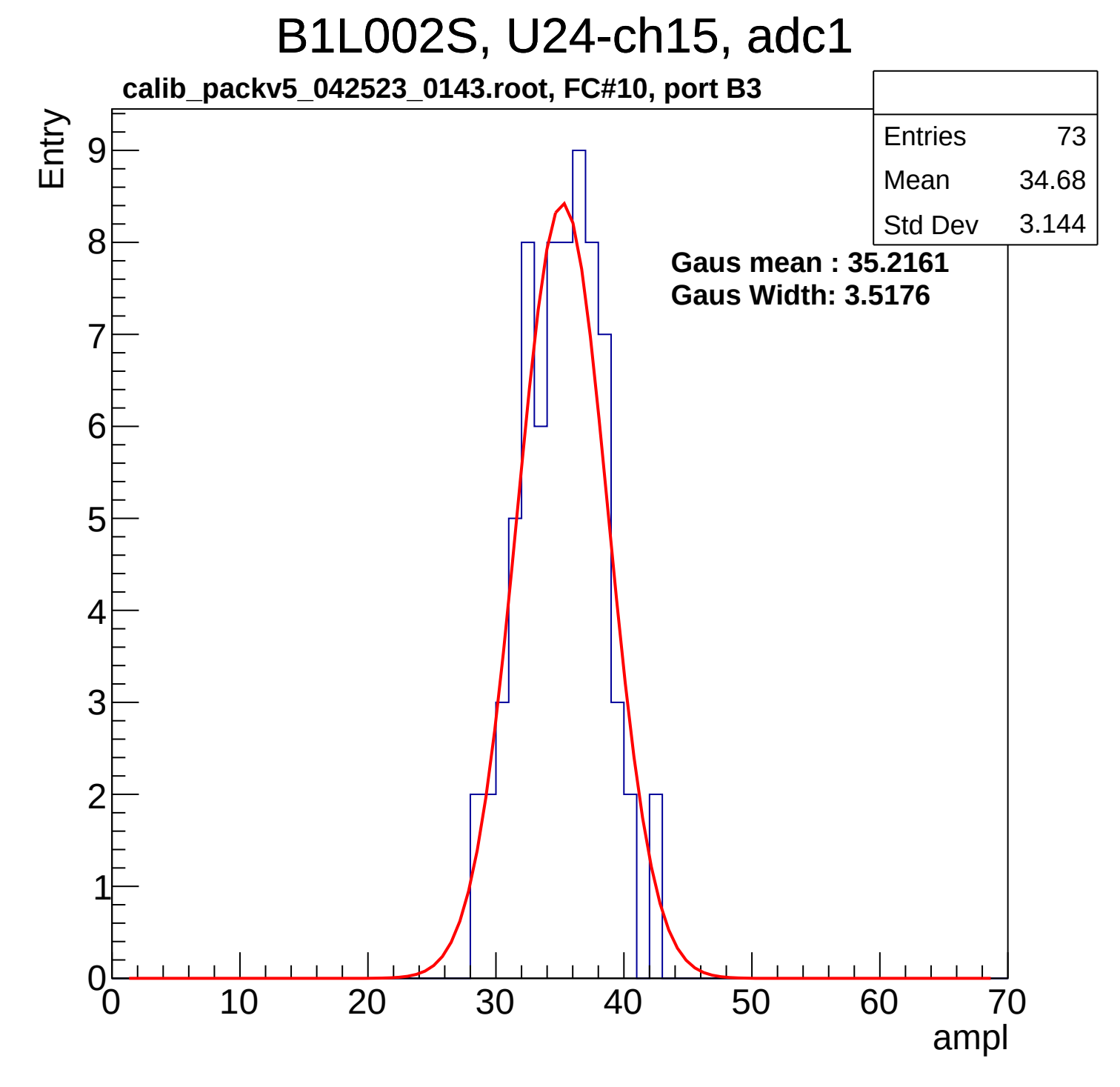
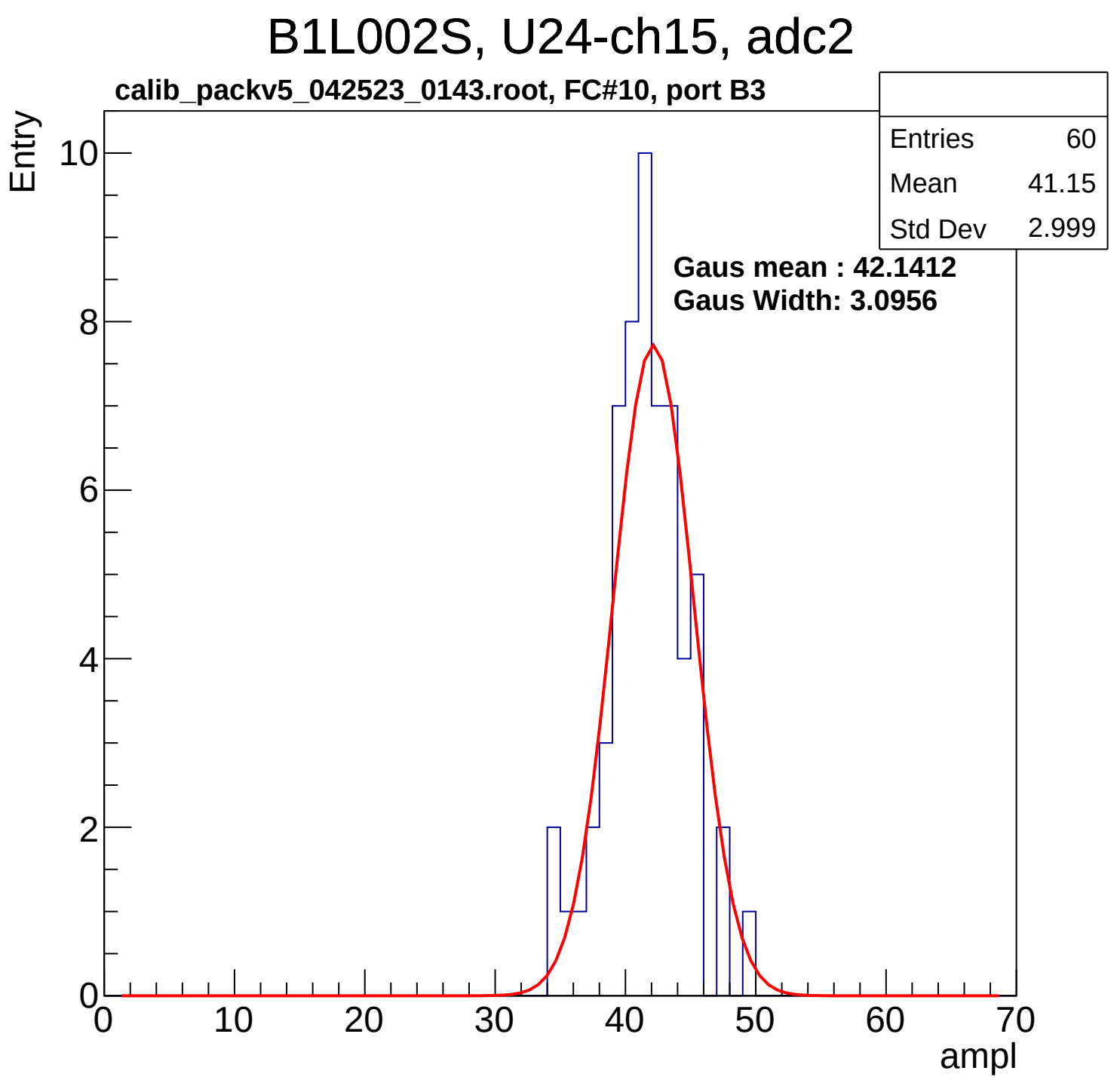
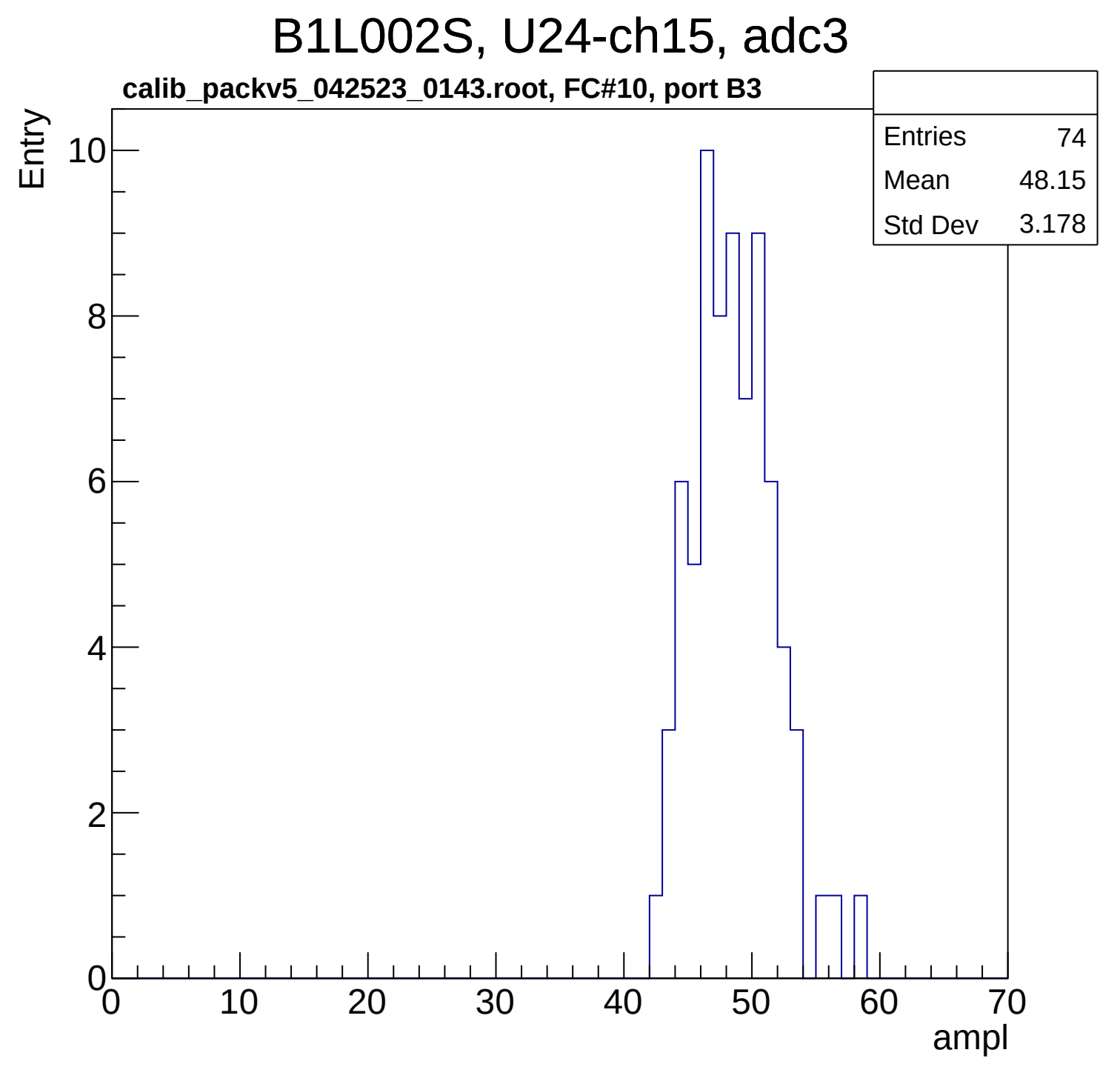

B1L002S, U24-ch14, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58
Std Dev 13.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

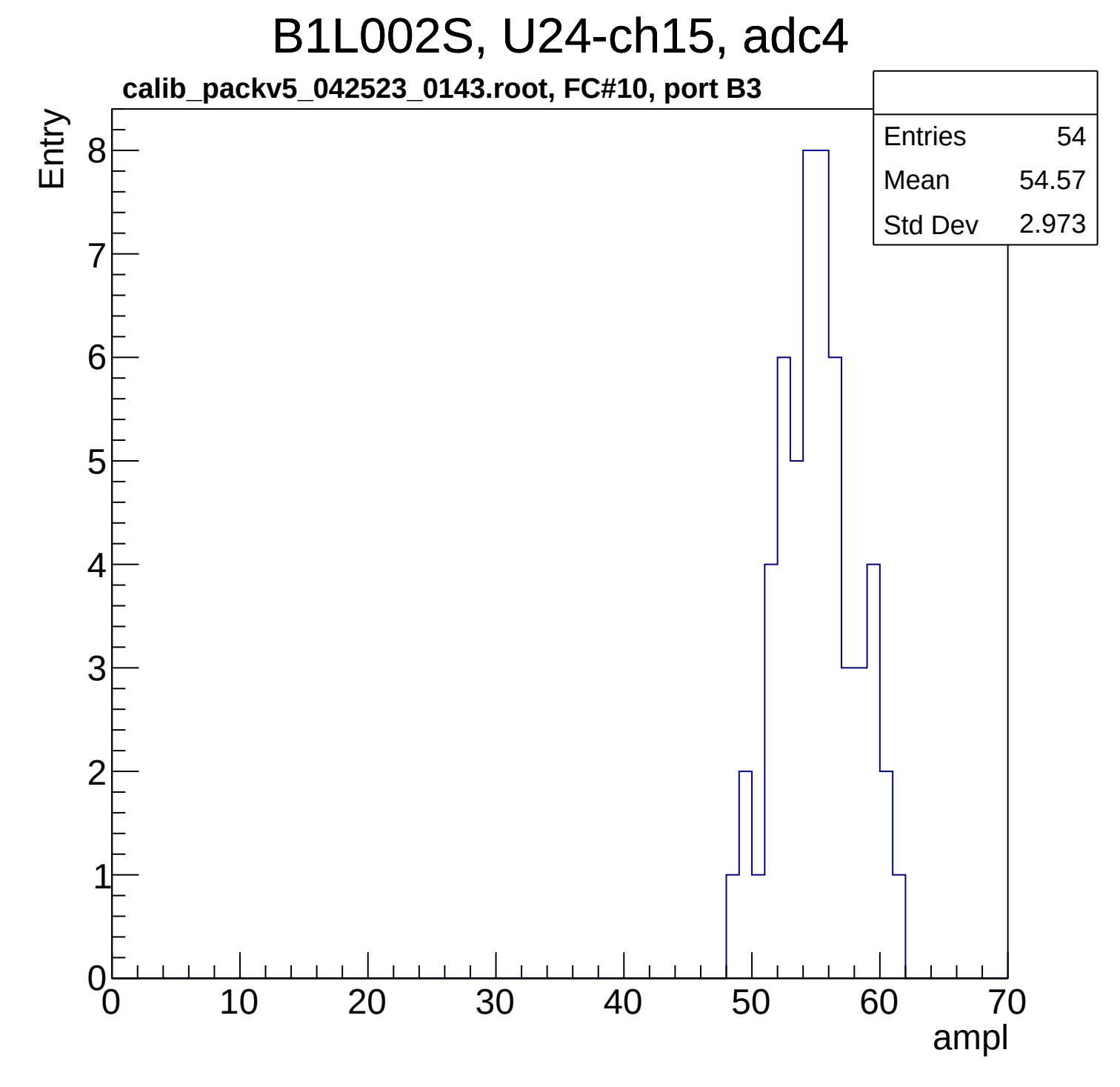

B1L002S, U24-ch14, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 42.2
Std Dev 26.1
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch15, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.92
Std Dev 3.073
Gaus mean : 28.3405
Gaus Width: 3.2802
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch15, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.68
Std Dev 3.144
Gaus mean : 35.2161
Gaus Width: 3.5176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch15, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.15
Std Dev 2.999
Gaus mean : 42.1412
Gaus Width: 3.0956
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch15, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 48.15
Std Dev 3.178
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch15, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 54.57
Std Dev 2.973
Entry
0
1
2
3
4
5
6
7
8
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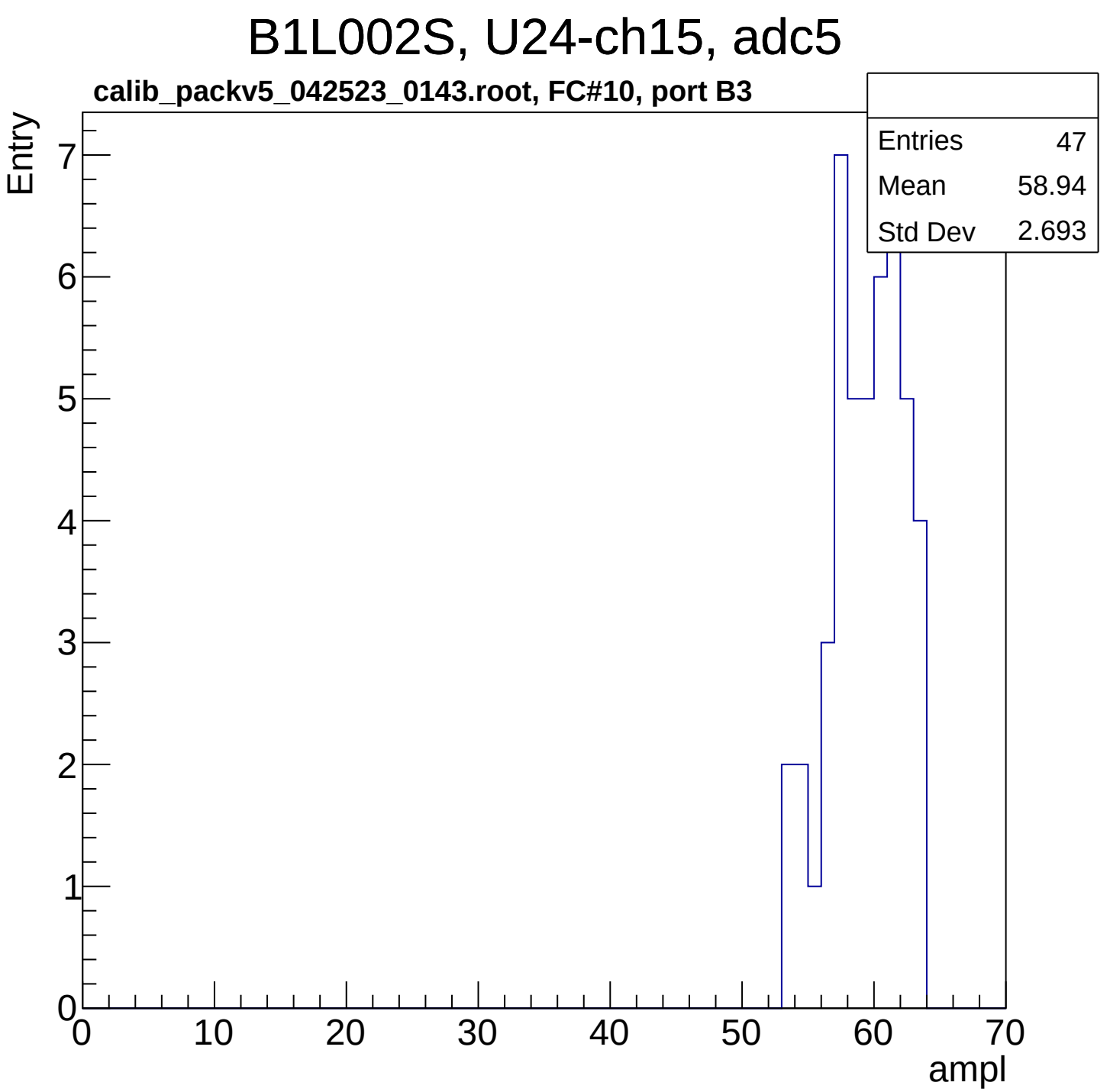
70
ampl


B1L002S, U24-ch15, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.94
Std Dev 2.693
Entry
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2
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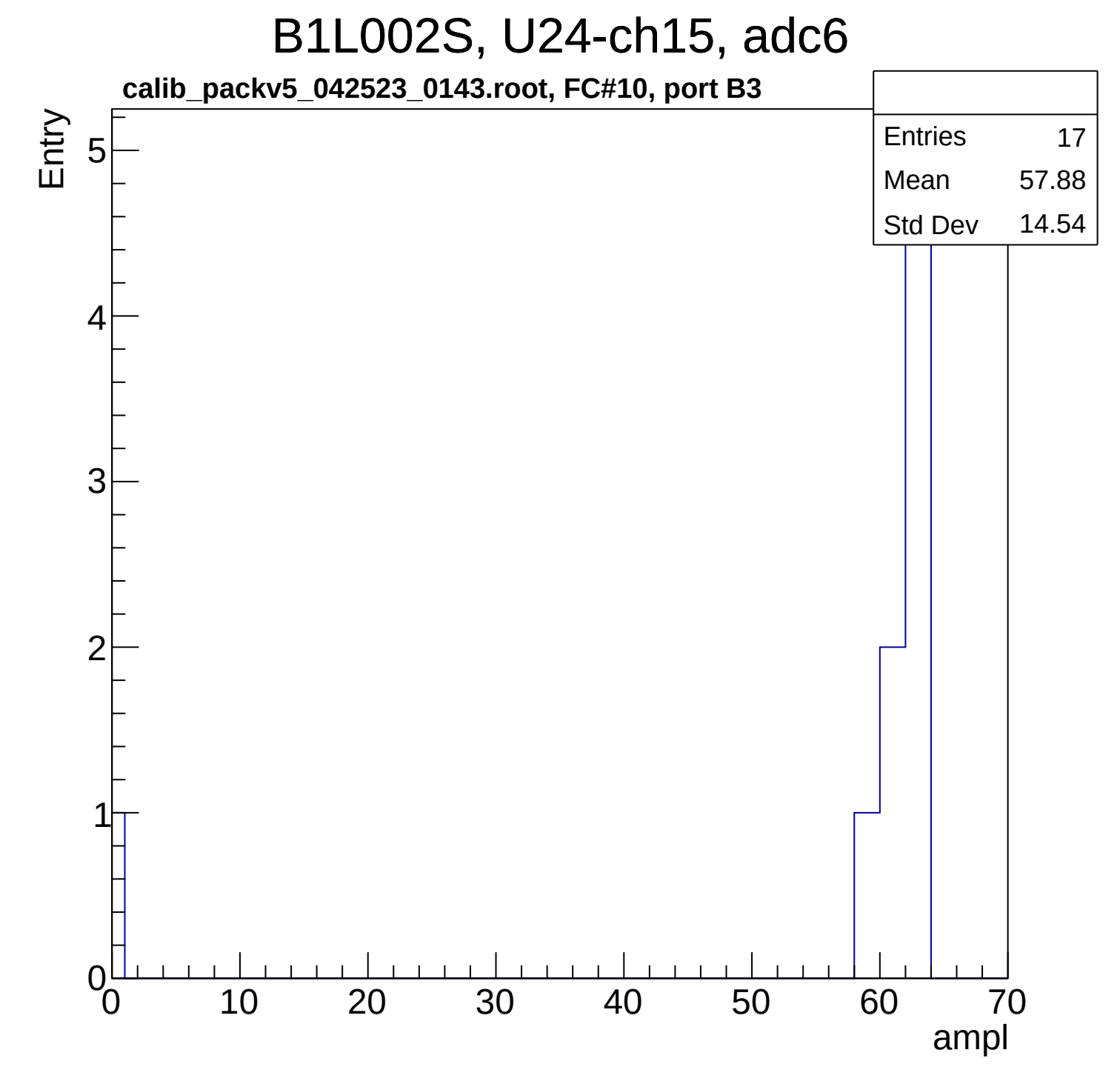
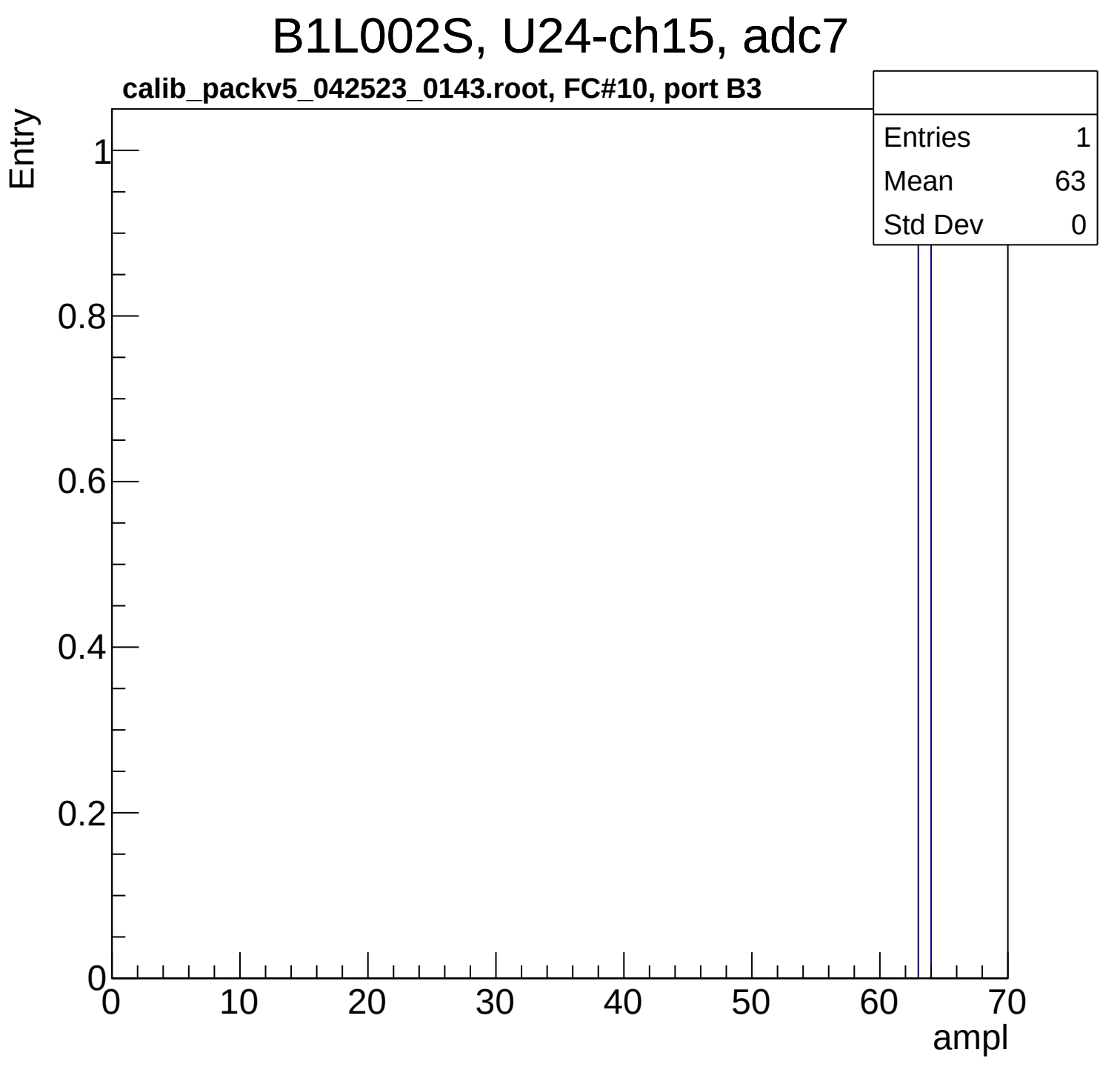
50
60
70
ampl


B1L002S, U24-ch15, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.88
Std Dev 14.54
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch15, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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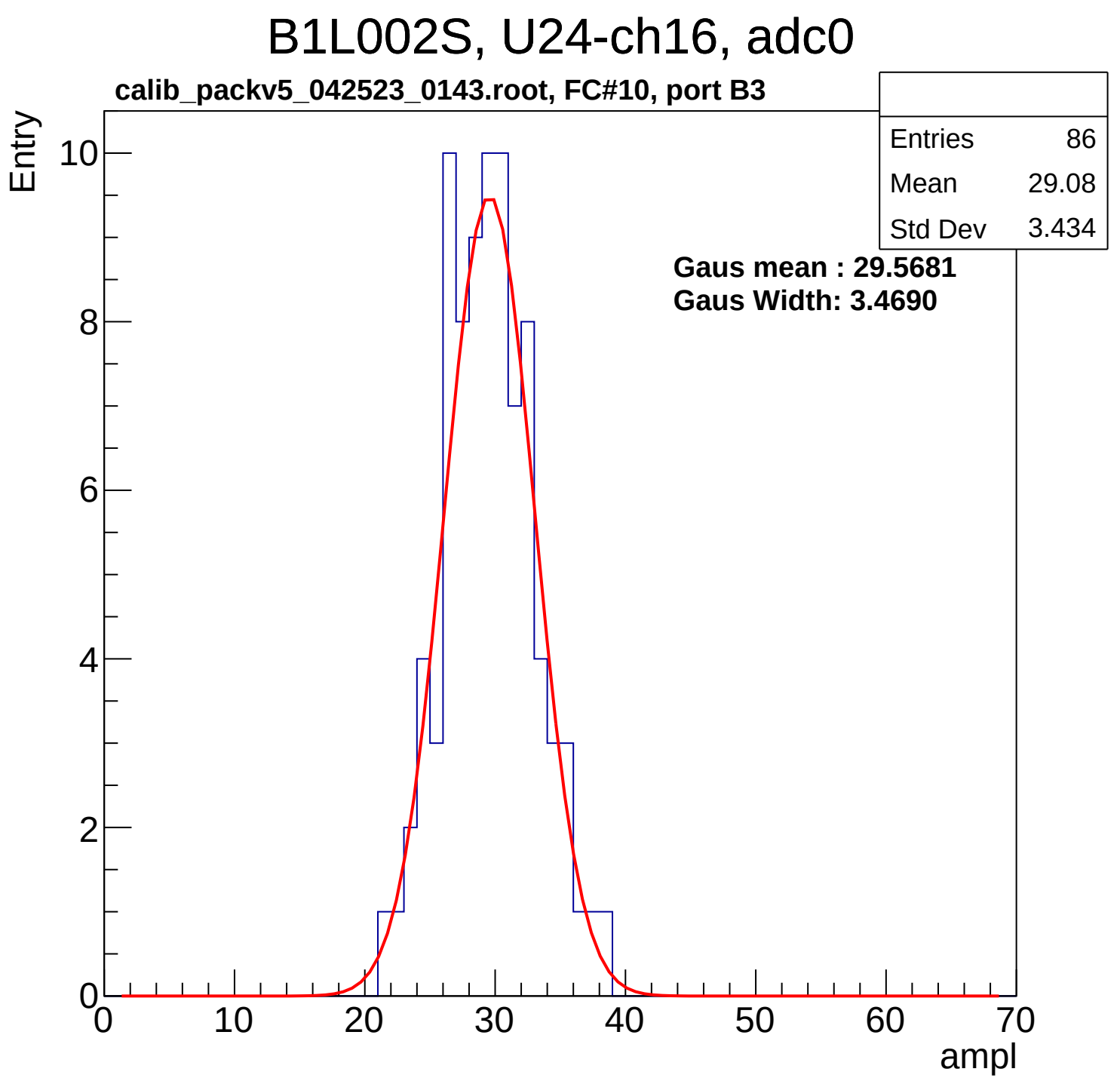
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch16, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 29.08
Std Dev 3.434
Gaus mean : 29.5681
Gaus Width: 3.4690
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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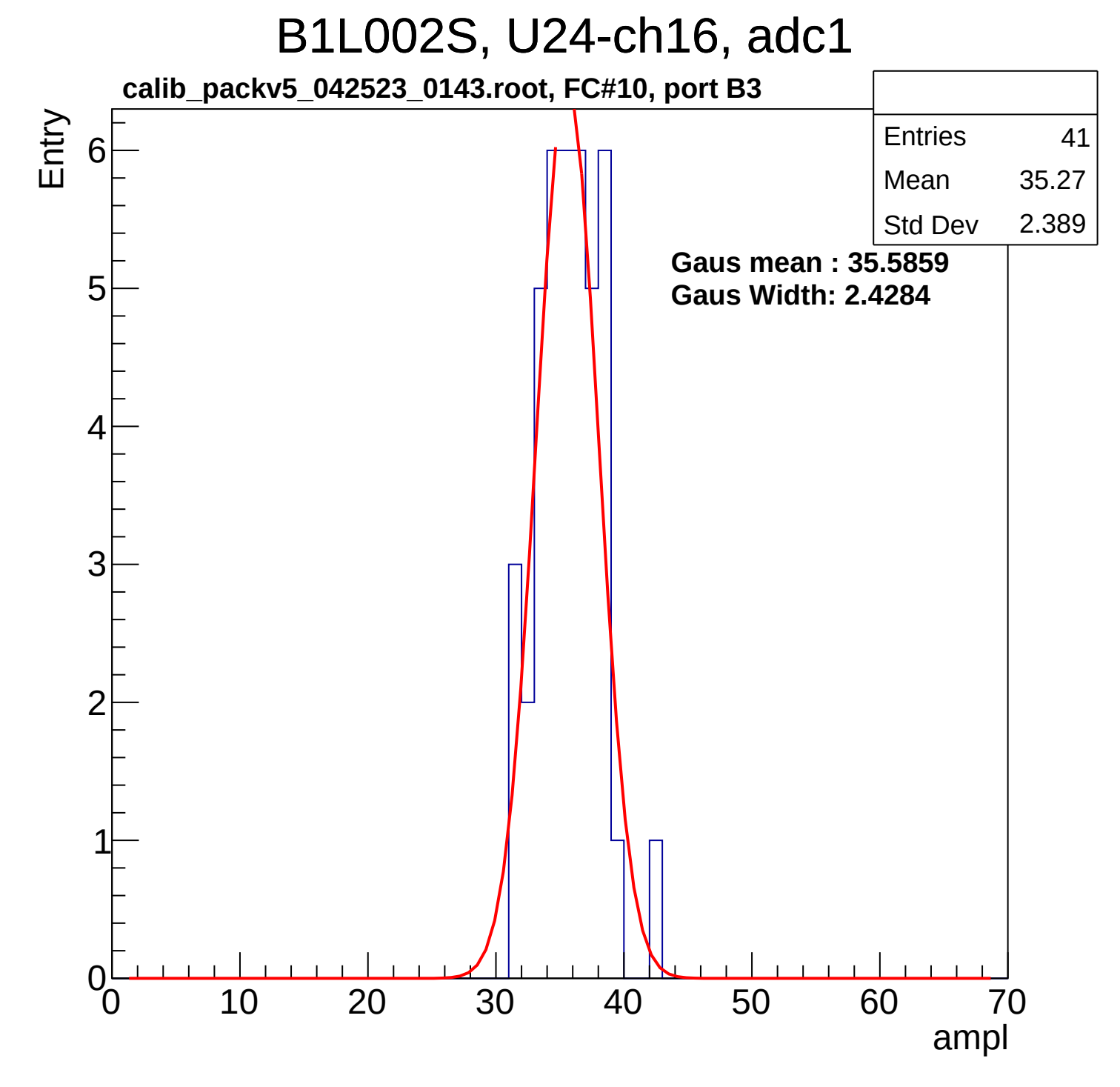
B1L002S, U24-ch16, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 35.27
Std Dev 2.389
Gaus mean : 35.5859
Gaus Width: 2.4284
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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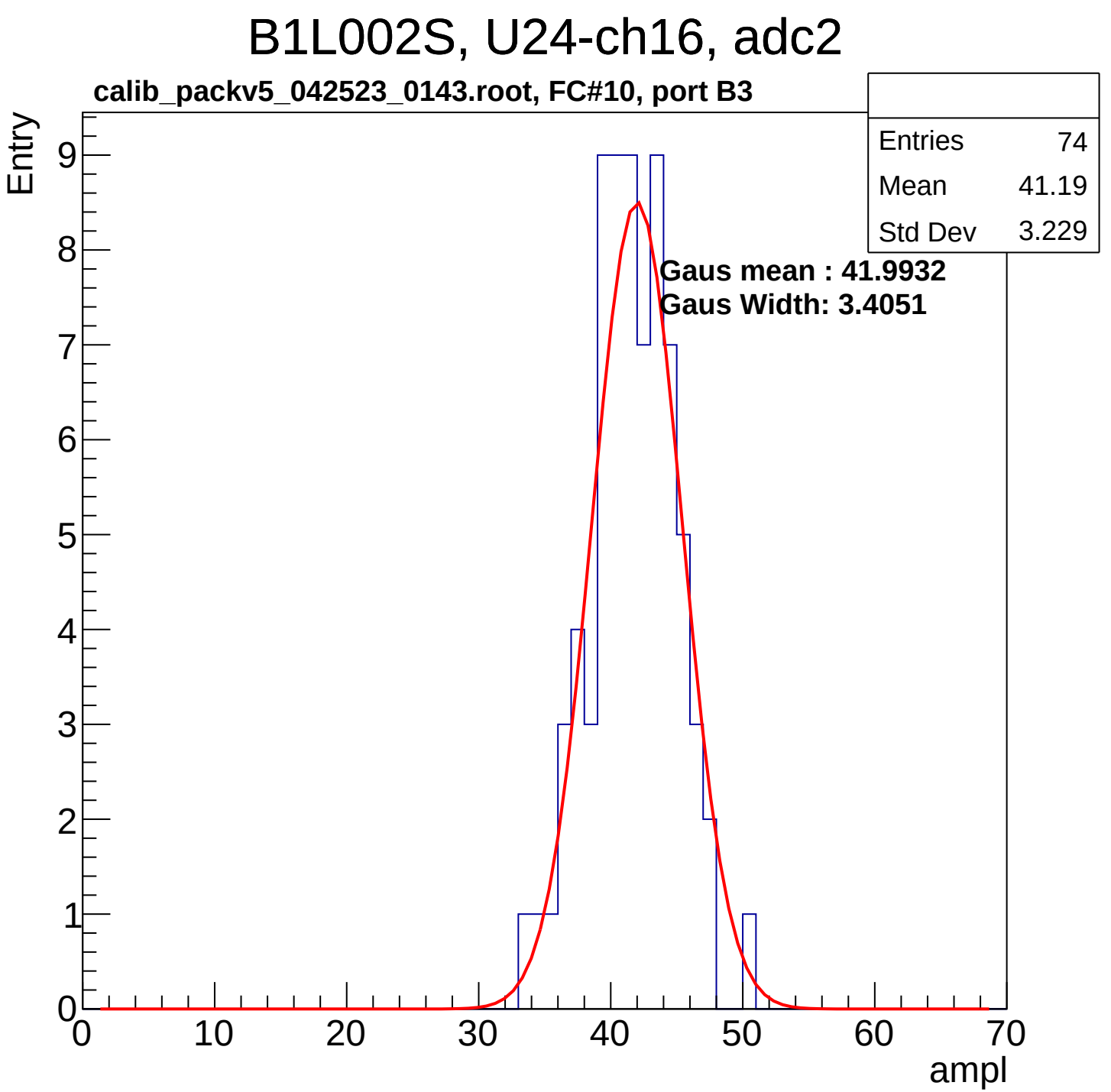
B1L002S, U24-ch16, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 41.19
Std Dev 3.229
Gaus mean : 41.9932
Gaus Width: 3.4051
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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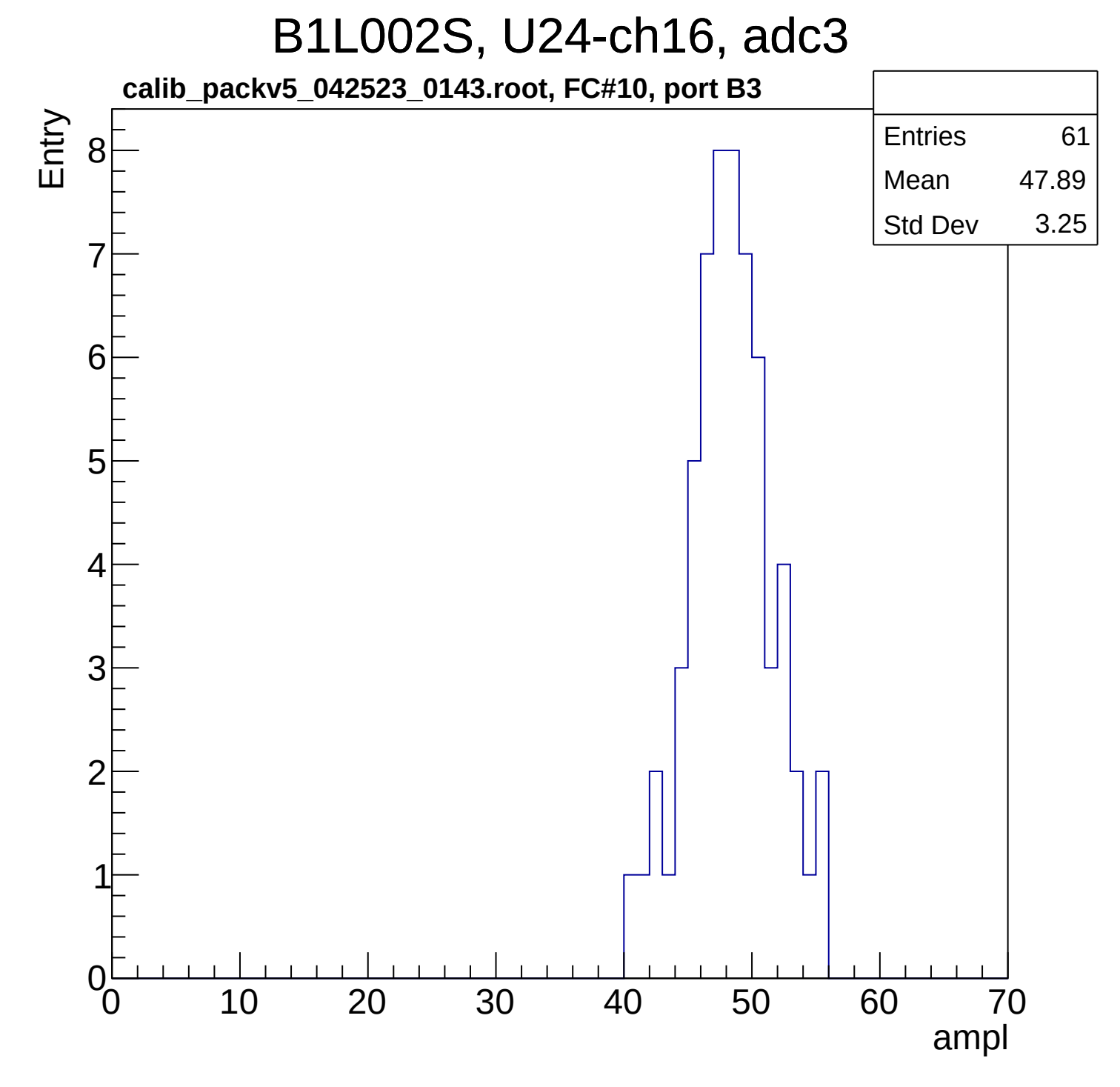
B1L002S, U24-ch16, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.89
Std Dev 3.25
Entry
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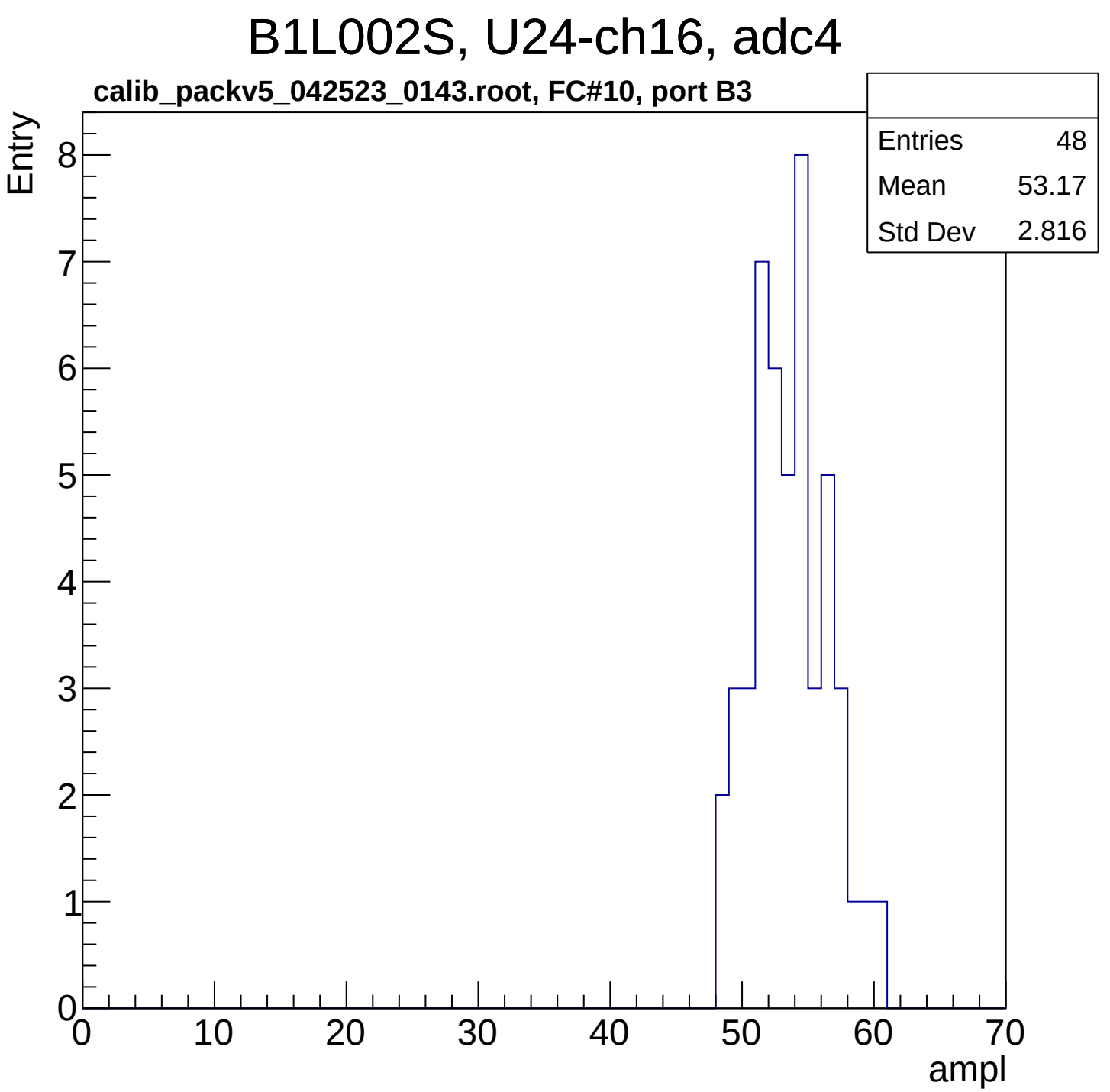
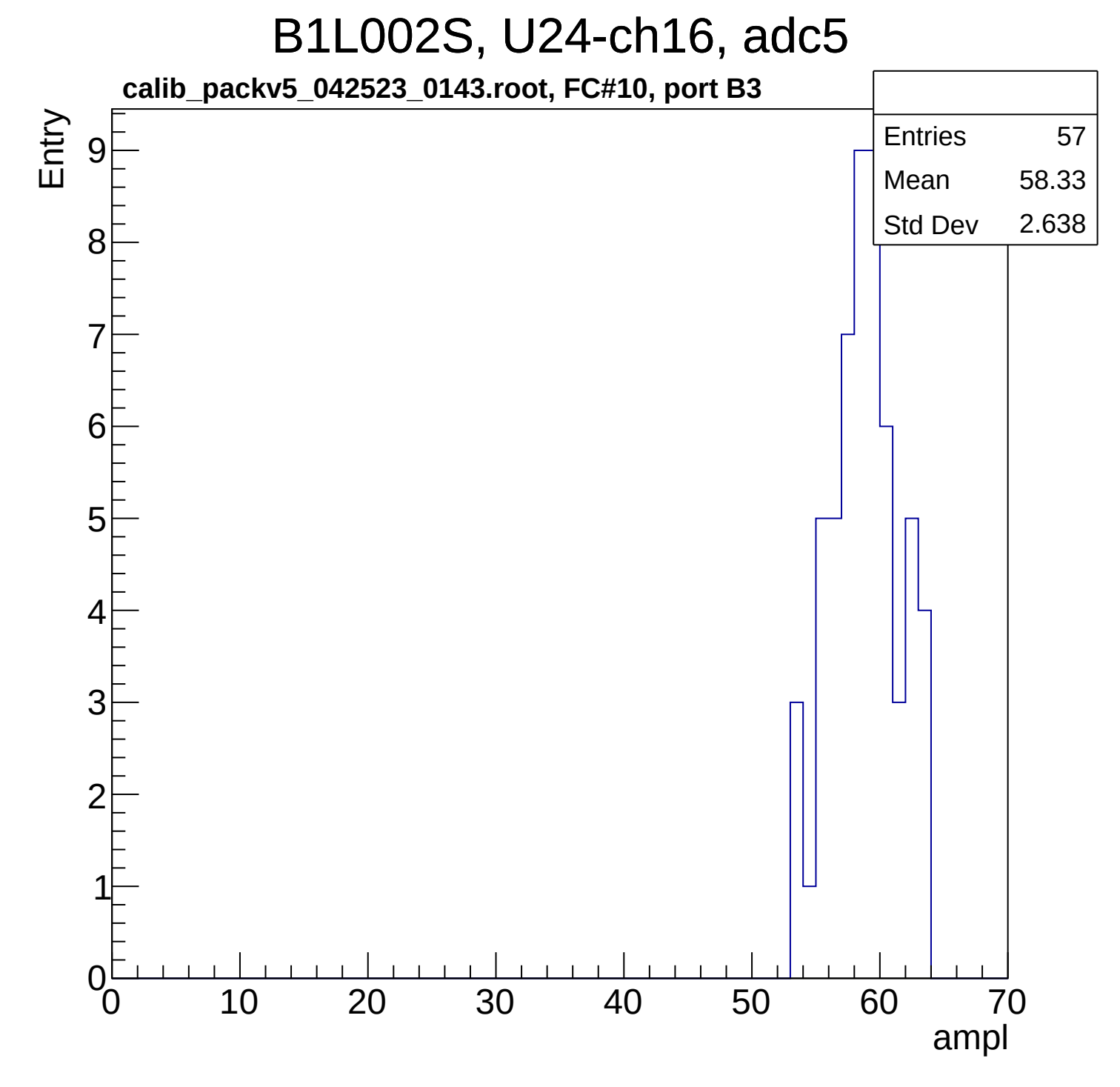
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch16, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.17
Std Dev 2.816
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch16, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.33
Std Dev 2.638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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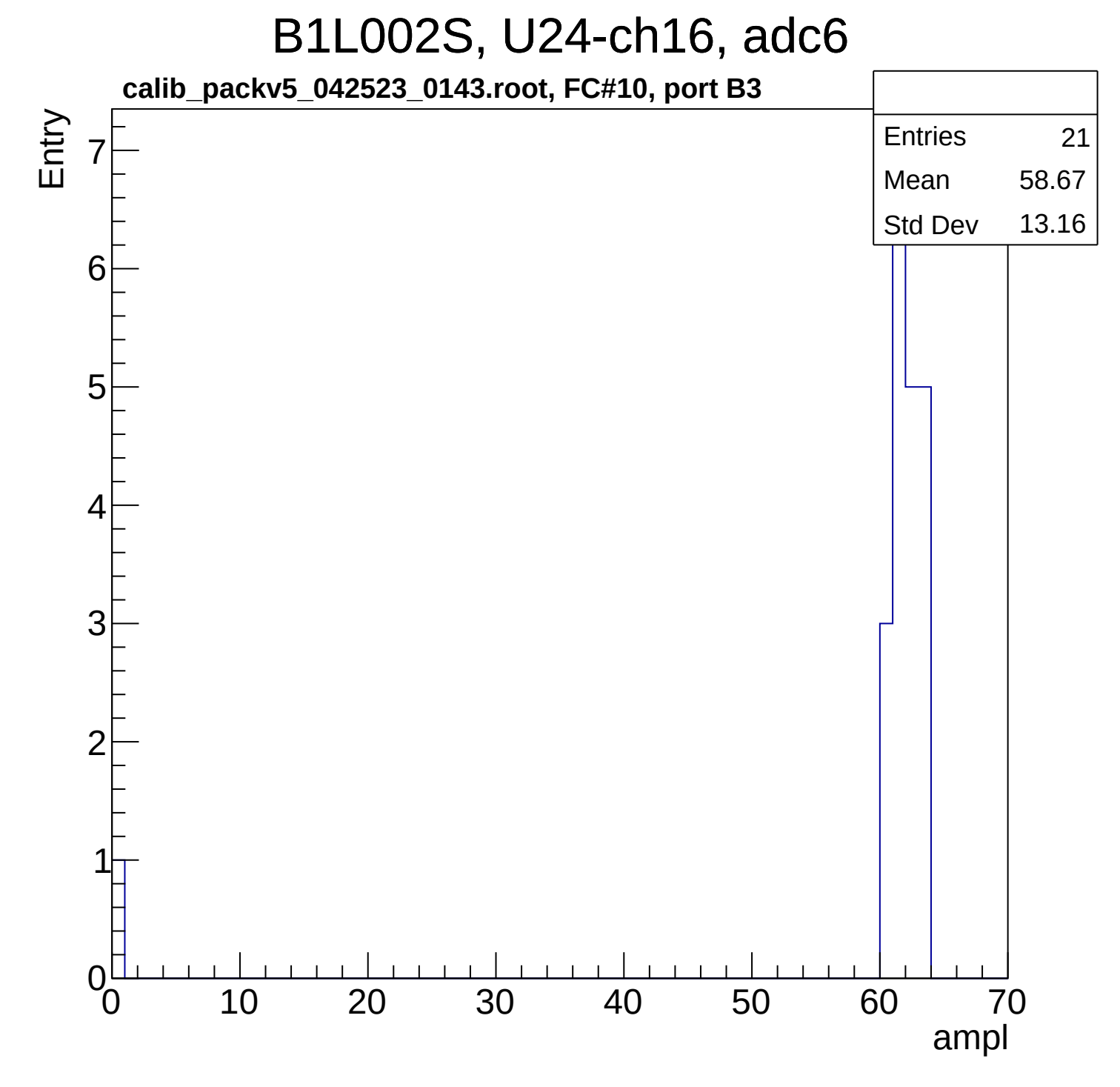
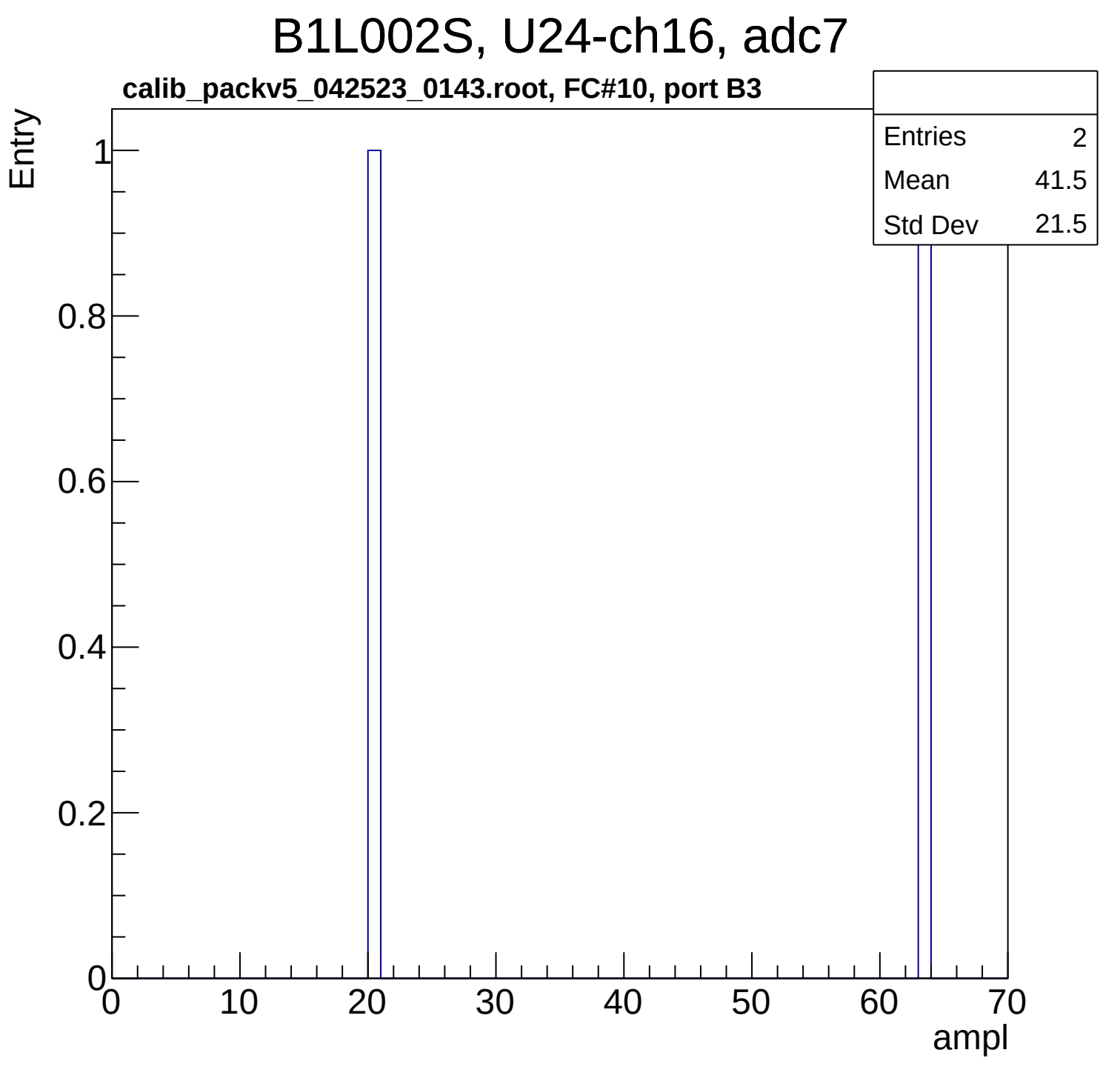
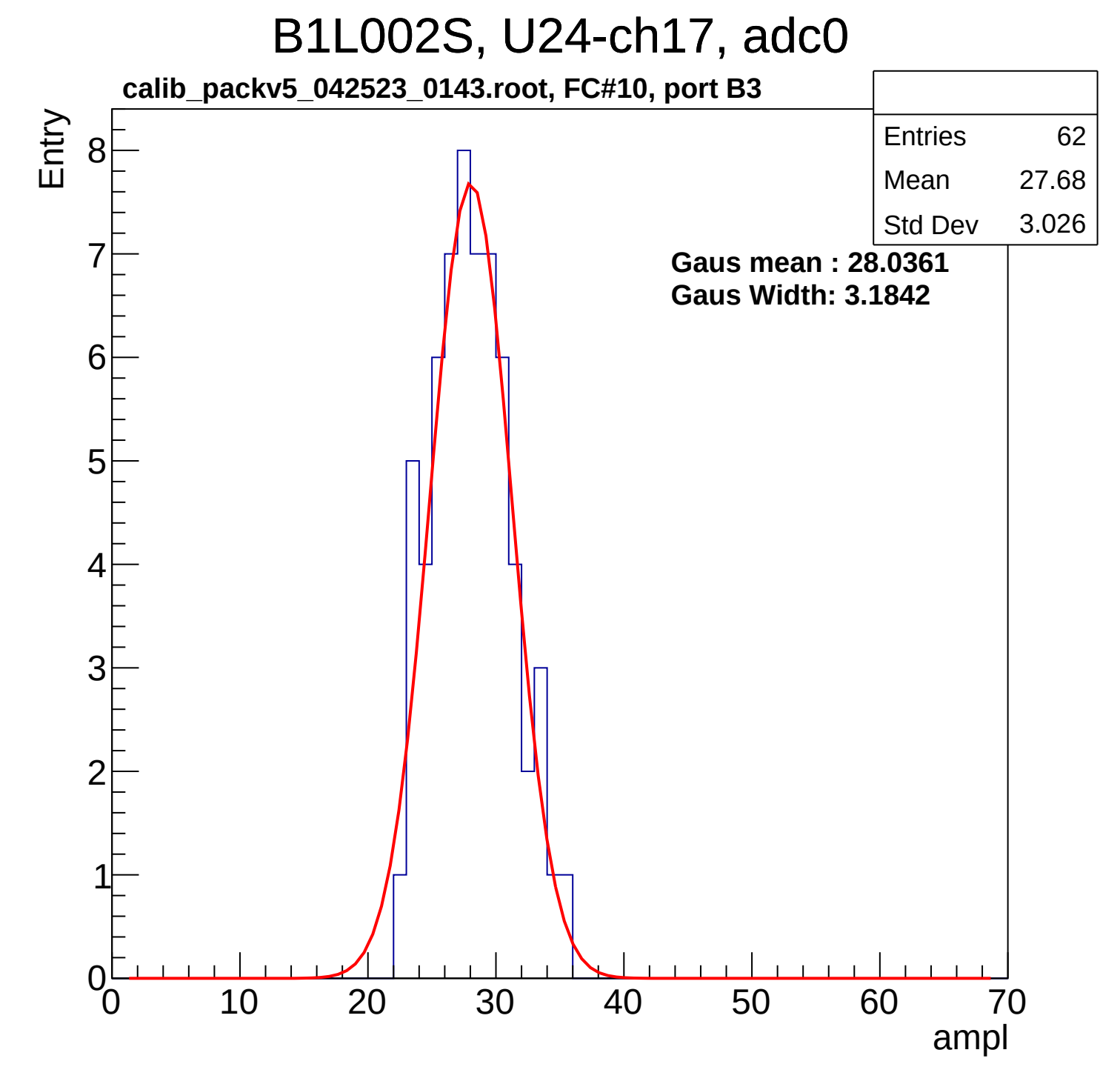
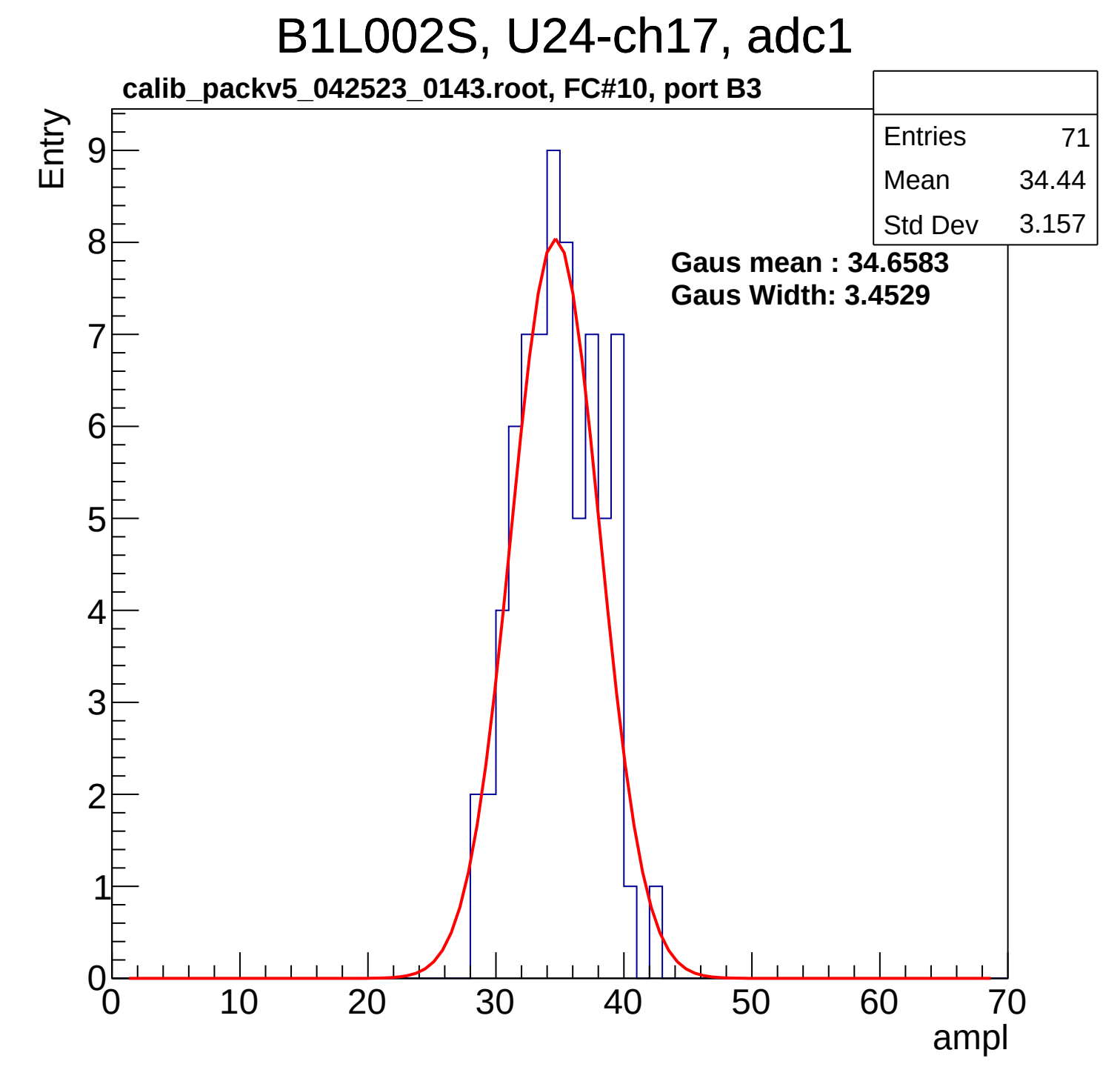
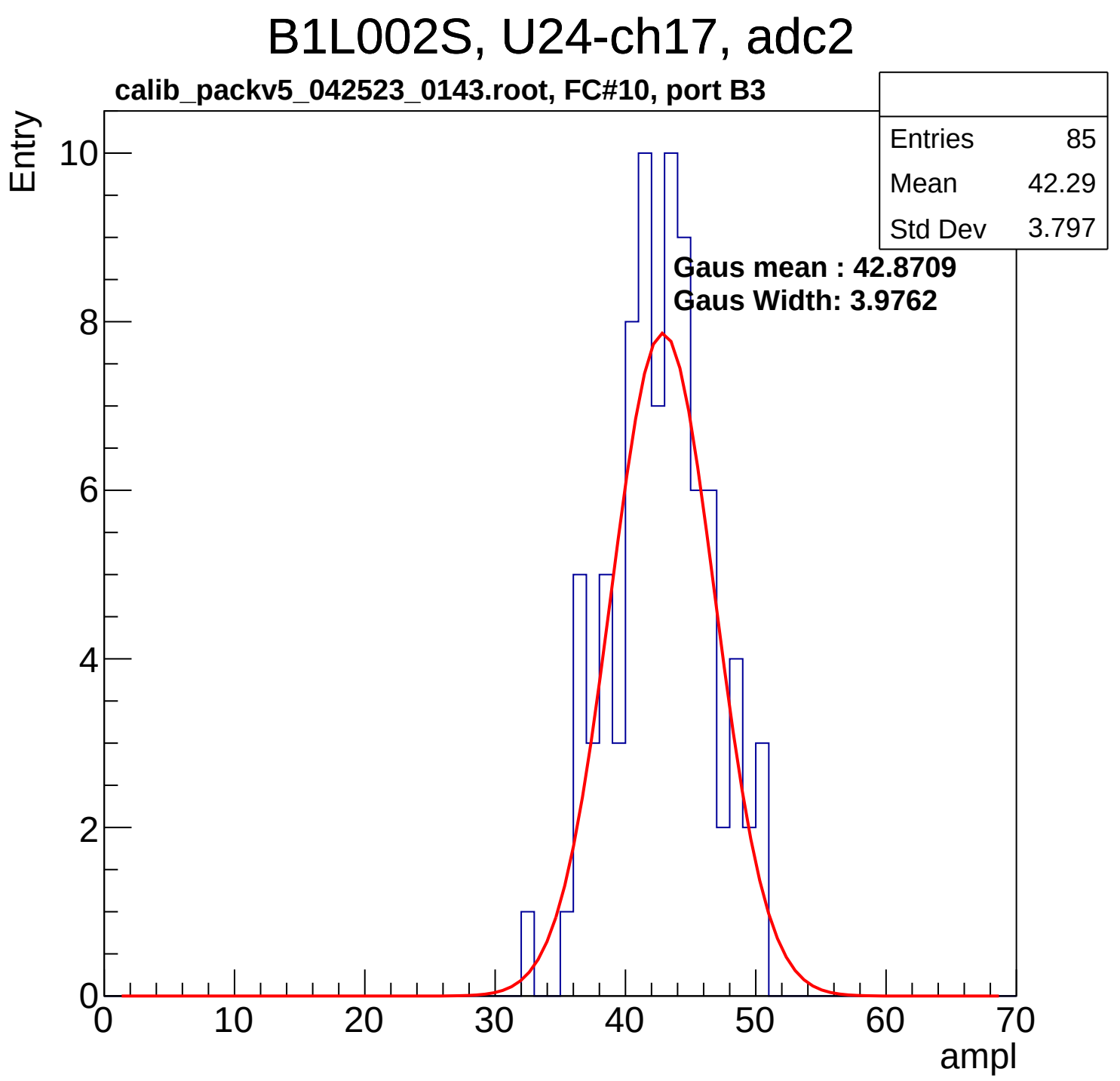
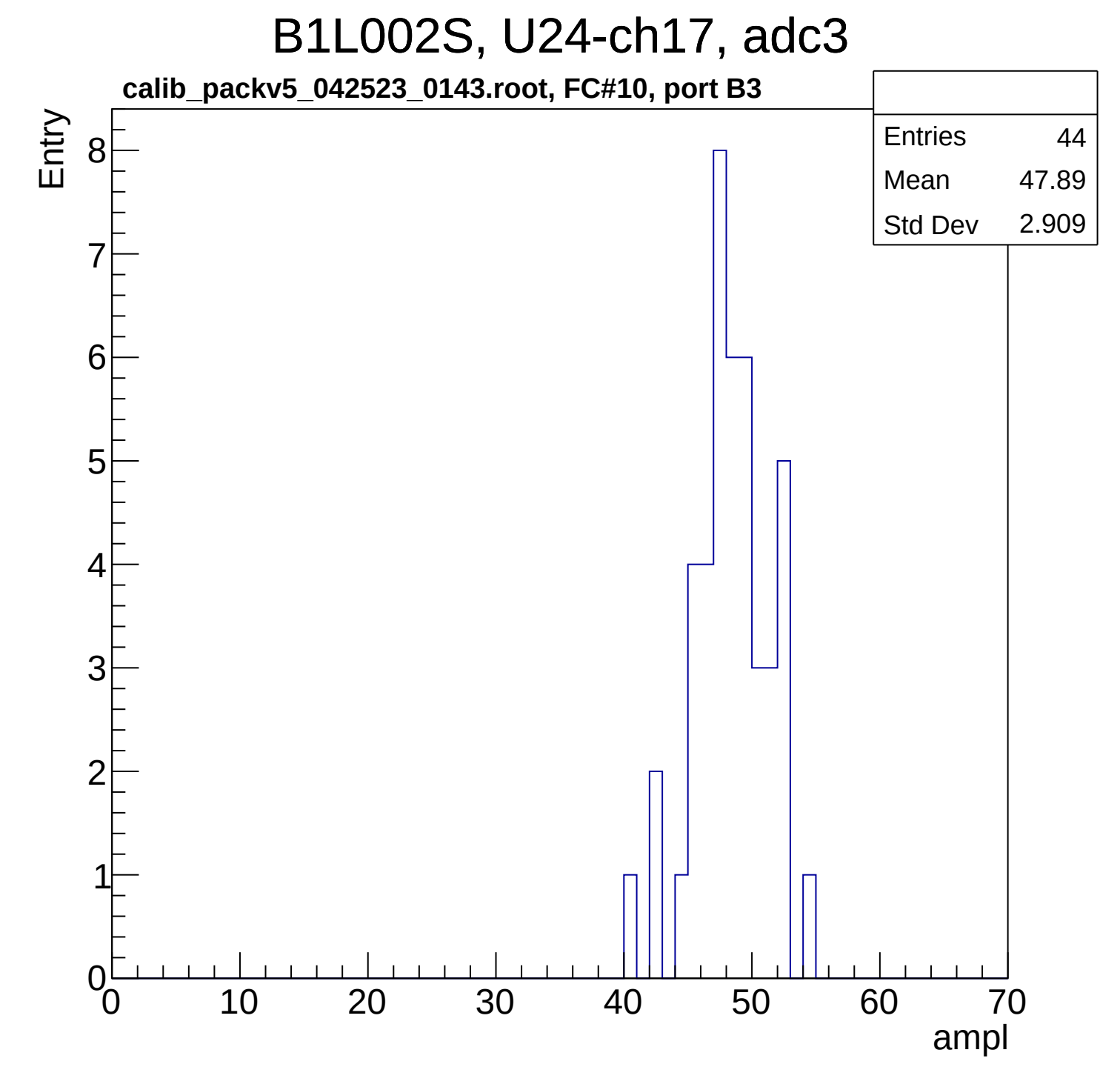

B1L002S, U24-ch16, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.67
Std Dev 13.16
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

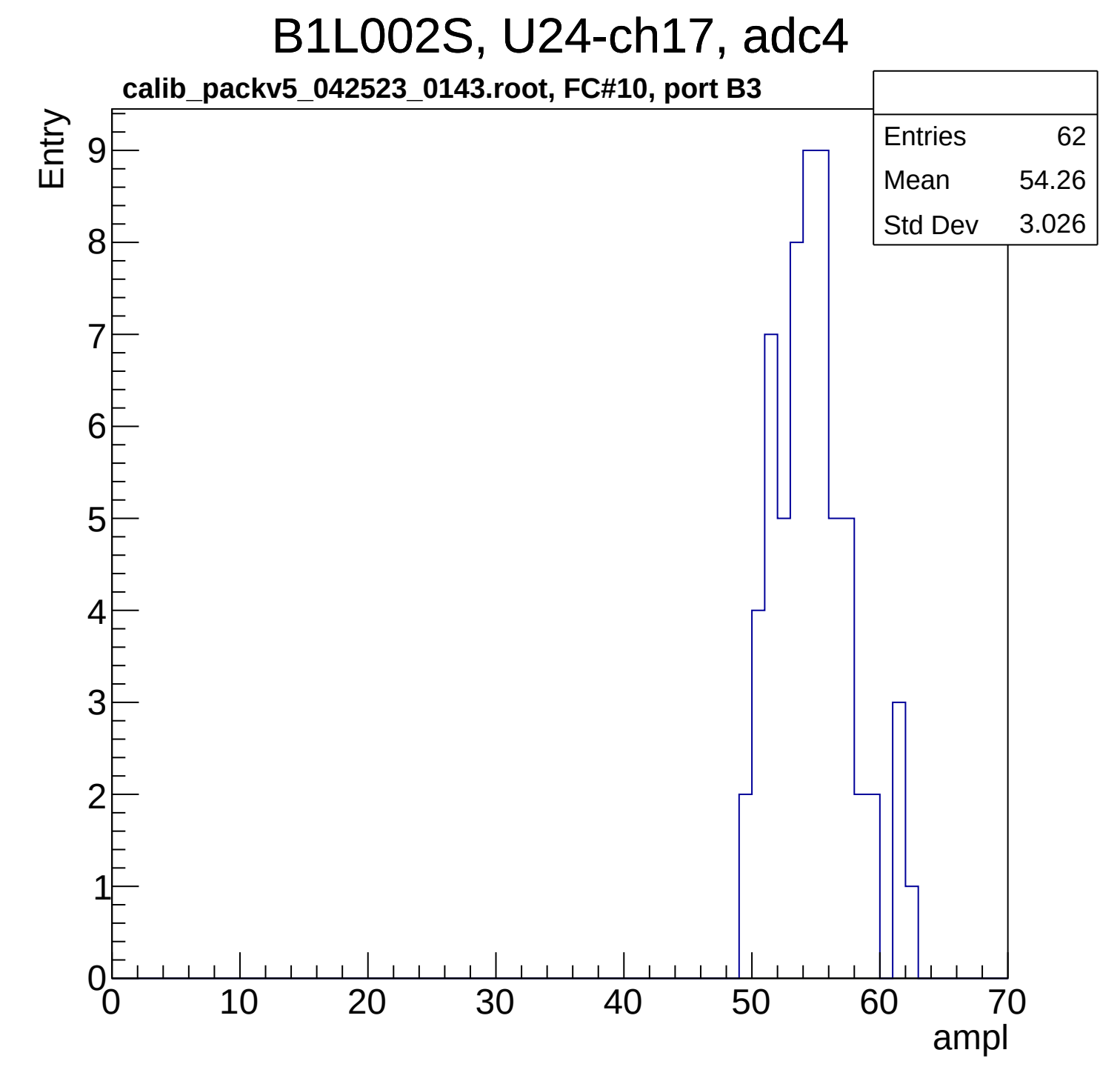

B1L002S, U24-ch16, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41.5
Std Dev 21.5
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl

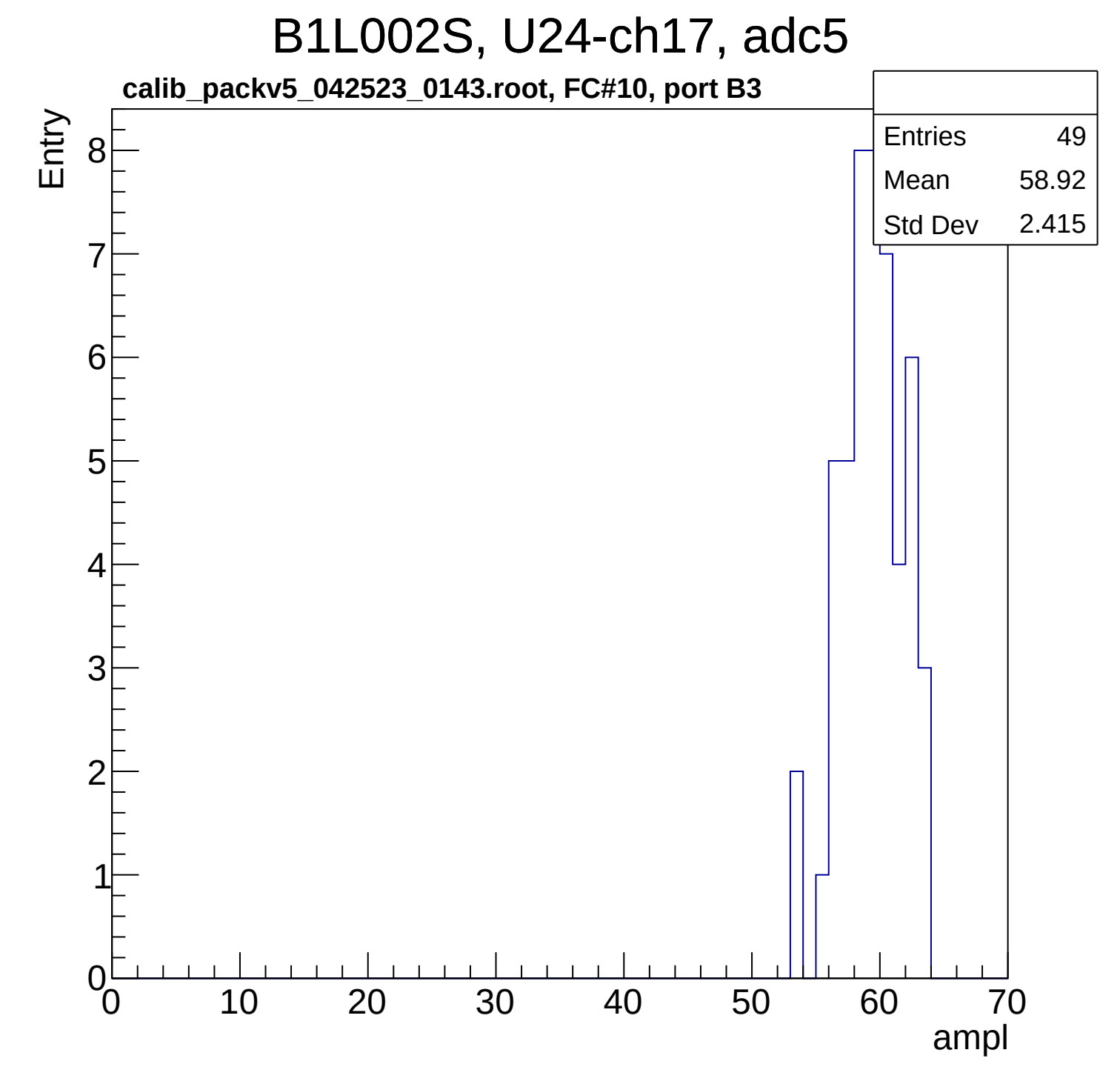

B1L002S, U24-ch17, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 27.68
Std Dev 3.026
Gaus mean : 28.0361
Gaus Width: 3.1842
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch17, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 34.44
Std Dev 3.157
Gaus mean : 34.6583
Gaus Width: 3.4529
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch17, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 42.29
Std Dev 3.797
Gaus mean : 42.8709
Gaus Width: 3.9762
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch17, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 47.89
Std Dev 2.909
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch17, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.26
Std Dev 3.026
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch17, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.92
Std Dev 2.415
Entry
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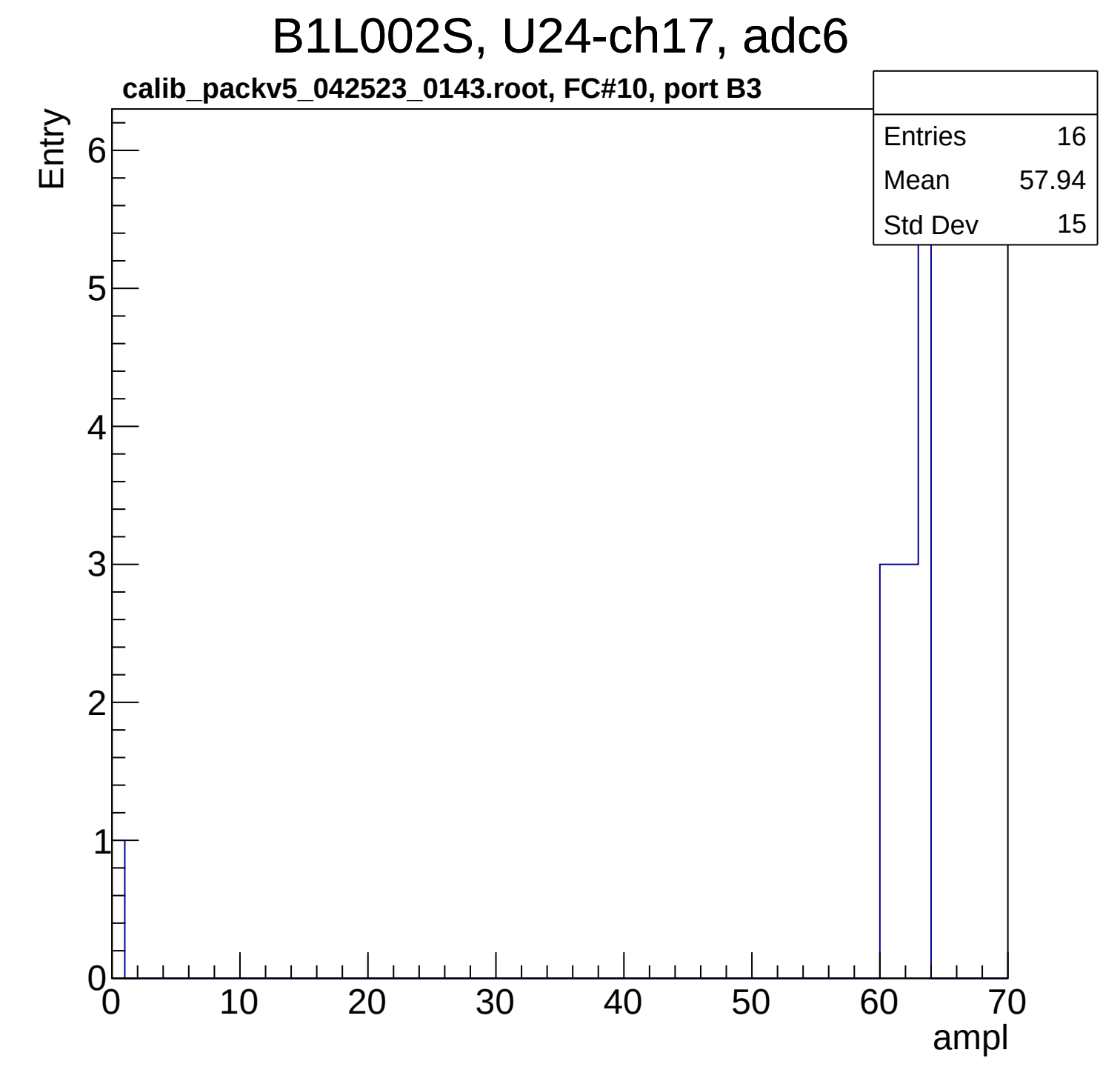
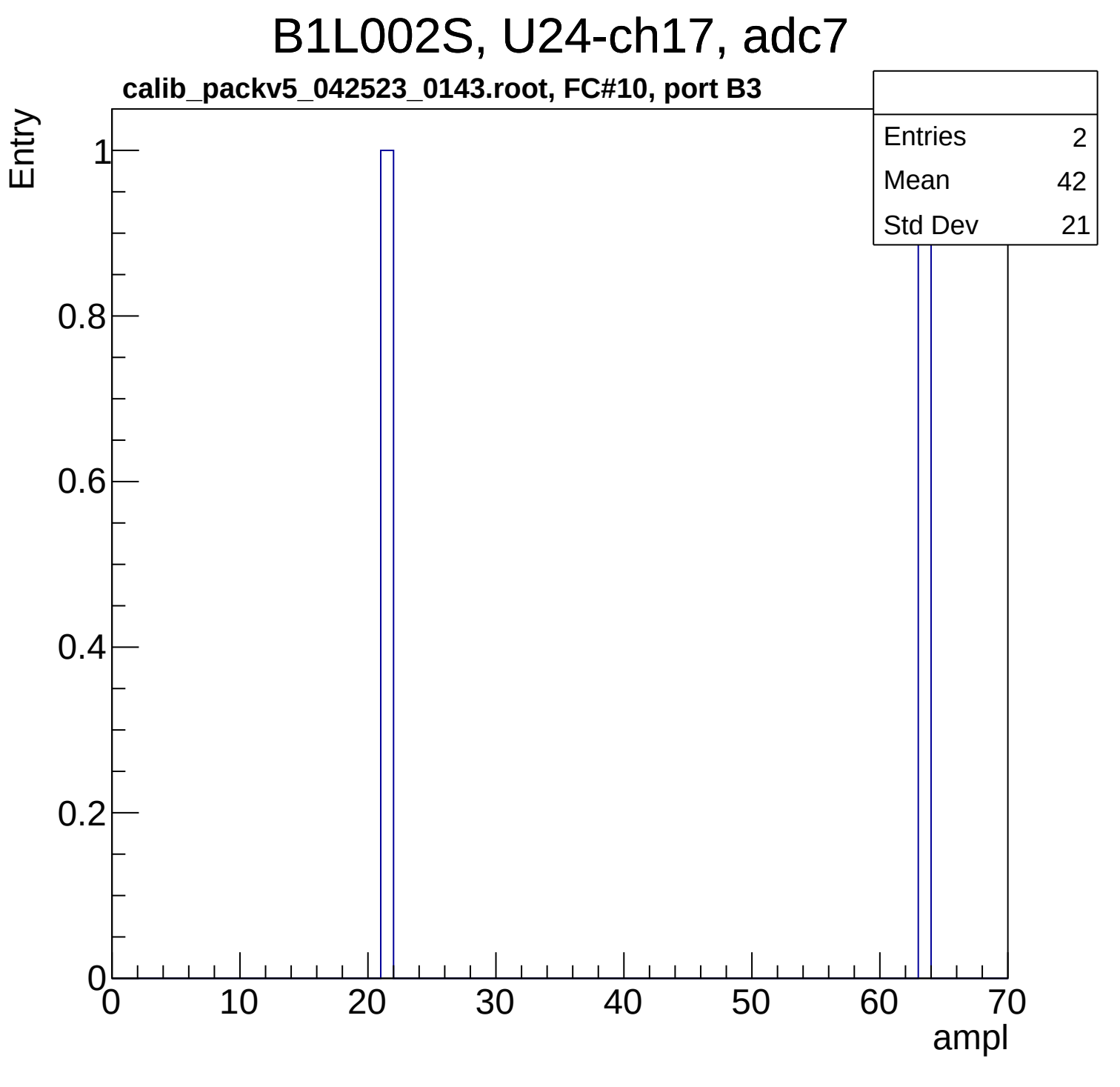
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch17, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.94
Std Dev 15
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch17, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 21
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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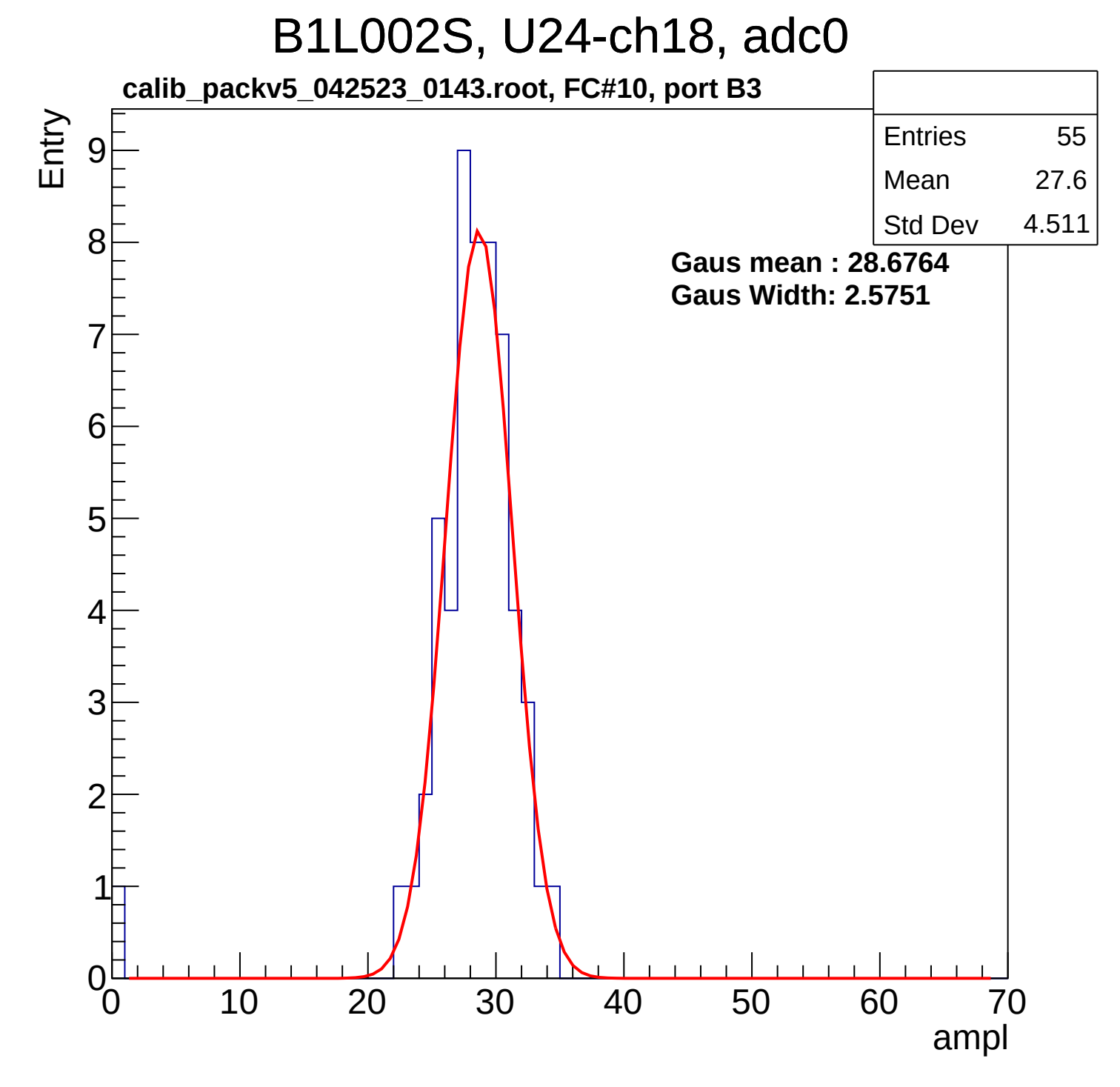
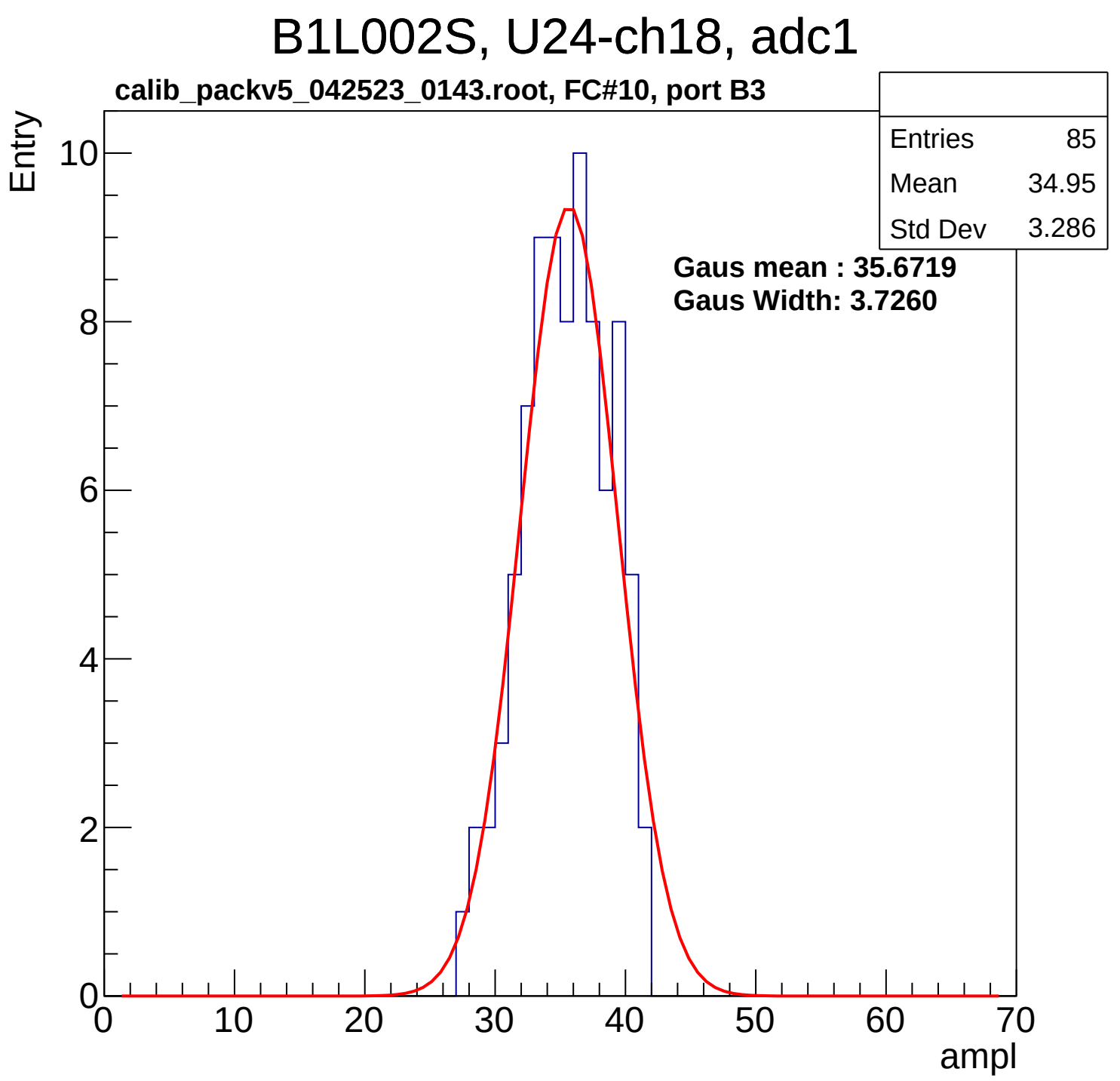
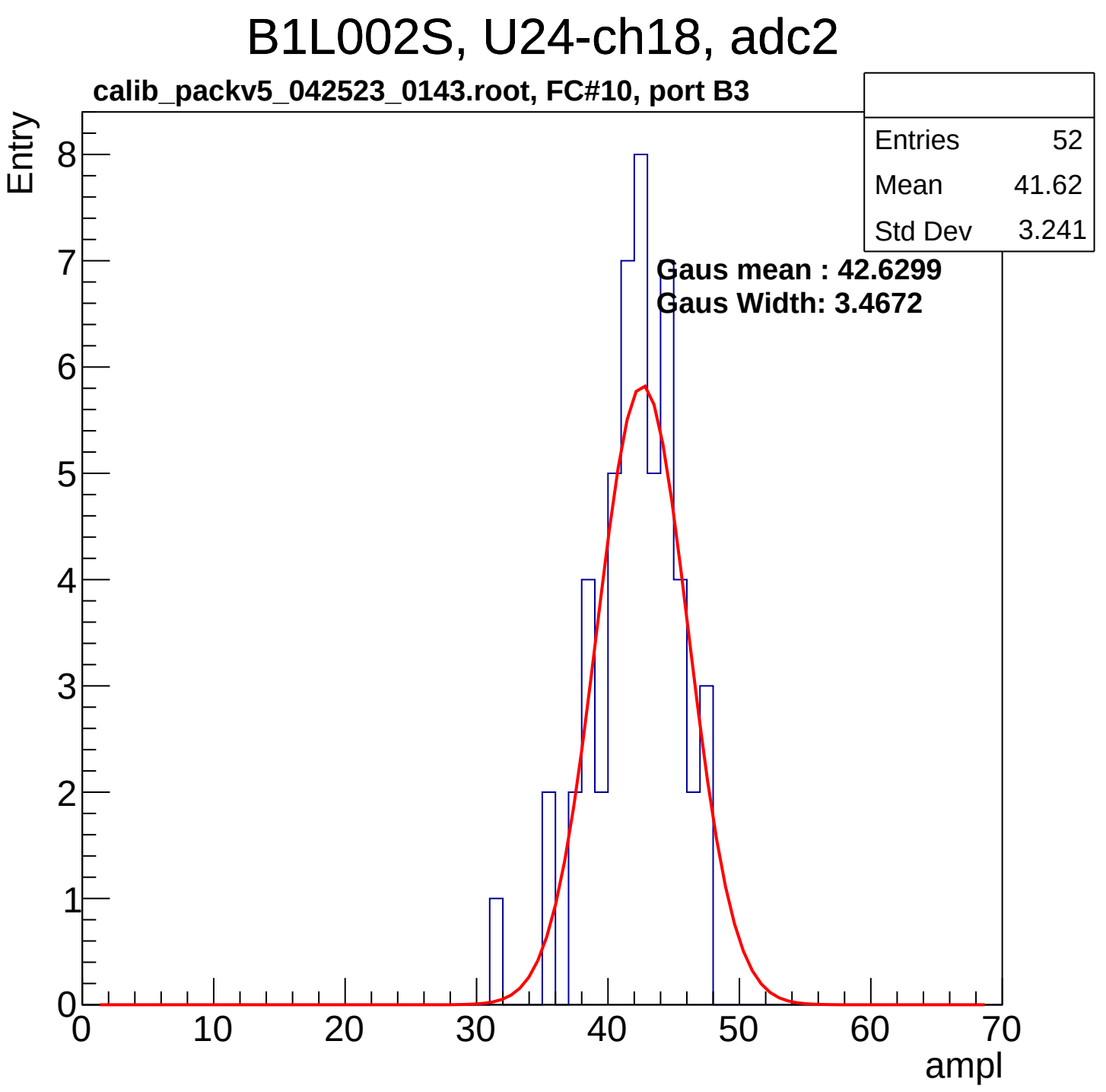

B1L002S, U24-ch18, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.6
Std Dev 4.511
Gaus mean : 28.6764
Gaus Width: 2.5751
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch18, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 34.95
Std Dev 3.286
Gaus mean : 35.6719
Gaus Width: 3.7260
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch18, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.62
Std Dev 3.241
Gaus mean : 42.6299
Gaus Width: 3.4672
Entry
ampl
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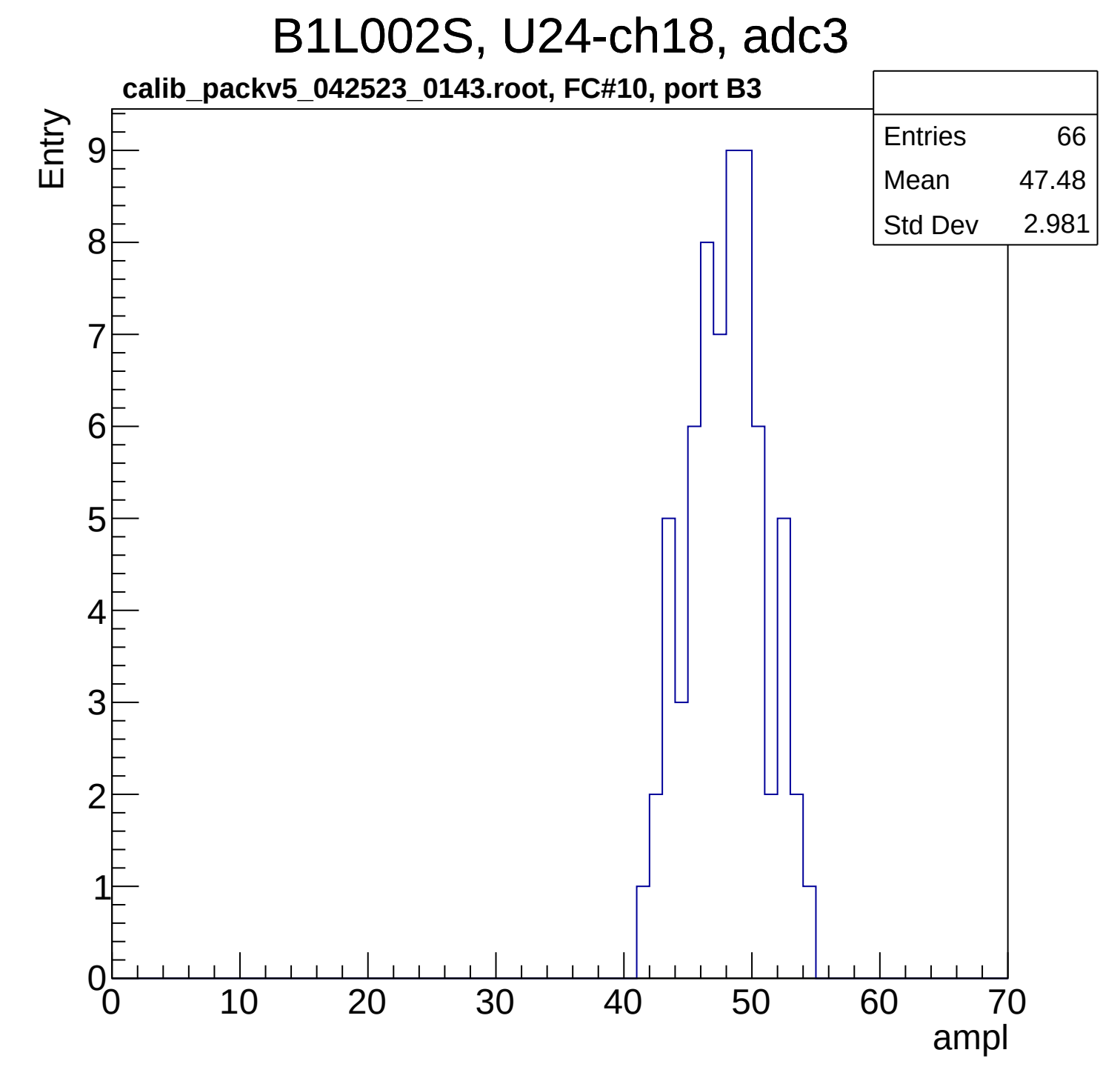
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch18, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.48
Std Dev 2.981
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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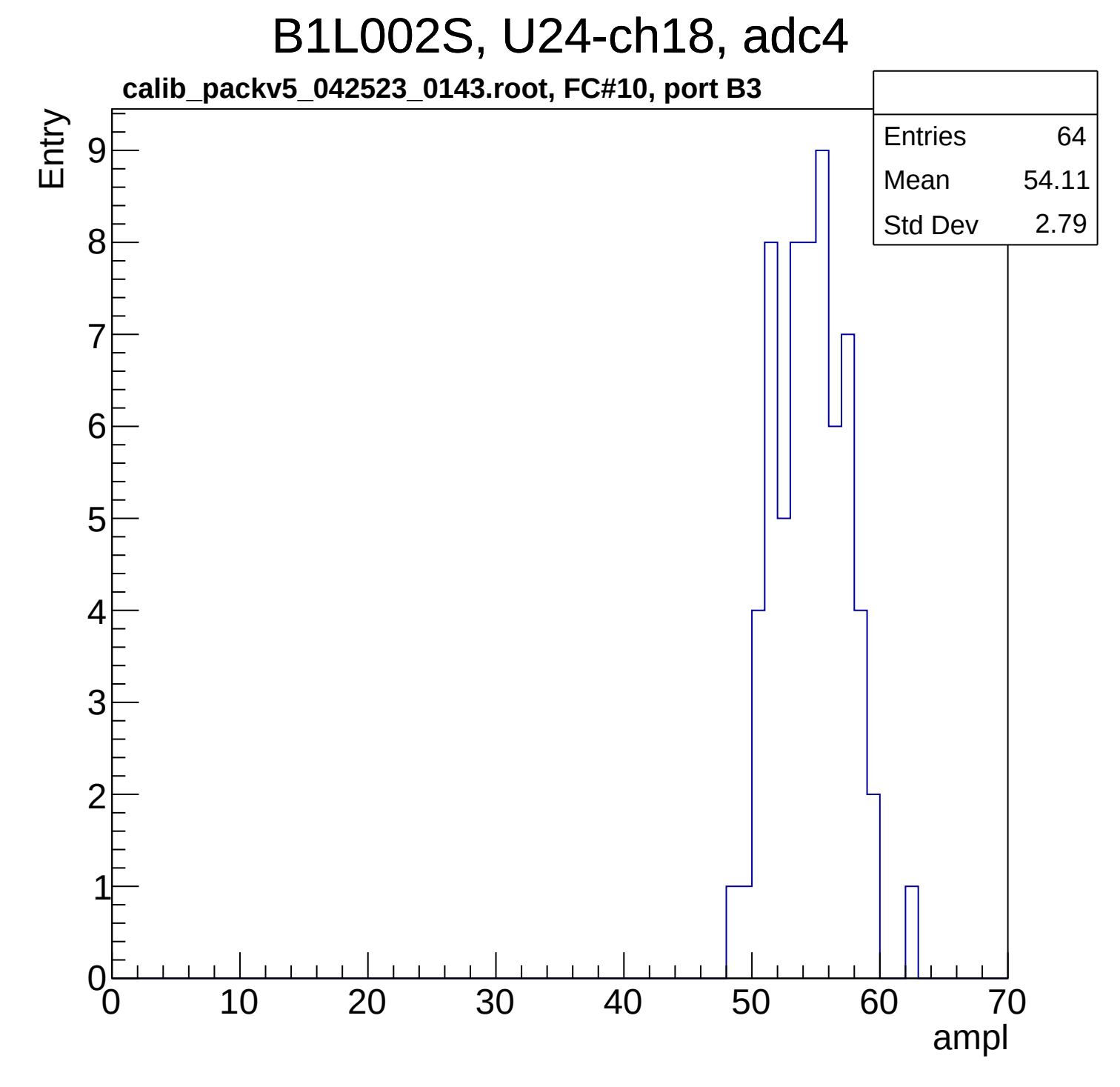
40
50
60
70


B1L002S, U24-ch18, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 54.11
Std Dev 2.79
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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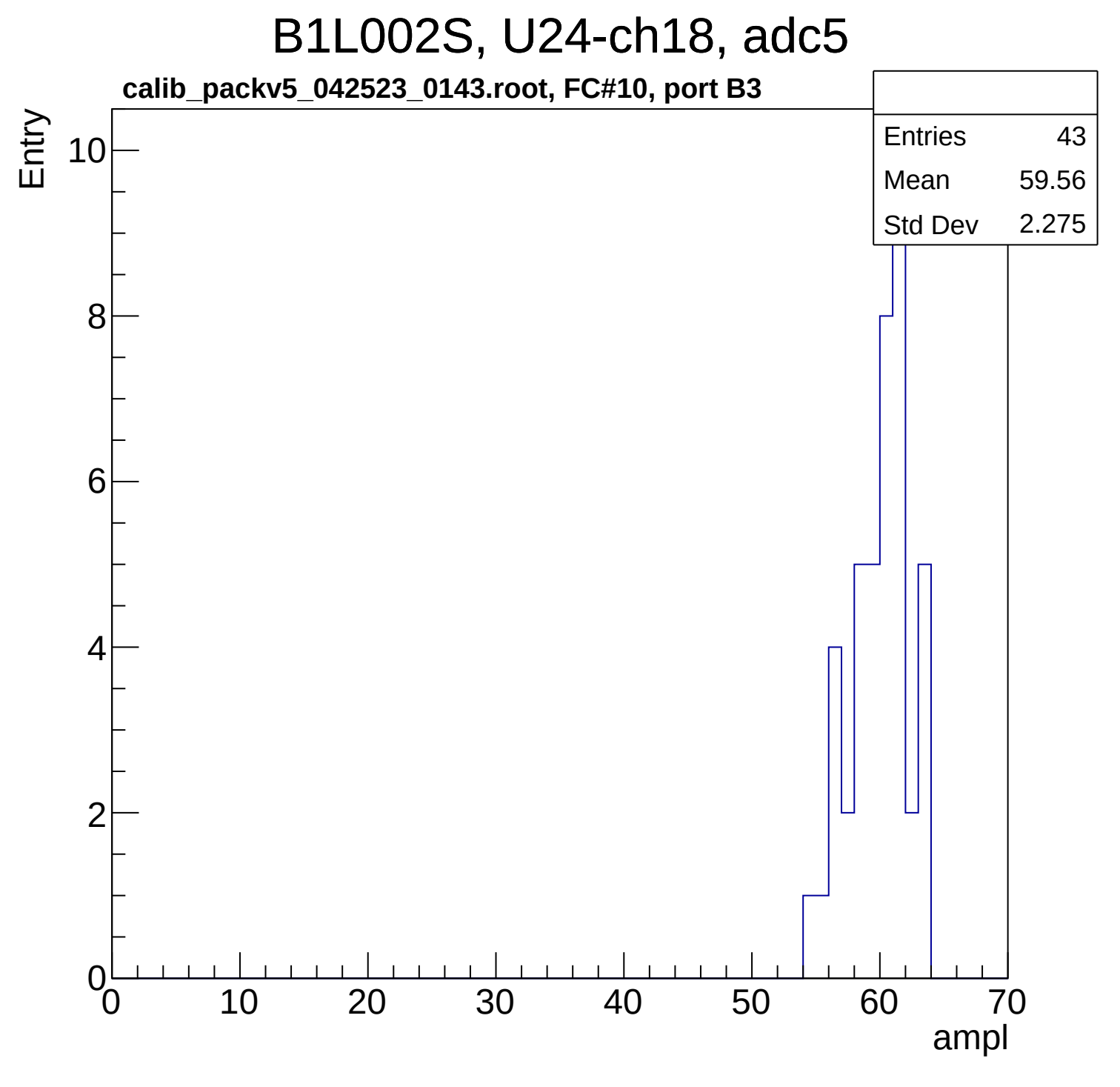
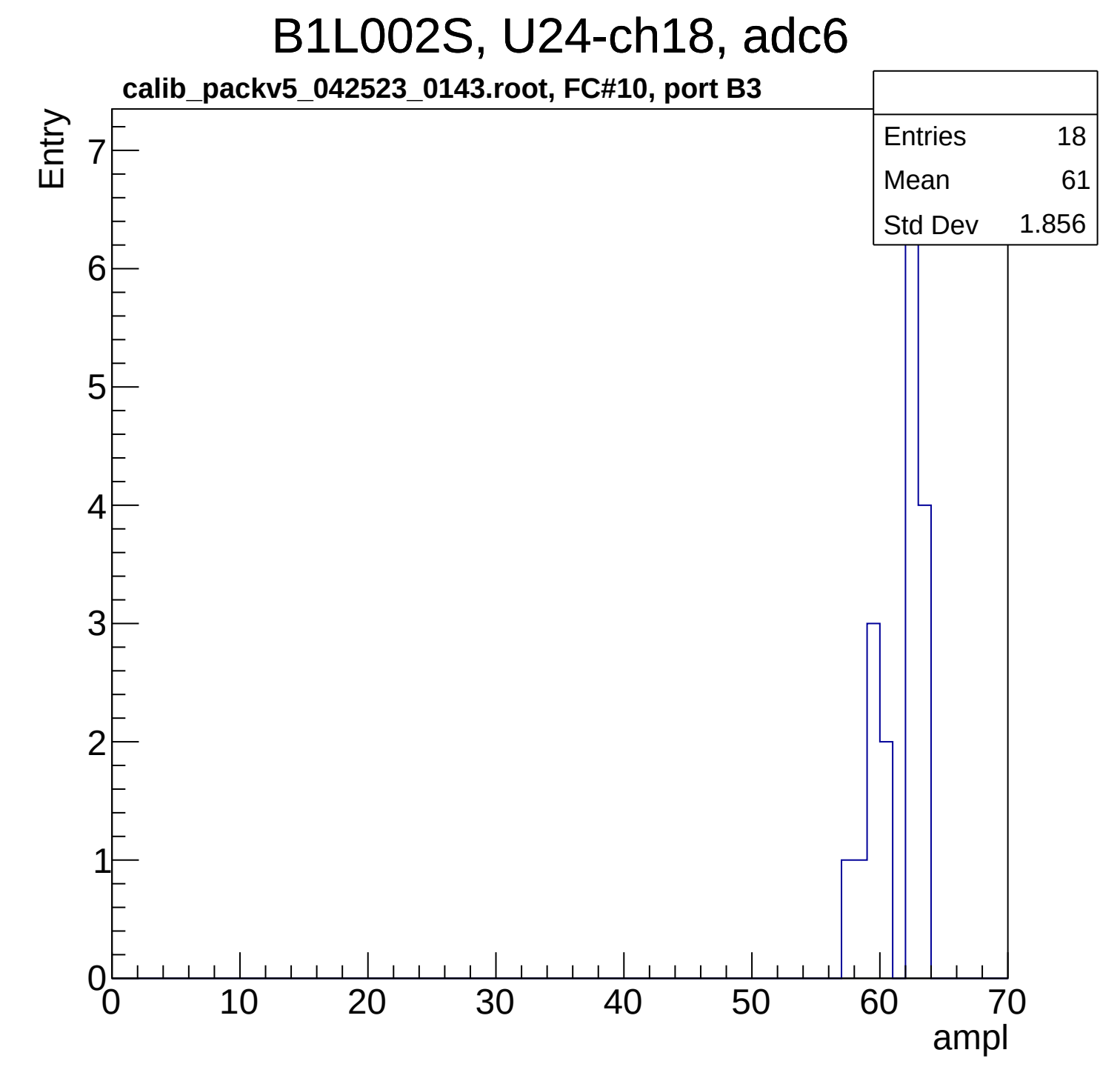

B1L002S, U24-ch18, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 59.56
Std Dev 2.275
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch18, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61
Std Dev 1.856
Entry
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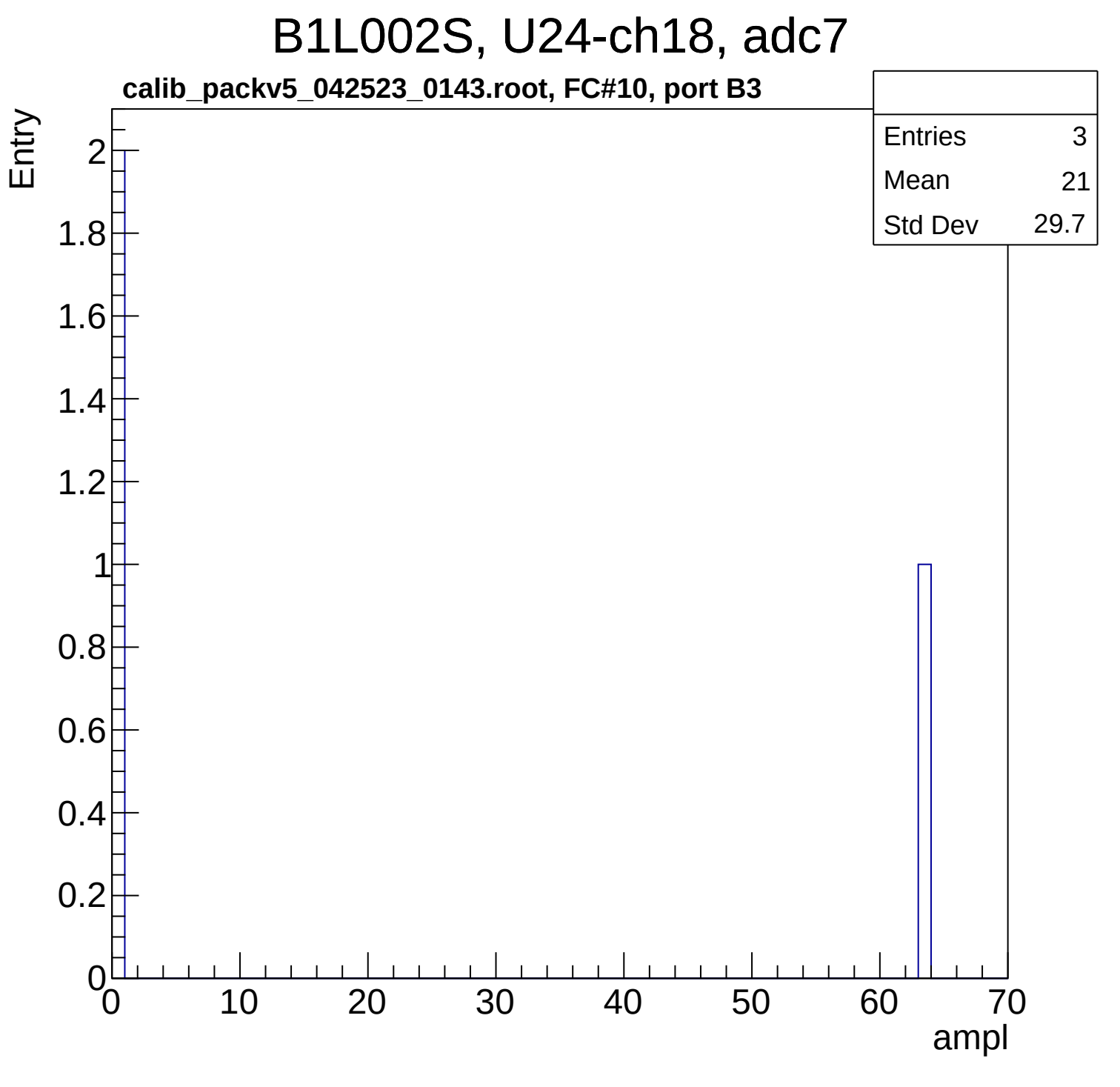
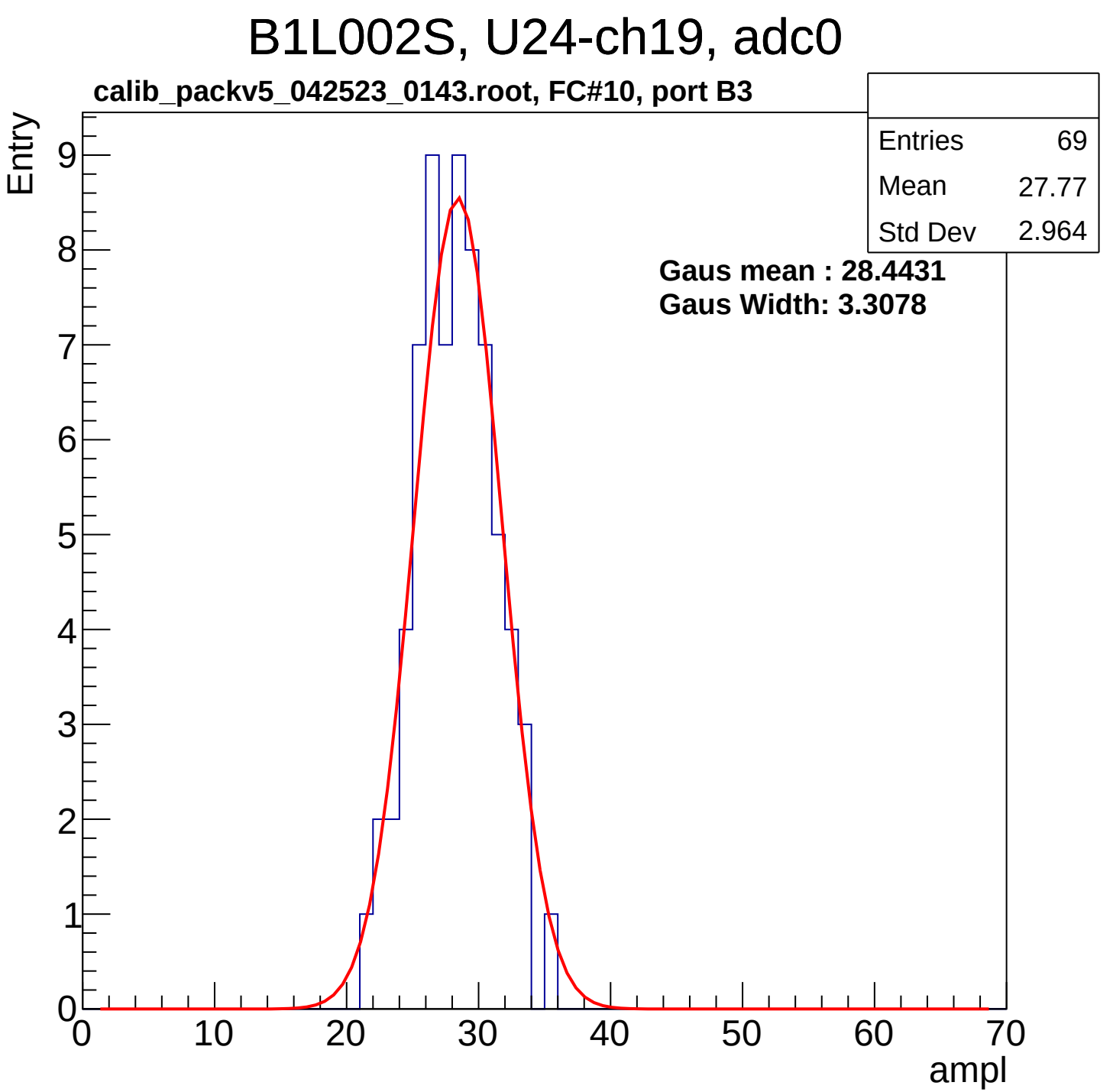
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch18, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch19, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.77
Std Dev 2.964
Gaus mean : 28.4431
Gaus Width: 3.3078
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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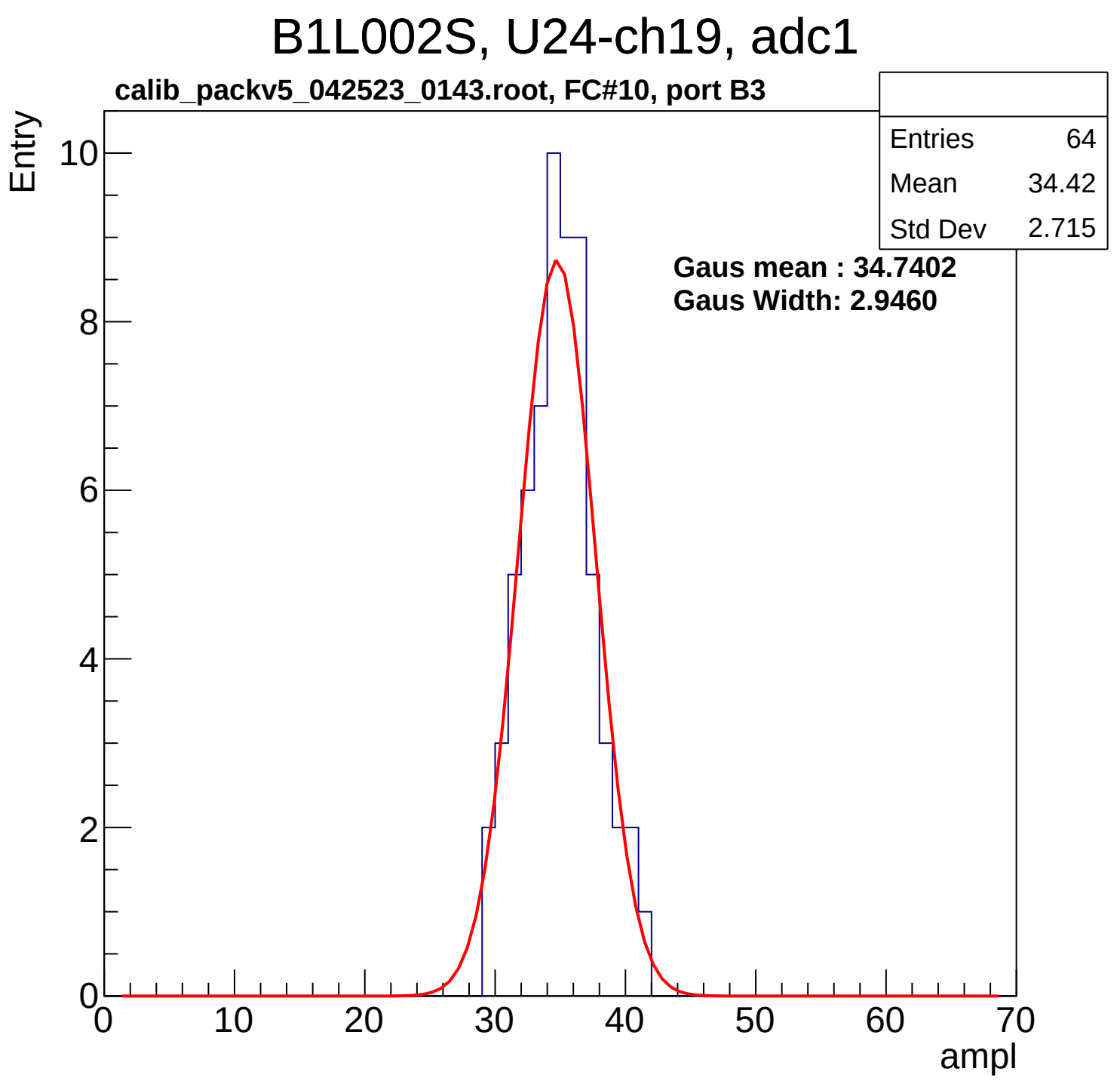
B1L002S, U24-ch19, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.42
Std Dev 2.715
Gaus mean : 34.7402
Gaus Width: 2.9460
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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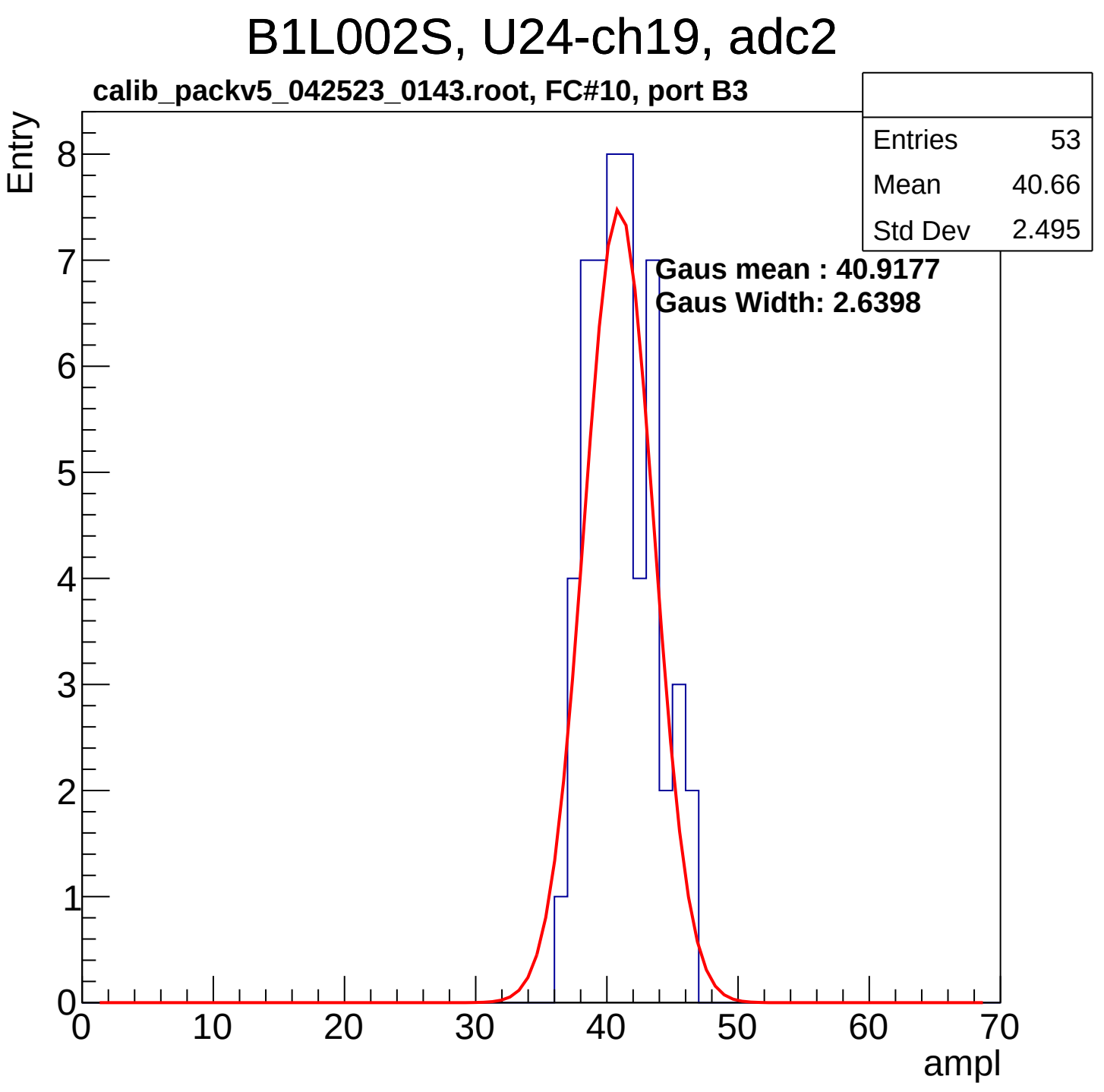
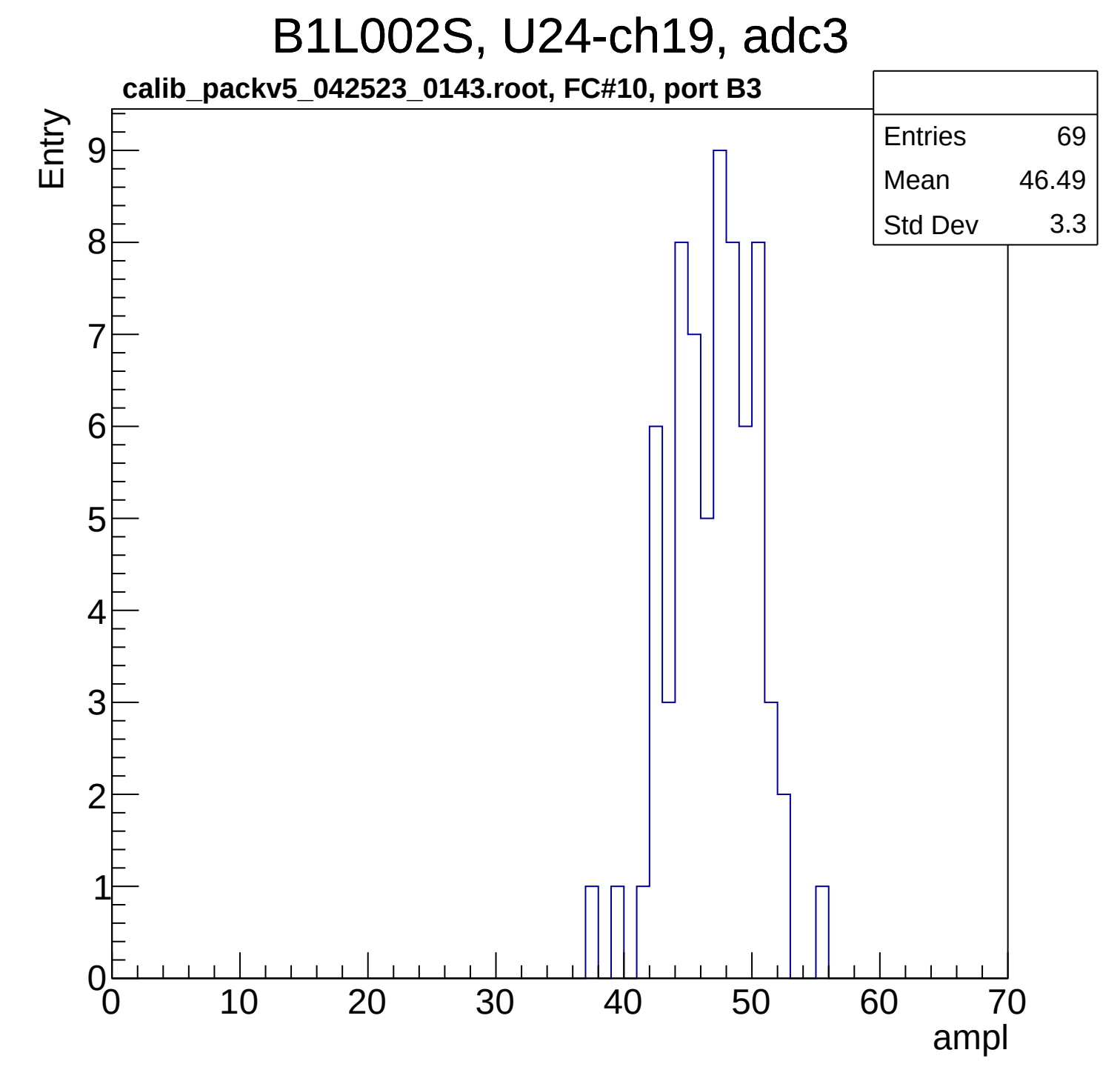

B1L002S, U24-ch19, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 40.66
Std Dev 2.495
Gaus mean : 40.9177
Gaus Width: 2.6398
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch19, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 46.49
Std Dev 3.3
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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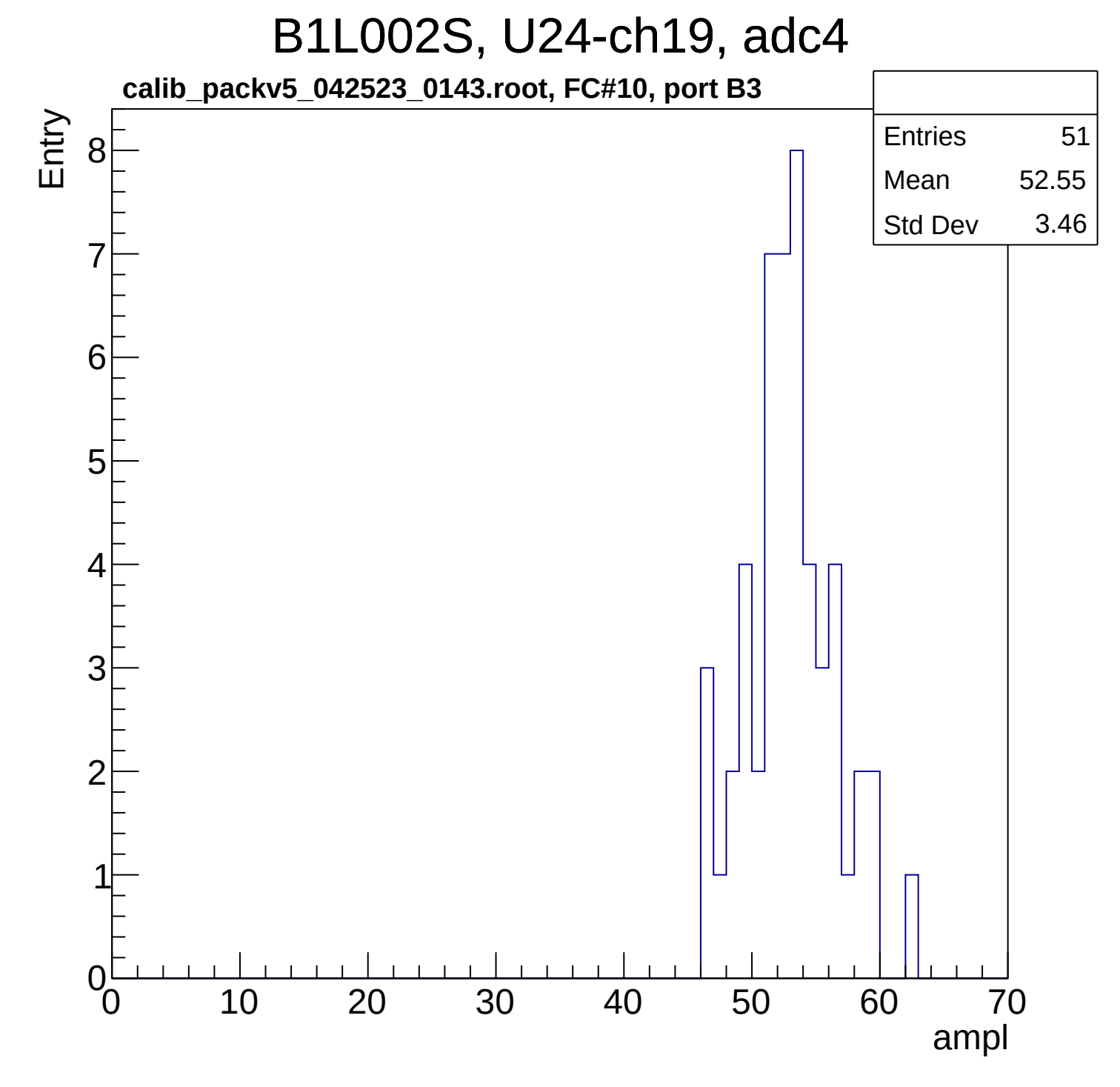
B1L002S, U24-ch19, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 52.55
Std Dev 3.46
Entry
ampl
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1
2
3
4
5
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7
8
0
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40
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60
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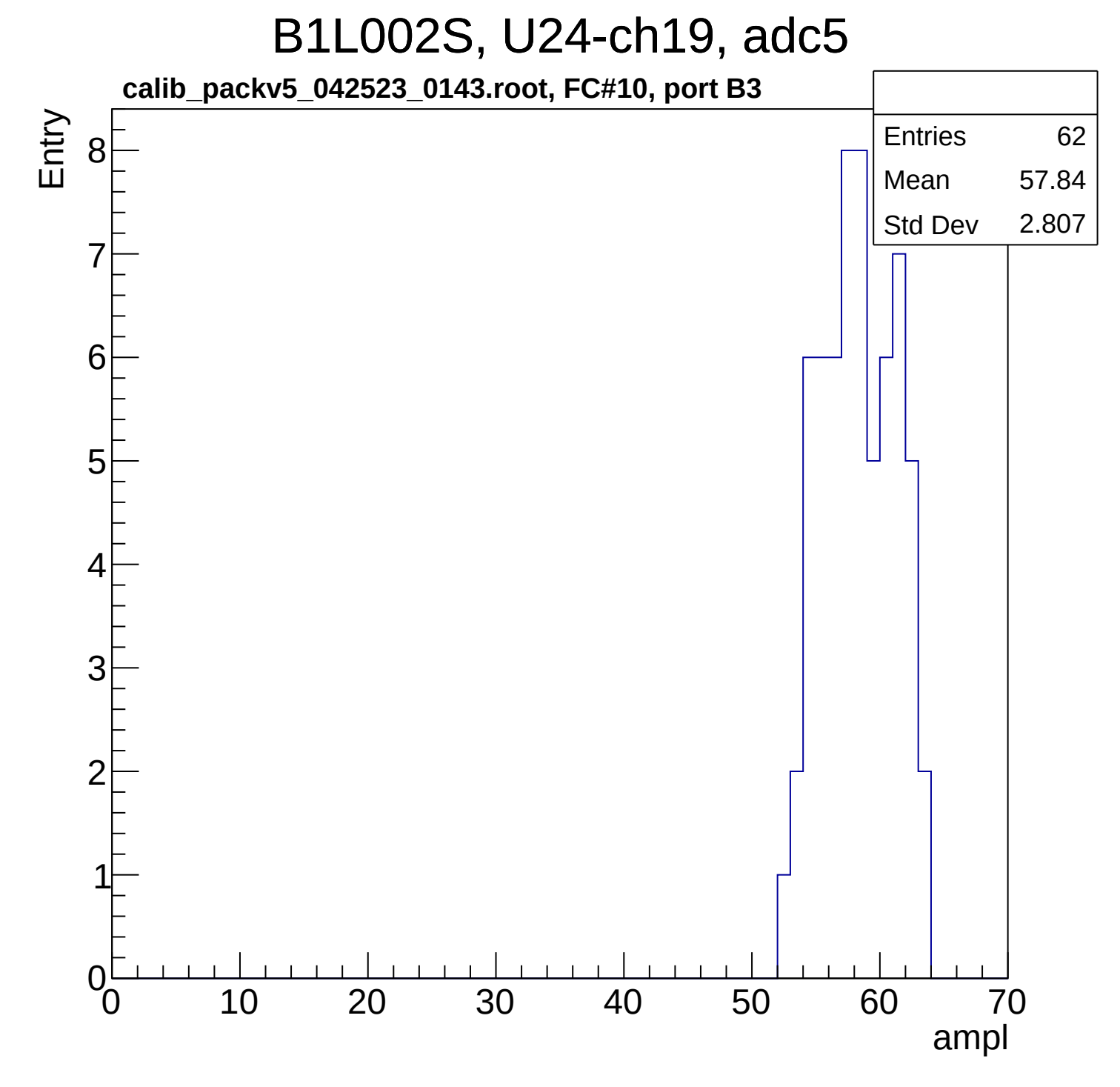
B1L002S, U24-ch19, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 57.84
Std Dev 2.807
Entry
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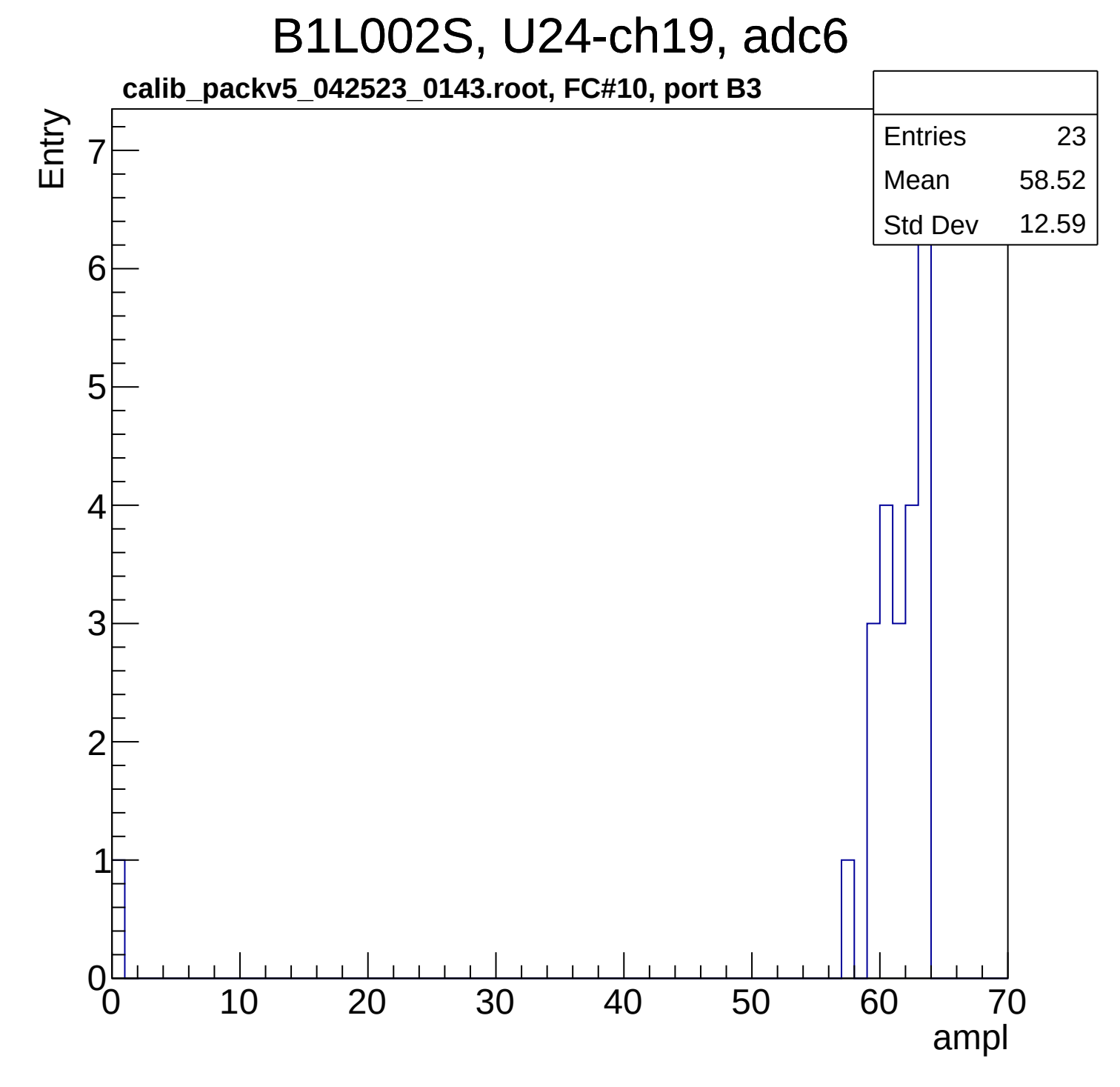
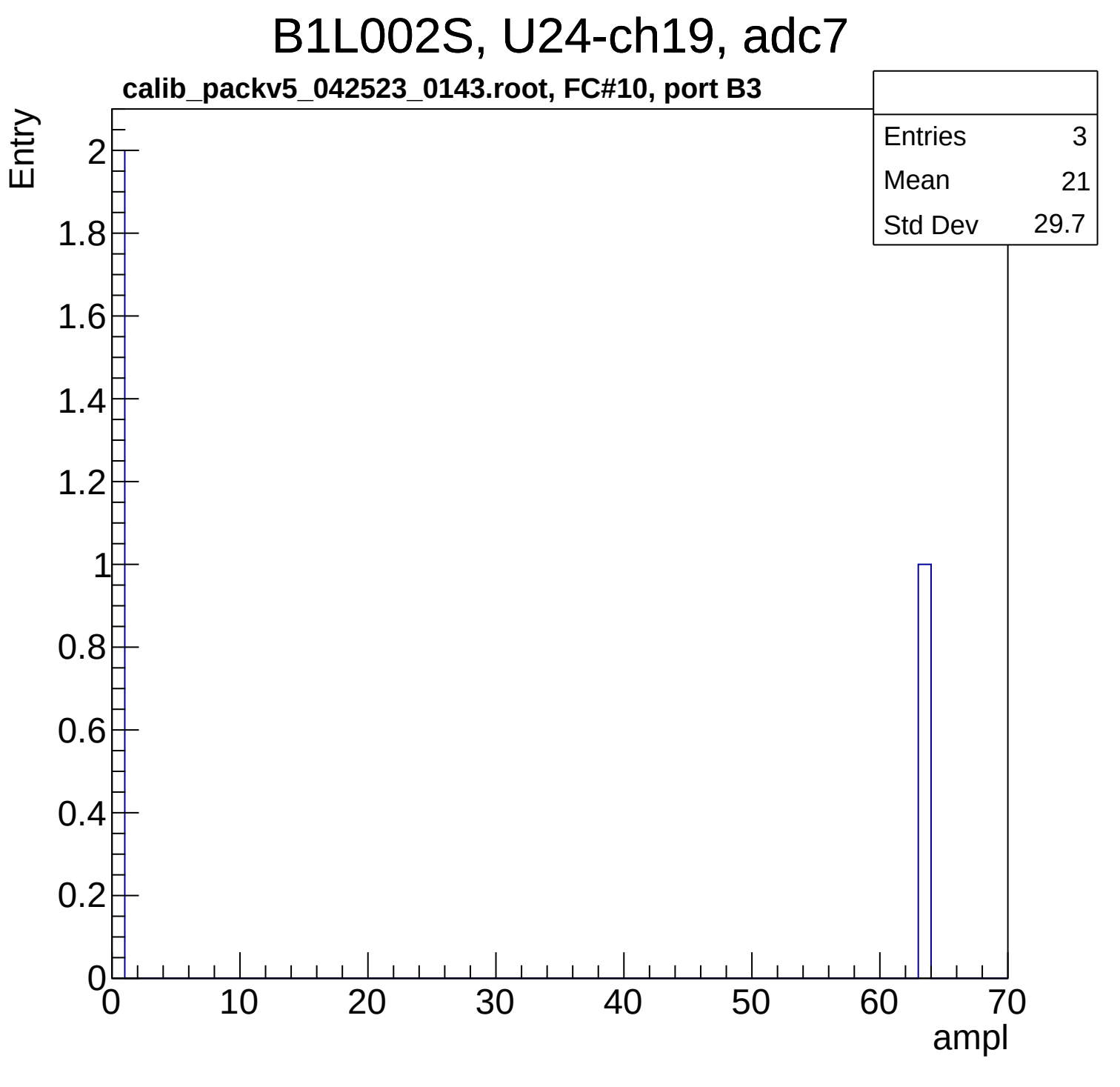
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch19, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 58.52
Std Dev 12.59
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch19, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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40
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60
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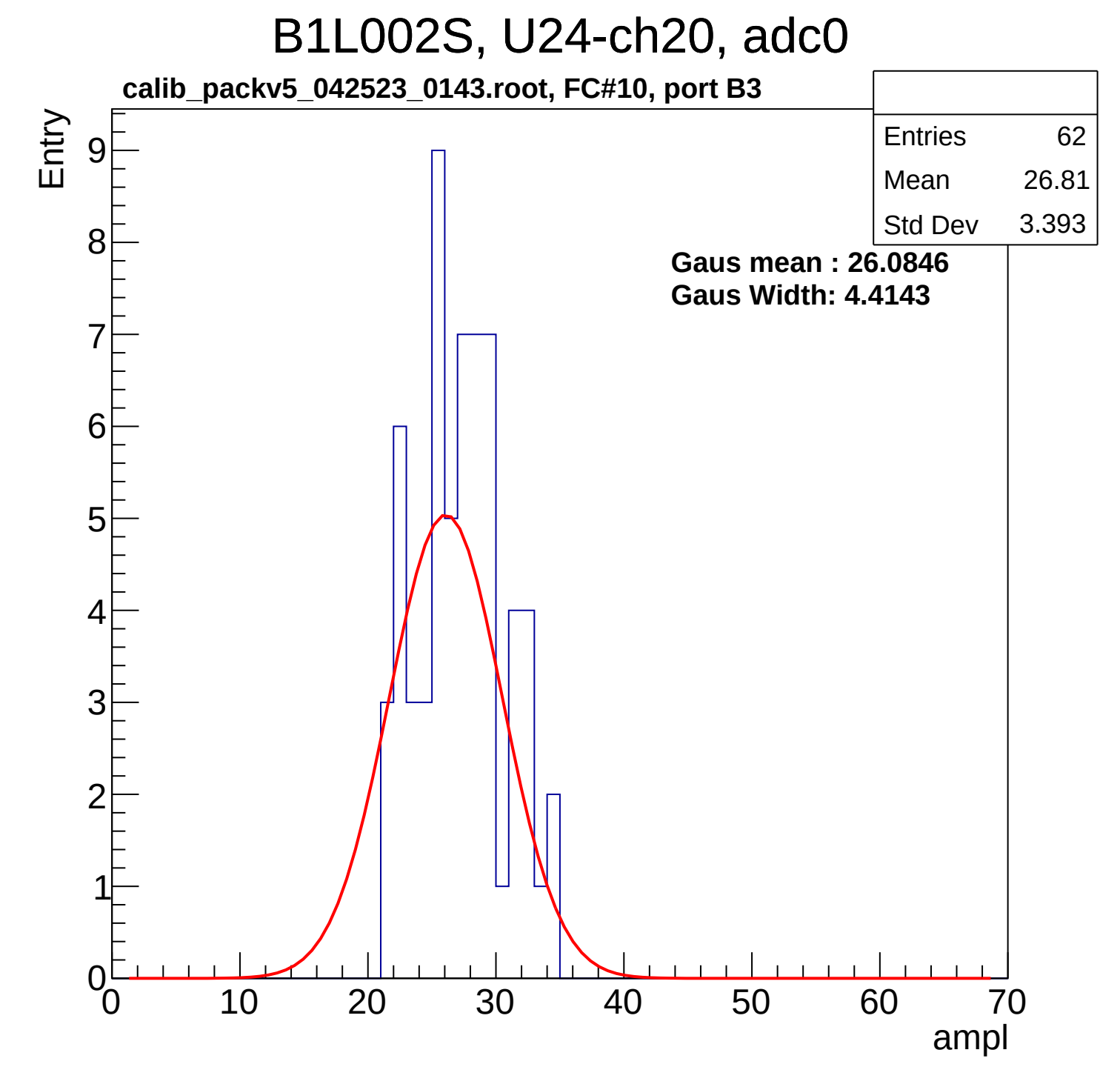
B1L002S, U24-ch20, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.81
Std Dev 3.393
Gaus mean : 26.0846
Gaus Width: 4.4143
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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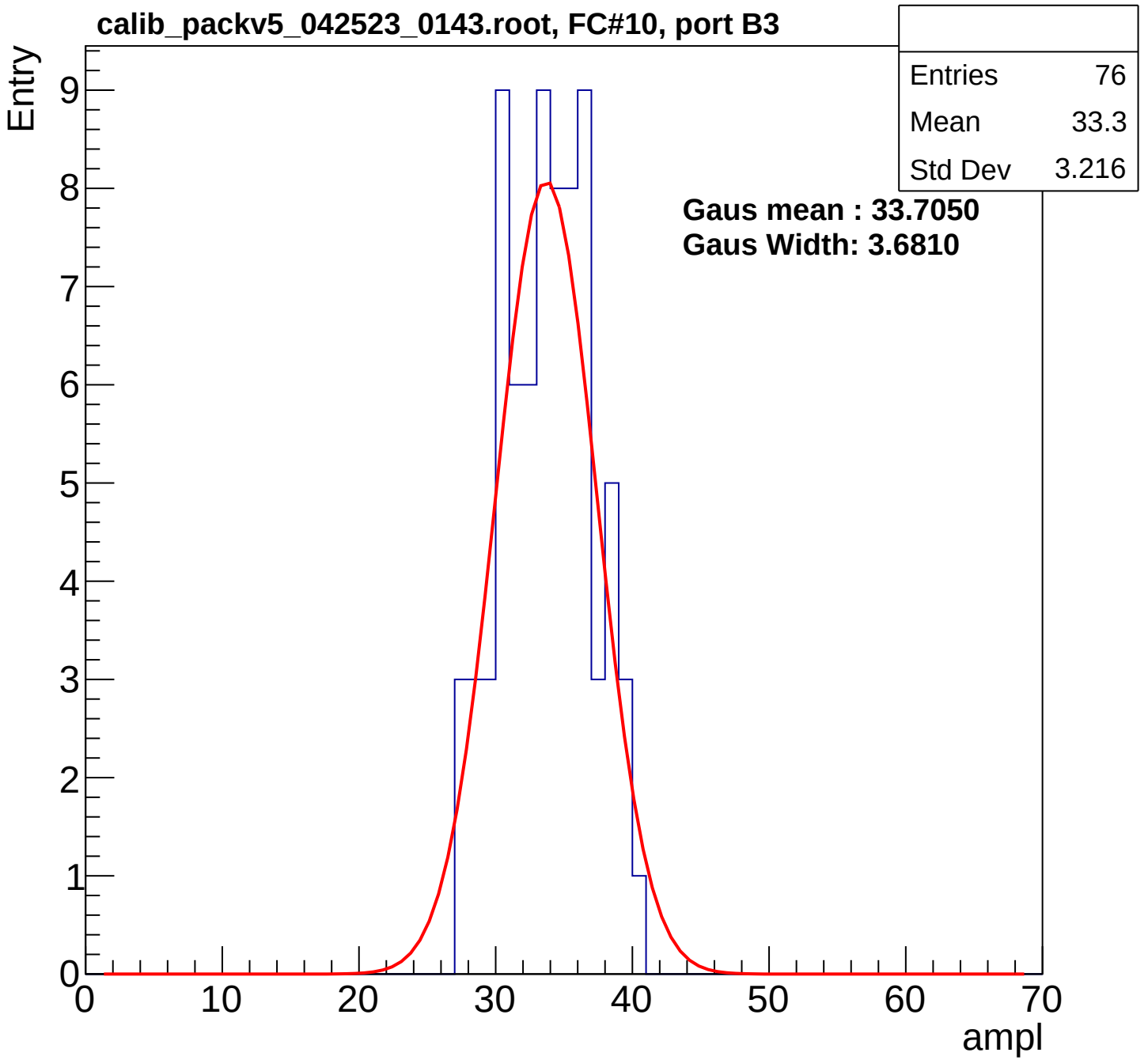
B1L002S, U24-ch20, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.3
Std Dev 3.216
Gaus mean : 33.7050
Gaus Width: 3.6810
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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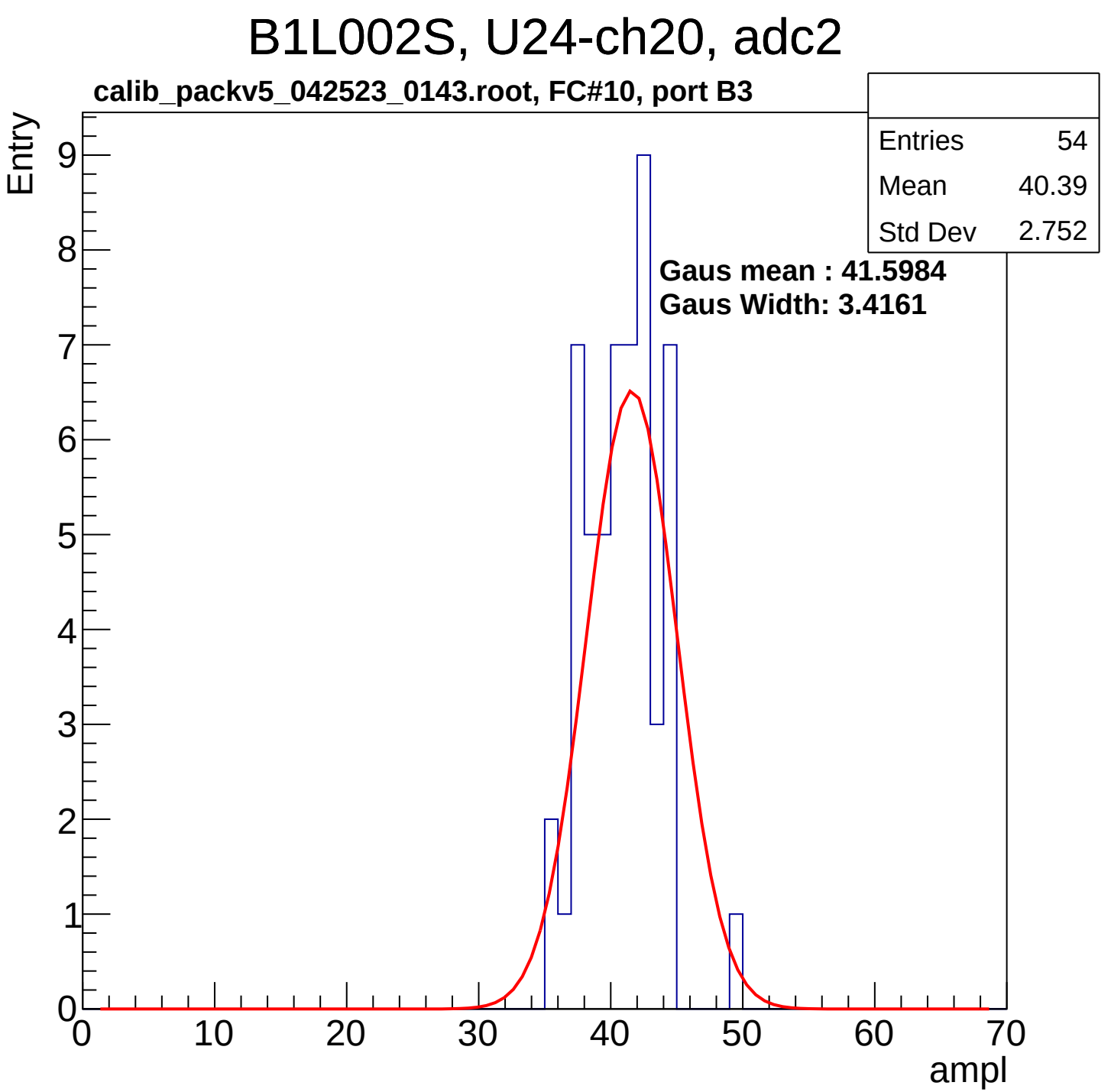
B1L002S, U24-ch20, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.39
Std Dev 2.752
Gaus mean : 41.5984
Gaus Width: 3.4161
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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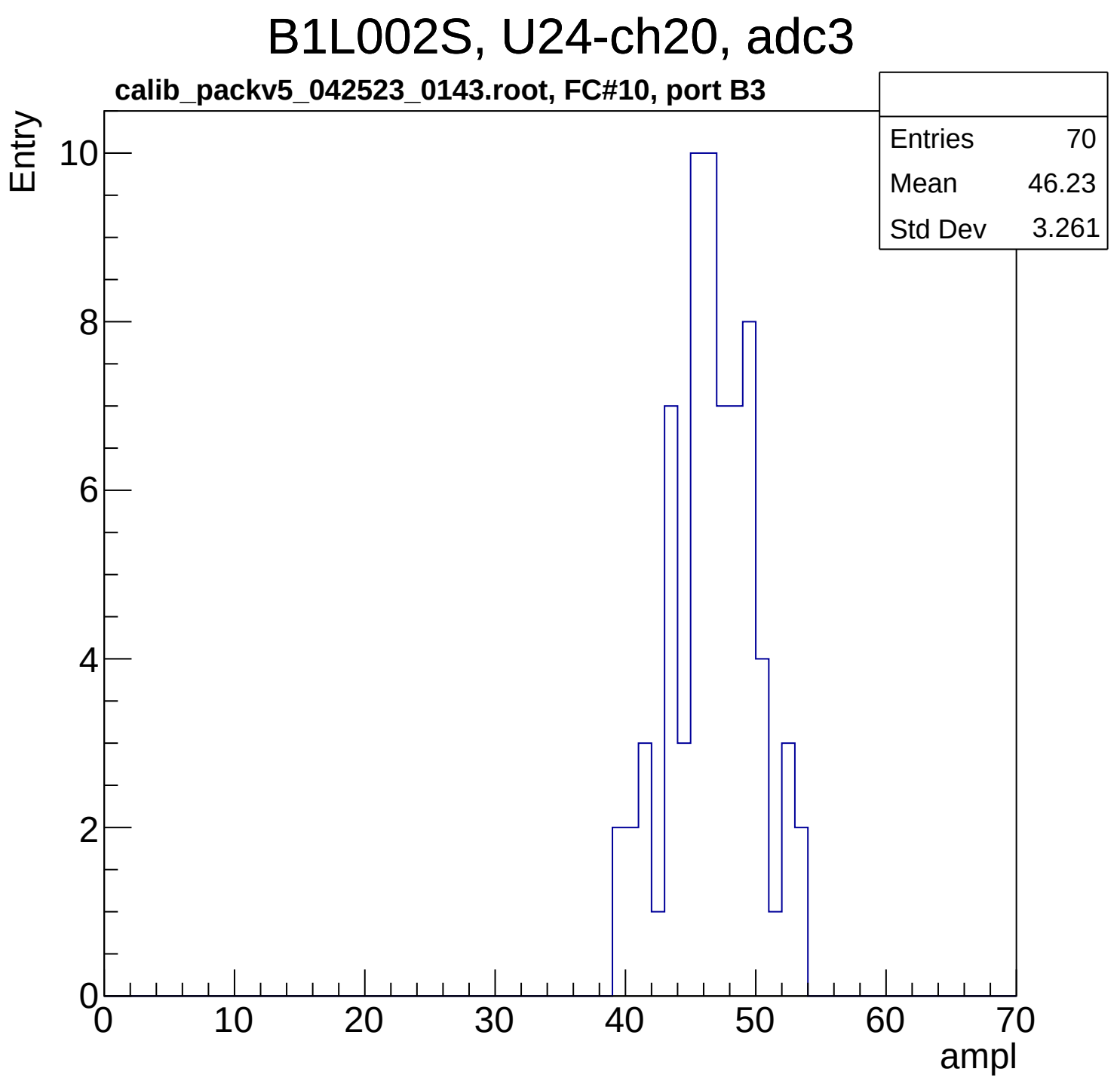
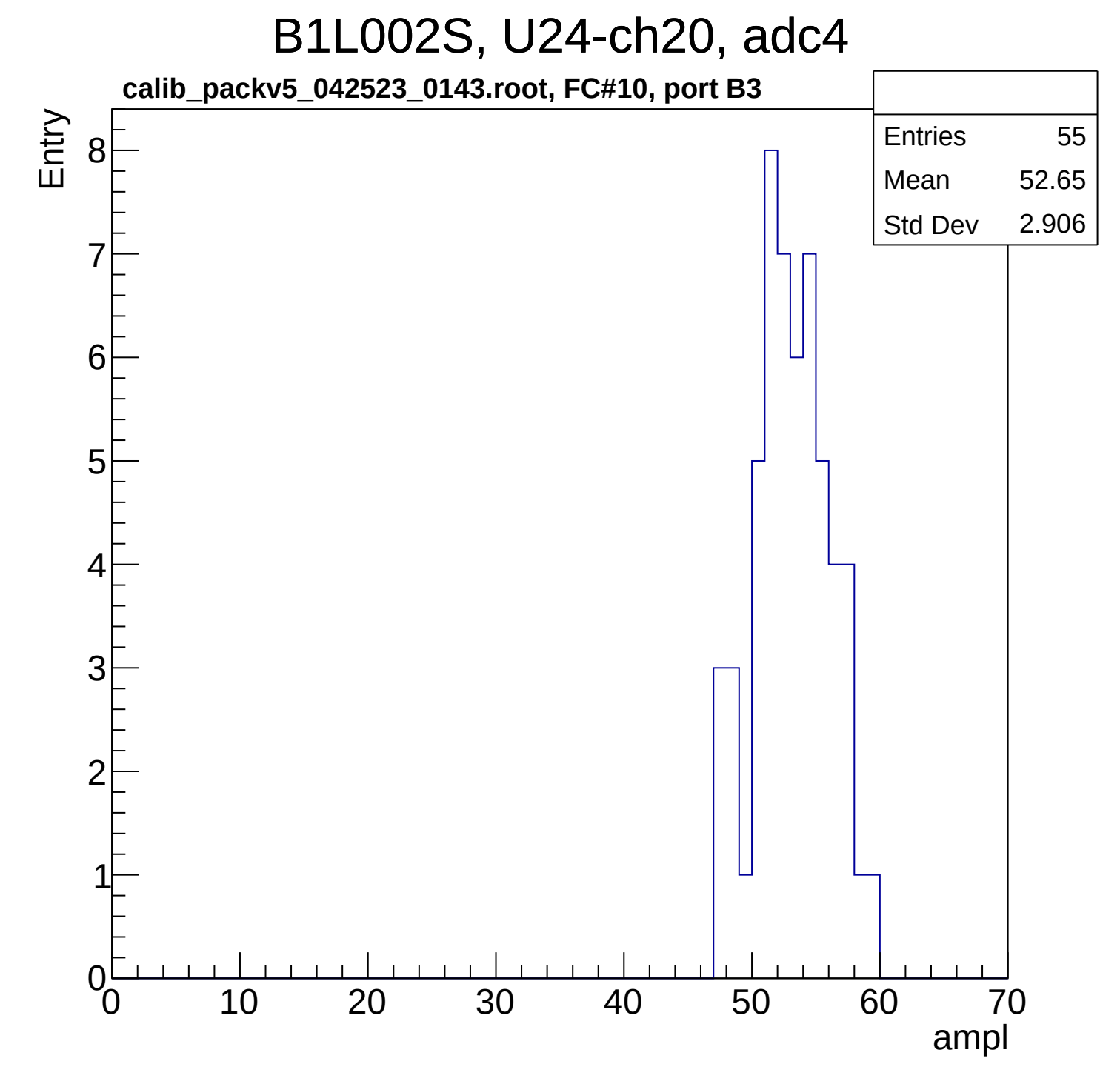

B1L002S, U24-ch20, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.23
Std Dev 3.261
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch20, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.65
Std Dev 2.906
Entry
ampl
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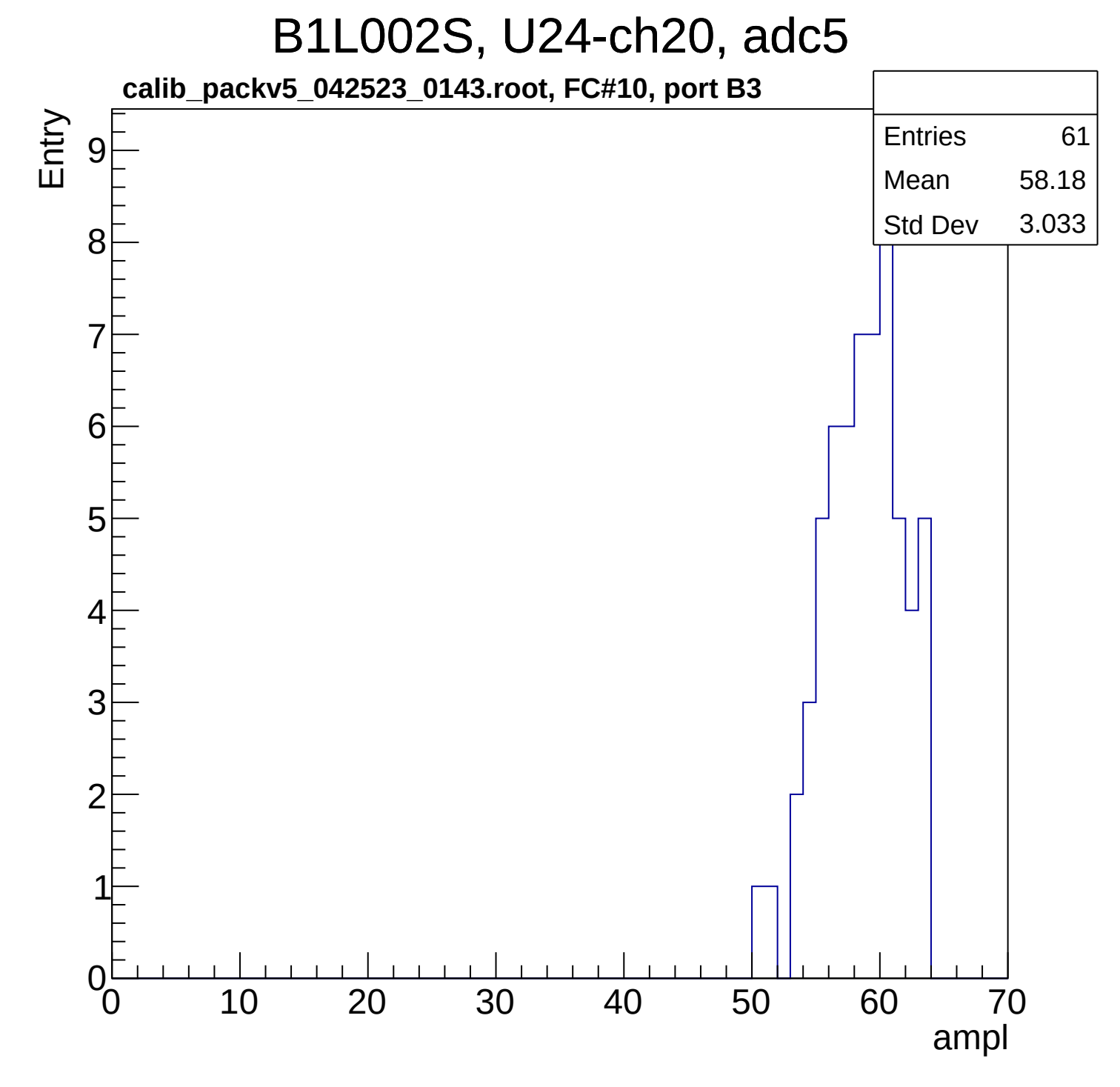
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch20, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.18
Std Dev 3.033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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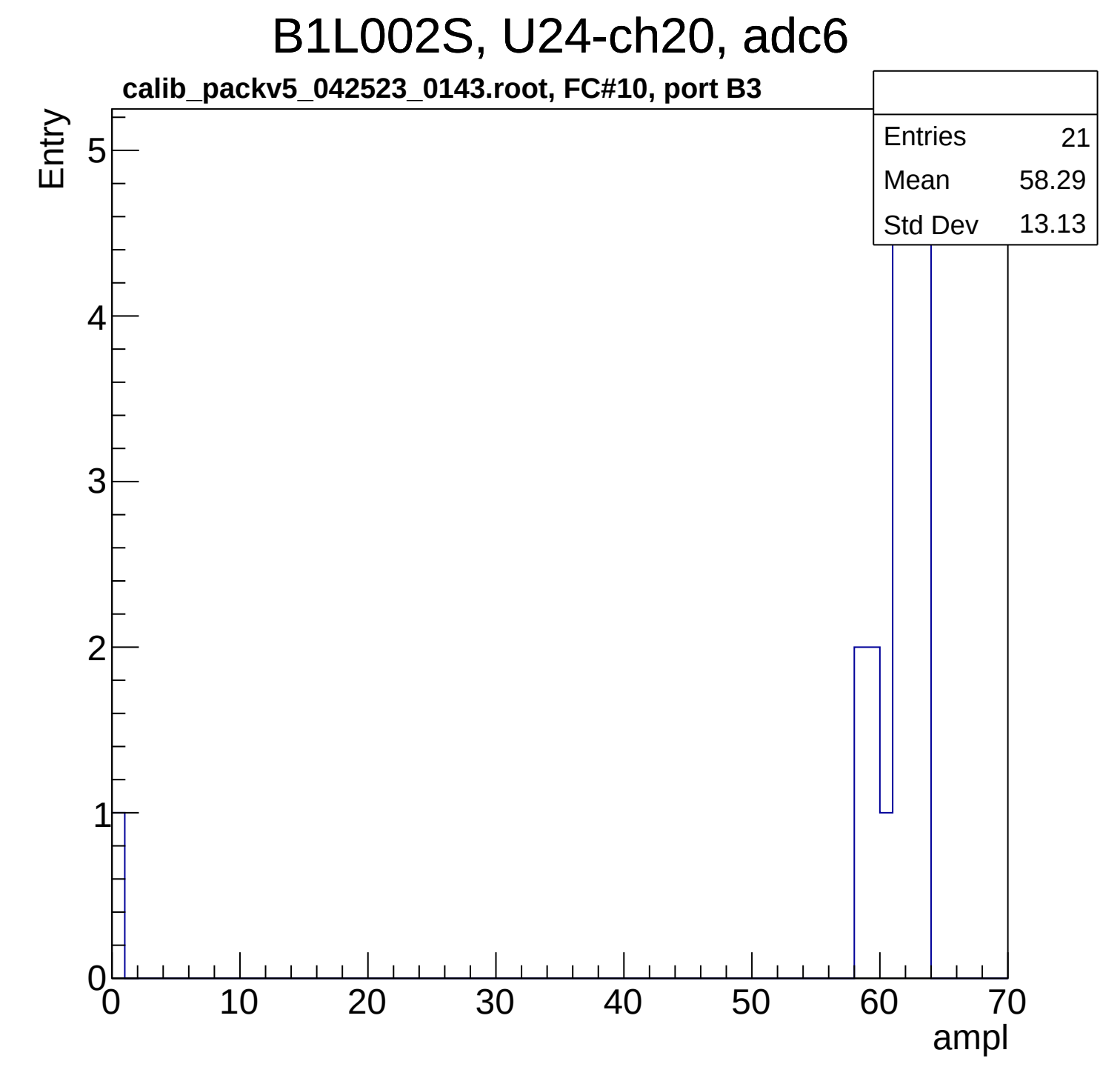
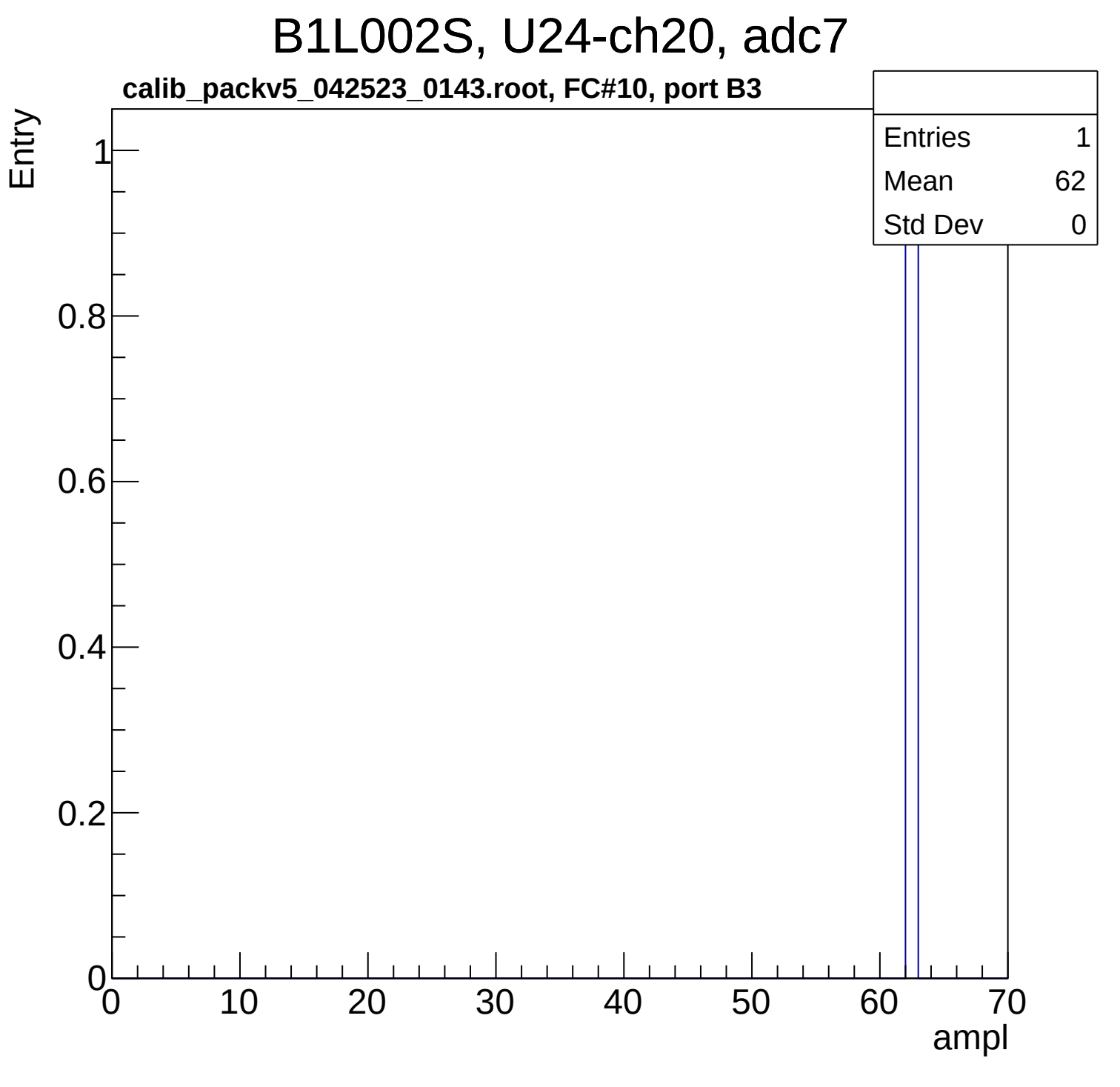
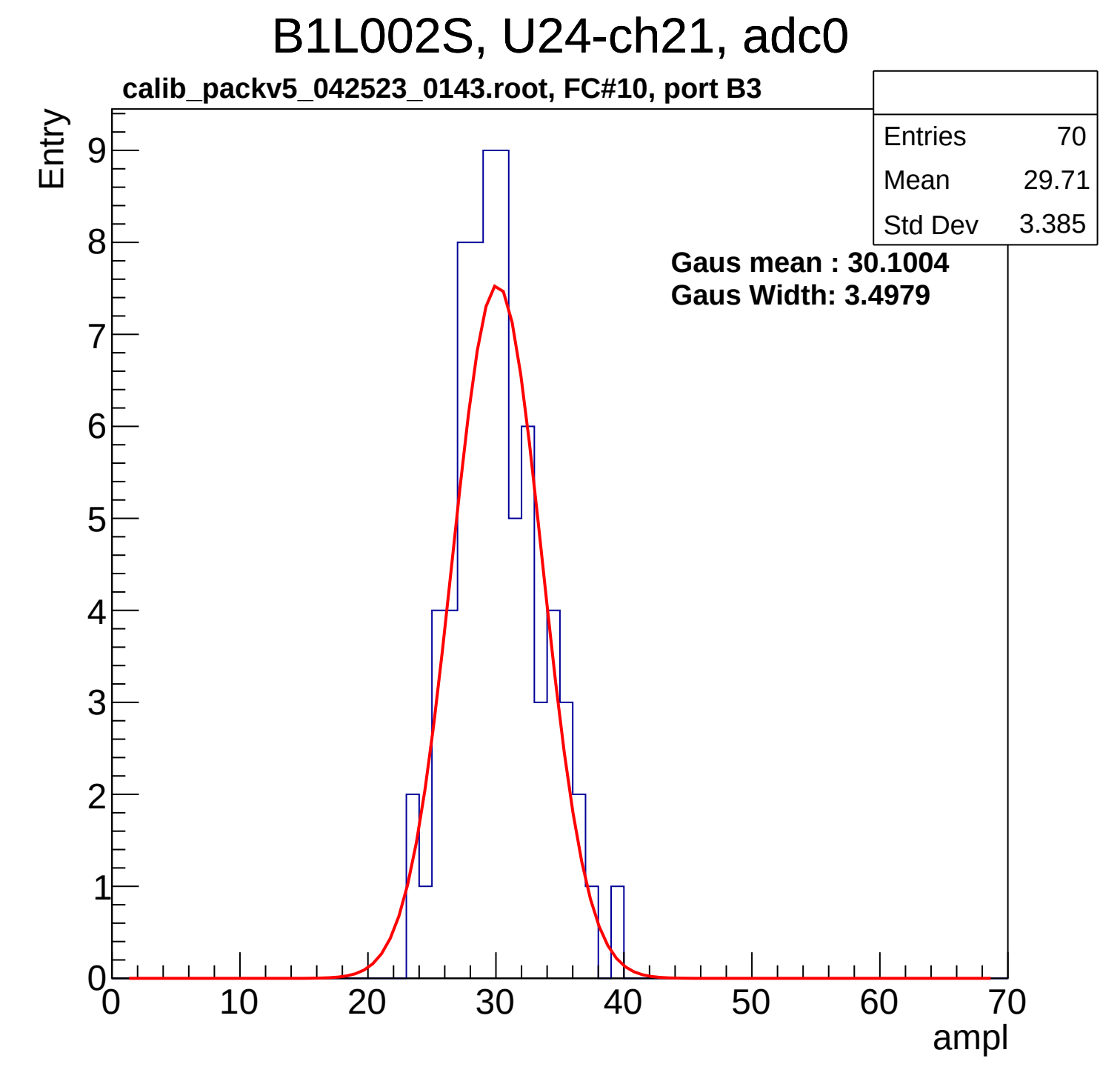
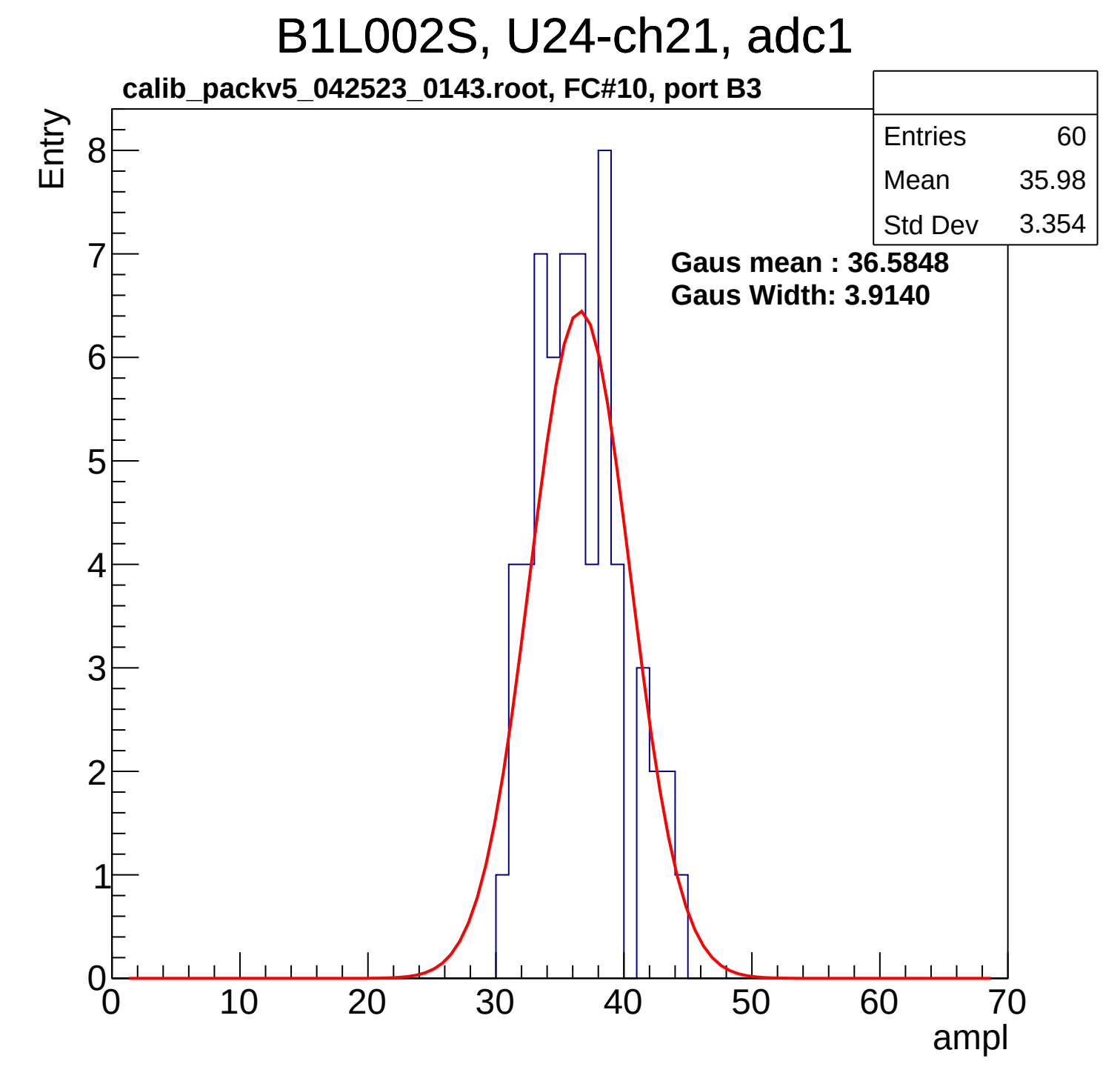
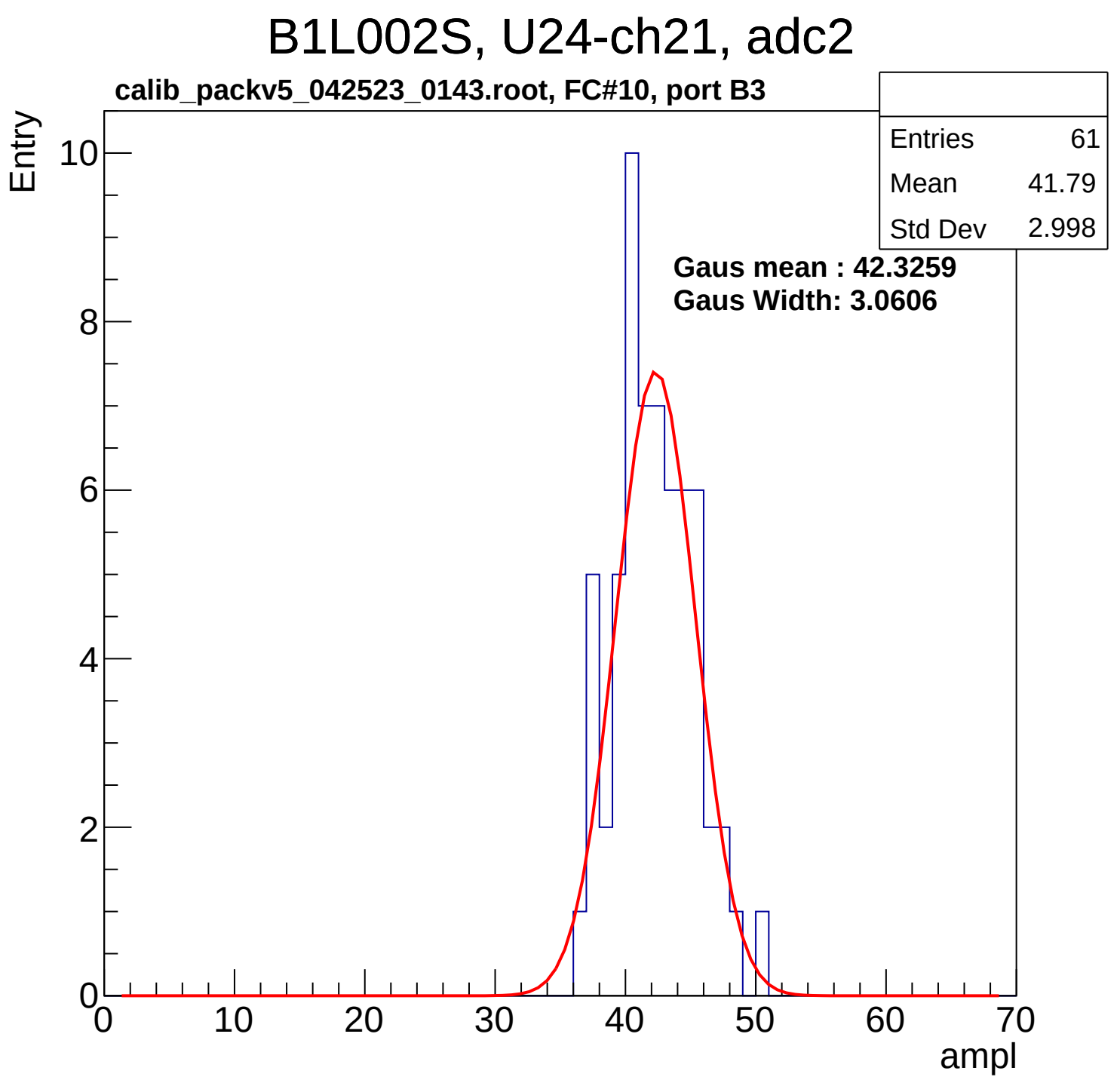
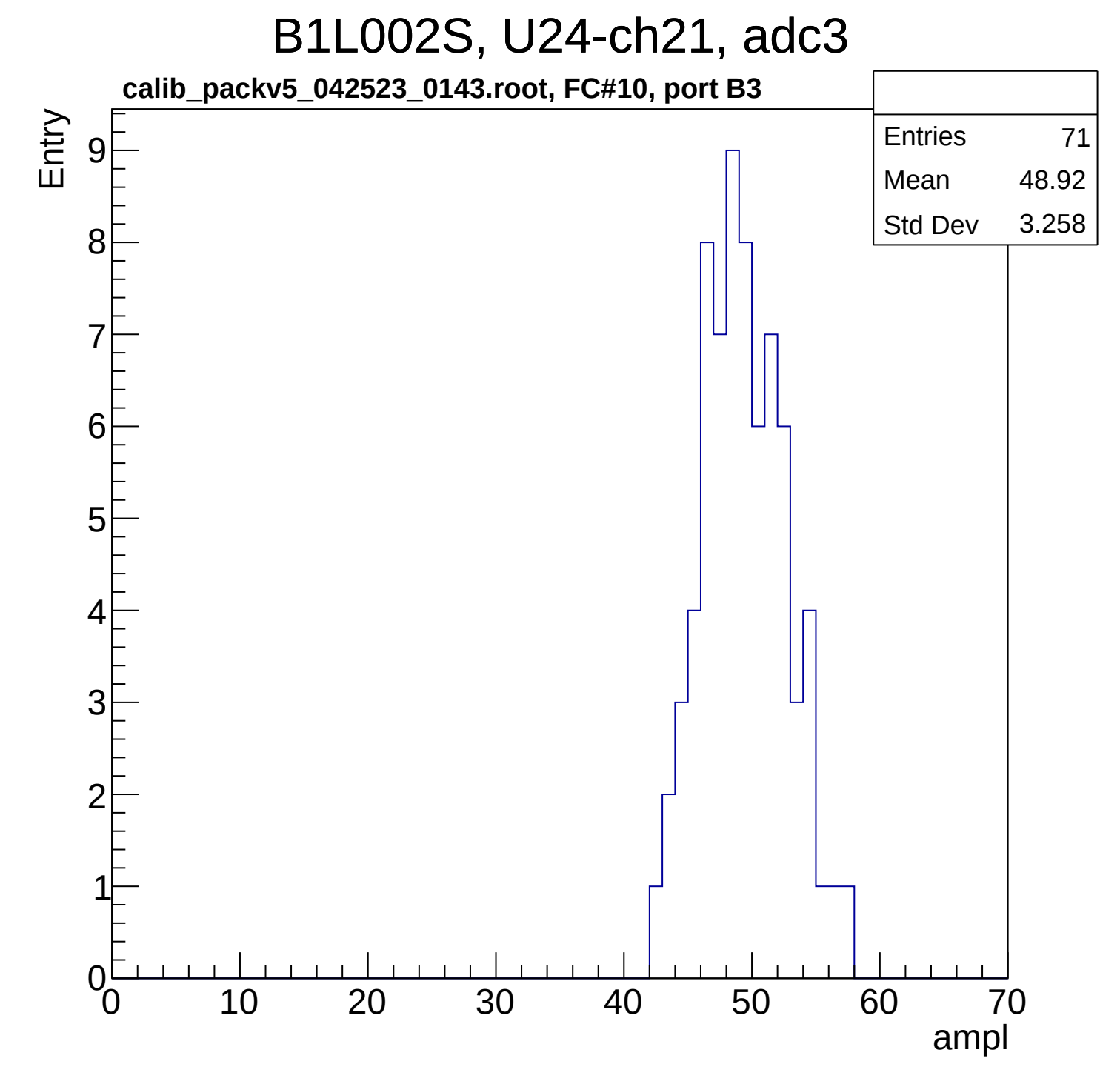

B1L002S, U24-ch20, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.29
Std Dev 13.13
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

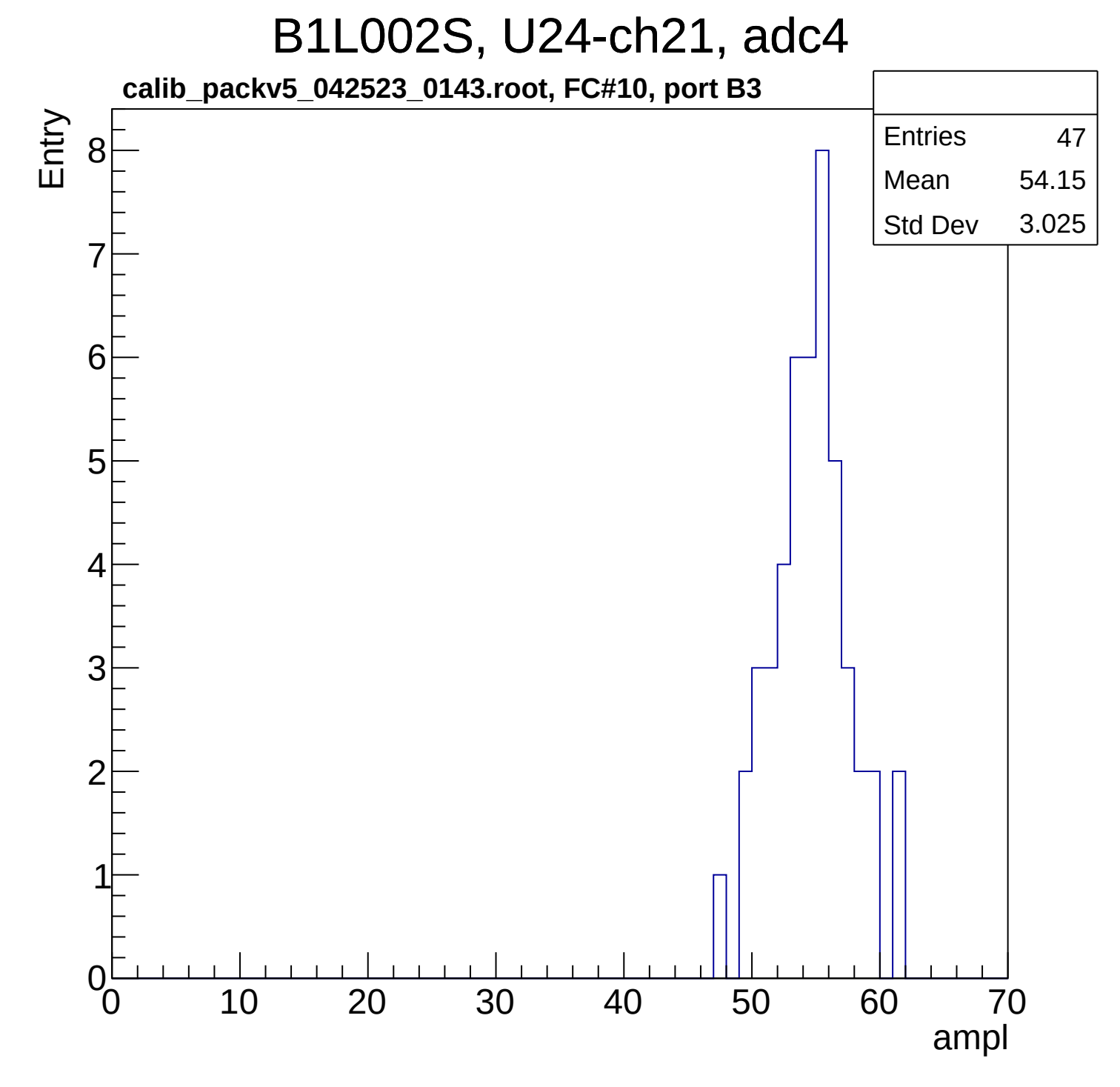

B1L002S, U24-ch20, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch21, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.71
Std Dev 3.385
Gaus mean : 30.1004
Gaus Width: 3.4979
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch21, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.98
Std Dev 3.354
Gaus mean : 36.5848
Gaus Width: 3.9140
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch21, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.79
Std Dev 2.998
Gaus mean : 42.3259
Gaus Width: 3.0606
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch21, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 48.92
Std Dev 3.258
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch21, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 54.15
Std Dev 3.025
Entry
ampl
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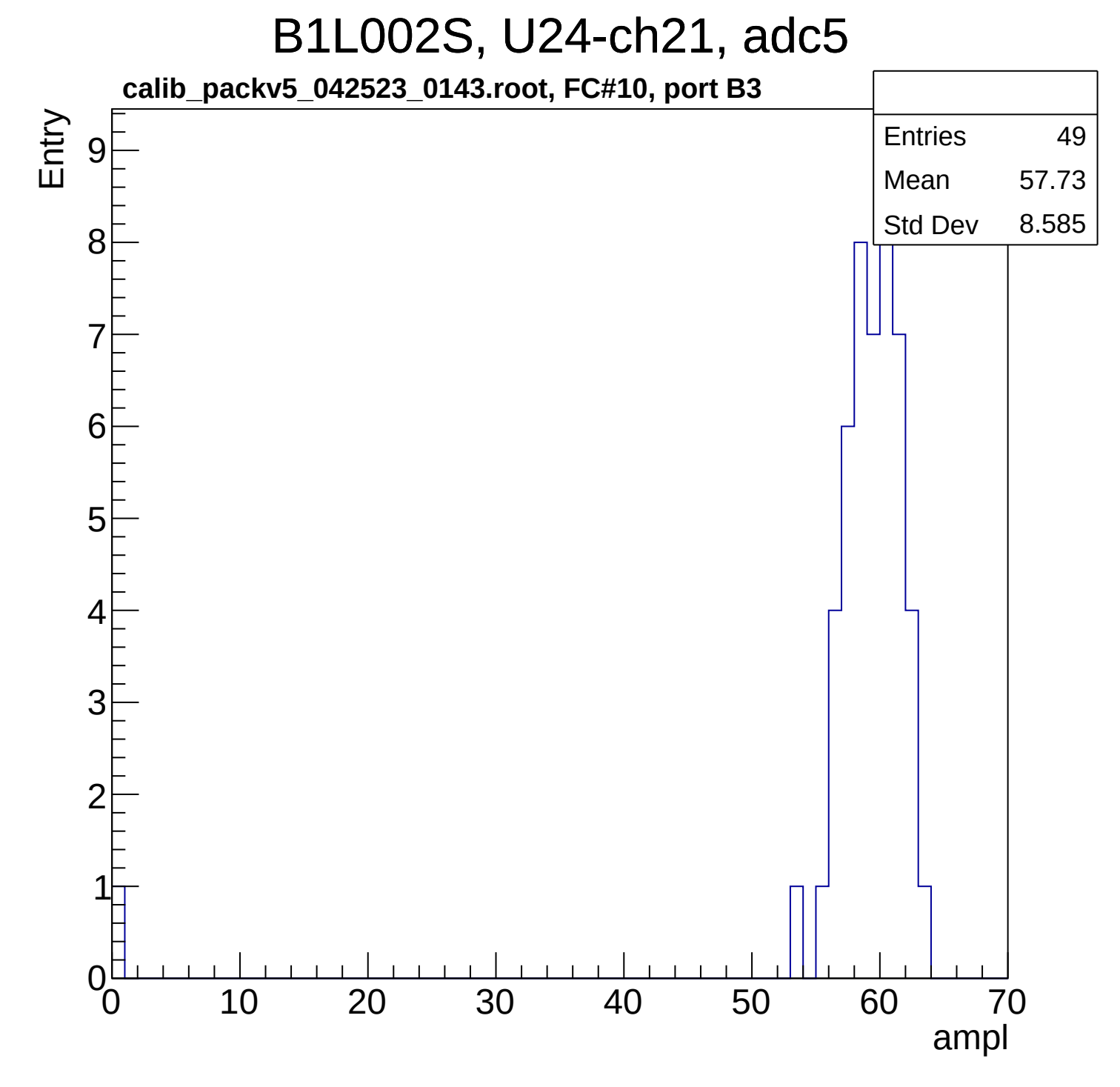
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch21, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.73
Std Dev 8.585
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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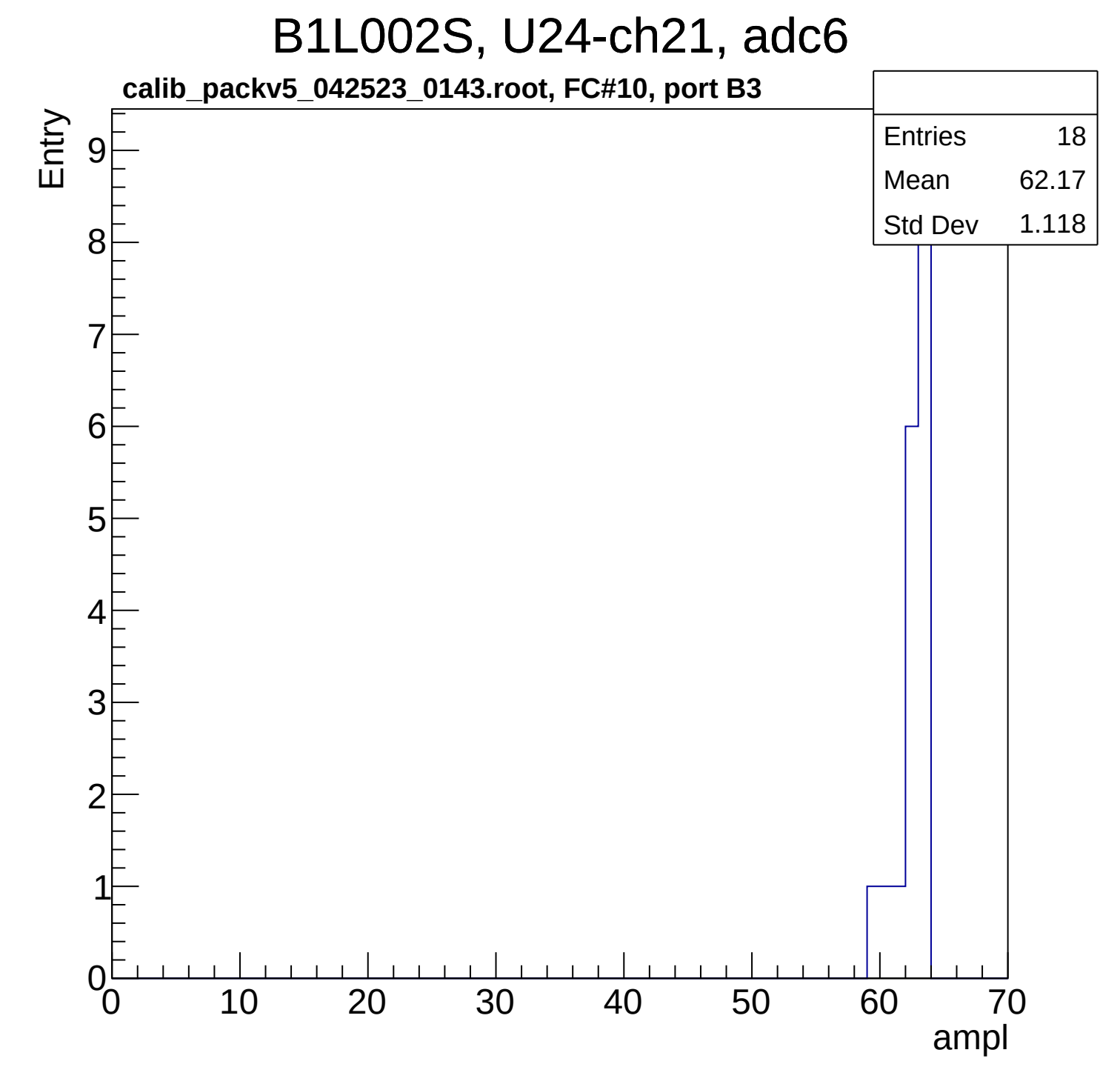
B1L002S, U24-ch21, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 62.17
Std Dev 1.118
Entry
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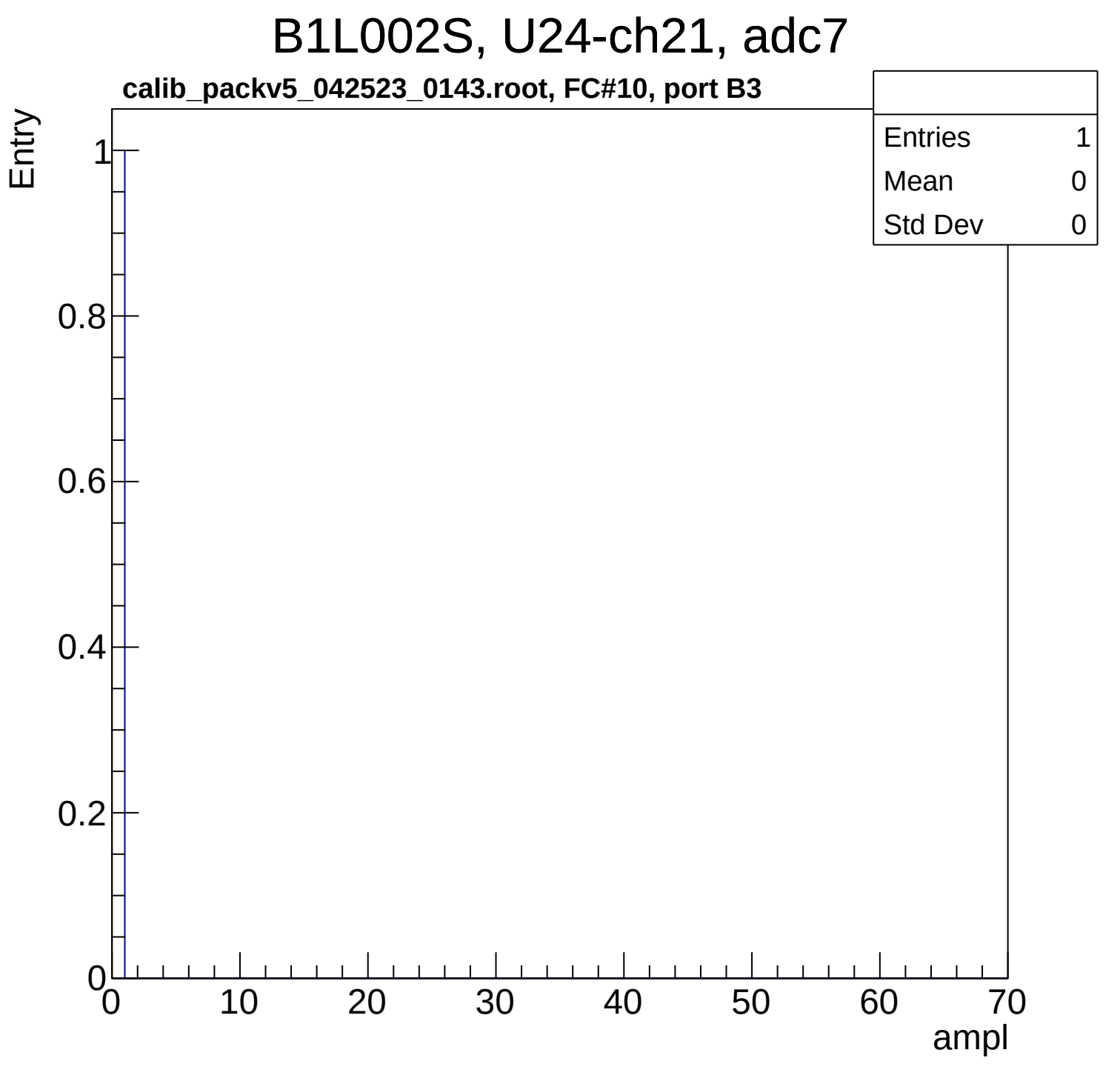
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch21, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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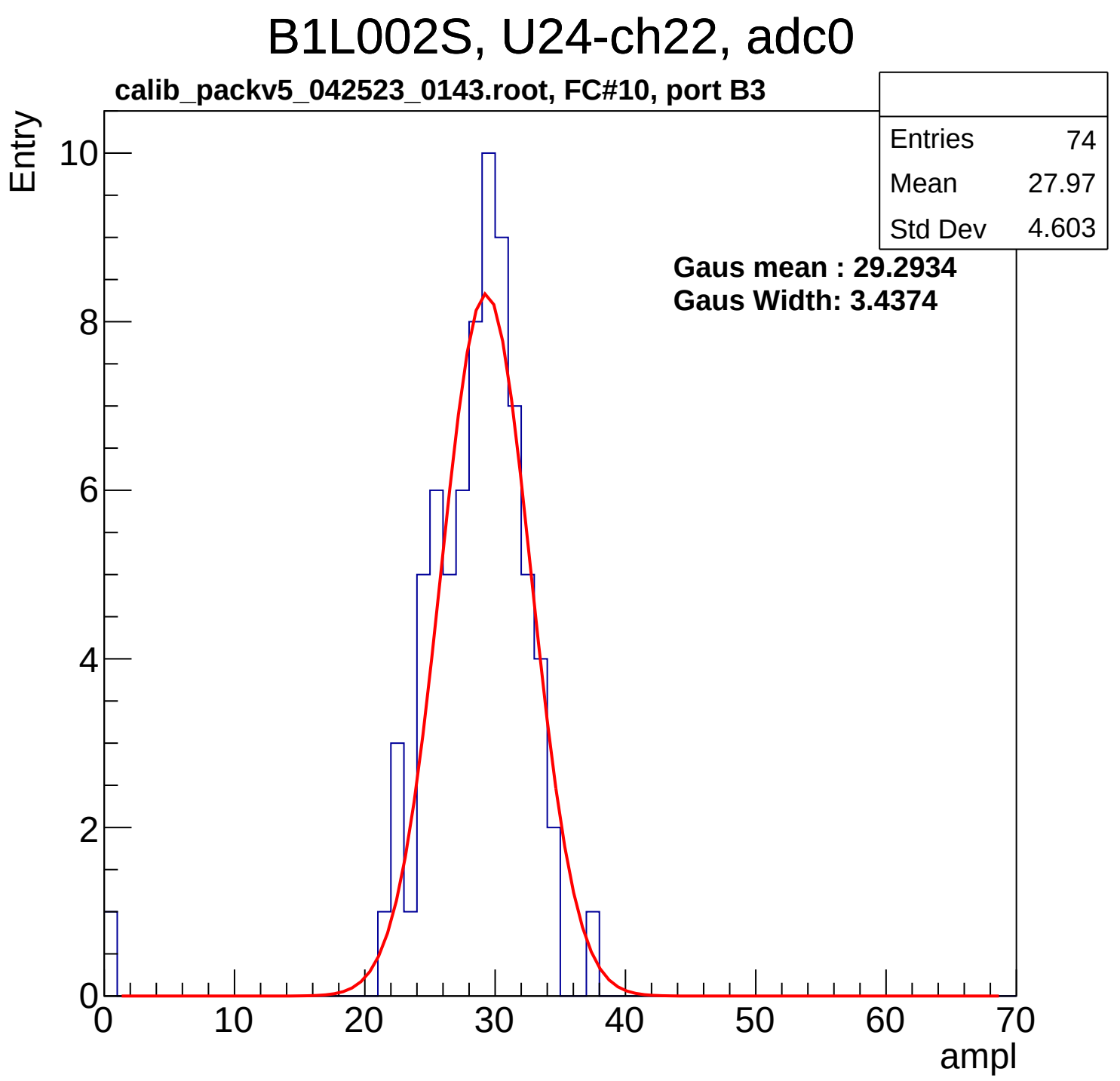
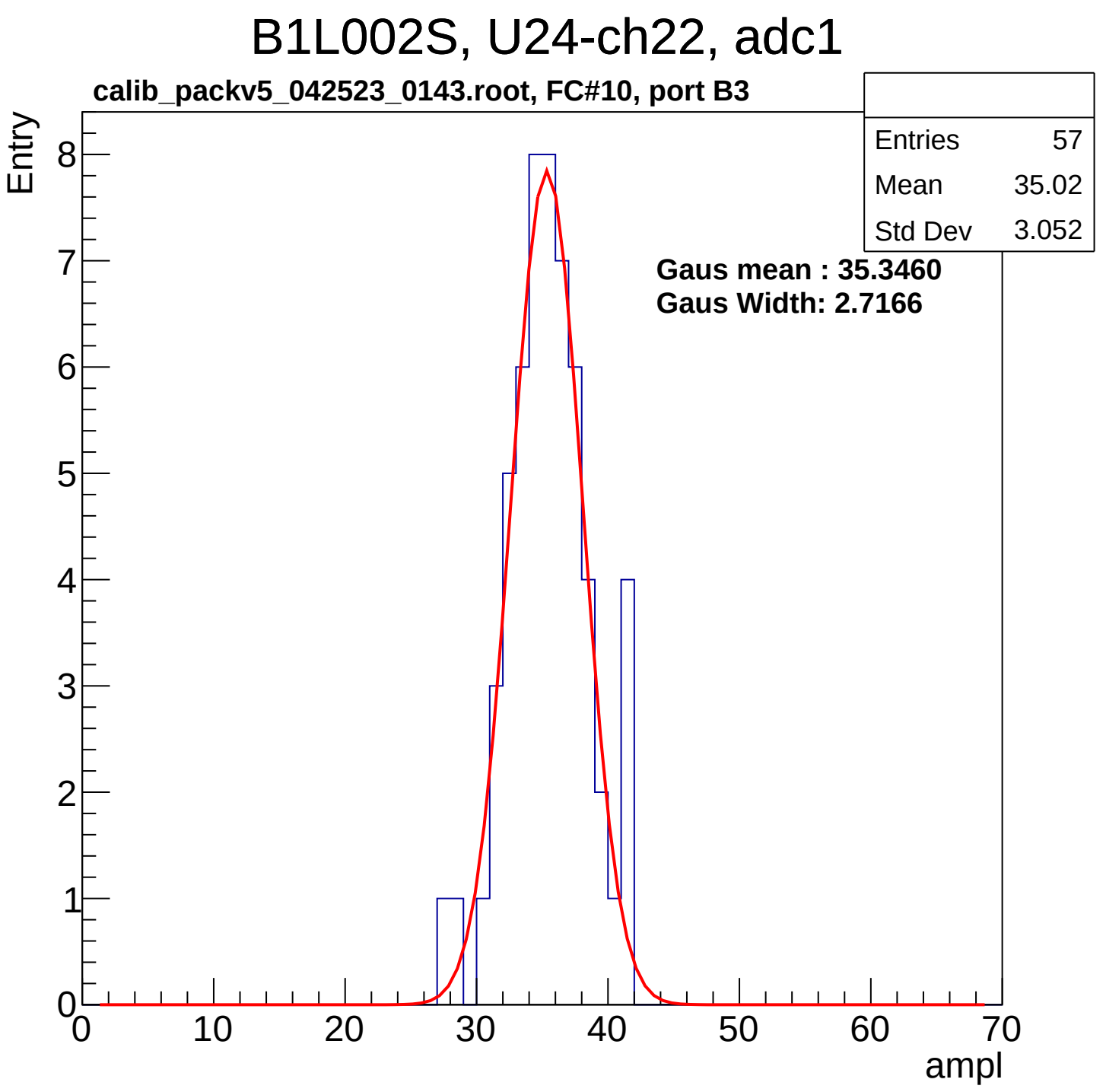
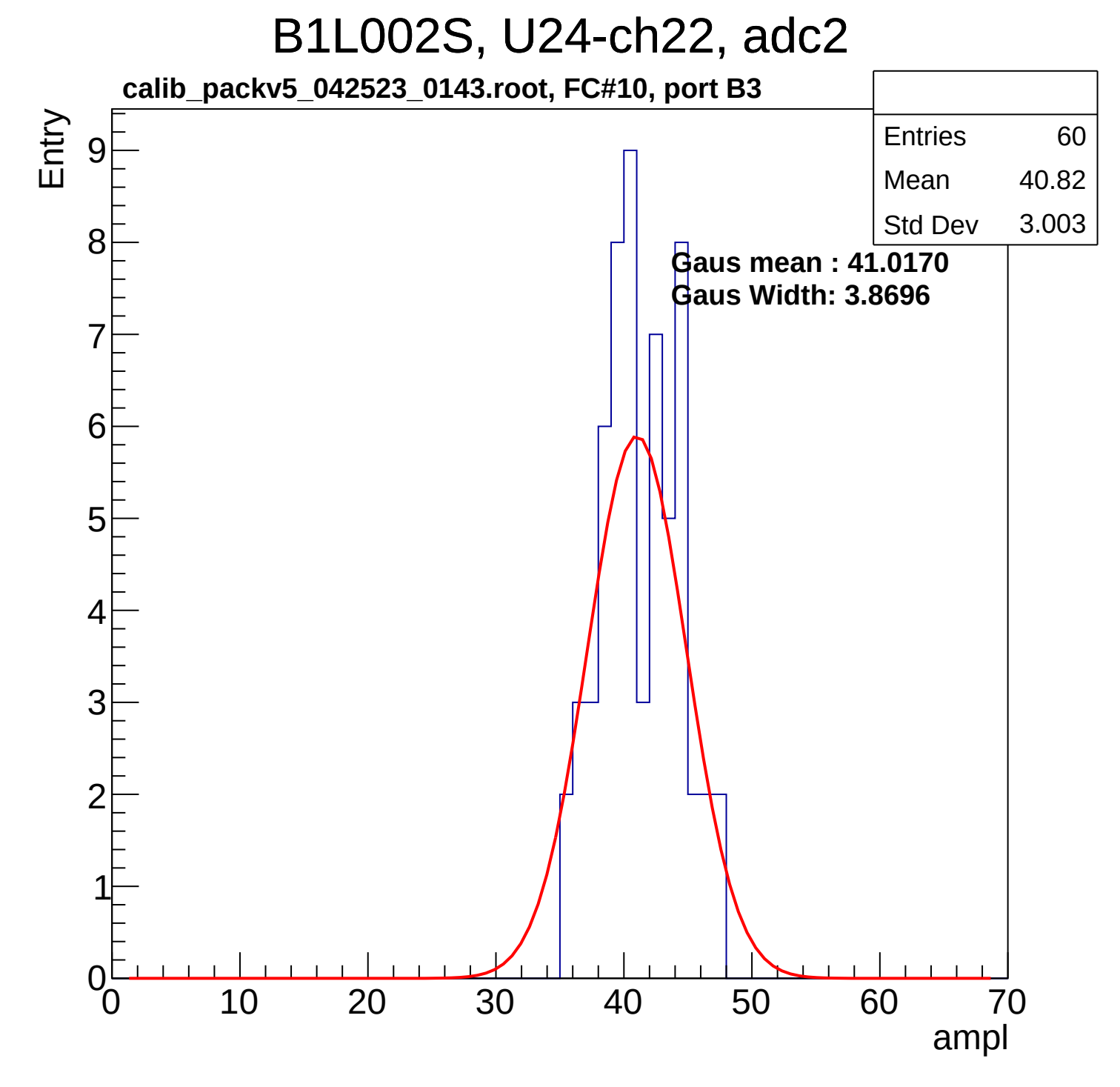
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch22, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.97
Std Dev 4.603
Gaus mean : 29.2934
Gaus Width: 3.4374
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.02
Std Dev 3.052
Gaus mean : 35.3460
Gaus Width: 2.7166
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.82
Std Dev 3.003
Gaus mean : 41.0170
Gaus Width: 3.8696
Entry
ampl
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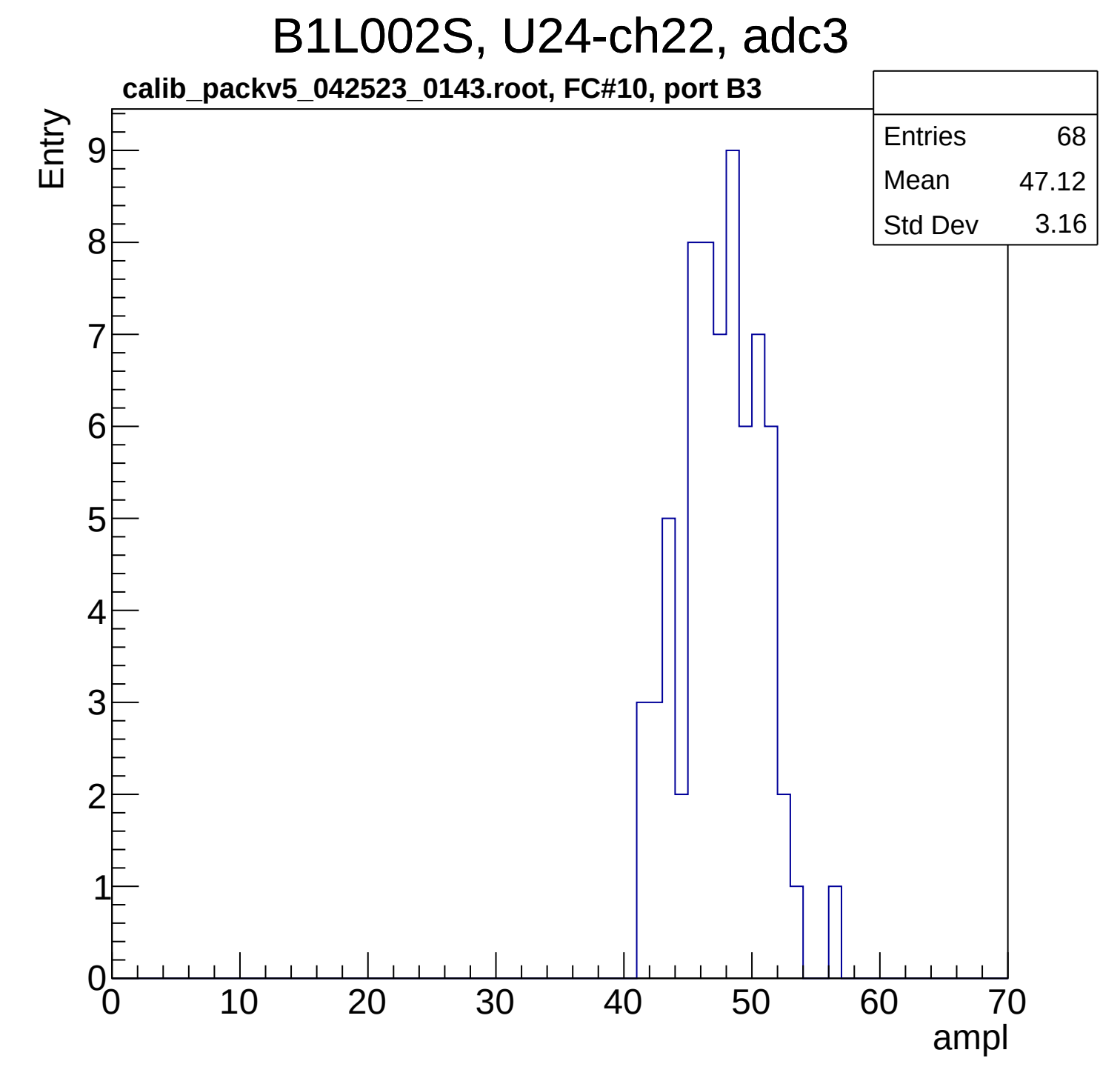
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.12
Std Dev 3.16
Entry
ampl
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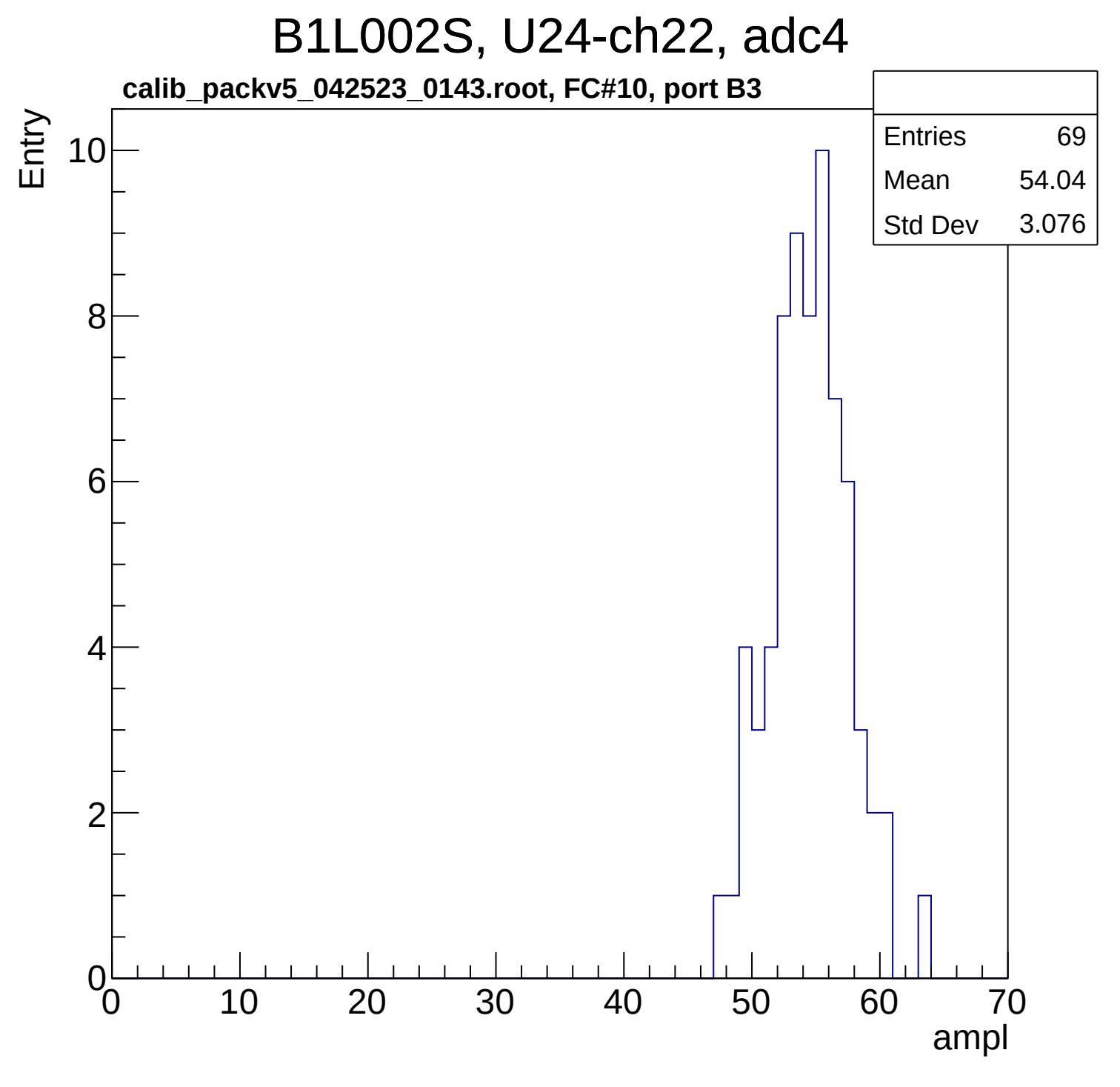
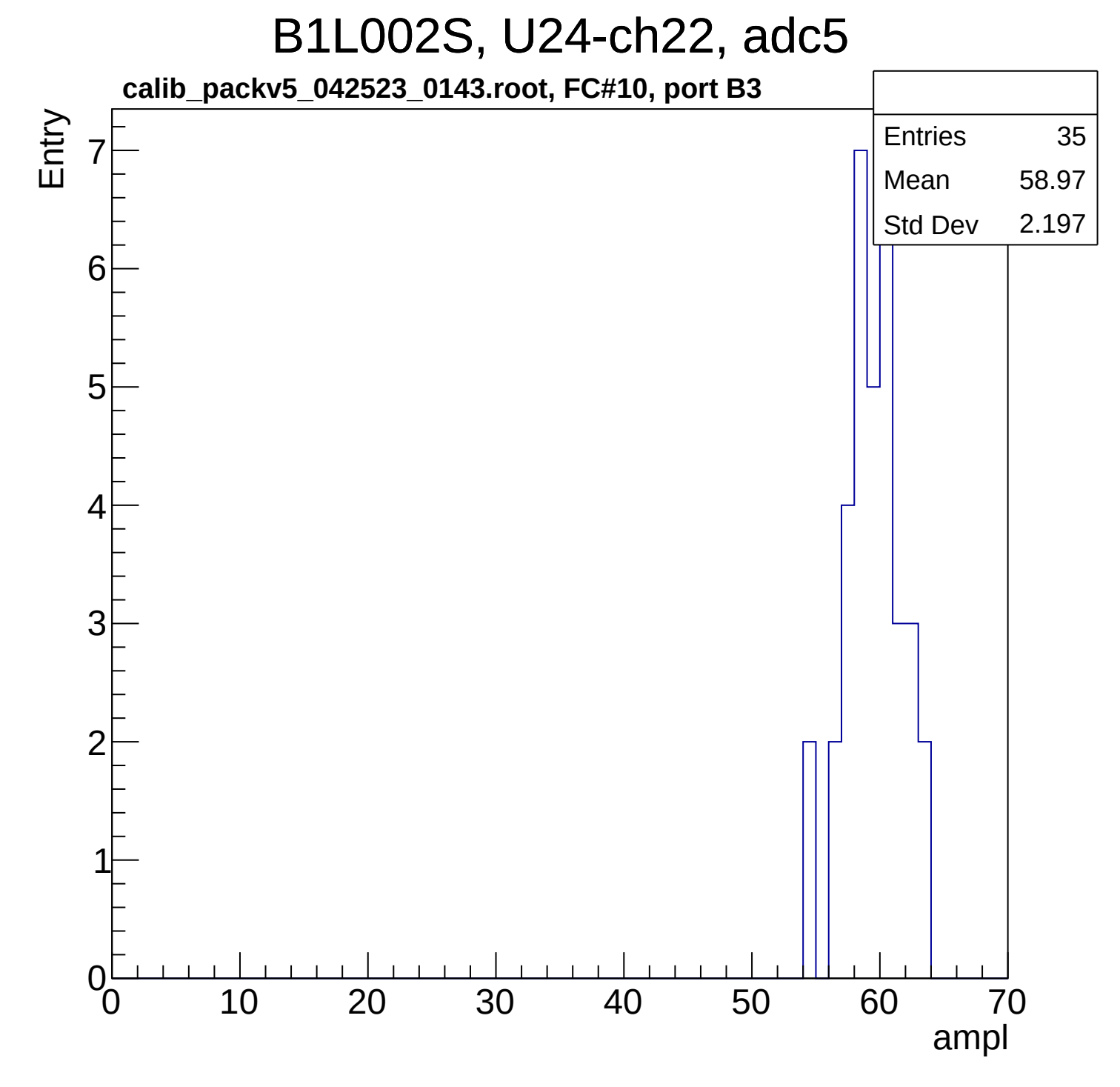
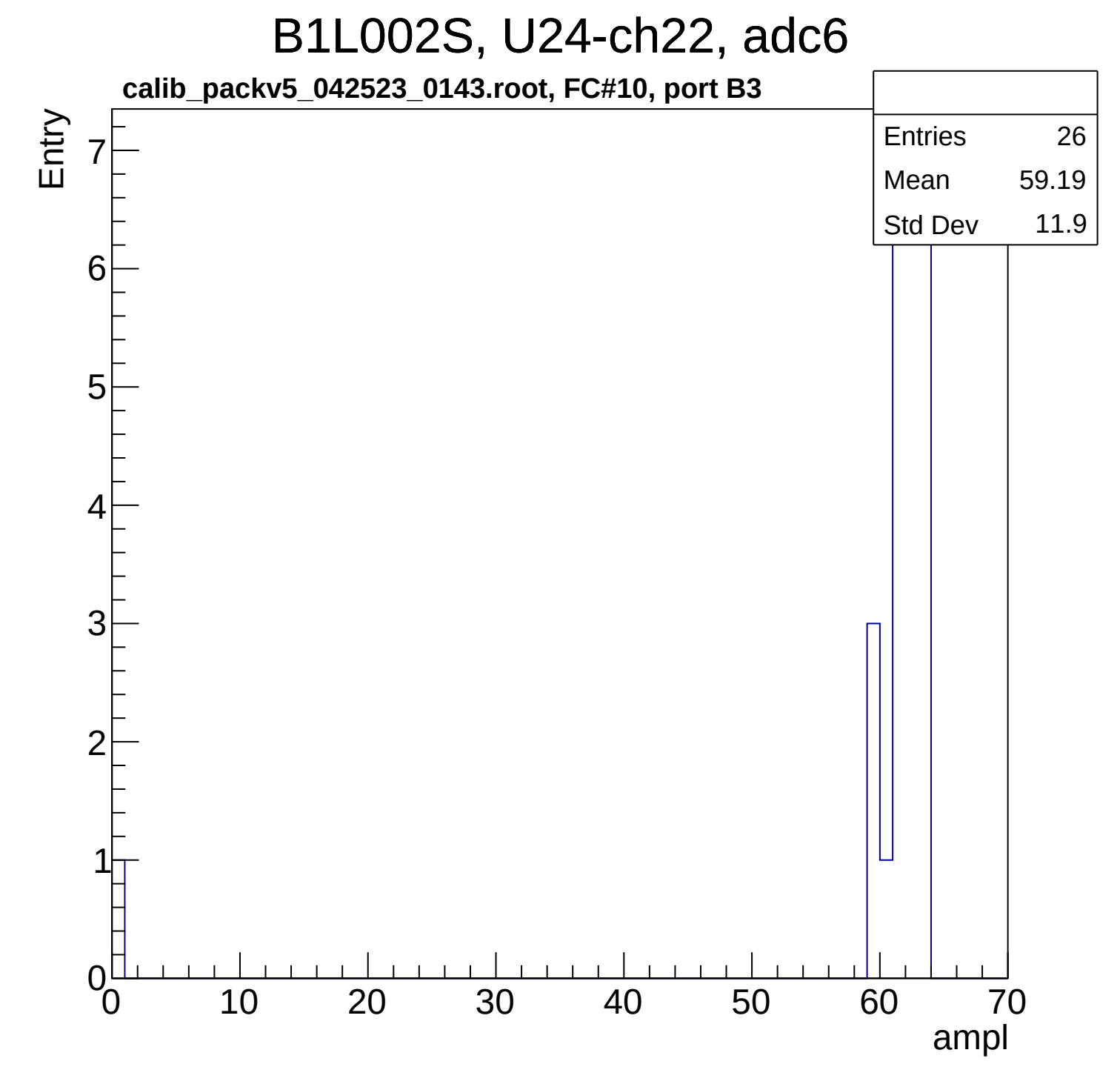
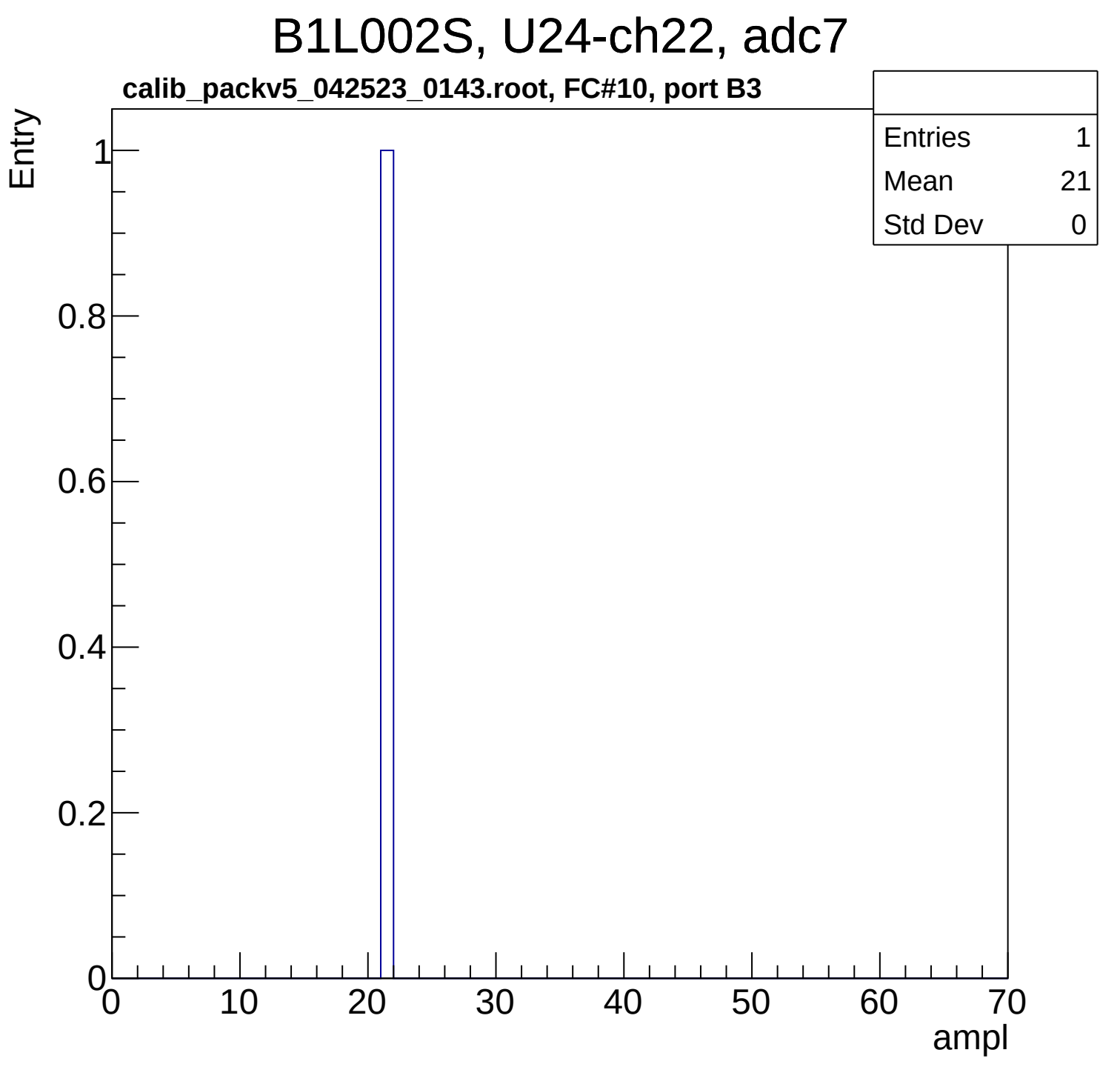
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 54.04
Std Dev 3.076
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch22, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 58.97
Std Dev 2.197
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.19
Std Dev 11.9
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch22, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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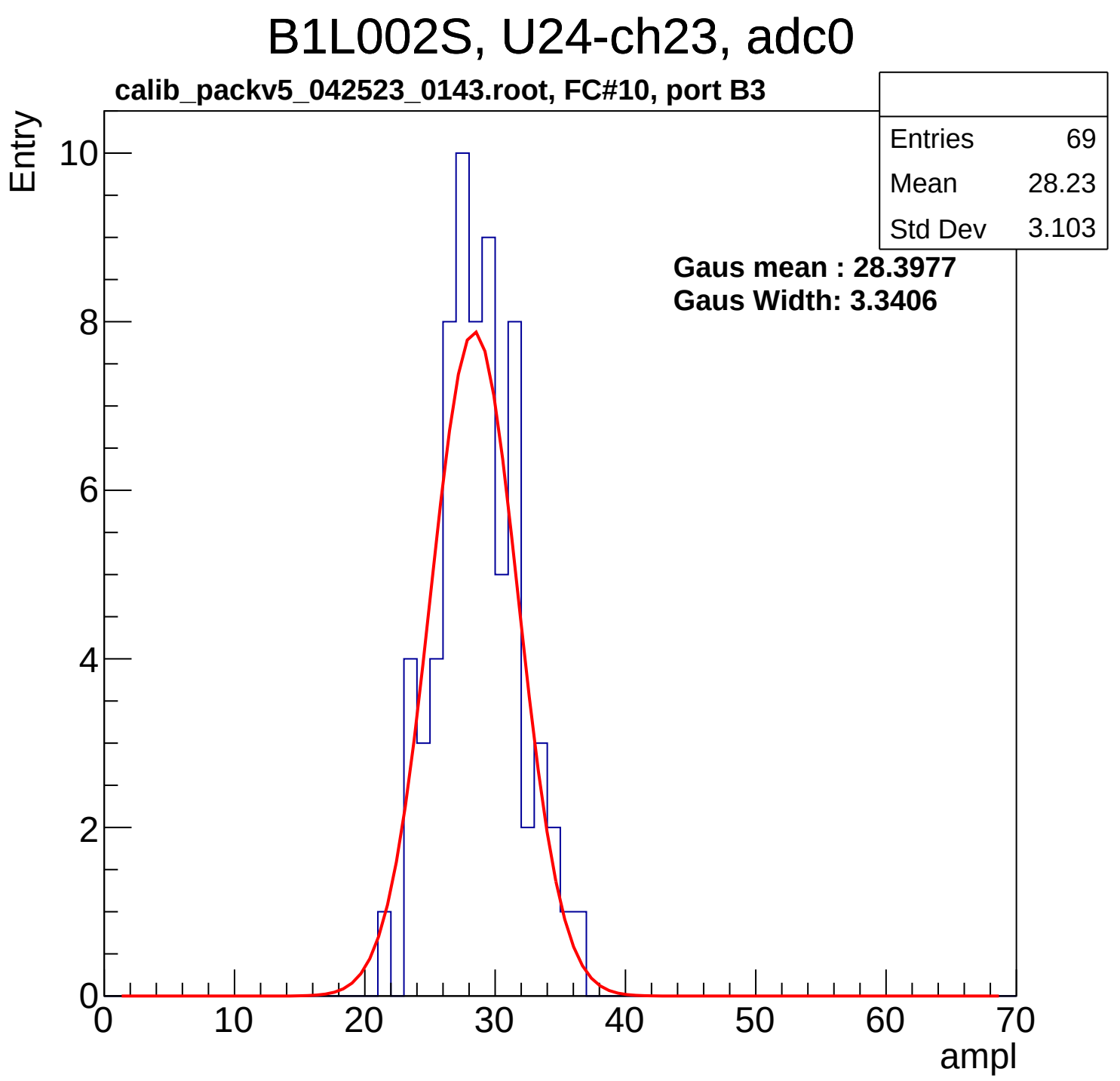
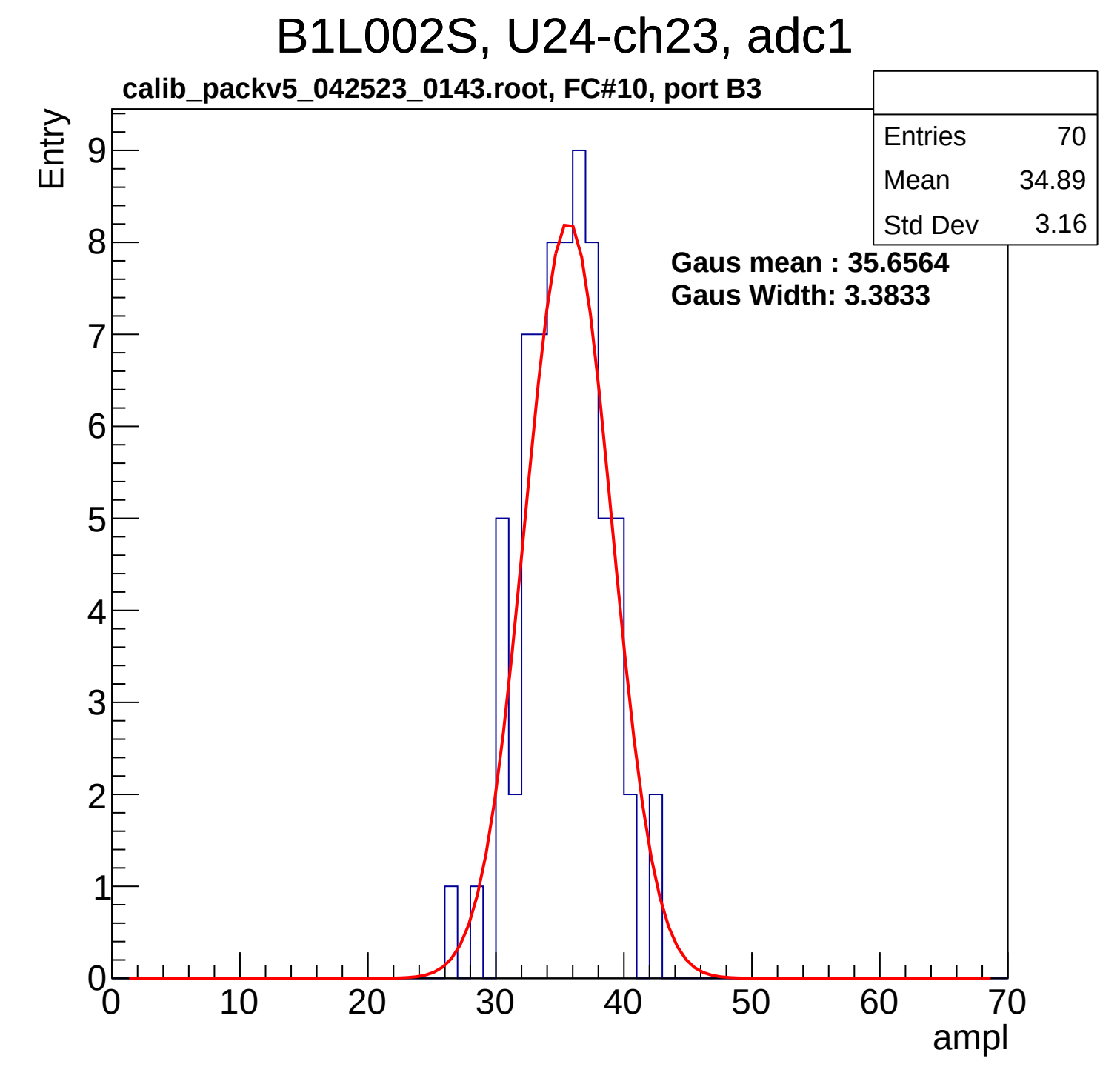
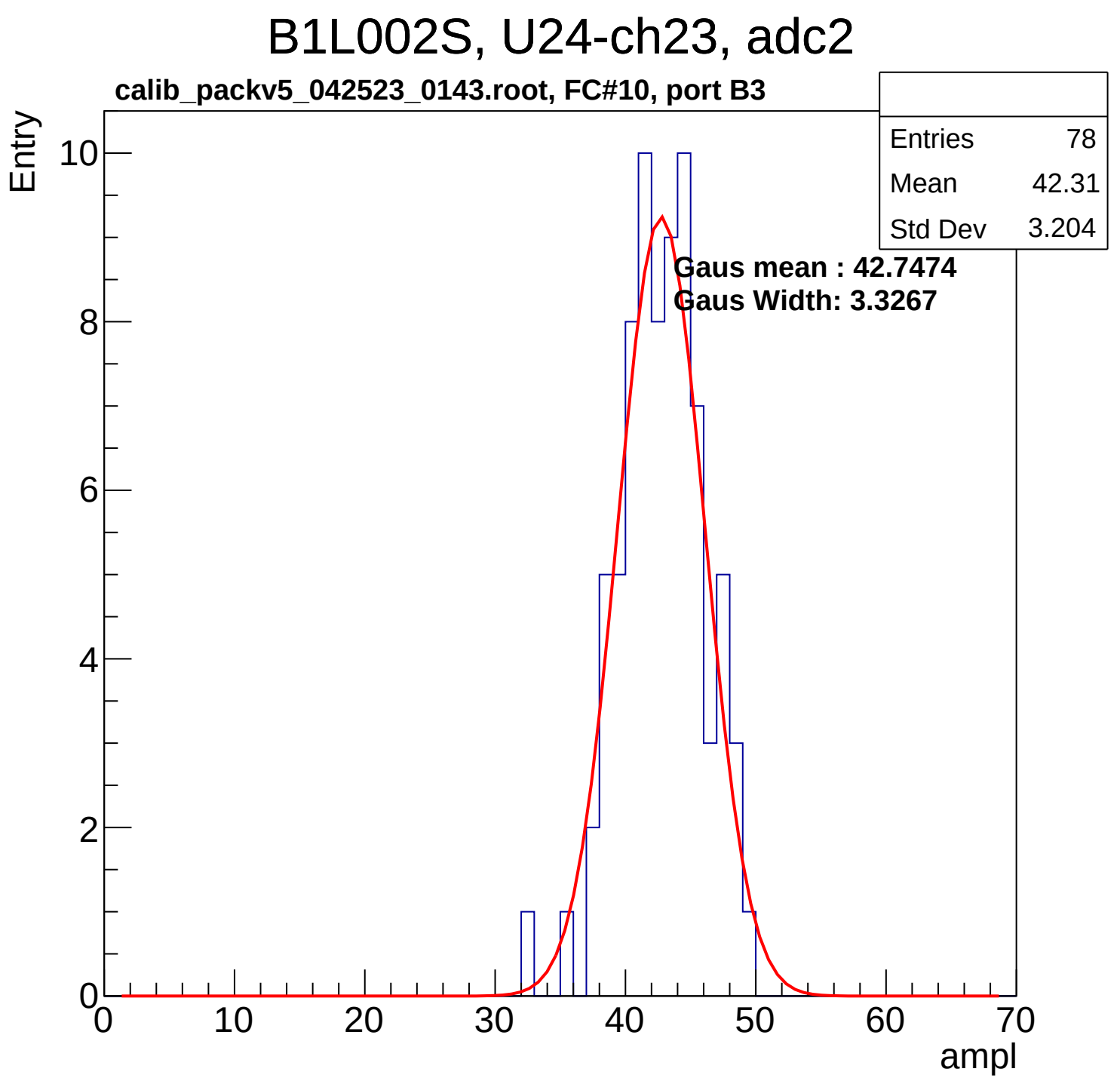
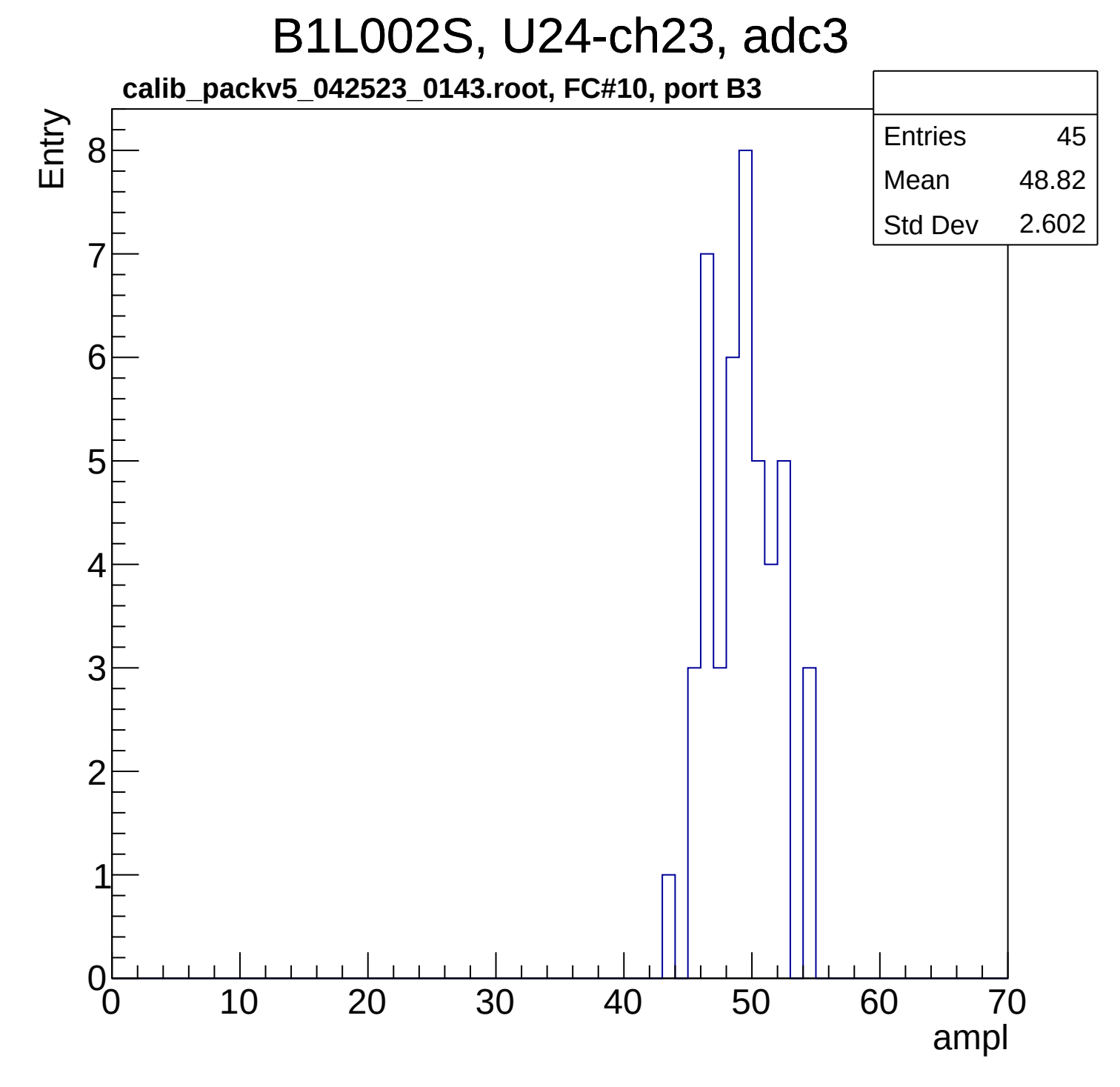
40
50
60
70
ampl


B1L002S, U24-ch23, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 28.23
Std Dev 3.103
Gaus mean : 28.3977
Gaus Width: 3.3406
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch23, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.89
Std Dev 3.16
Gaus mean : 35.6564
Gaus Width: 3.3833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch23, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 42.31
Std Dev 3.204
Gaus mean : 42.7474
Gaus Width: 3.3267
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch23, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 48.82
Std Dev 2.602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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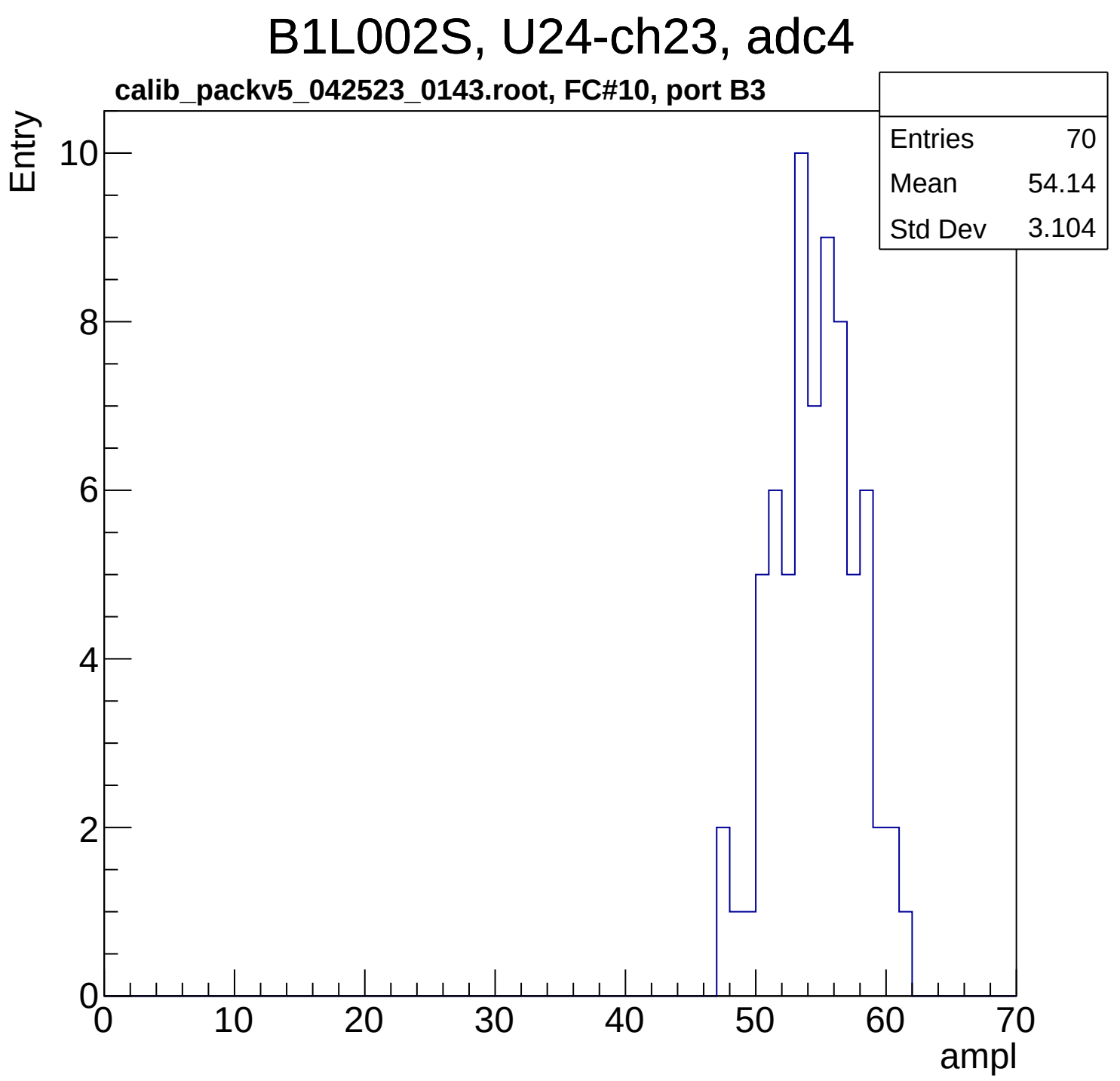
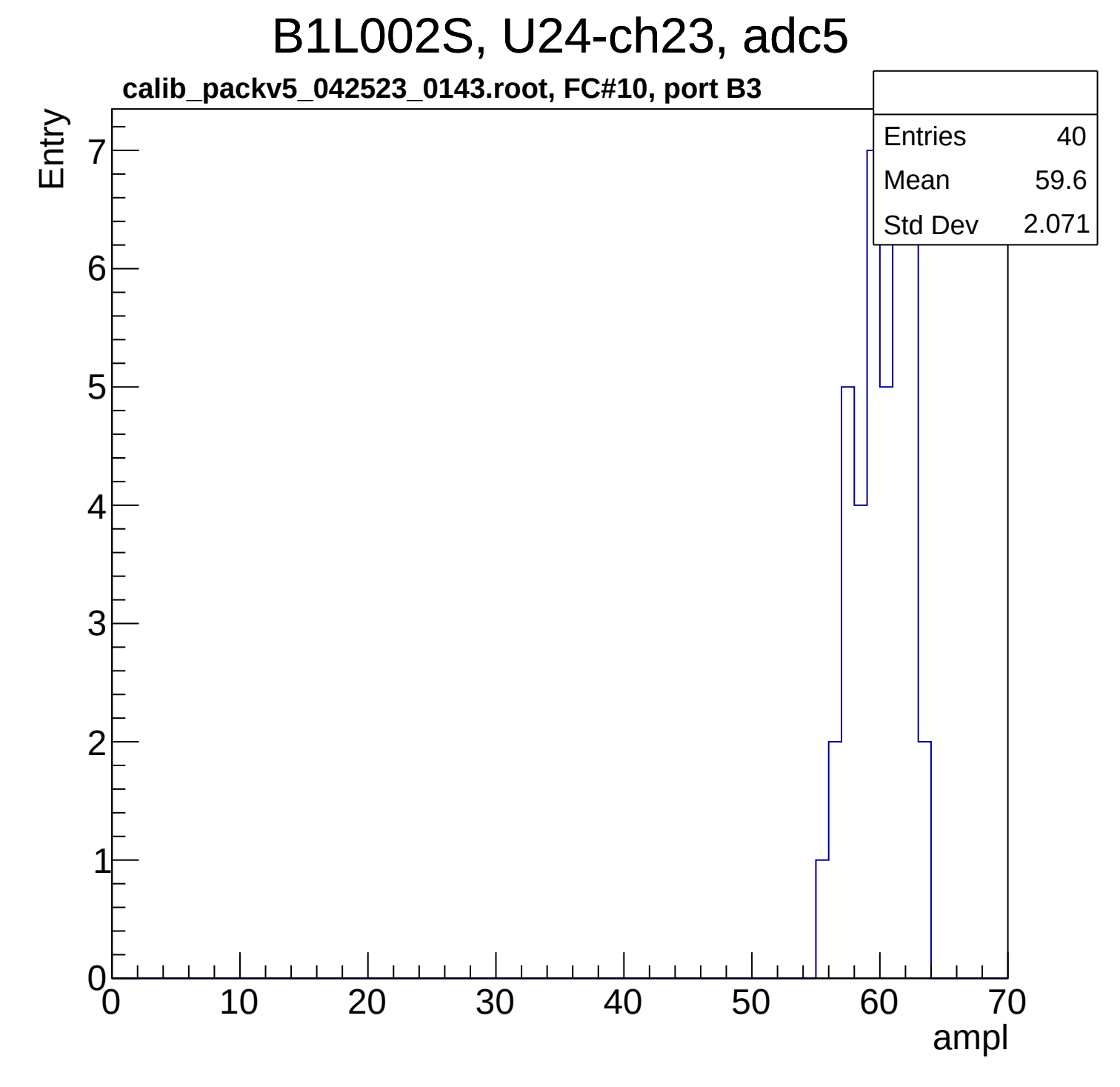
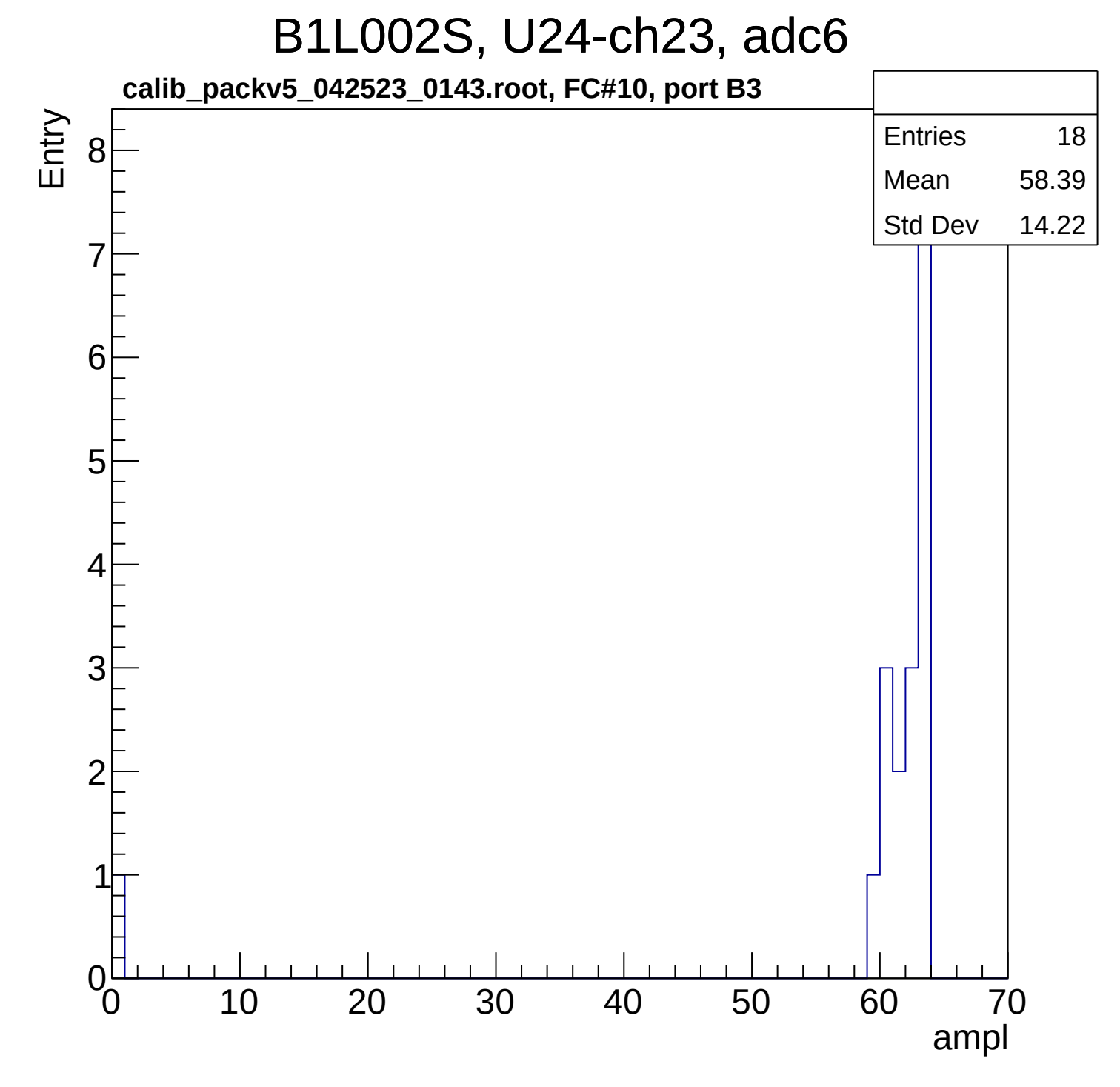
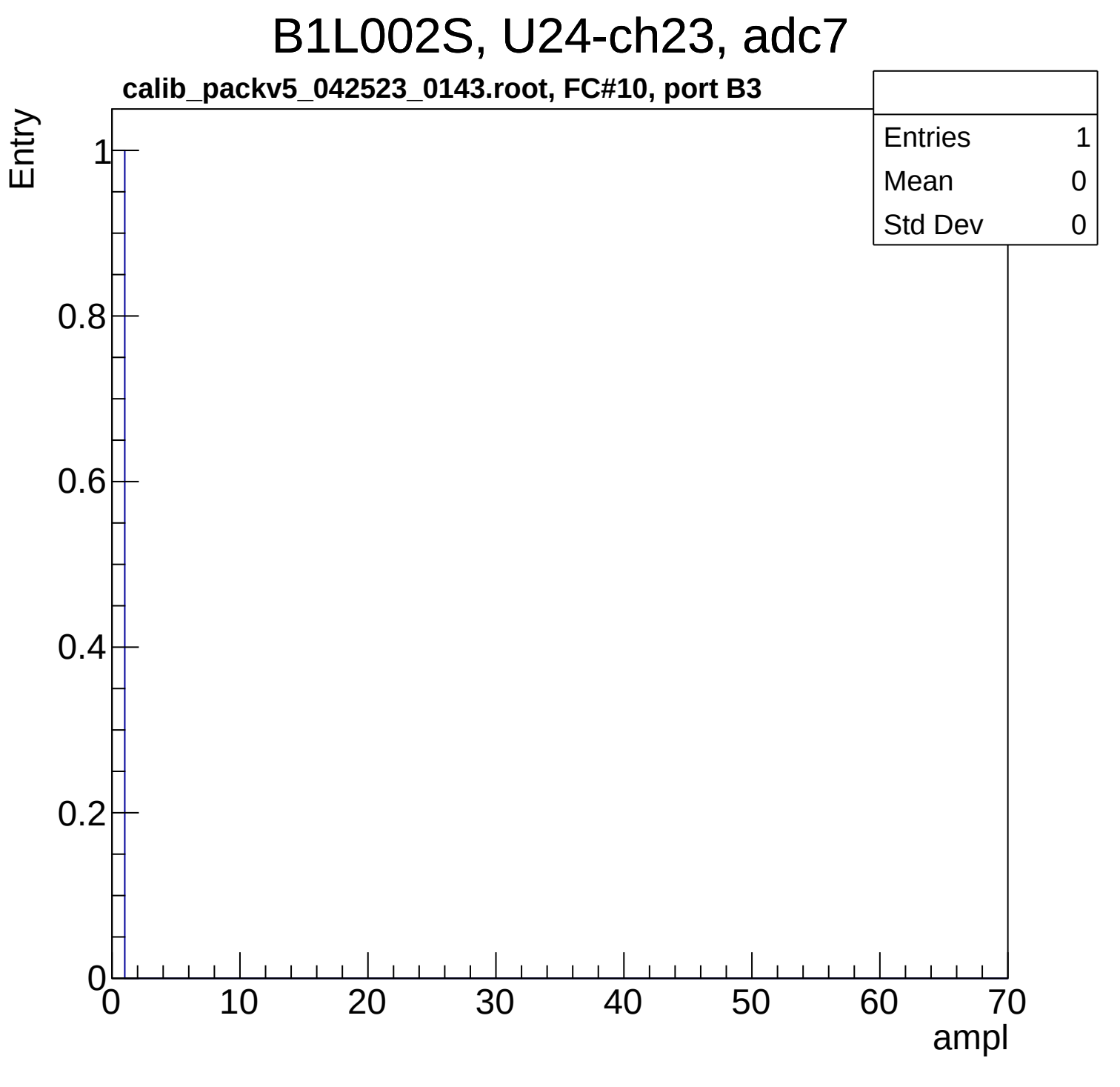
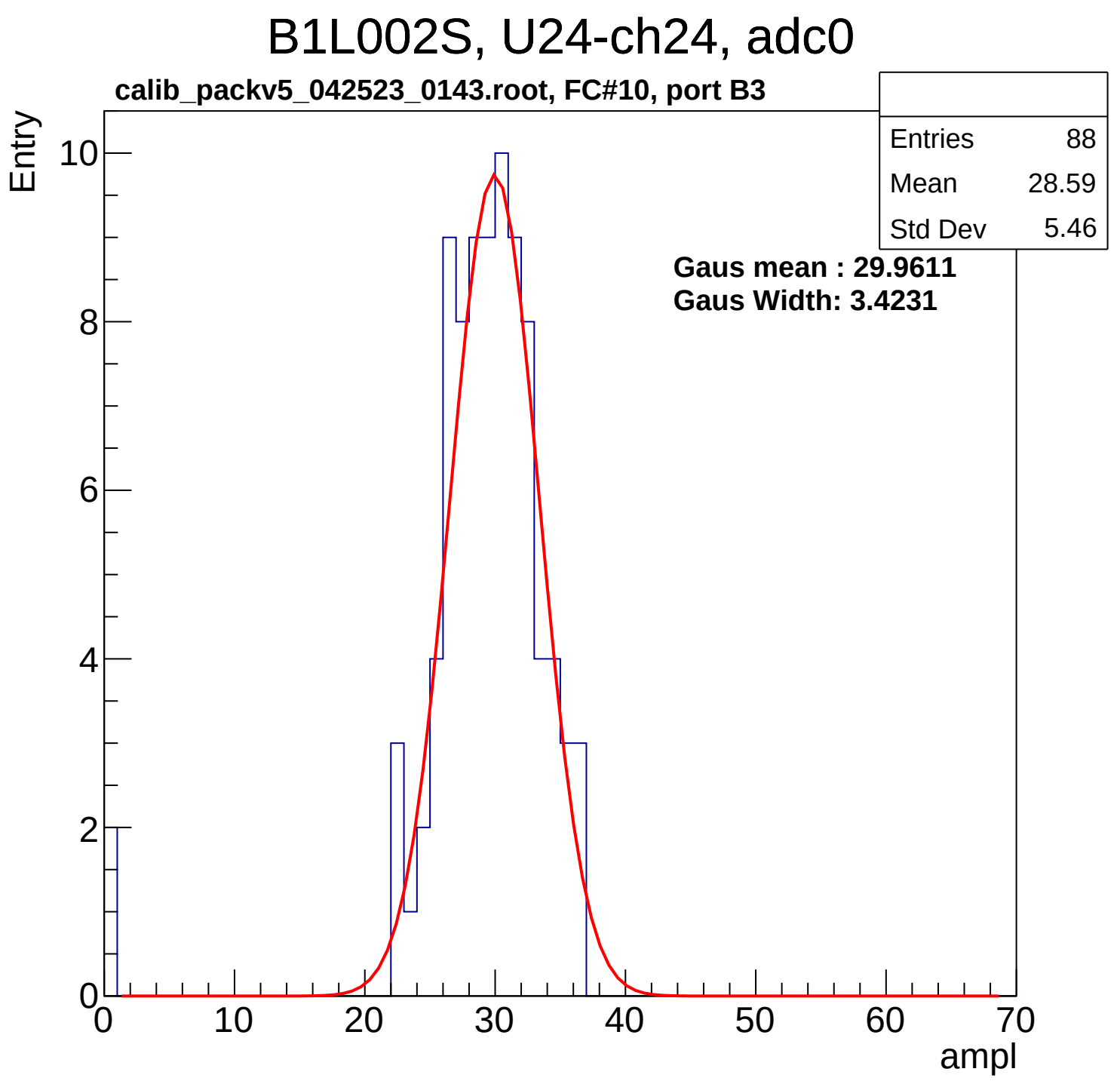
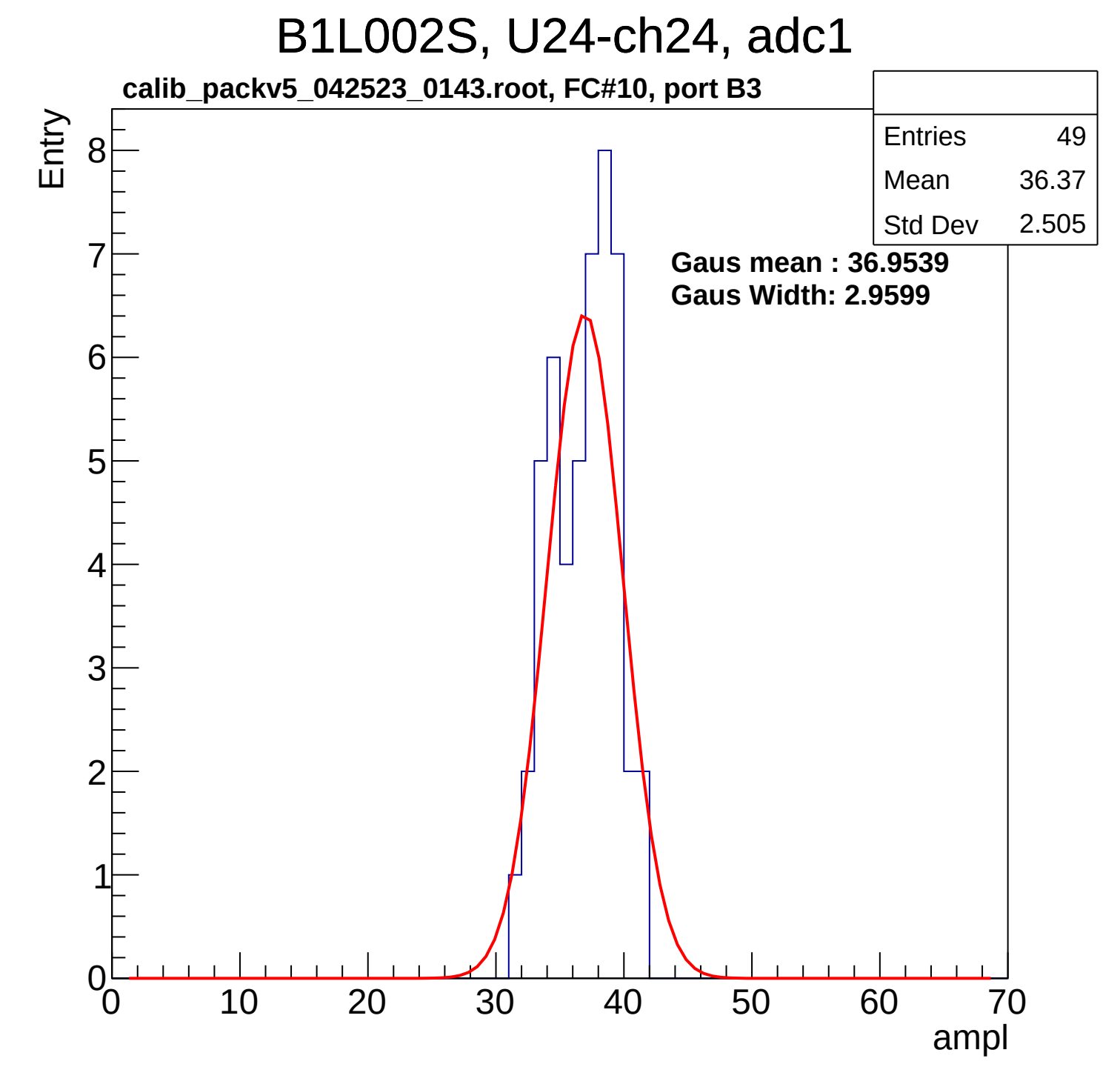

B1L002S, U24-ch23, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 54.14
Std Dev 3.104
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

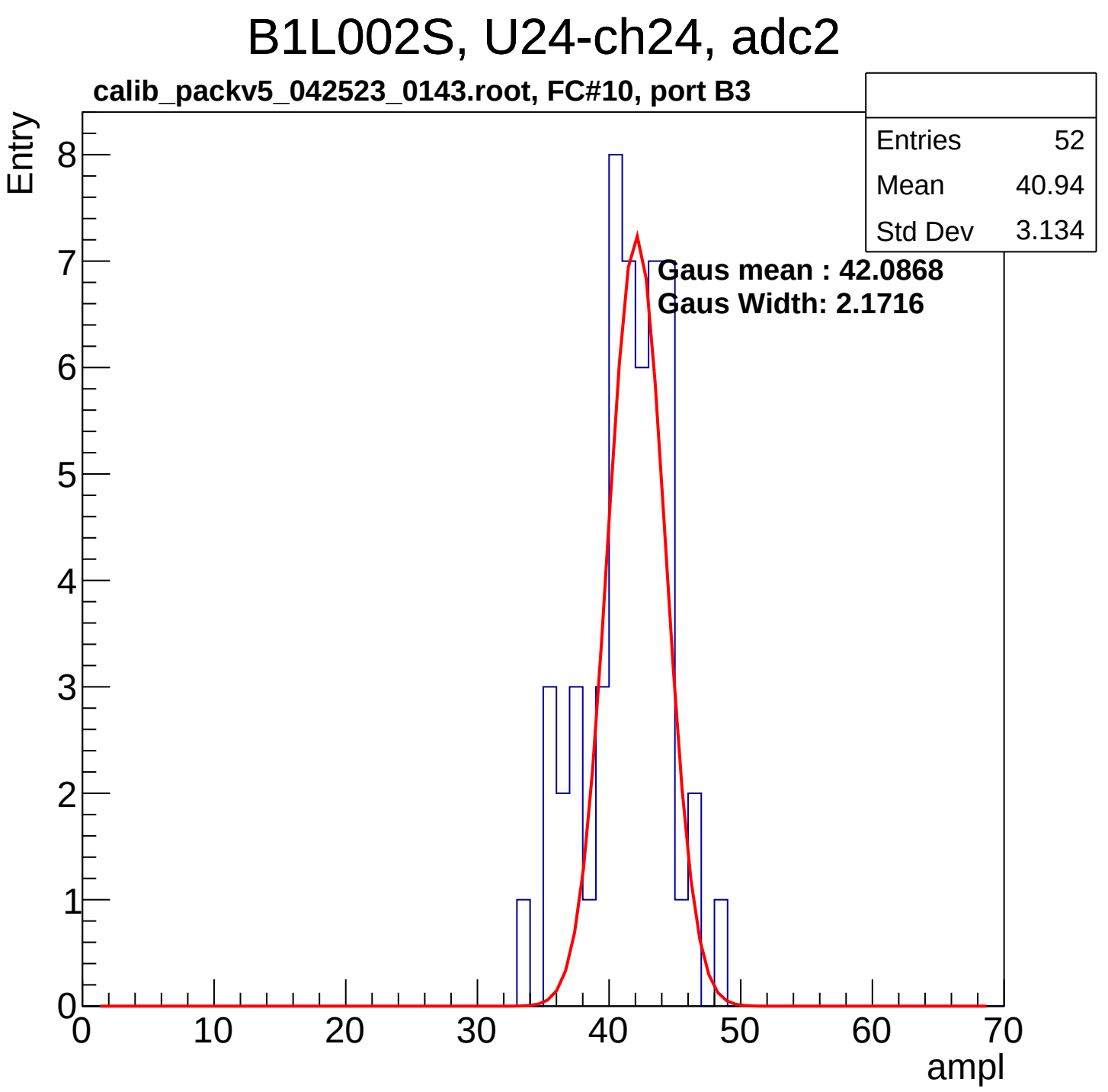

B1L002S, U24-ch23, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 59.6
Std Dev 2.071
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

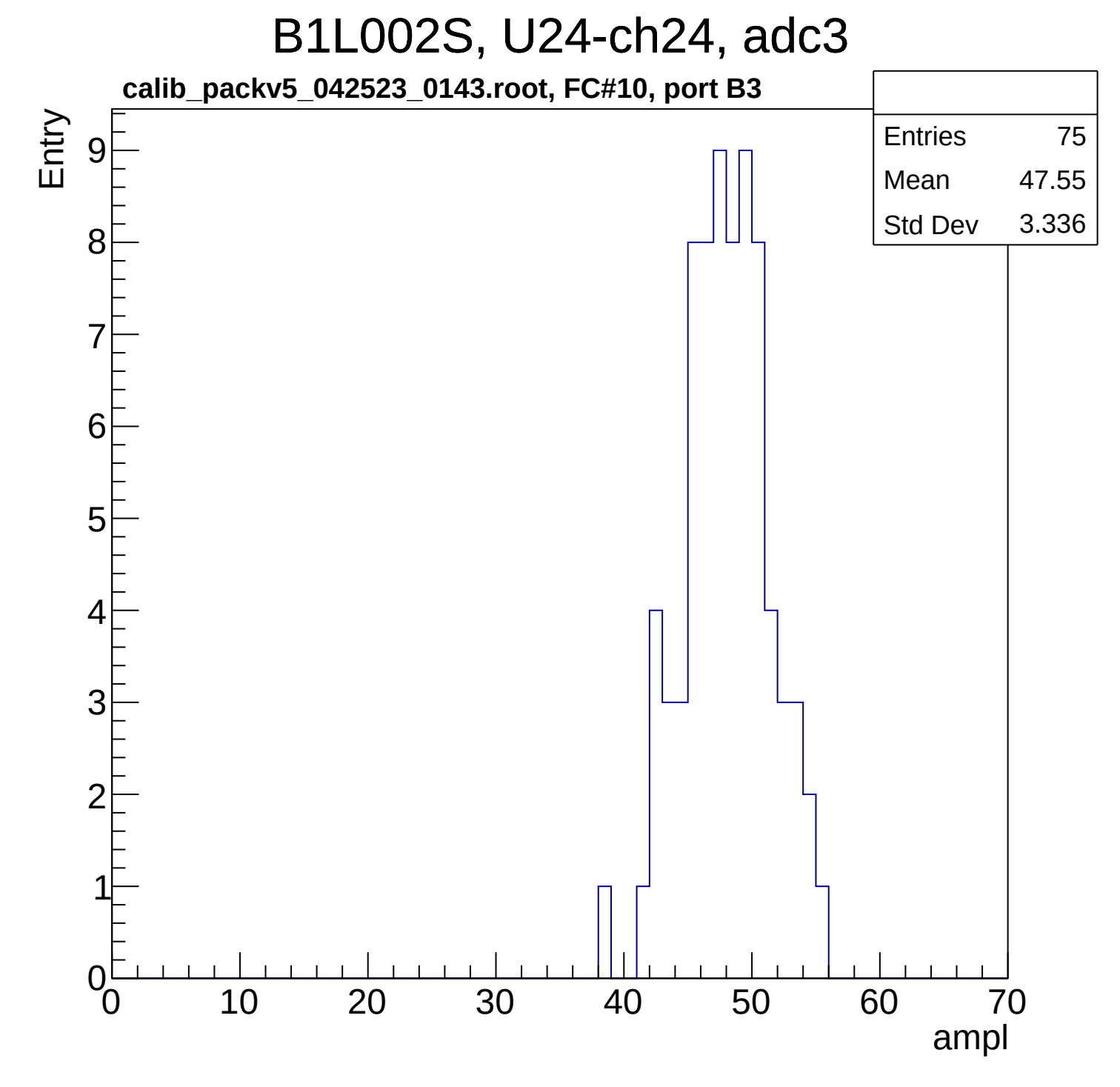

B1L002S, U24-ch23, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.39
Std Dev 14.22
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

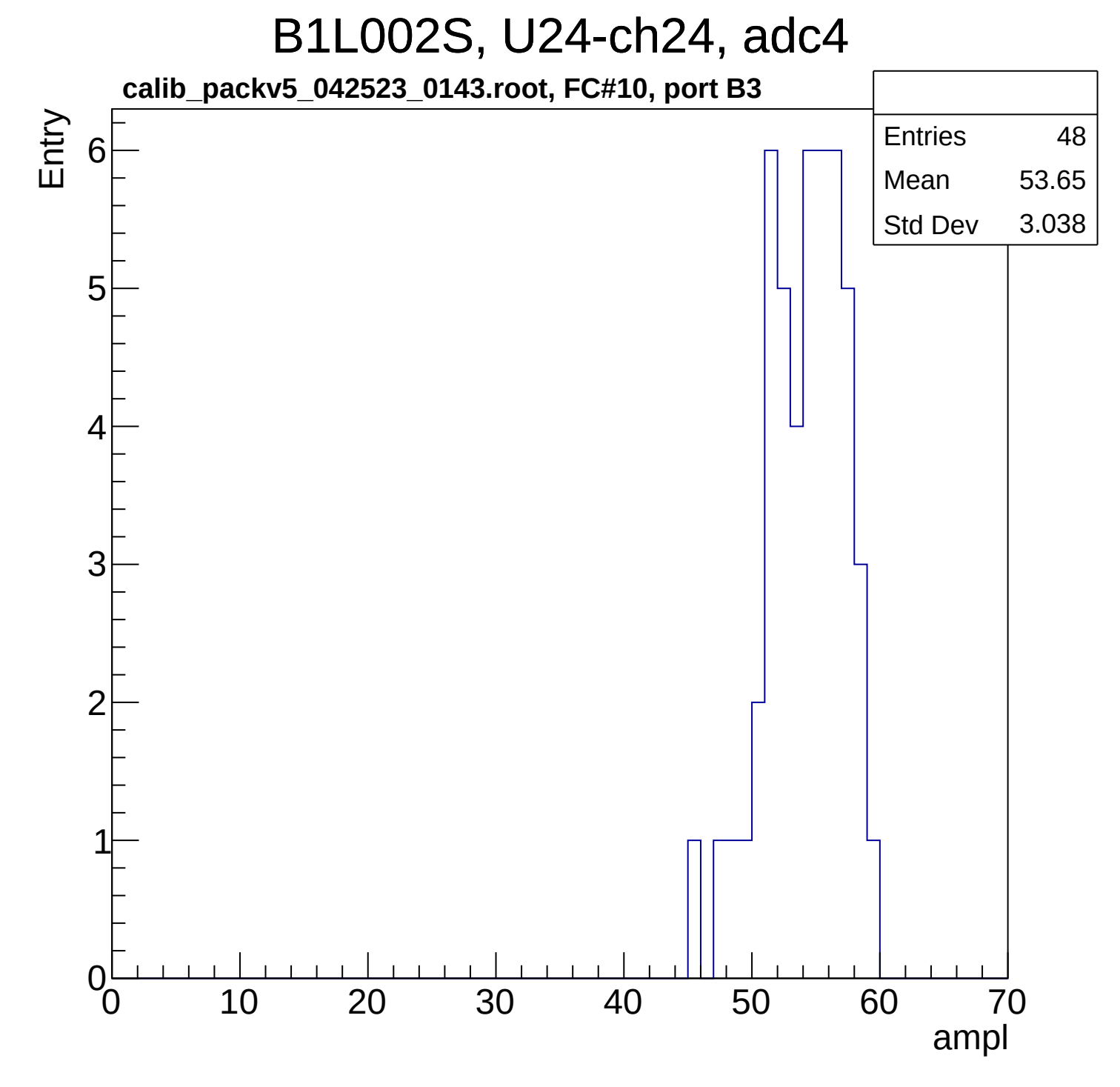

B1L002S, U24-ch23, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

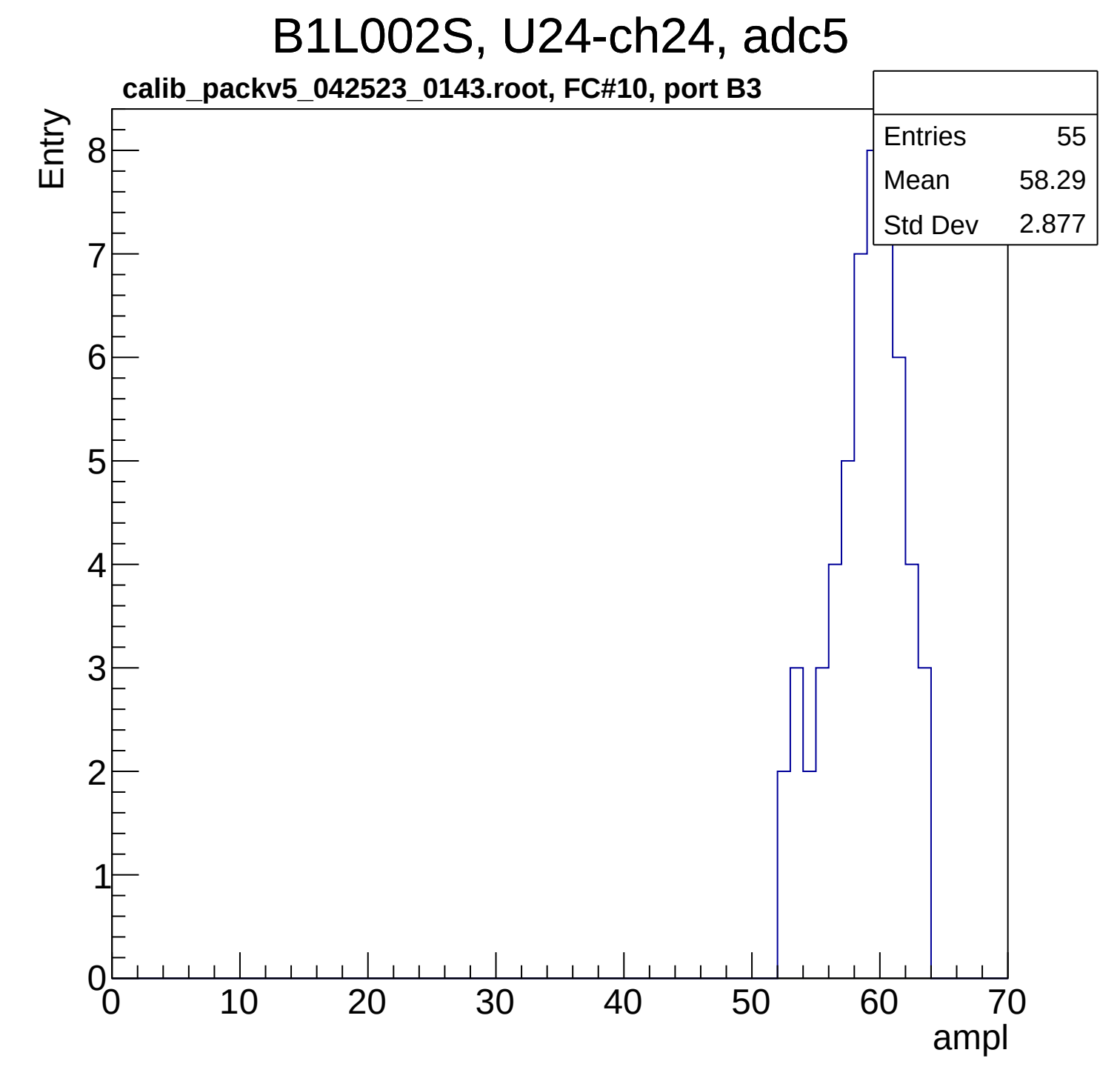

B1L002S, U24-ch24, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 88
Mean 28.59
Std Dev 5.46
Gaus mean : 29.9611
Gaus Width: 3.4231
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

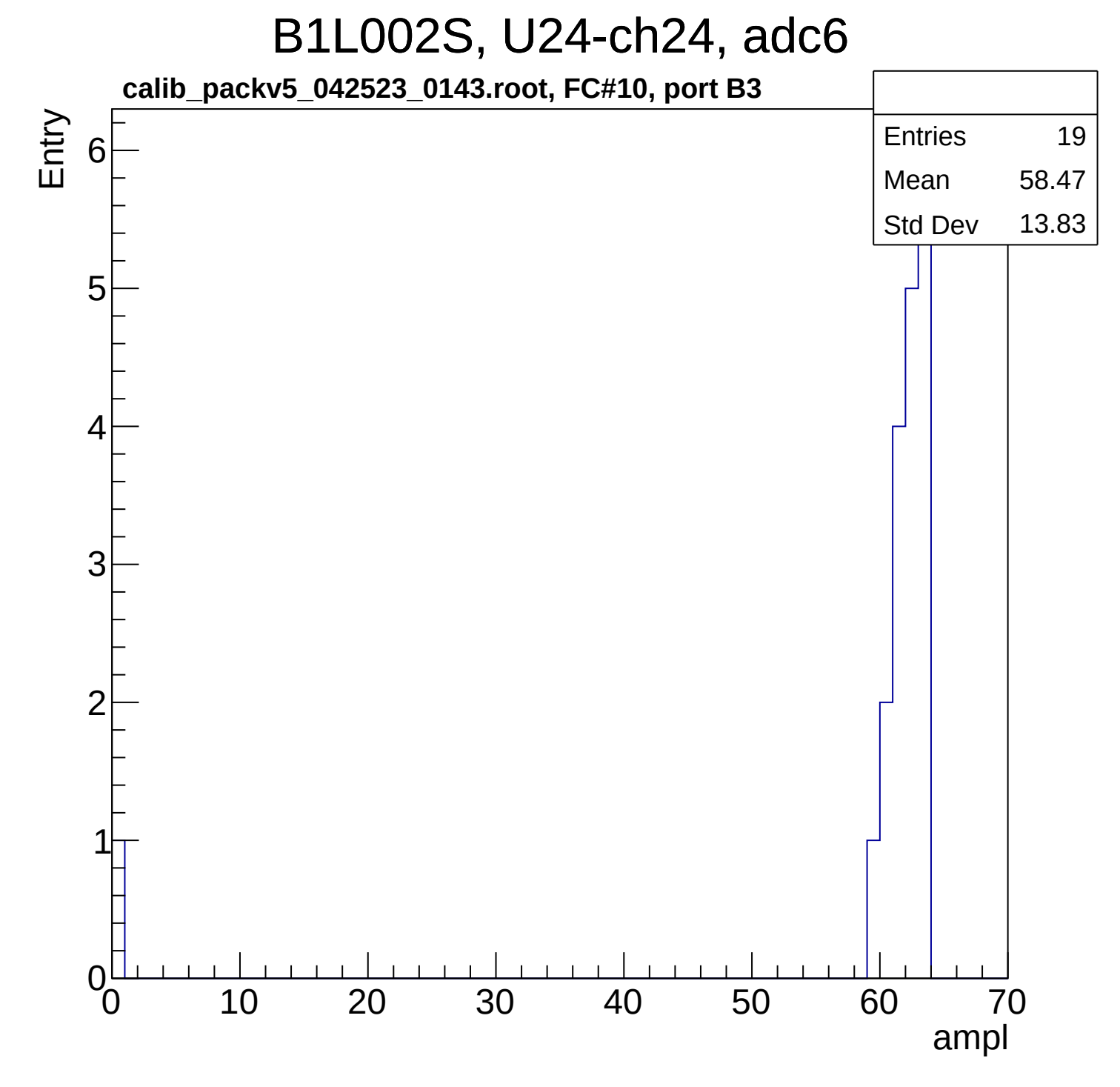

B1L002S, U24-ch24, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 36.37
Std Dev 2.505
Gaus mean : 36.9539
Gaus Width: 2.9599
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

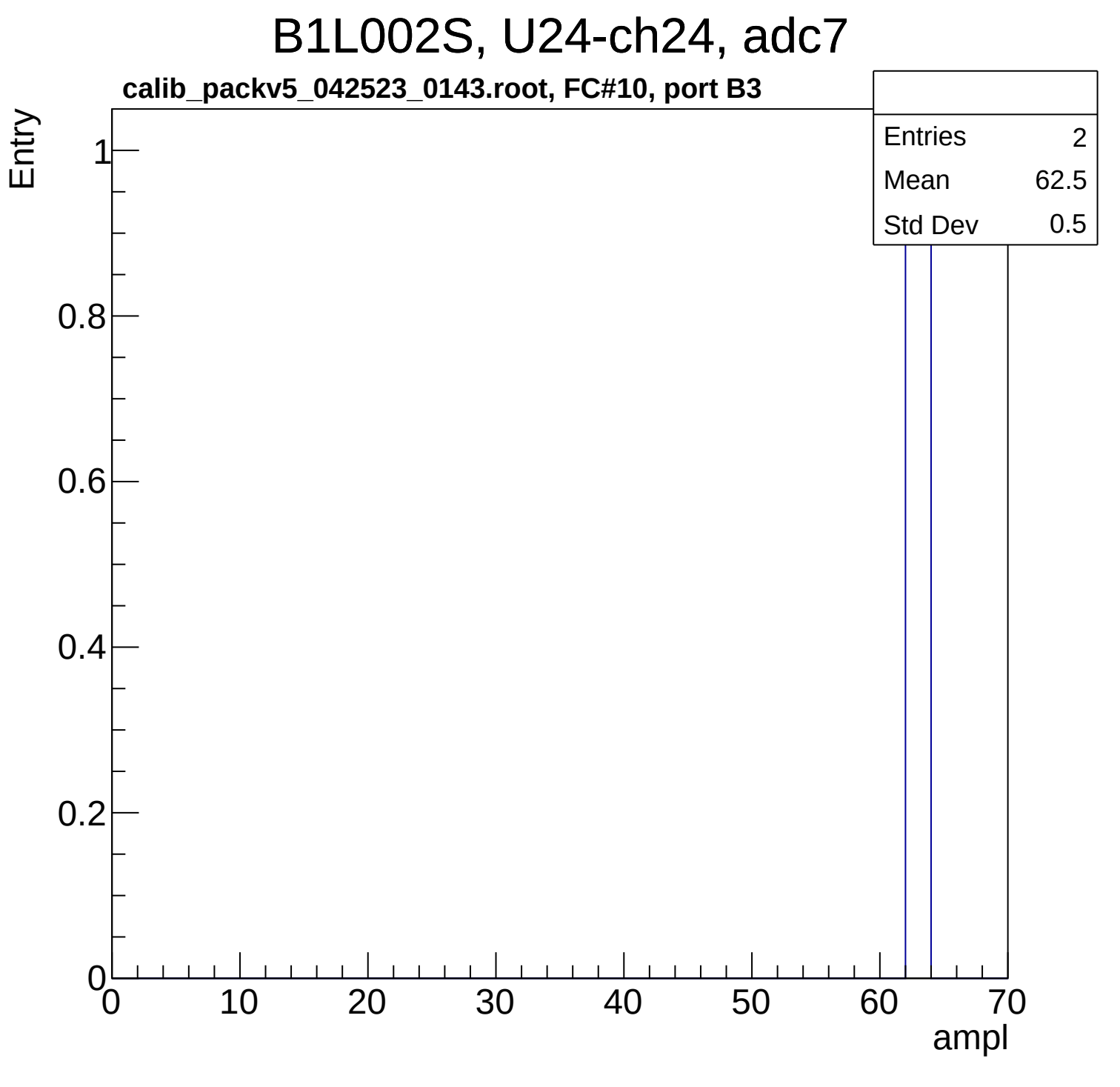

B1L002S, U24-ch24, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.94
Std Dev 3.134
Gaus mean : 42.0868
Gaus Width: 2.1716
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

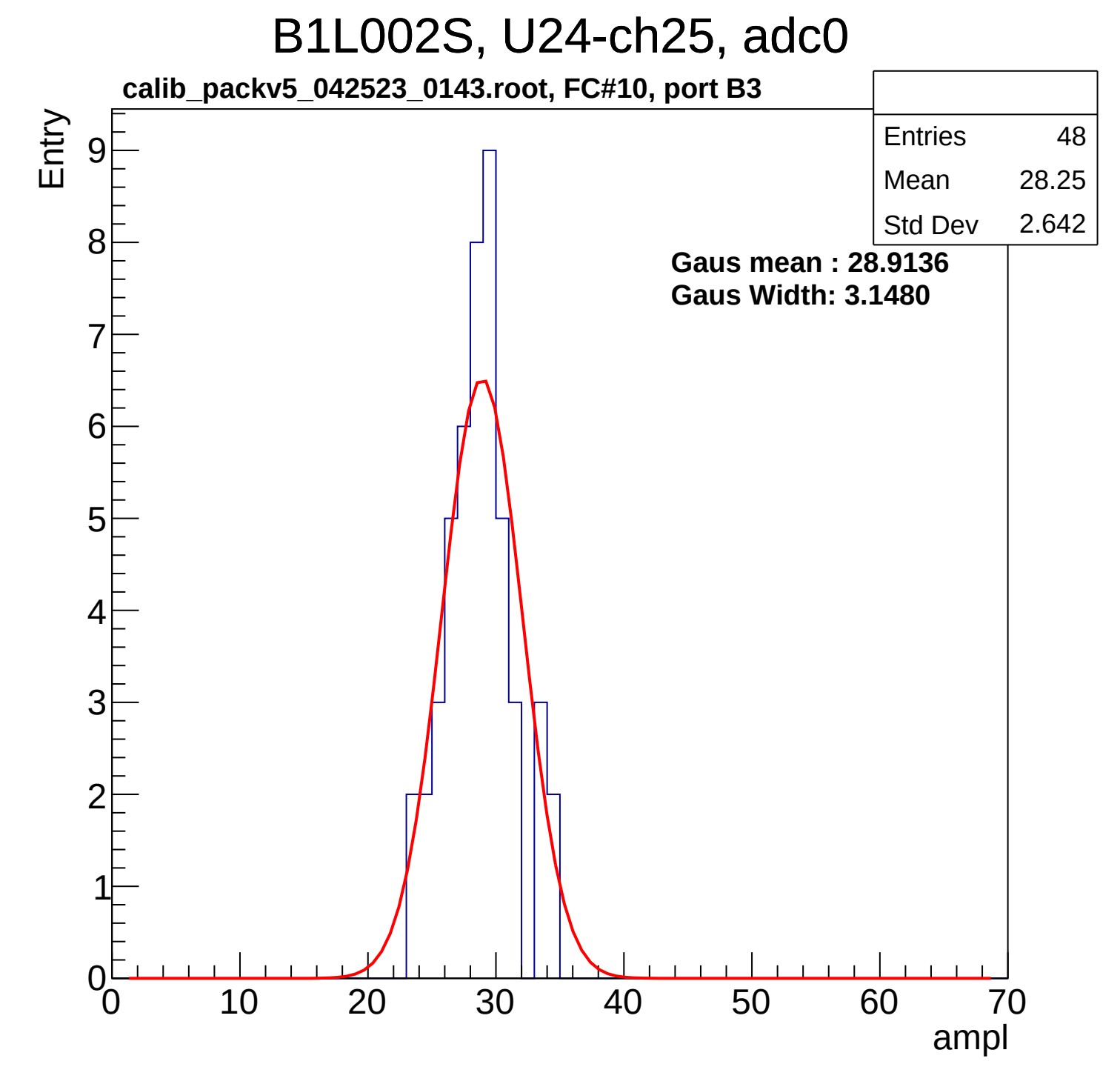

B1L002S, U24-ch24, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 47.55
Std Dev 3.336
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

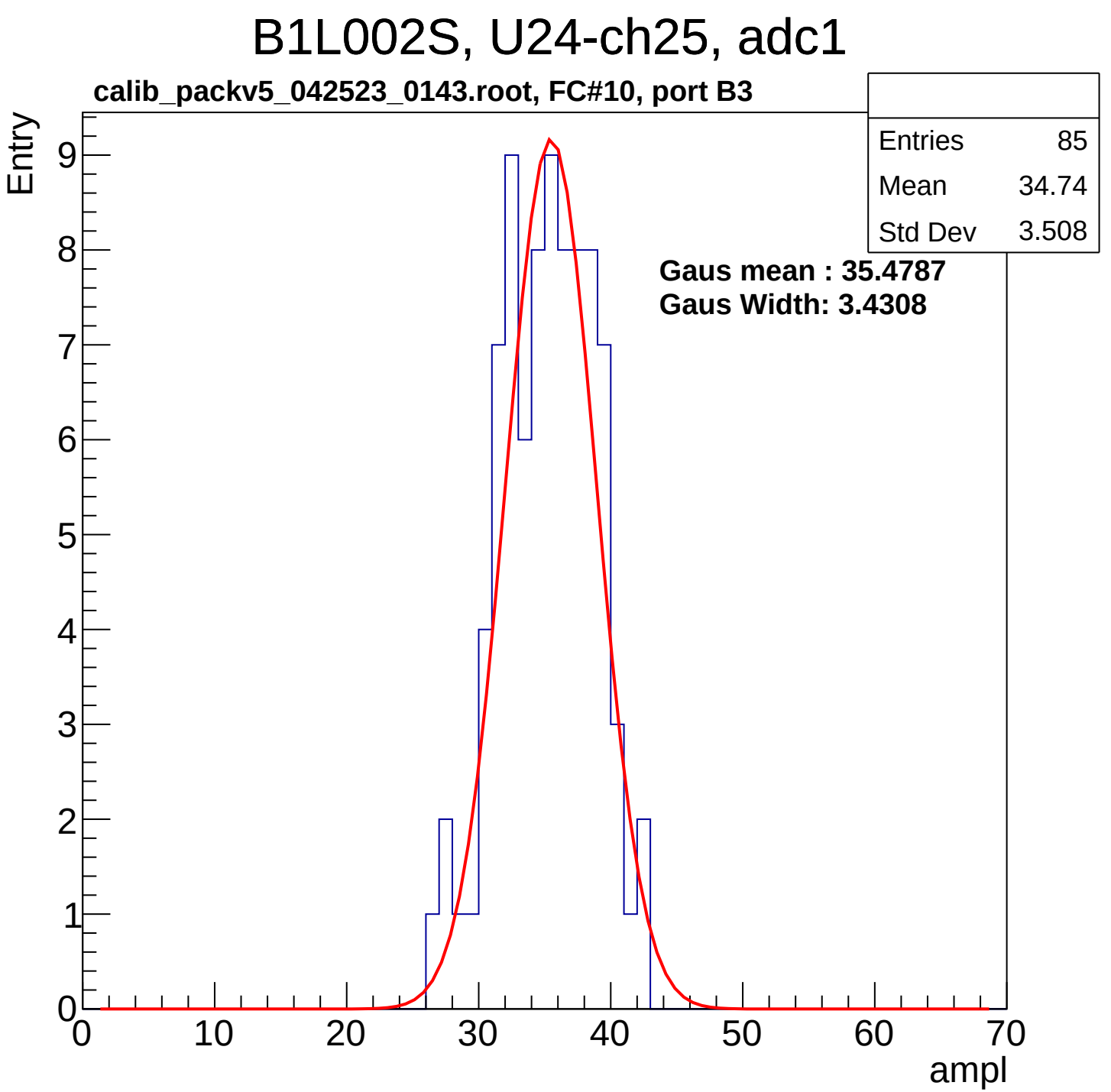

B1L002S, U24-ch24, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.65
Std Dev 3.038
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

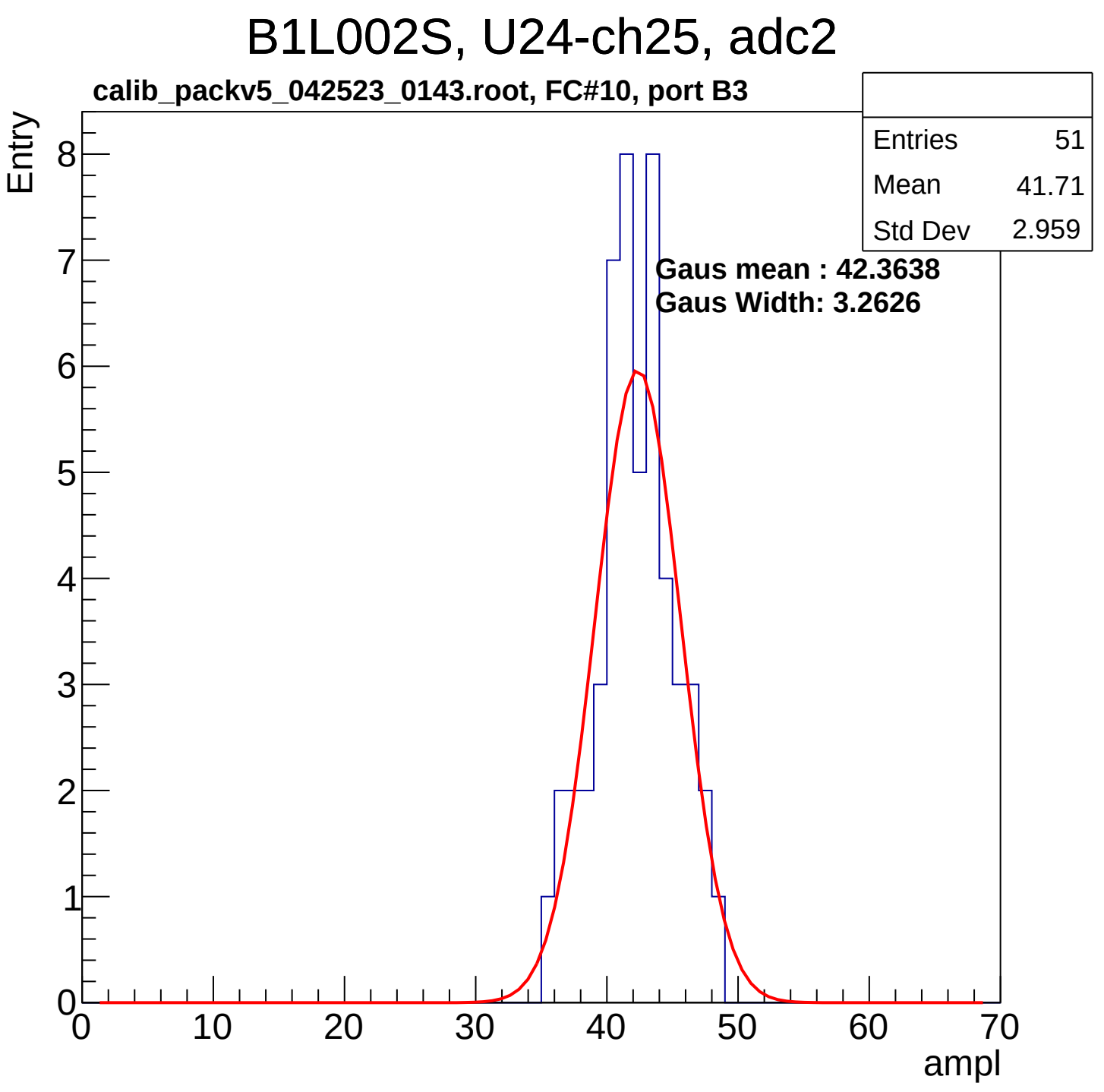

B1L002S, U24-ch24, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 58.29
Std Dev 2.877
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

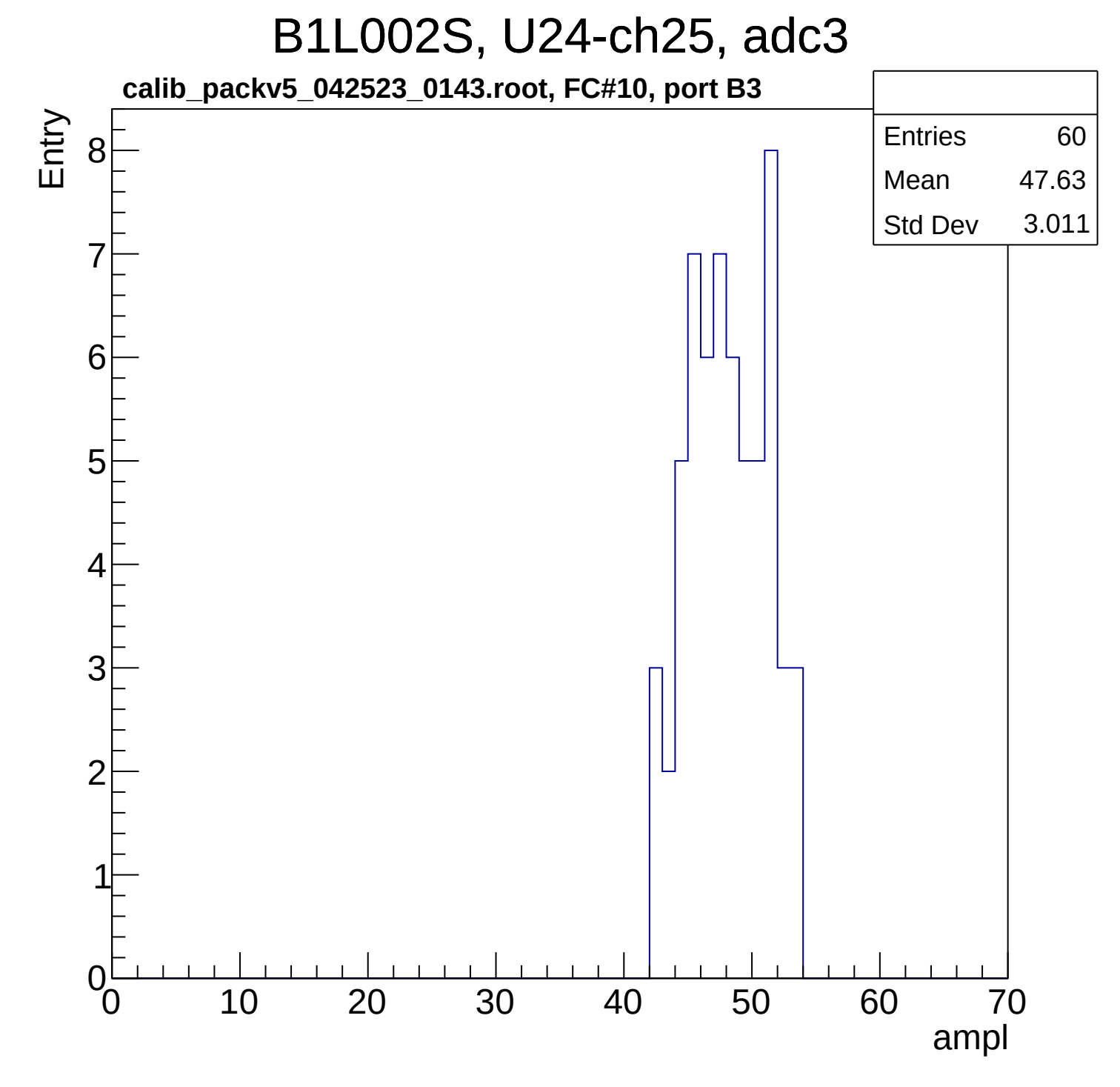

B1L002S, U24-ch24, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.47
Std Dev 13.83
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

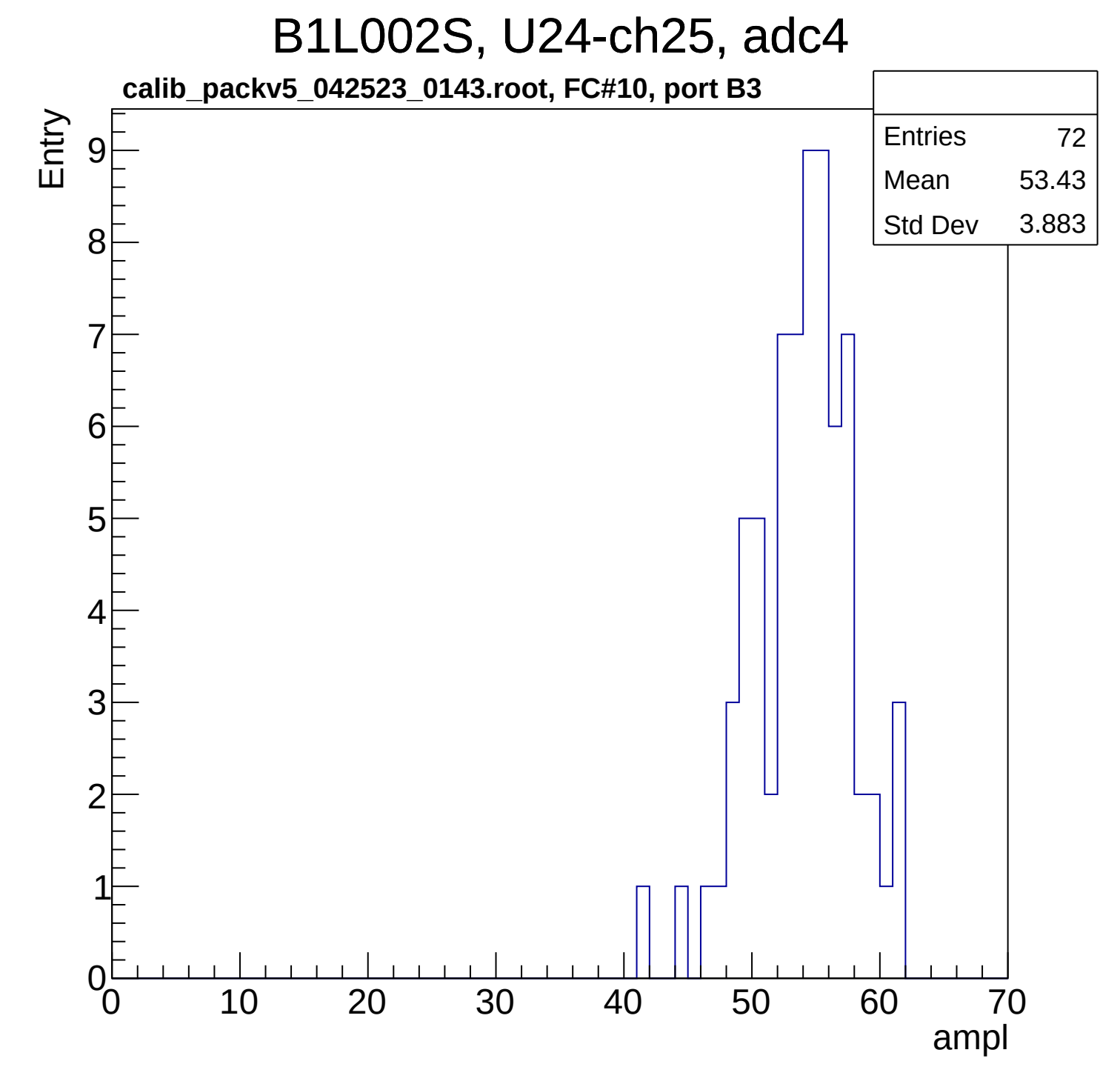

B1L002S, U24-ch24, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch25, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.25
Std Dev 2.642
Gaus mean : 28.9136
Gaus Width: 3.1480
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch25, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 85
Mean 34.74
Std Dev 3.508
Gaus mean : 35.4787
Gaus Width: 3.4308
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch25, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 41.71
Std Dev 2.959
Gaus mean : 42.3638
Gaus Width: 3.2626
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch25, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.63
Std Dev 3.011
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch25, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 53.43
Std Dev 3.883
Entry
ampl
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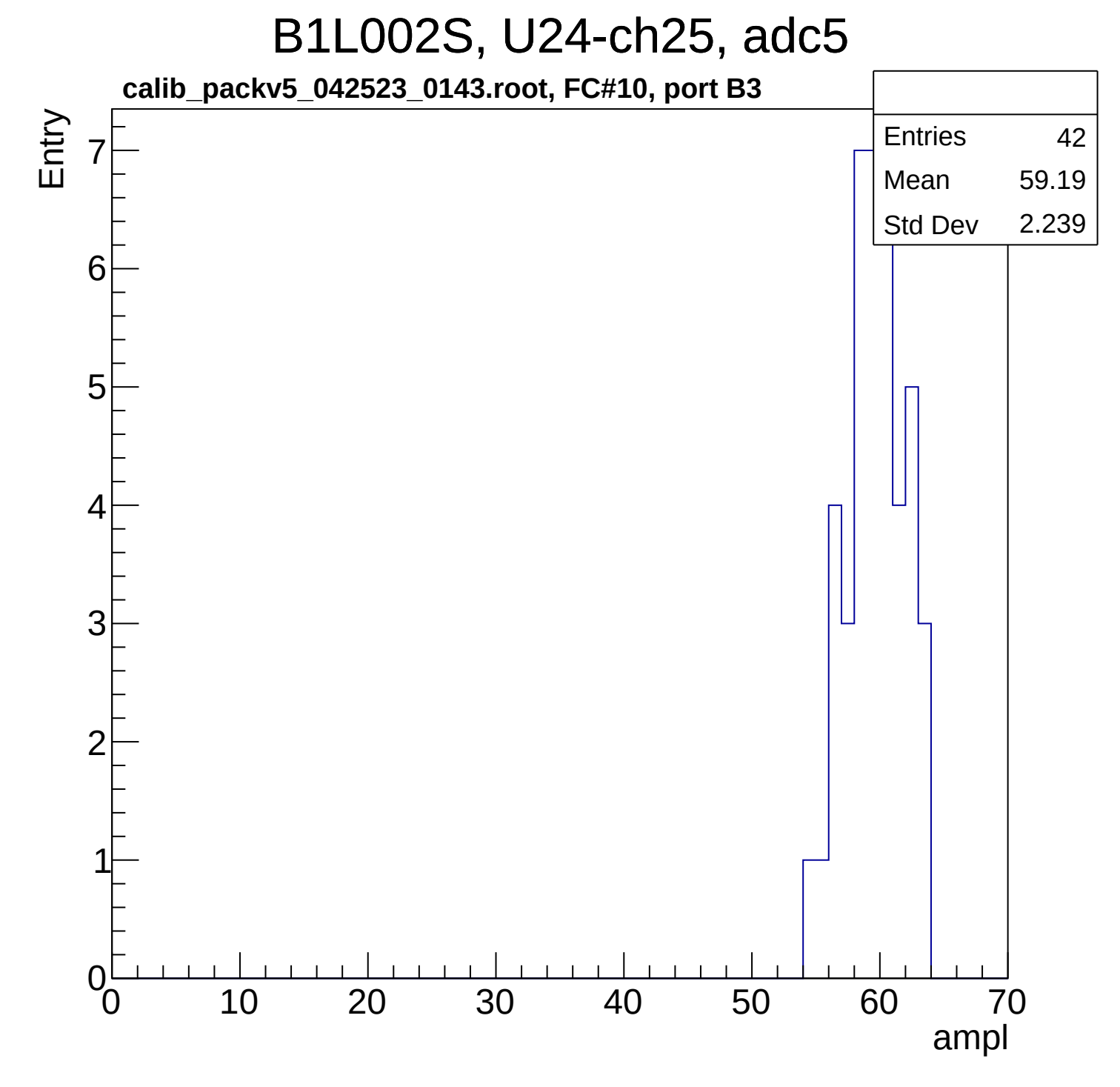
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch25, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.19
Std Dev 2.239
Entry
ampl
0
1
2
3
4
5
6
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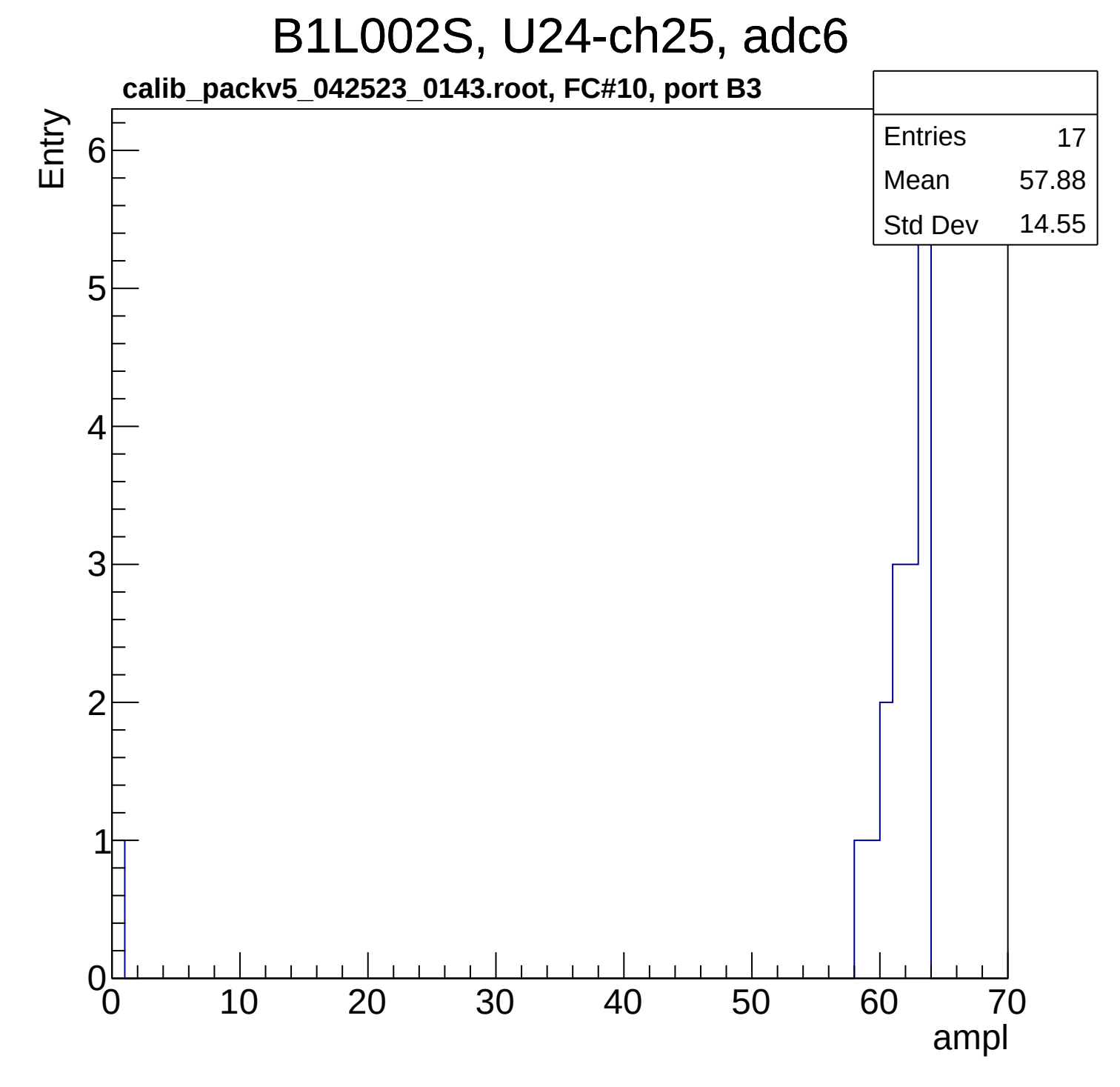
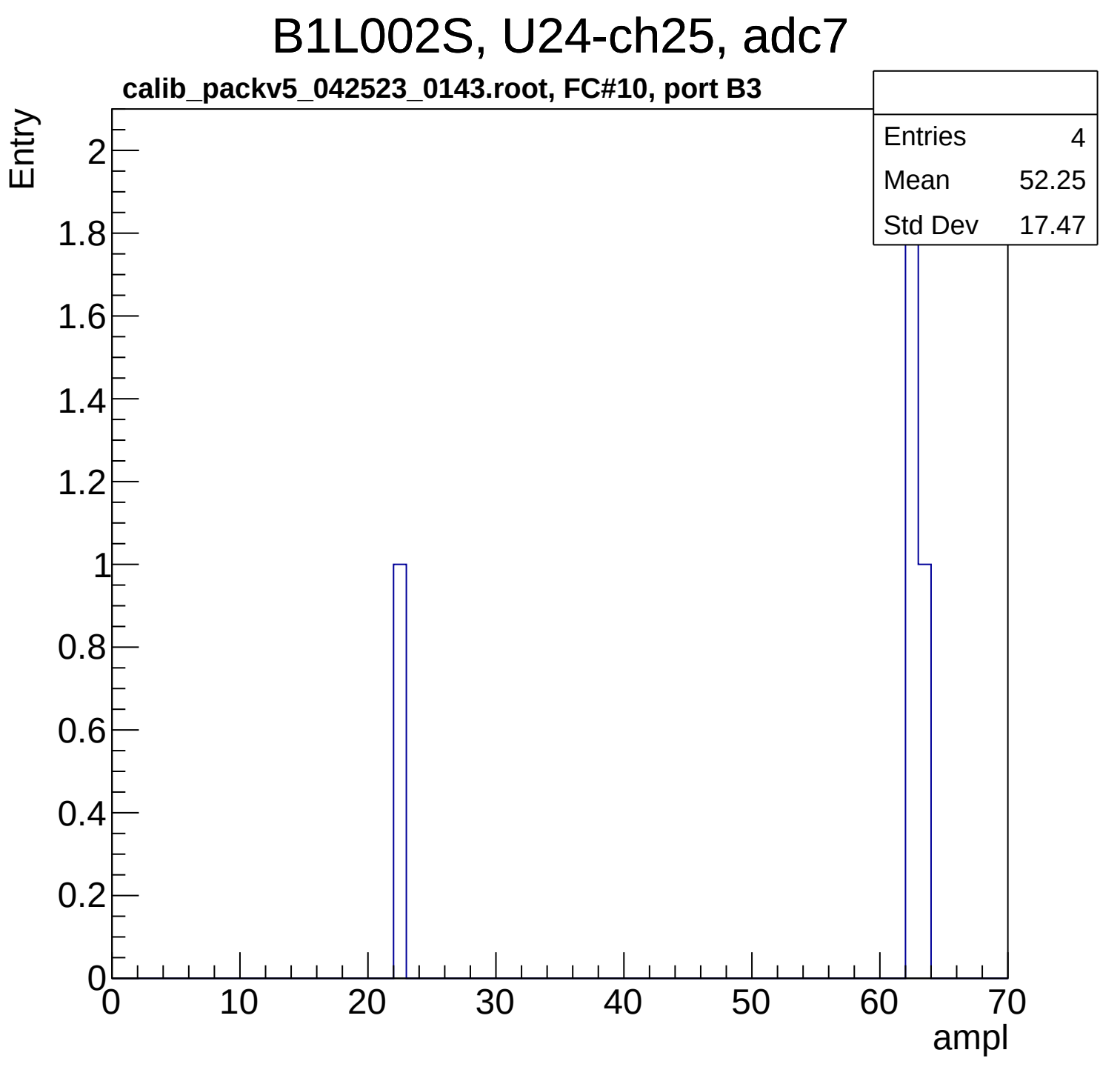
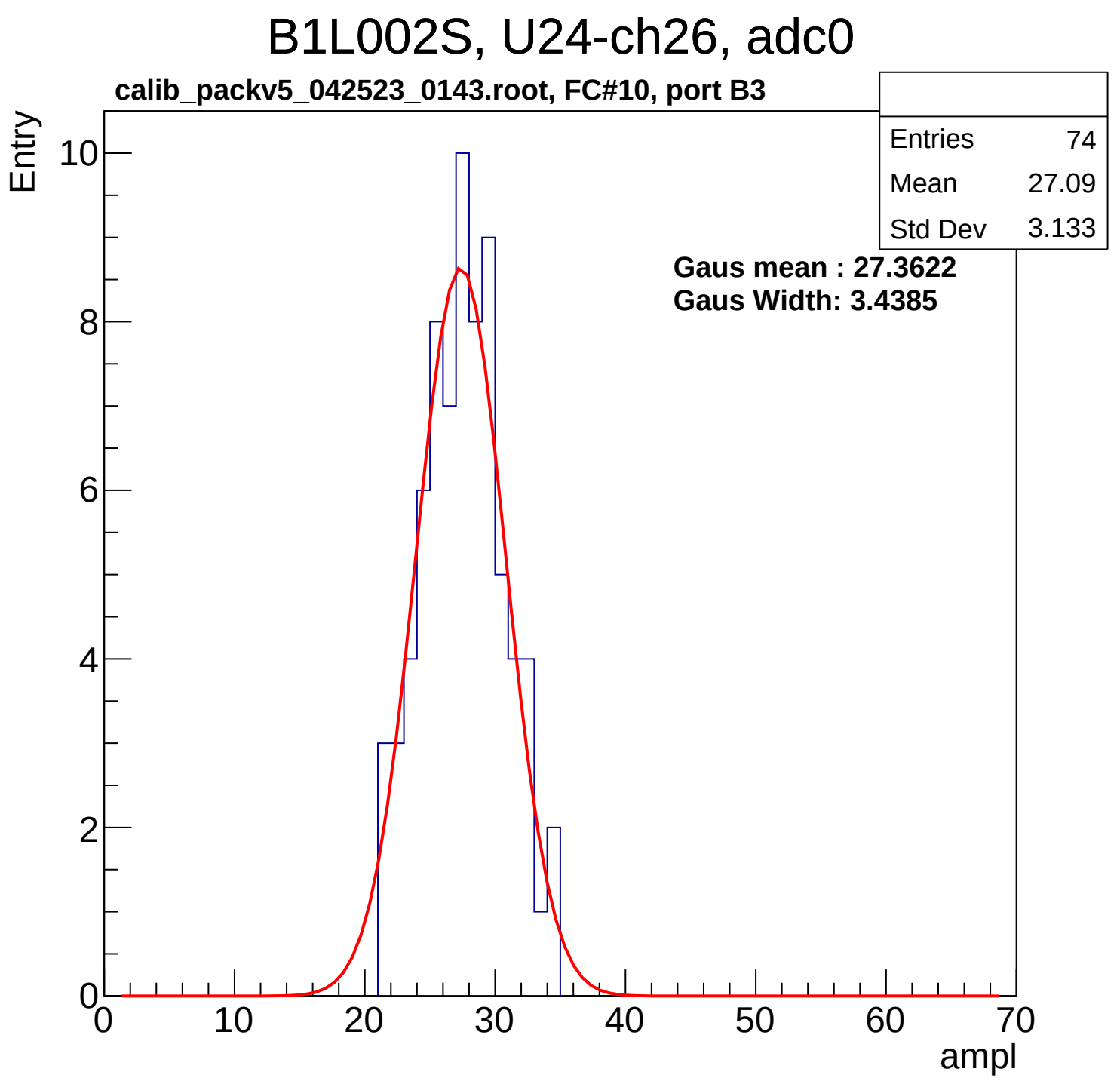
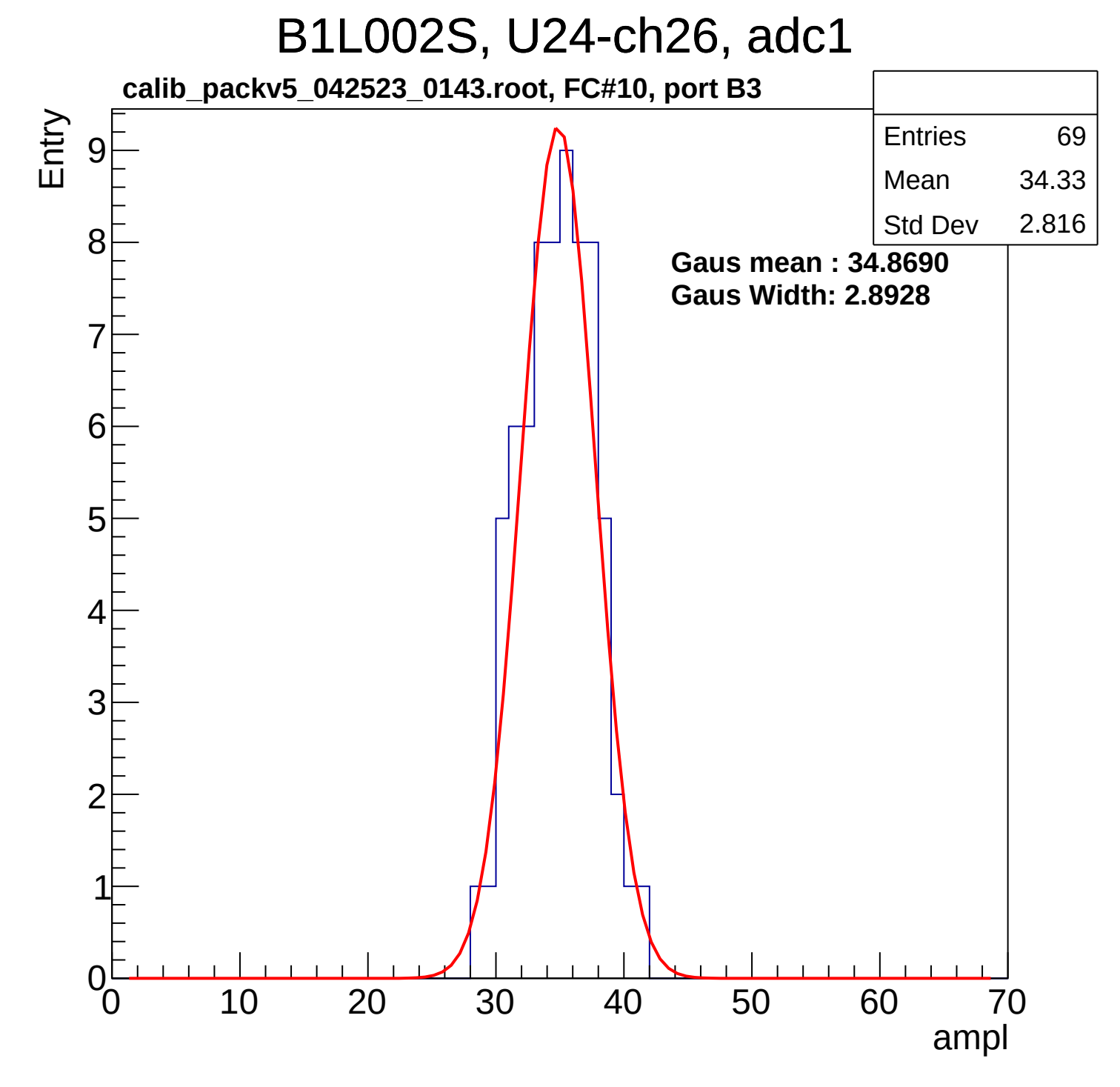
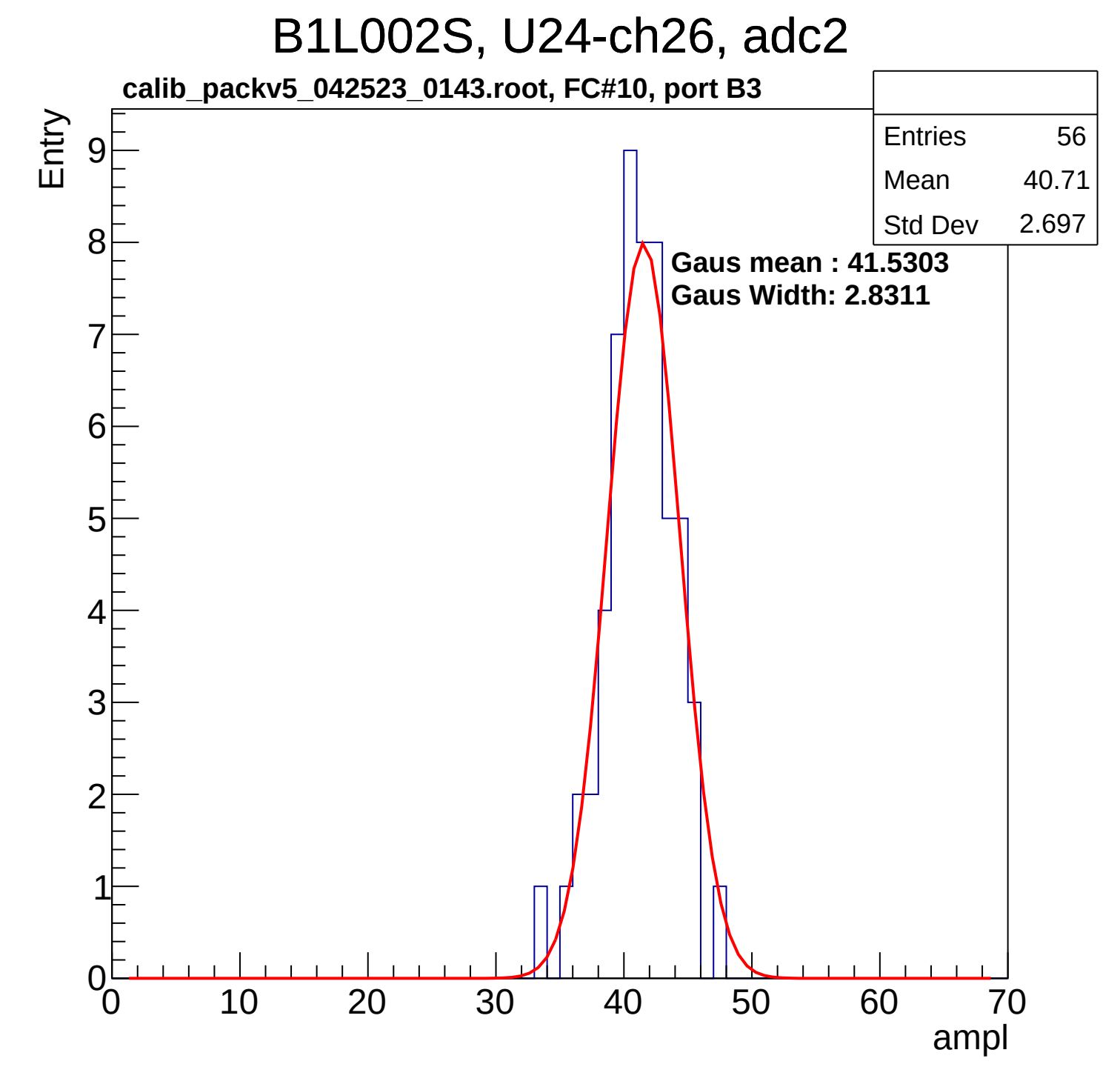
0
10
20
30
40
50
60
70

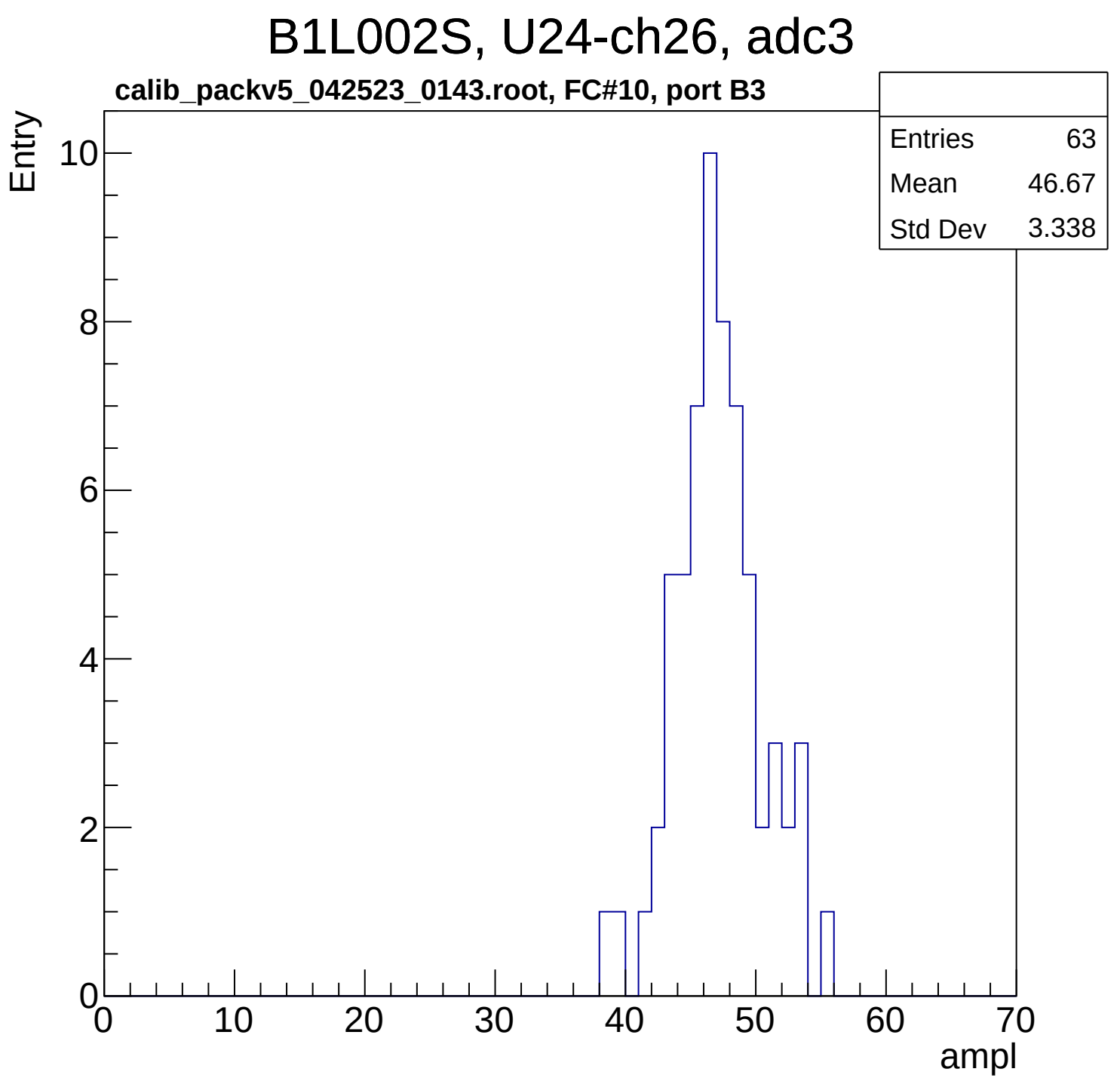

B1L002S, U24-ch25, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 57.88
Std Dev 14.55
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

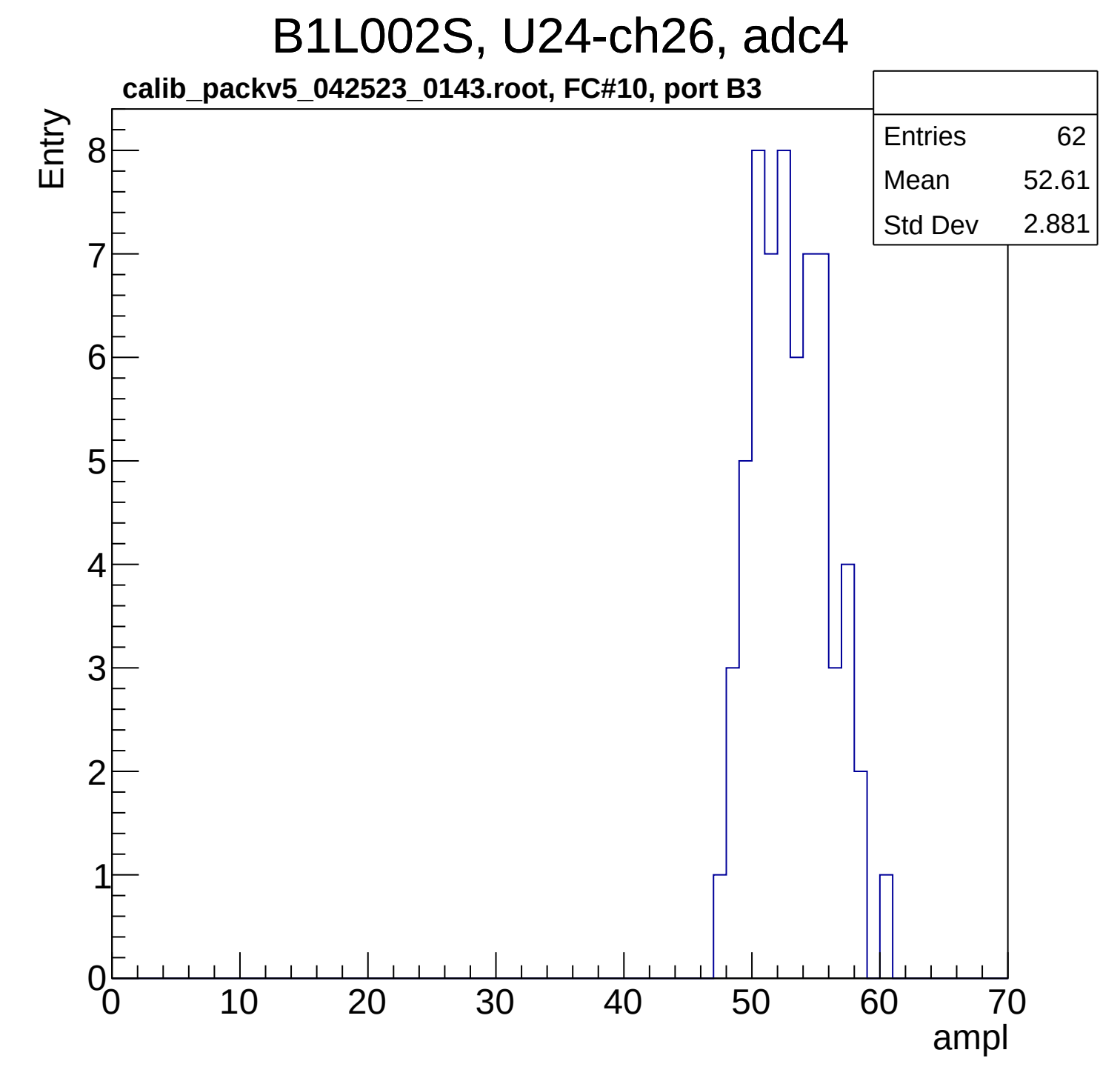

B1L002S, U24-ch25, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 52.25
Std Dev 17.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

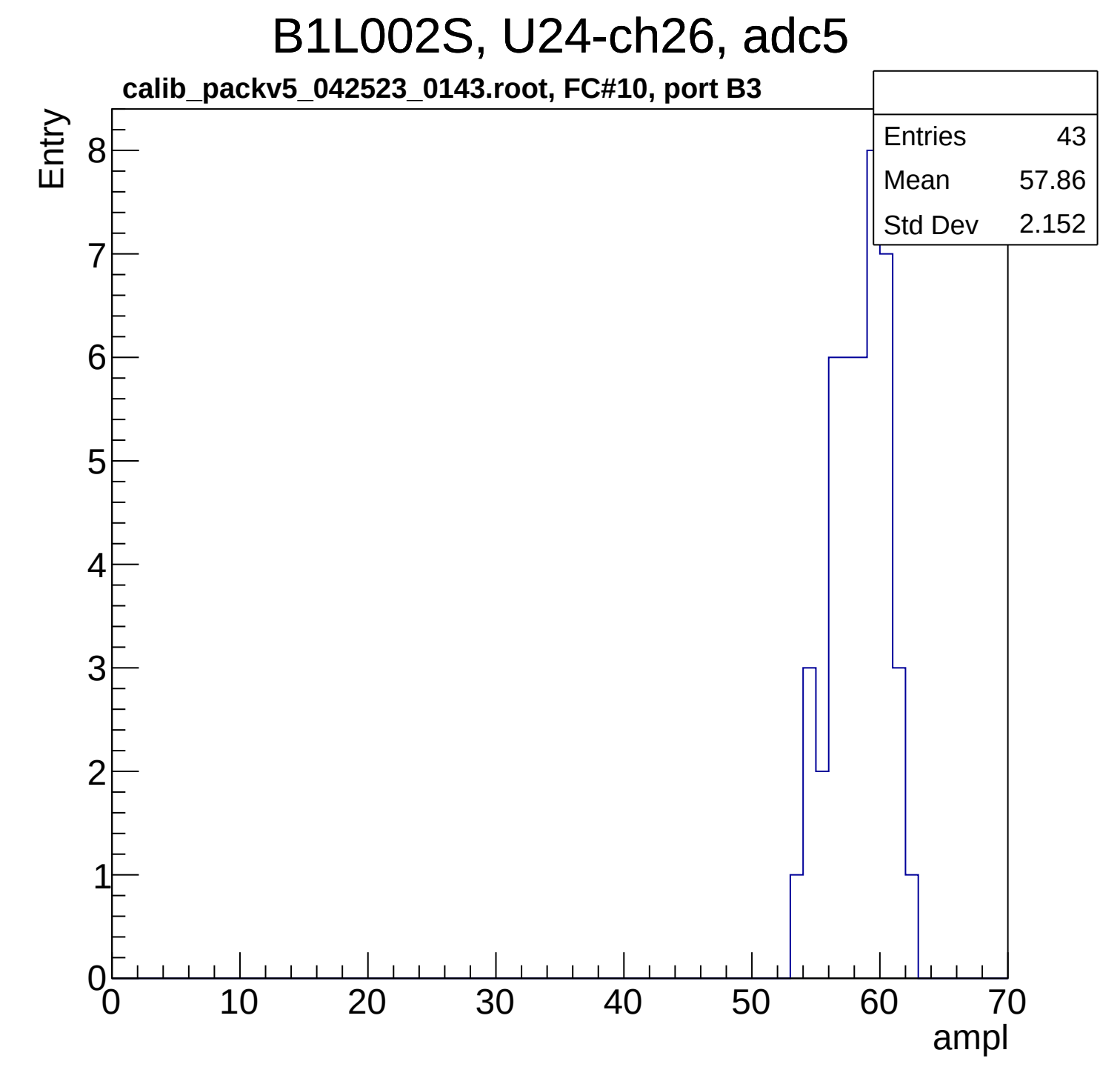

B1L002S, U24-ch26, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.09
Std Dev 3.133
Gaus mean : 27.3622
Gaus Width: 3.4385
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

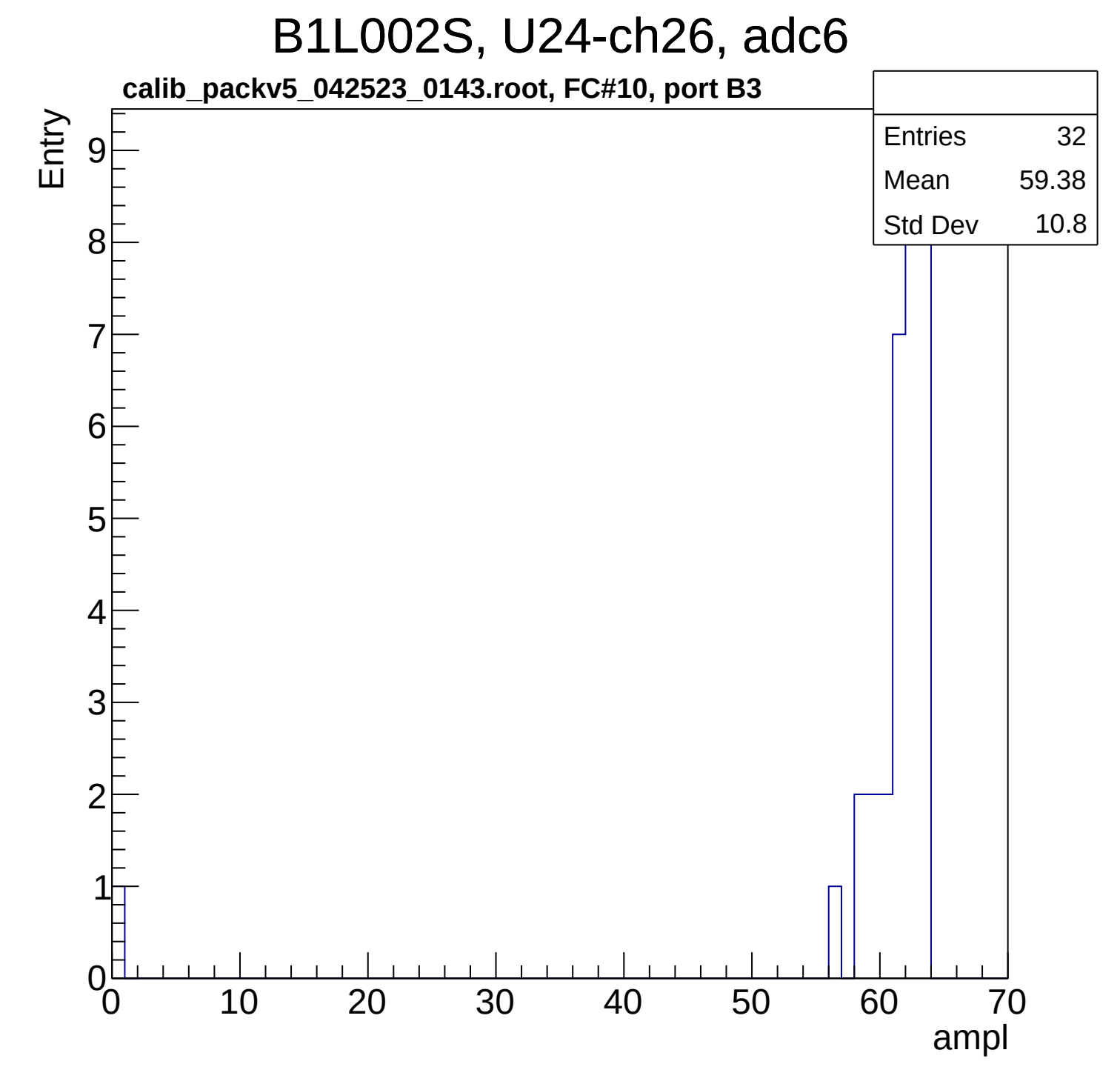

B1L002S, U24-ch26, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.33
Std Dev 2.816
Gaus mean : 34.8690
Gaus Width: 2.8928
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch26, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.71
Std Dev 2.697
Gaus mean : 41.5303
Gaus Width: 2.8311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch26, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.67
Std Dev 3.338
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch26, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 52.61
Std Dev 2.881
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch26, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 57.86
Std Dev 2.152
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch26, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 32
Mean 59.38
Std Dev 10.8
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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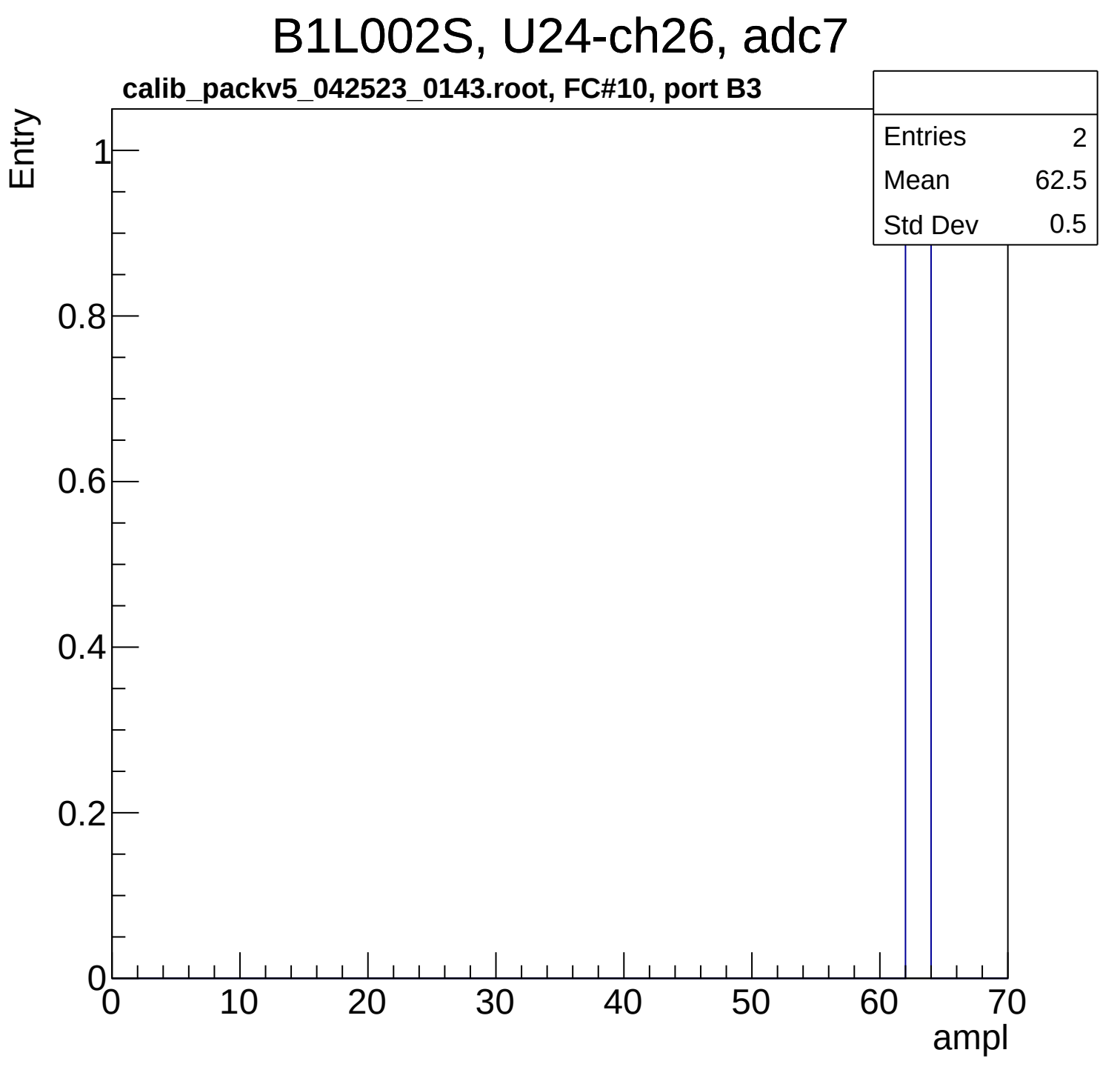
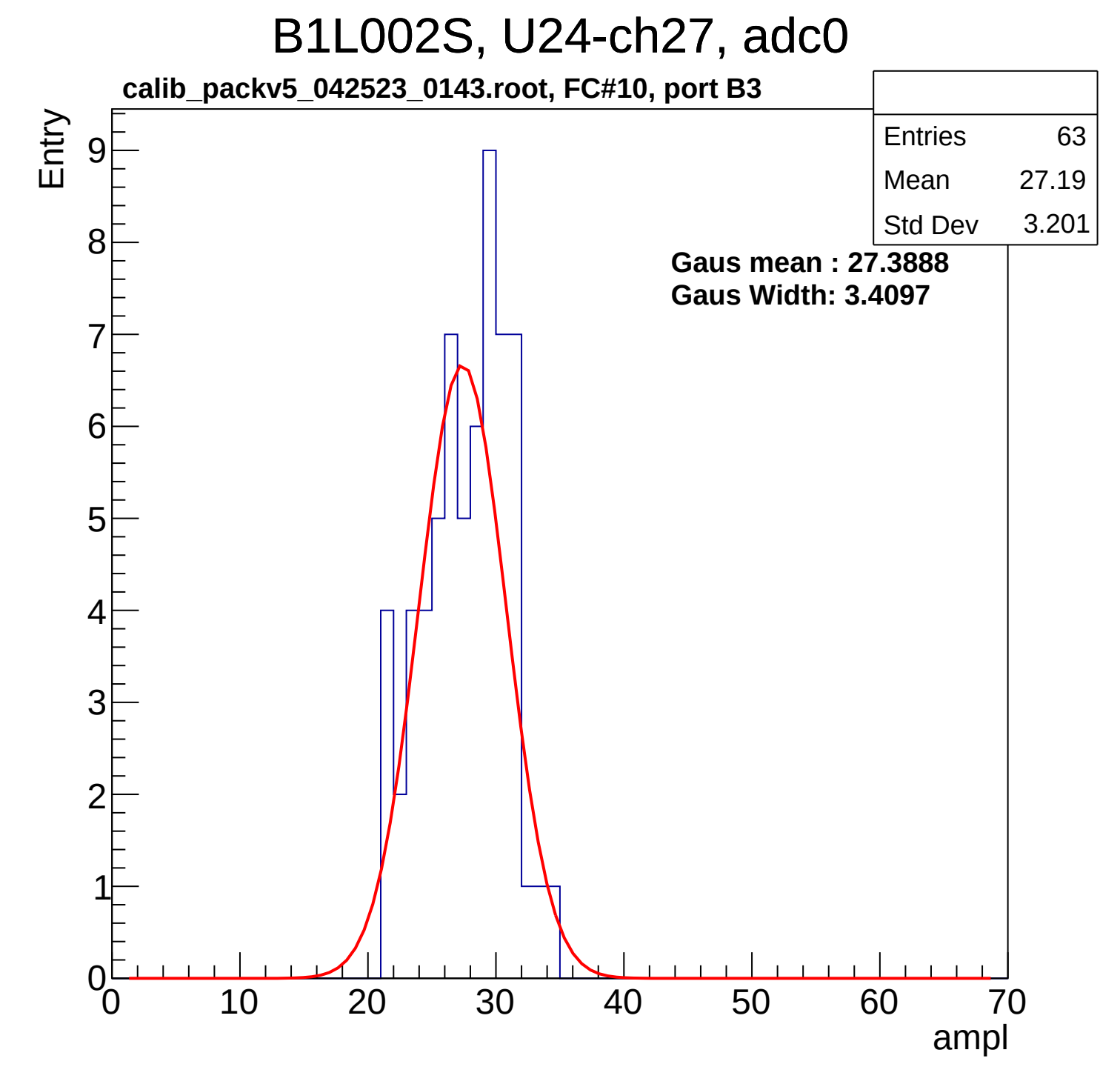
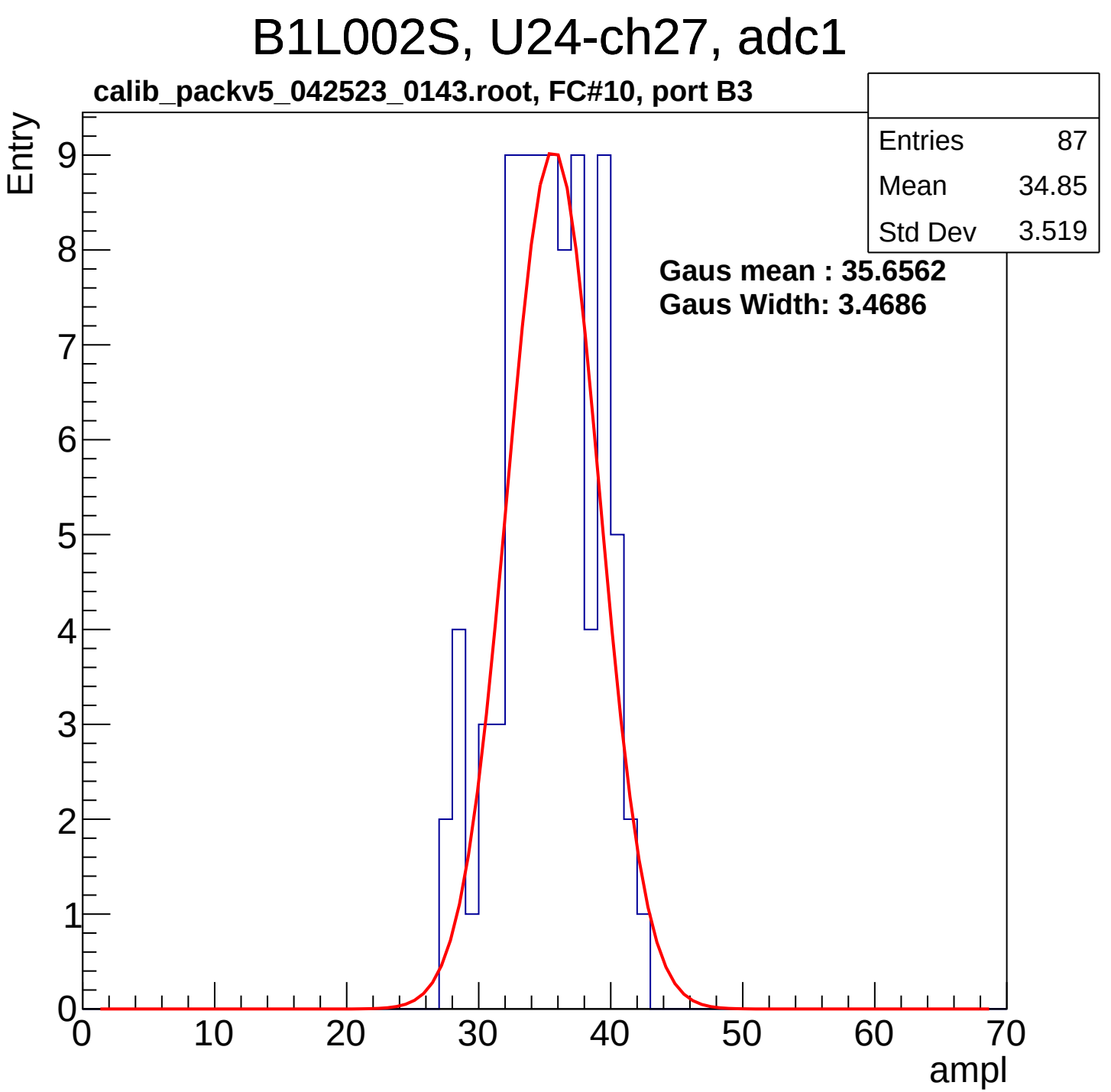

B1L002S, U24-ch26, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch27, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 27.19
Std Dev 3.201
Gaus mean : 27.3888
Gaus Width: 3.4097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch27, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 34.85
Std Dev 3.519
Gaus mean : 35.6562
Gaus Width: 3.4686
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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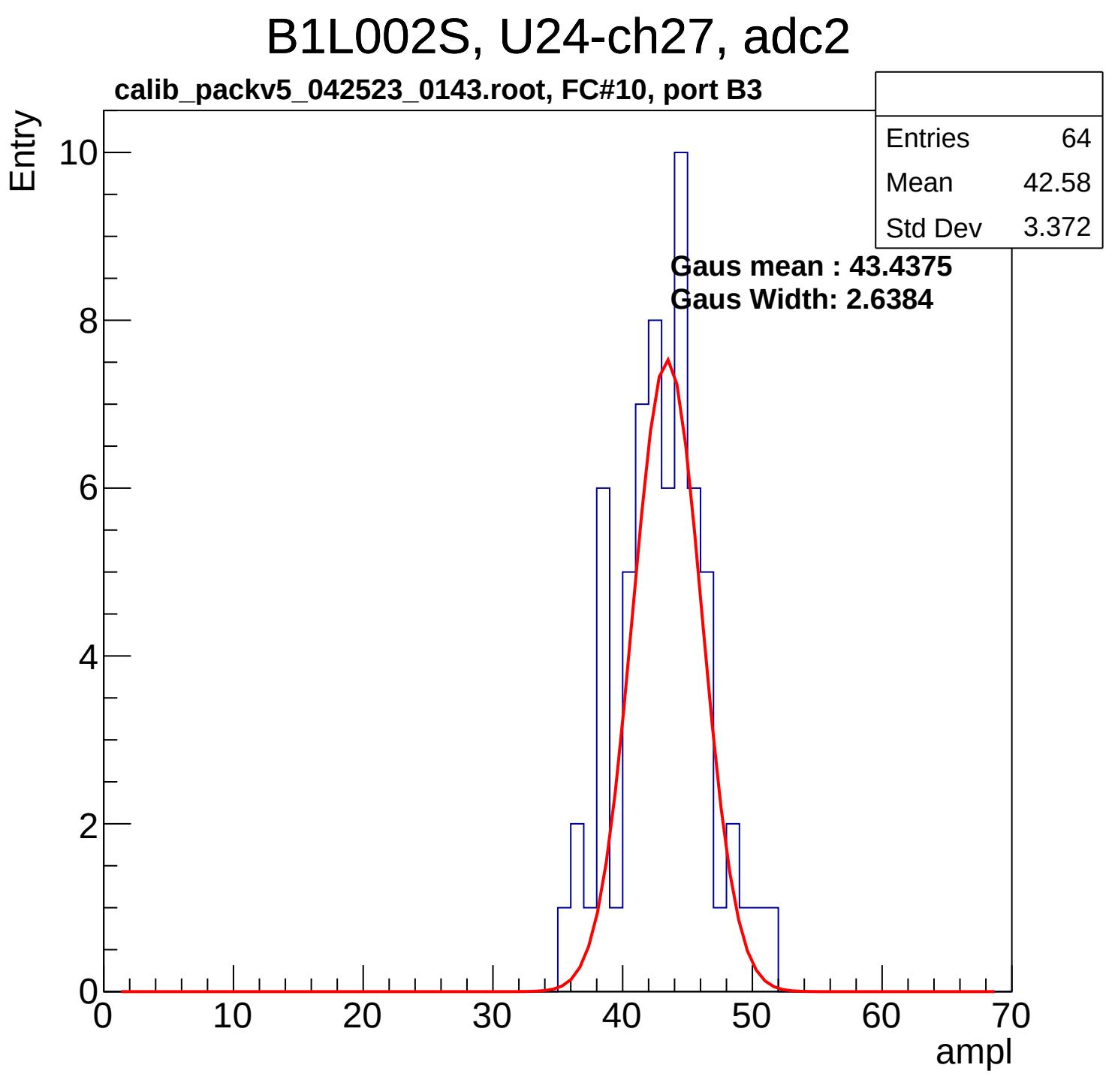
B1L002S, U24-ch27, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 42.58
Std Dev 3.372
Gaus mean : 43.4375
Gaus Width: 2.6384
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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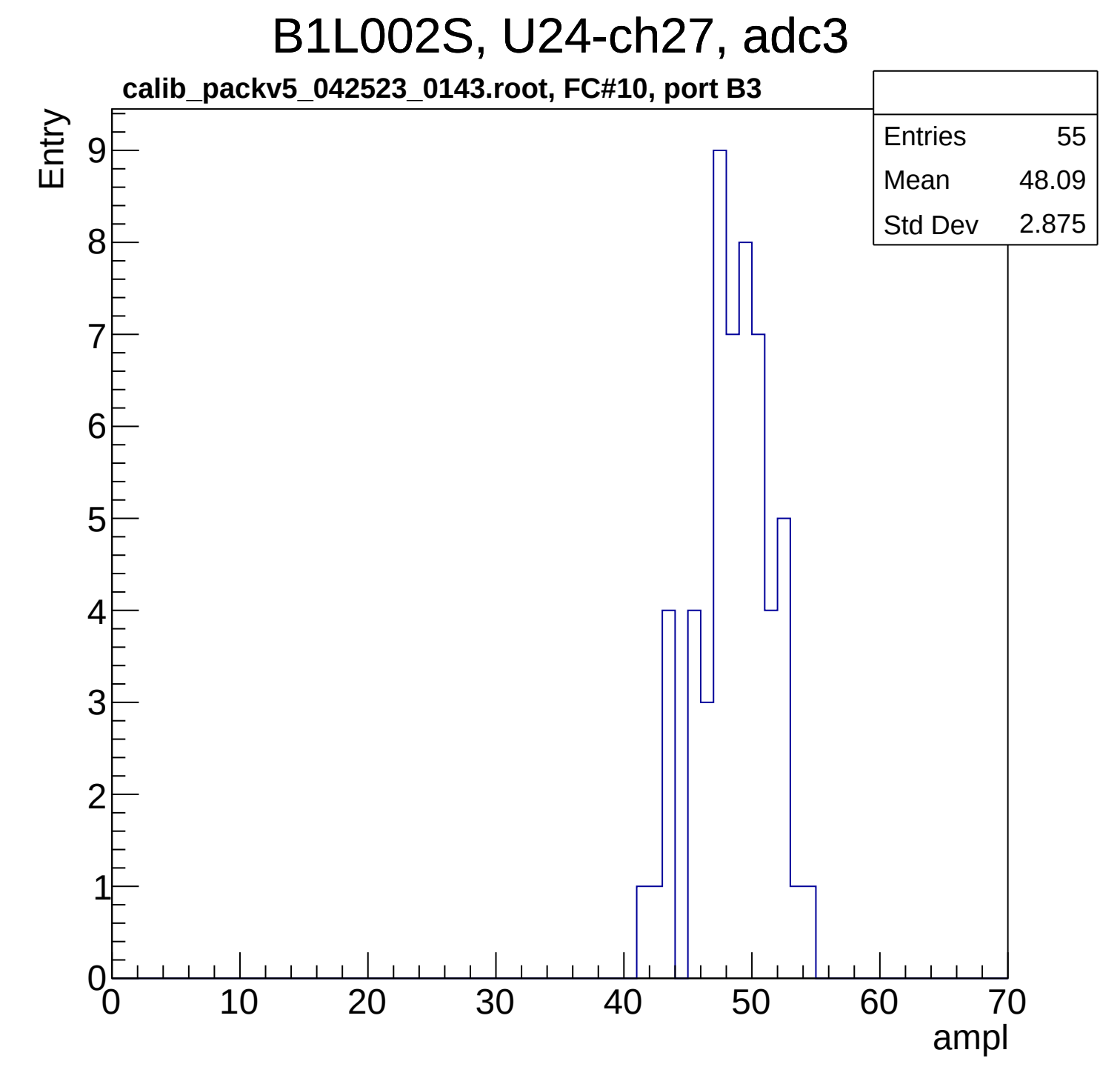
B1L002S, U24-ch27, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.09
Std Dev 2.875
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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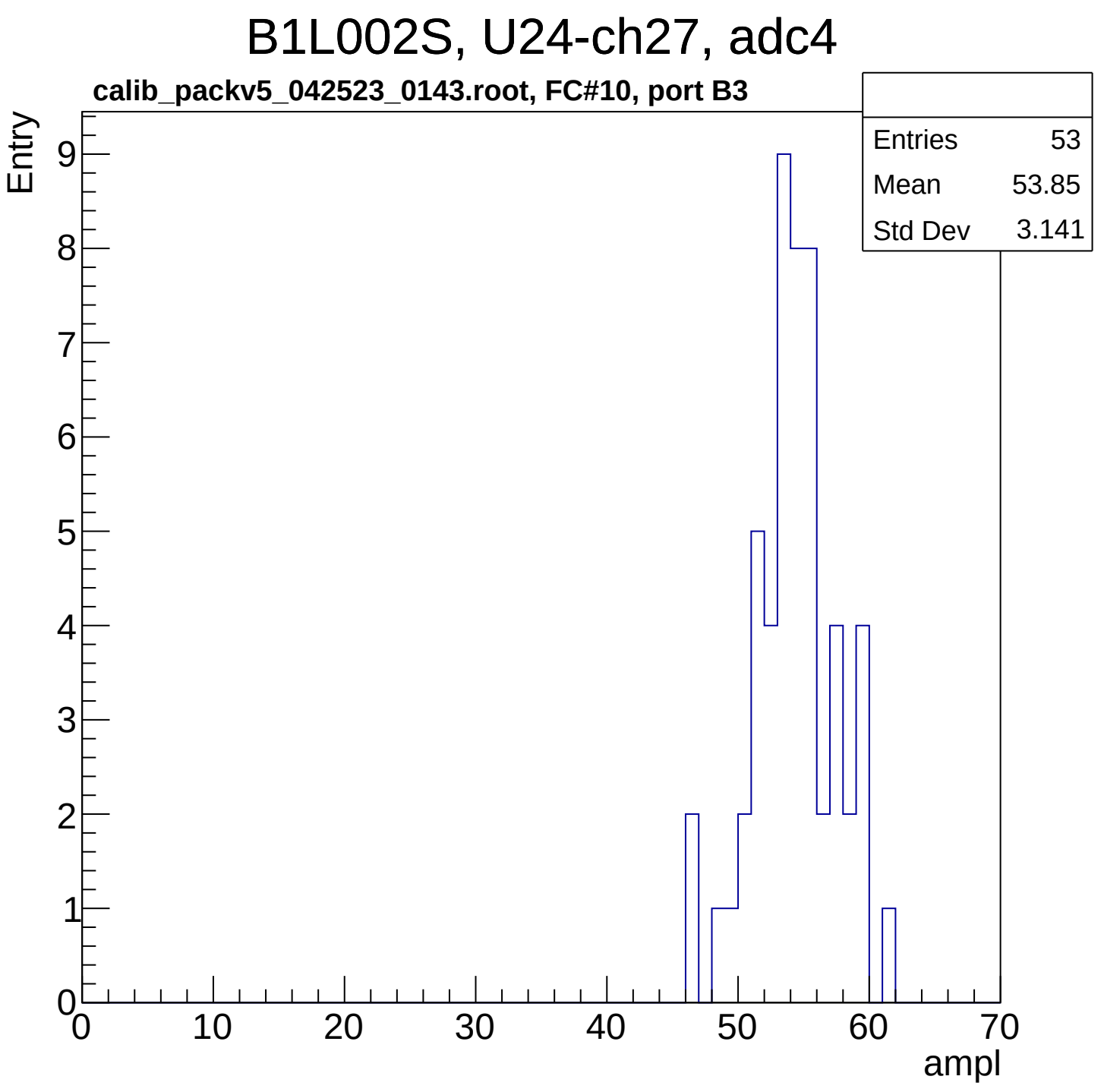
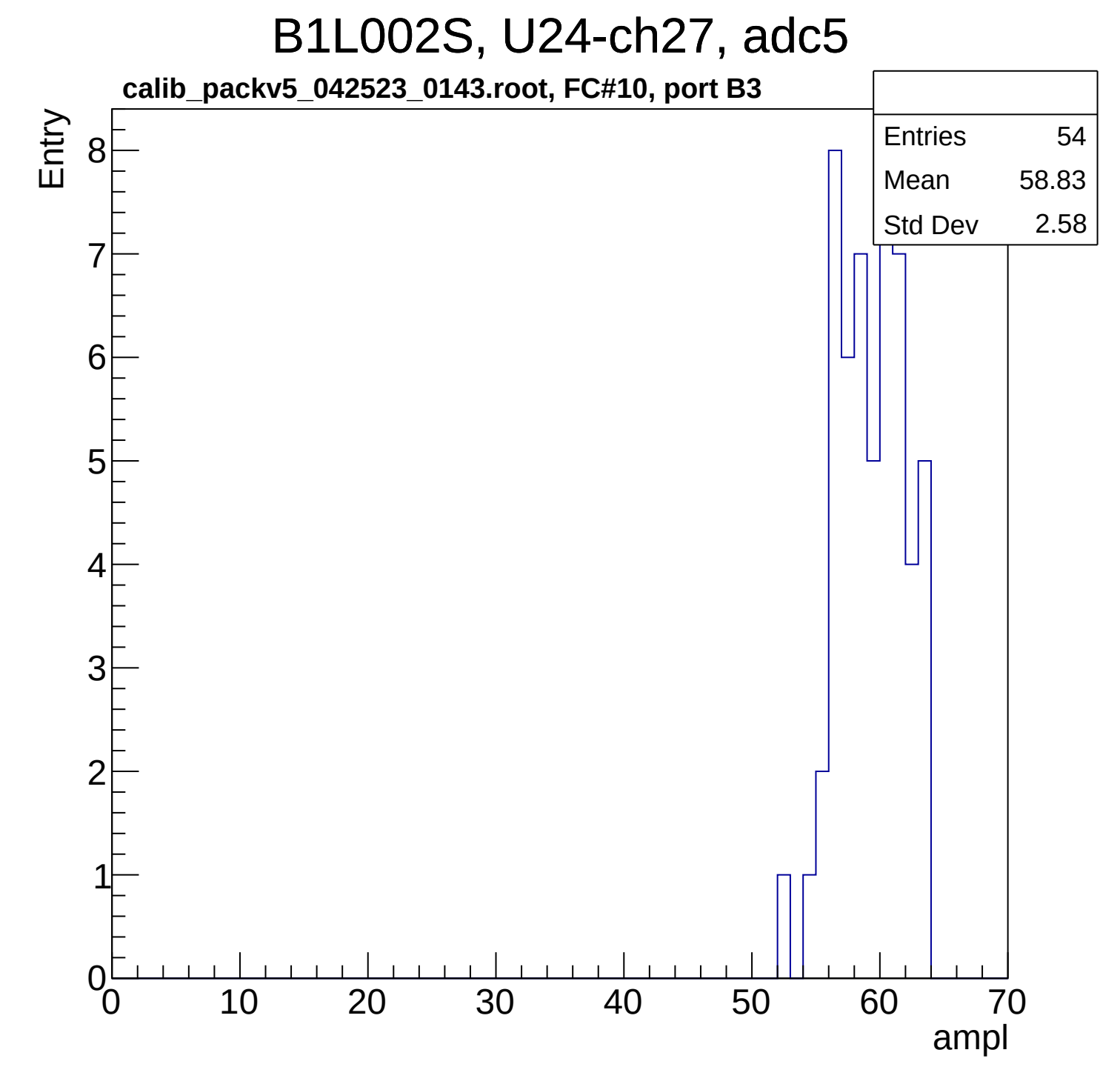

B1L002S, U24-ch27, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.85
Std Dev 3.141
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch27, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.83
Std Dev 2.58
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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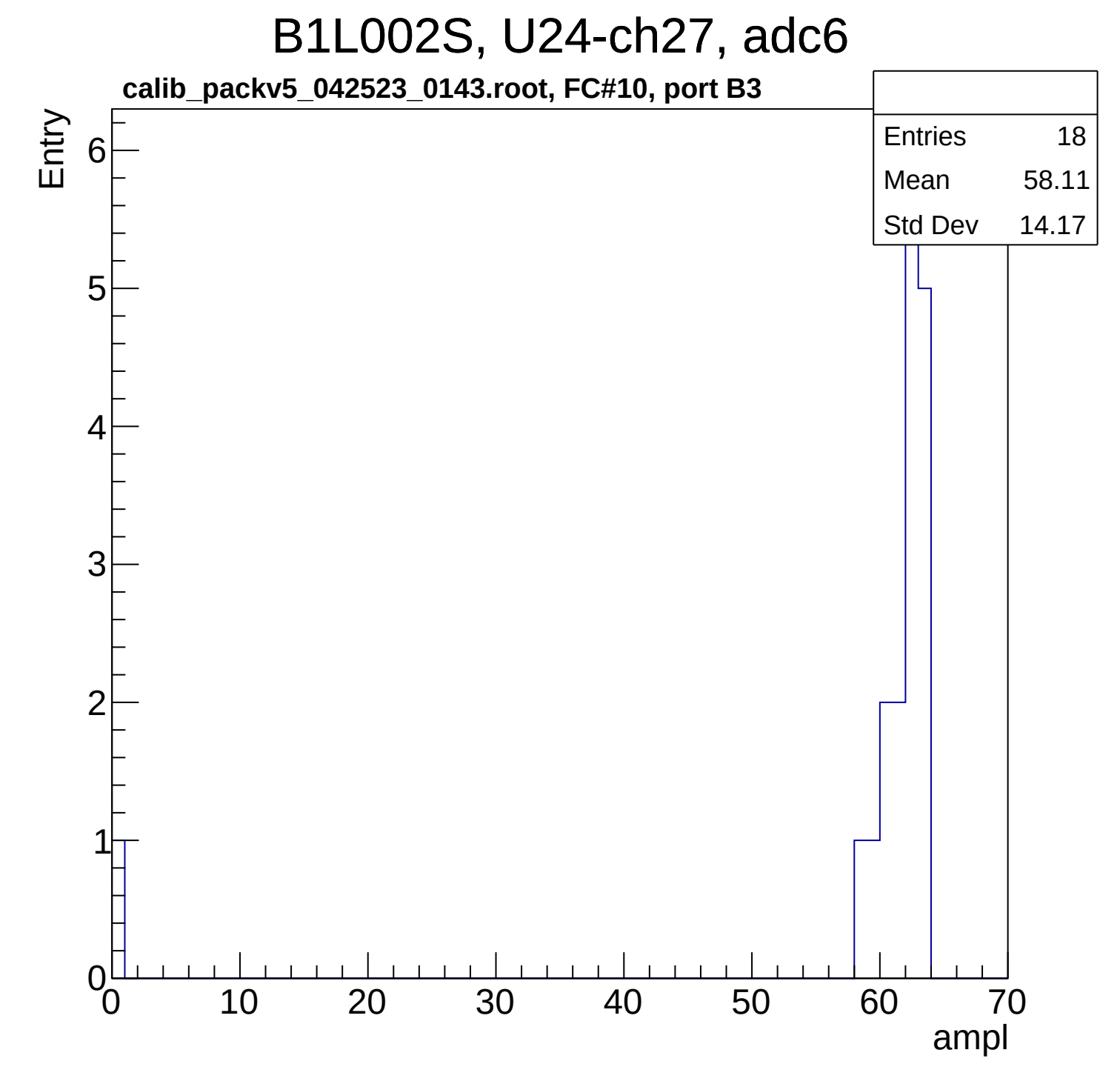
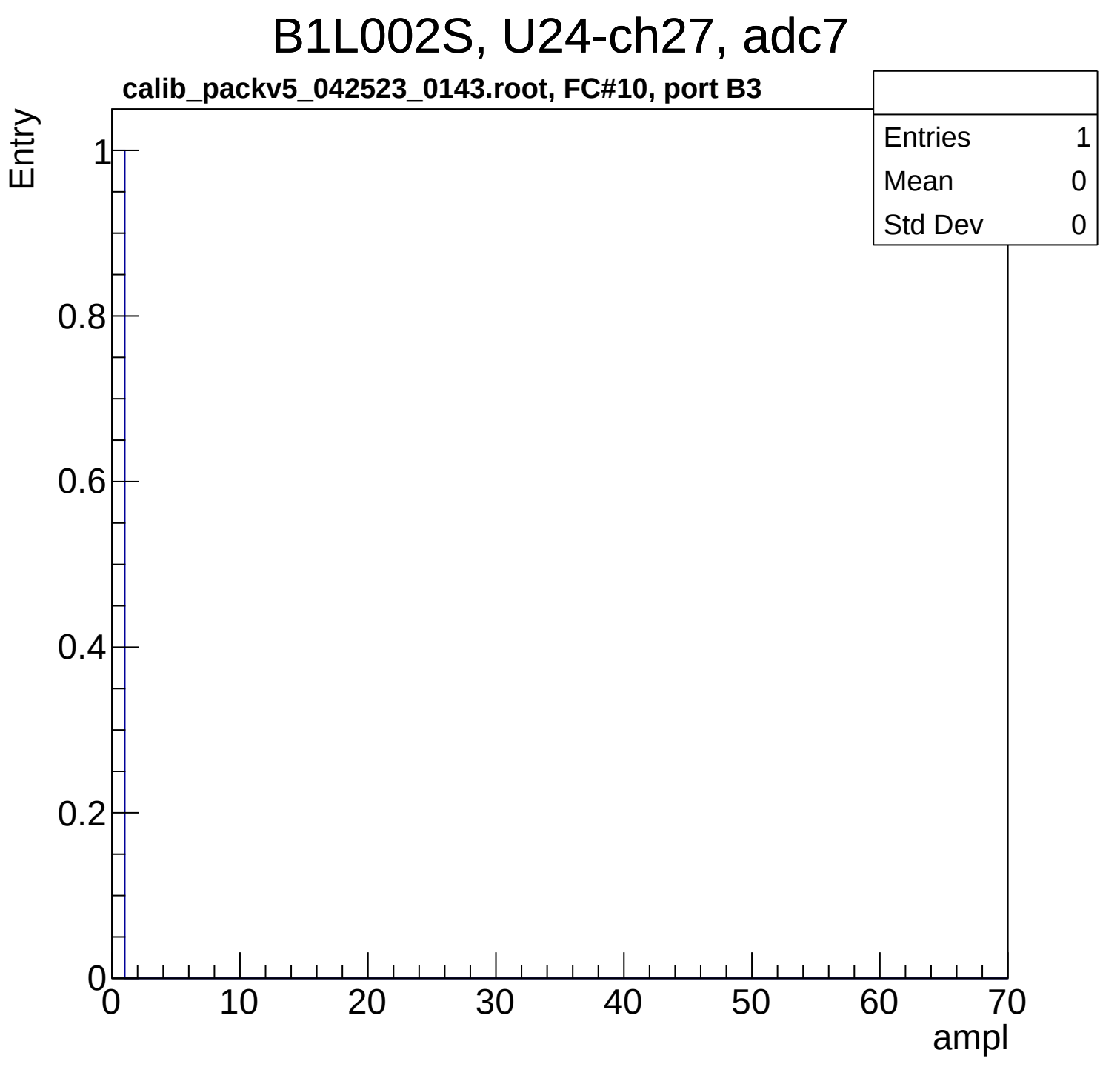
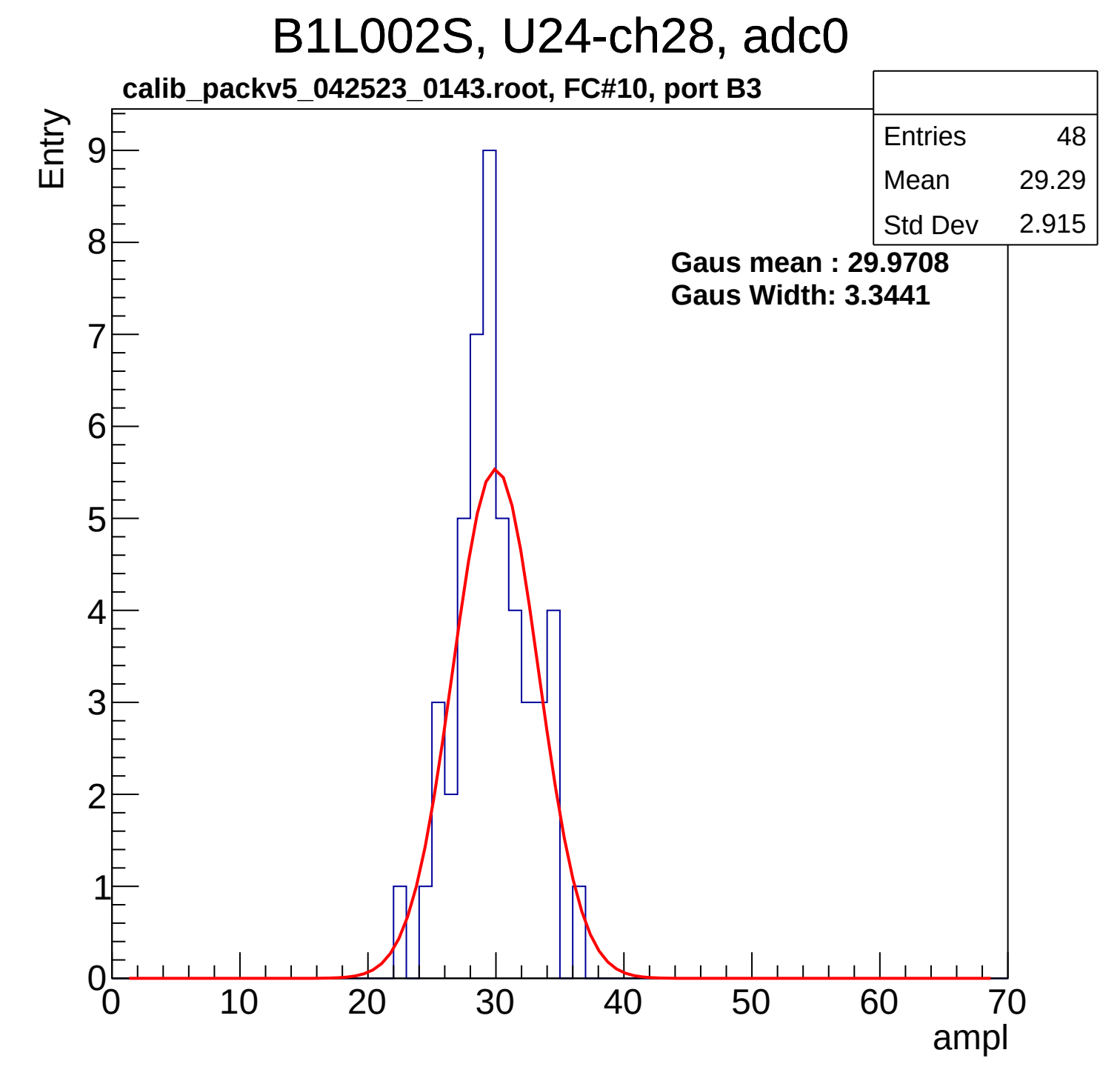
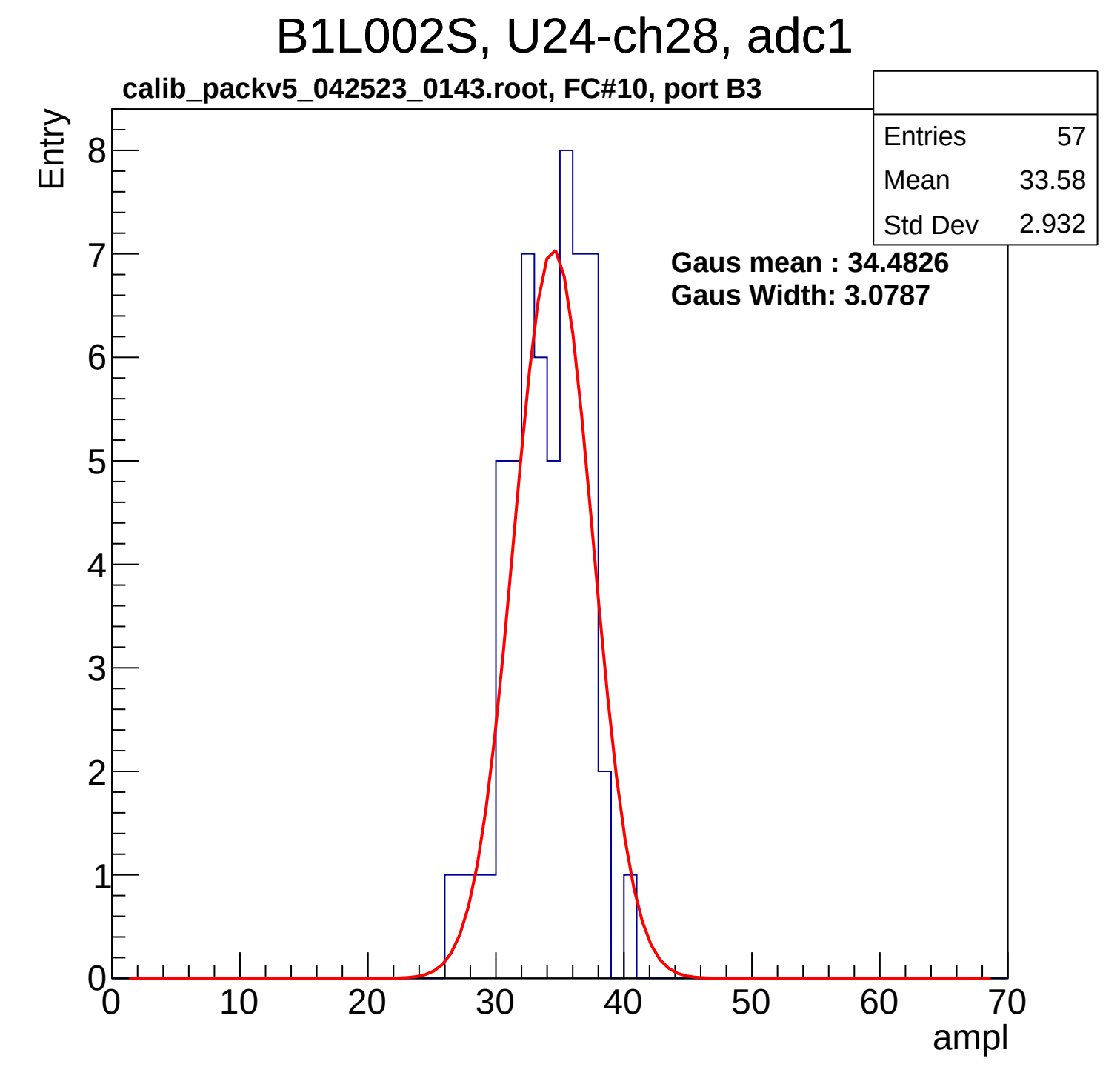
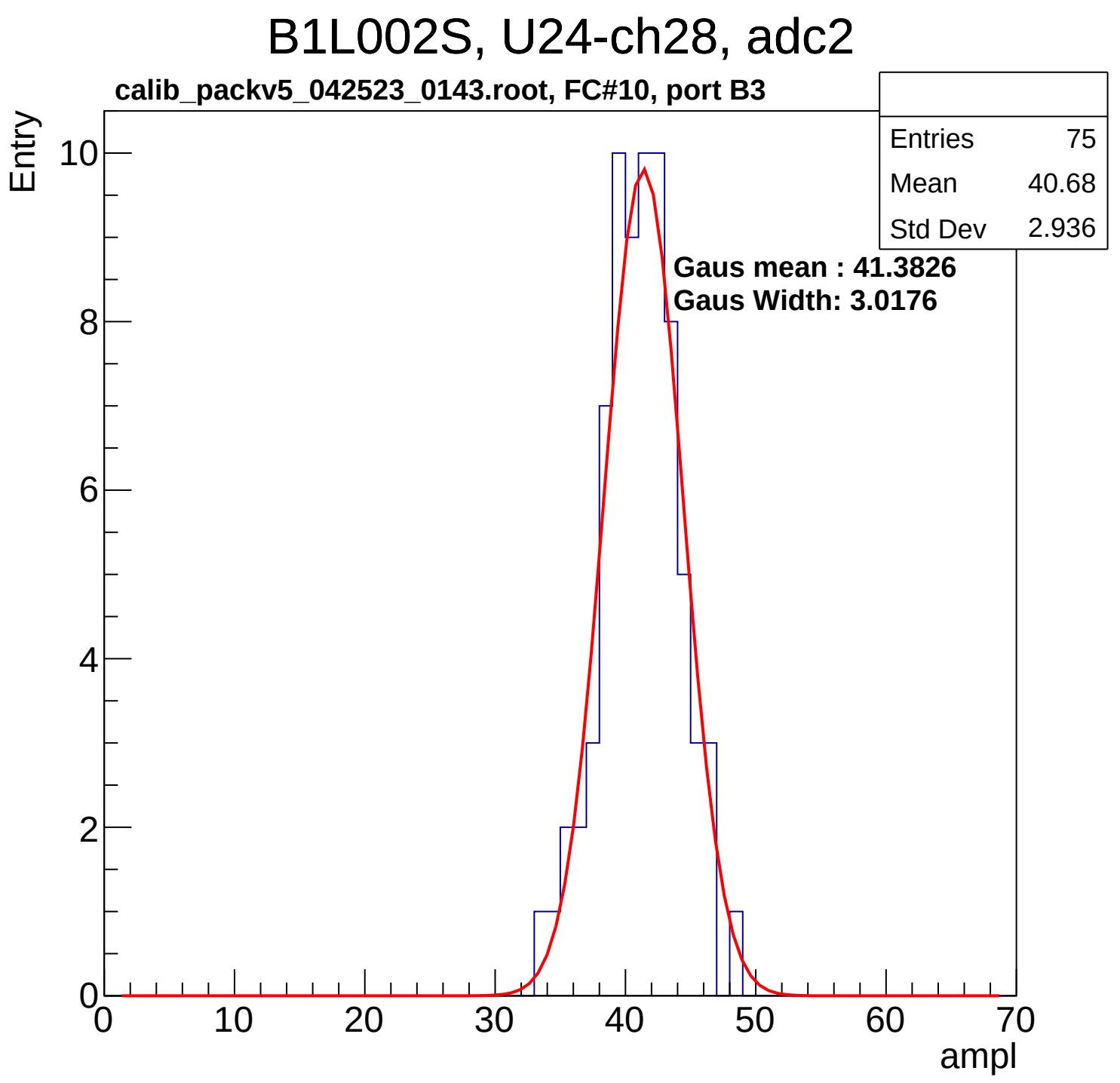
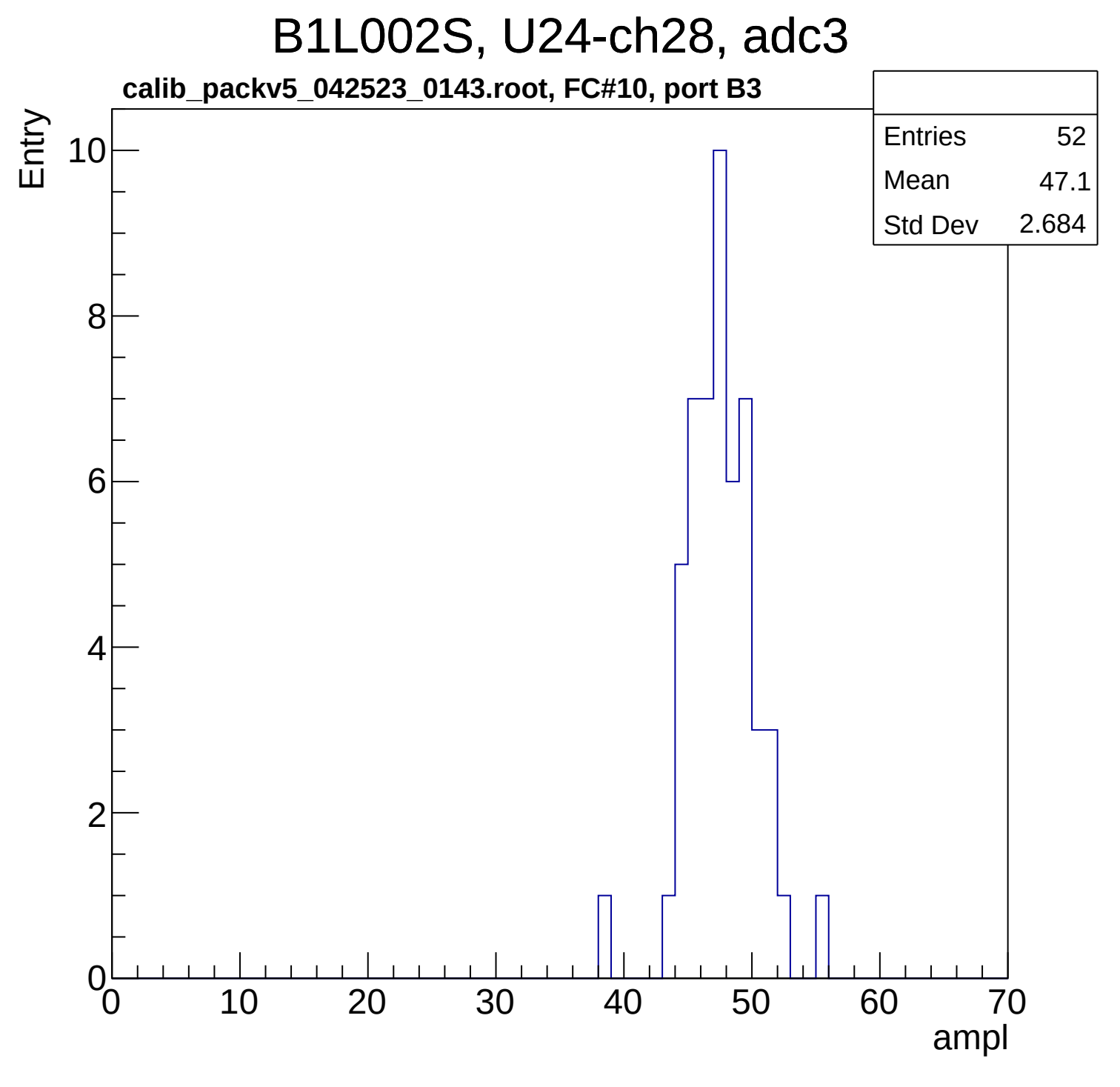

B1L002S, U24-ch27, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 58.11
Std Dev 14.17
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

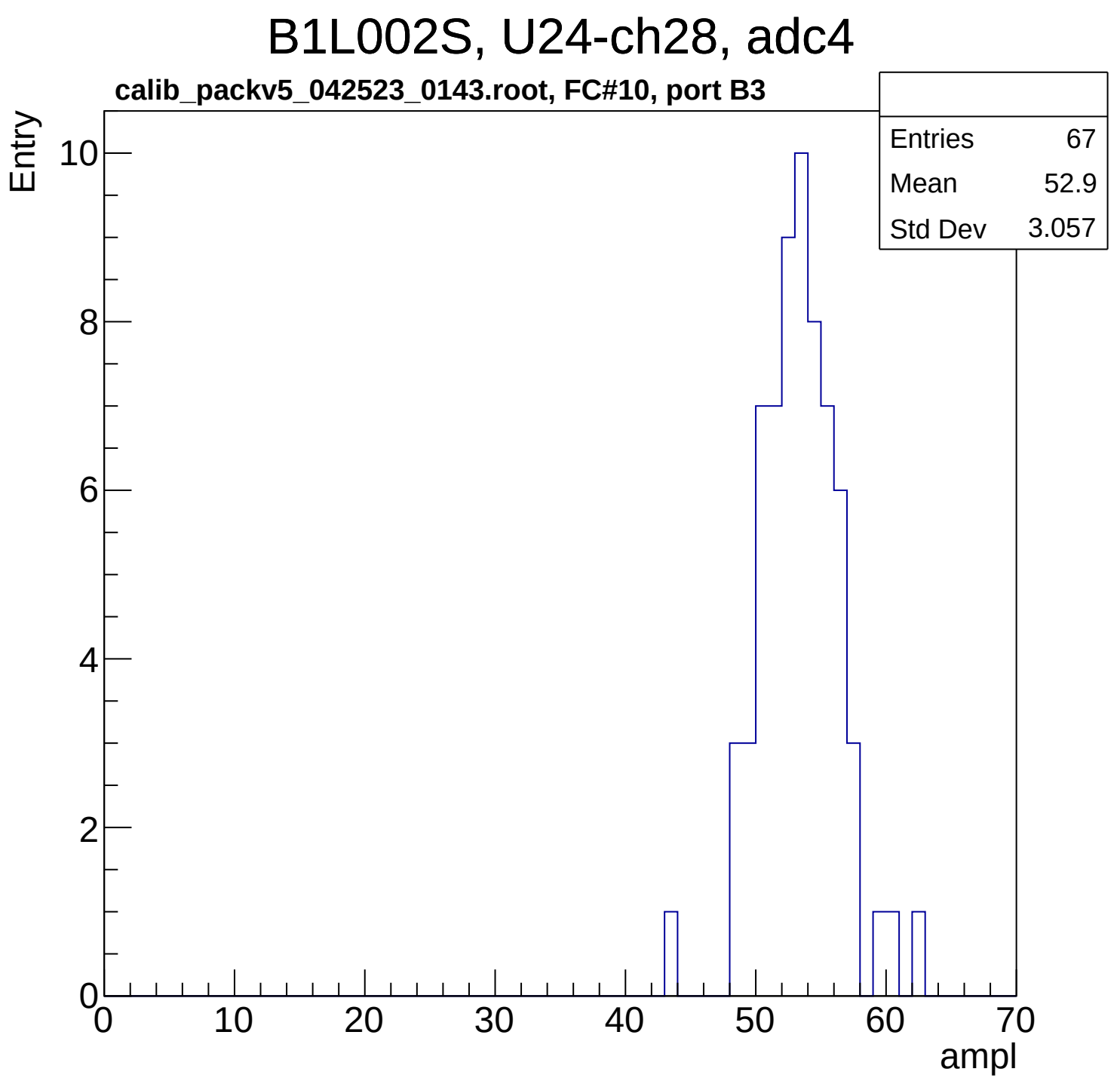

B1L002S, U24-ch27, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

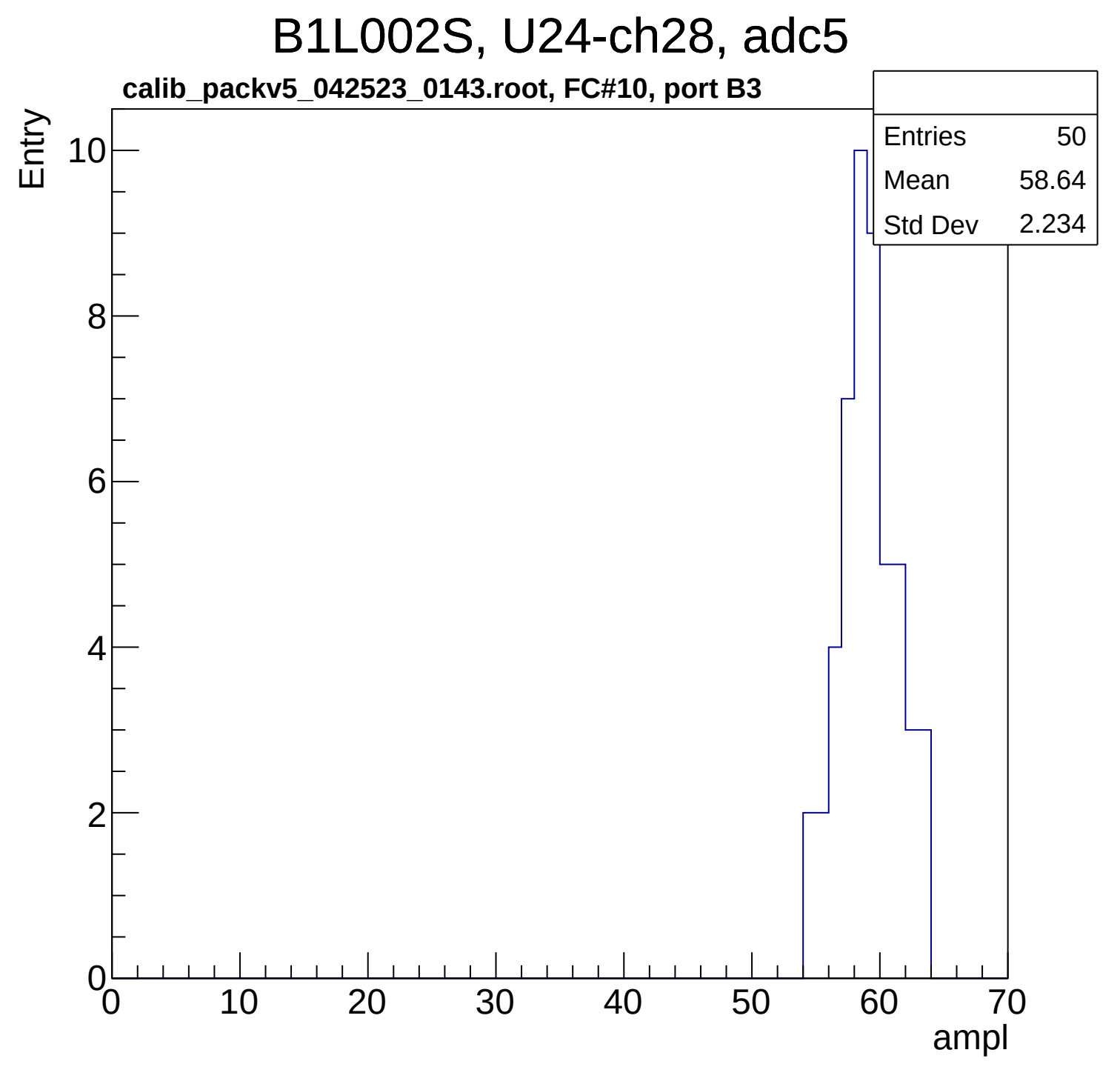

B1L002S, U24-ch28, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 29.29
Std Dev 2.915
Gaus mean : 29.9708
Gaus Width: 3.3441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

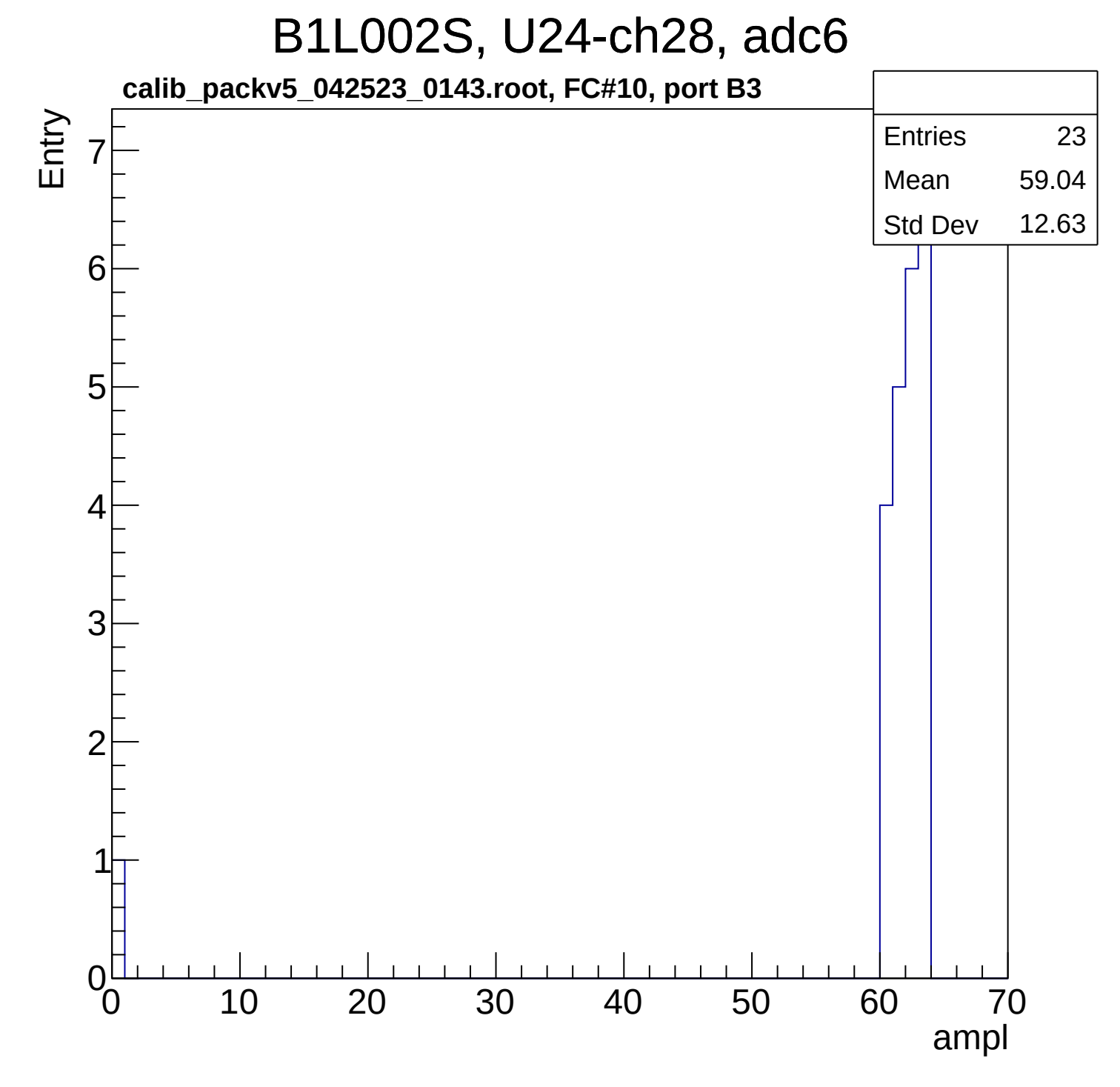

B1L002S, U24-ch28, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 33.58
Std Dev 2.932
Gaus mean : 34.4826
Gaus Width: 3.0787
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

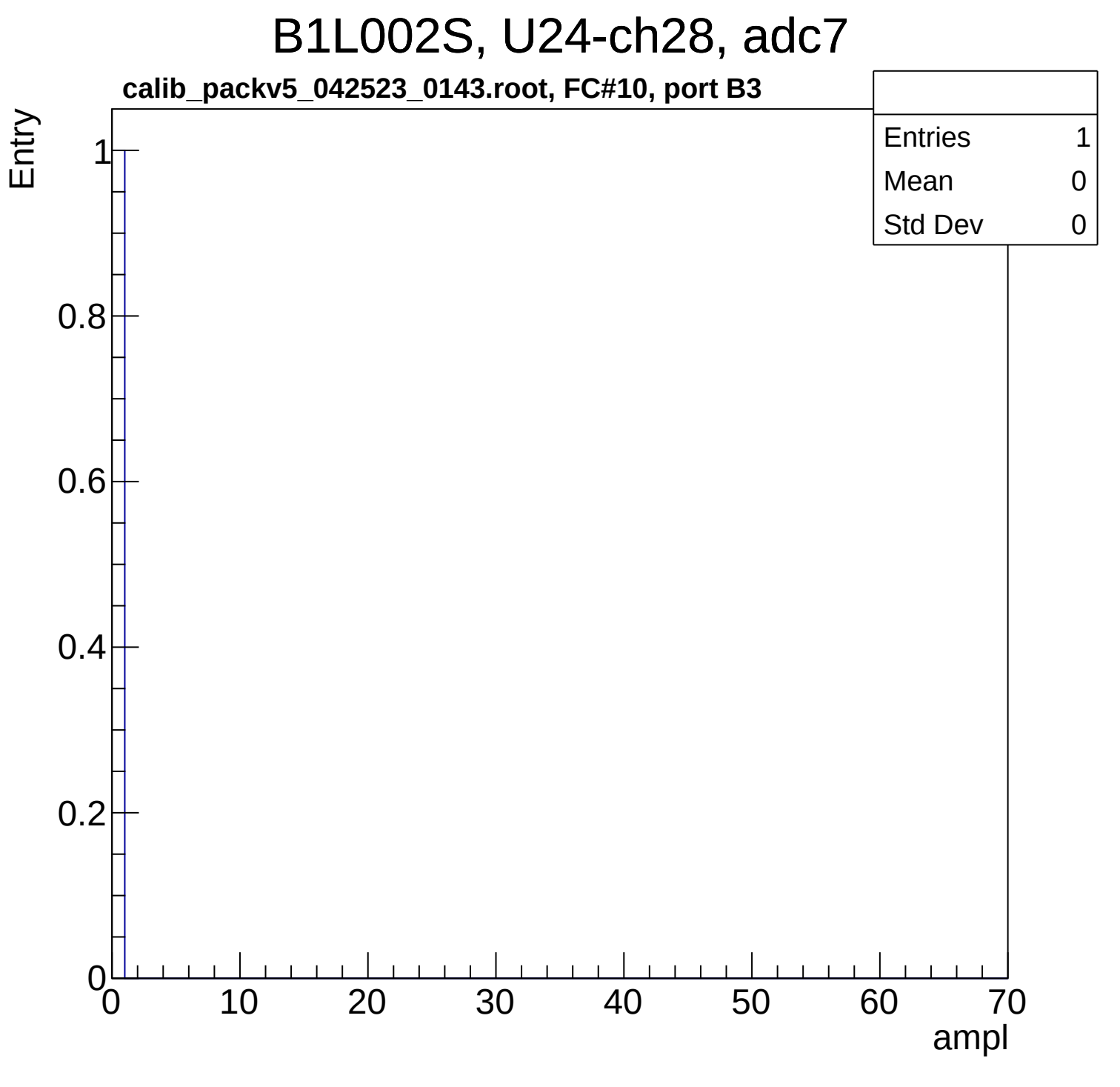

B1L002S, U24-ch28, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 40.68
Std Dev 2.936
Gaus mean : 41.3826
Gaus Width: 3.0176
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

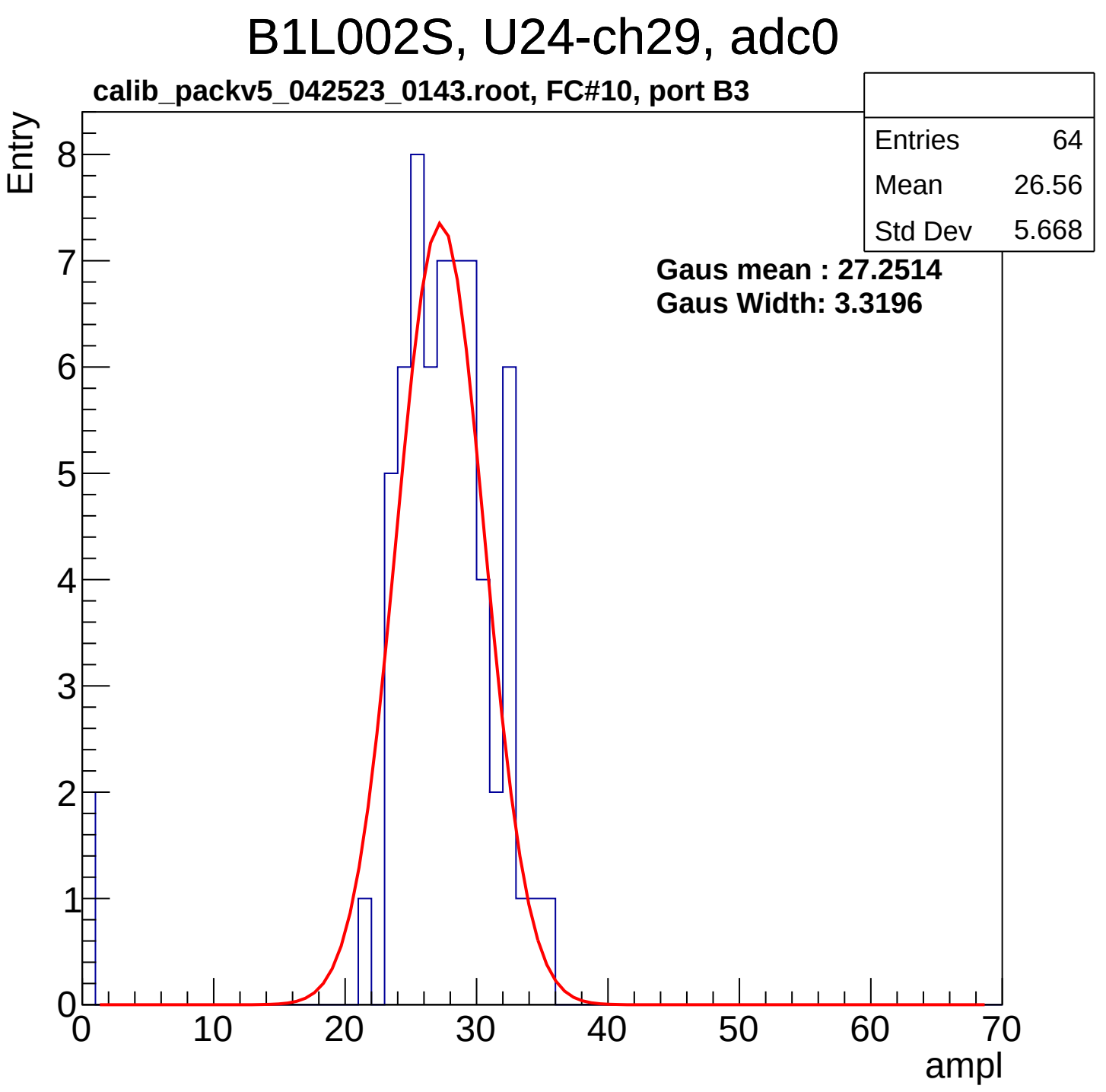

B1L002S, U24-ch28, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.1
Std Dev 2.684
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

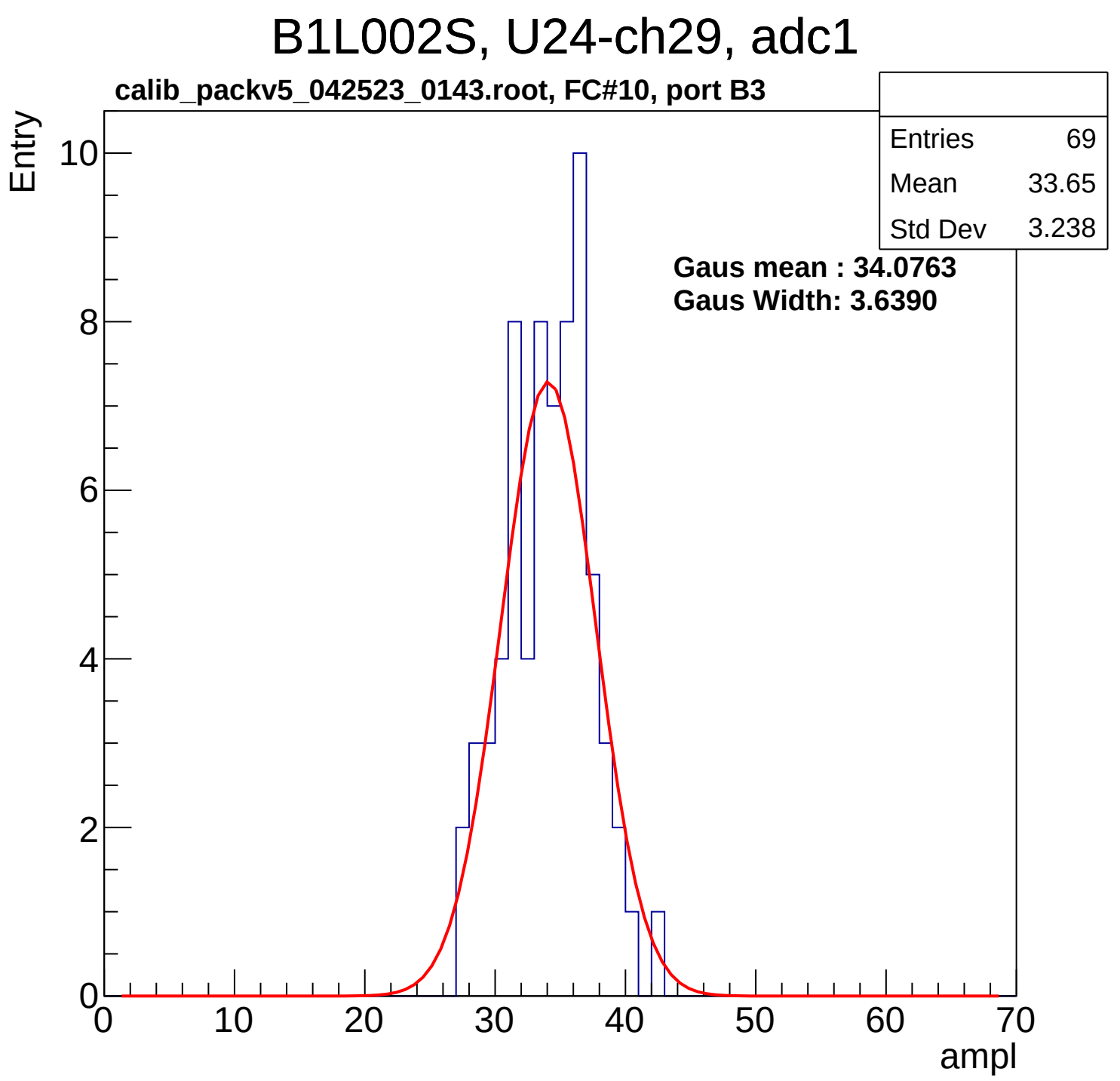

B1L002S, U24-ch28, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 52.9
Std Dev 3.057
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

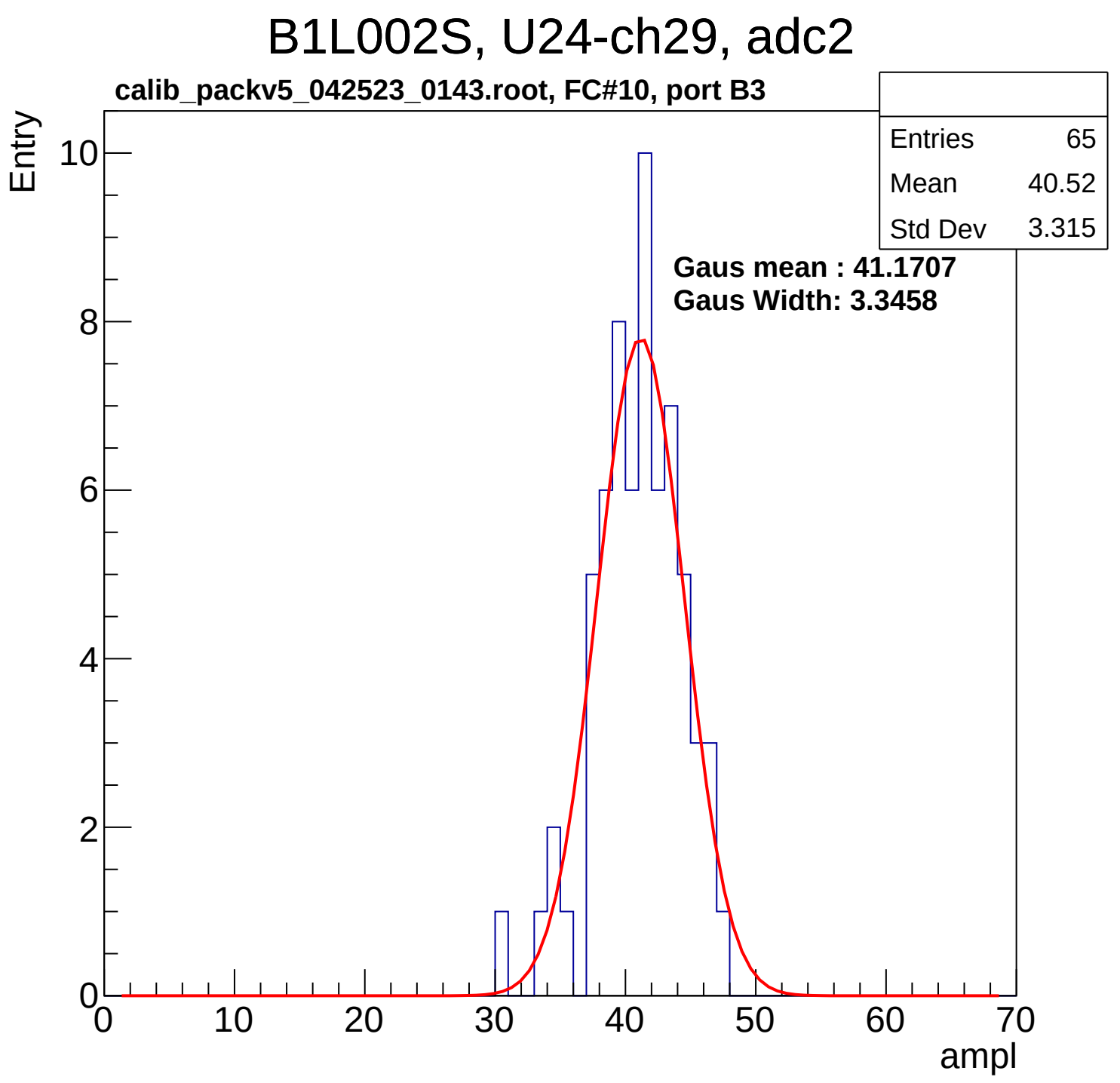

B1L002S, U24-ch28, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 58.64
Std Dev 2.234
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

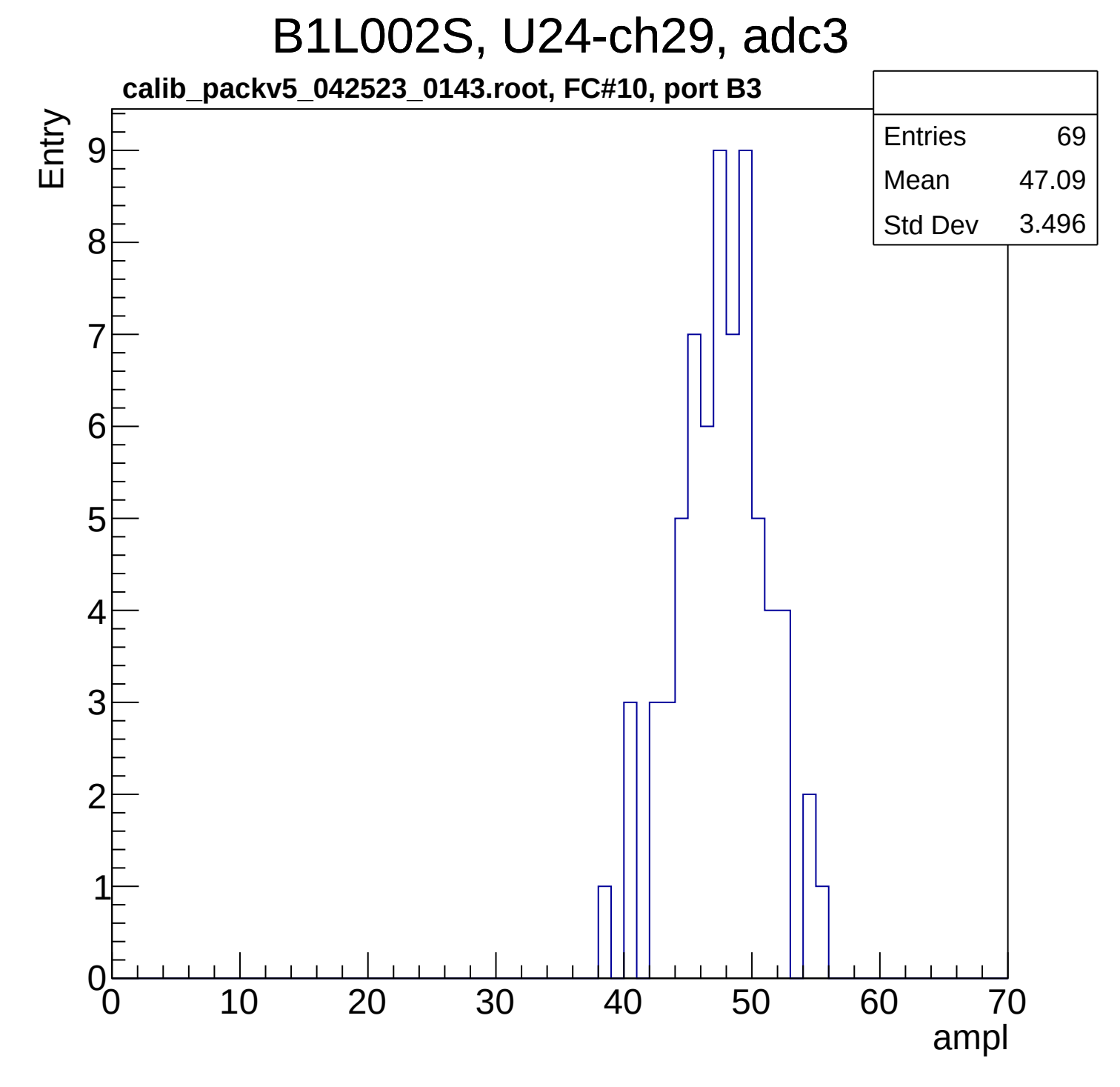

B1L002S, U24-ch28, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 59.04
Std Dev 12.63
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch28, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch29, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 26.56
Std Dev 5.668
Gaus mean : 27.2514
Gaus Width: 3.3196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.65
Std Dev 3.238
Gaus mean : 34.0763
Gaus Width: 3.6390
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 40.52
Std Dev 3.315
Gaus mean : 41.1707
Gaus Width: 3.3458
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.09
Std Dev 3.496
Entry
ampl
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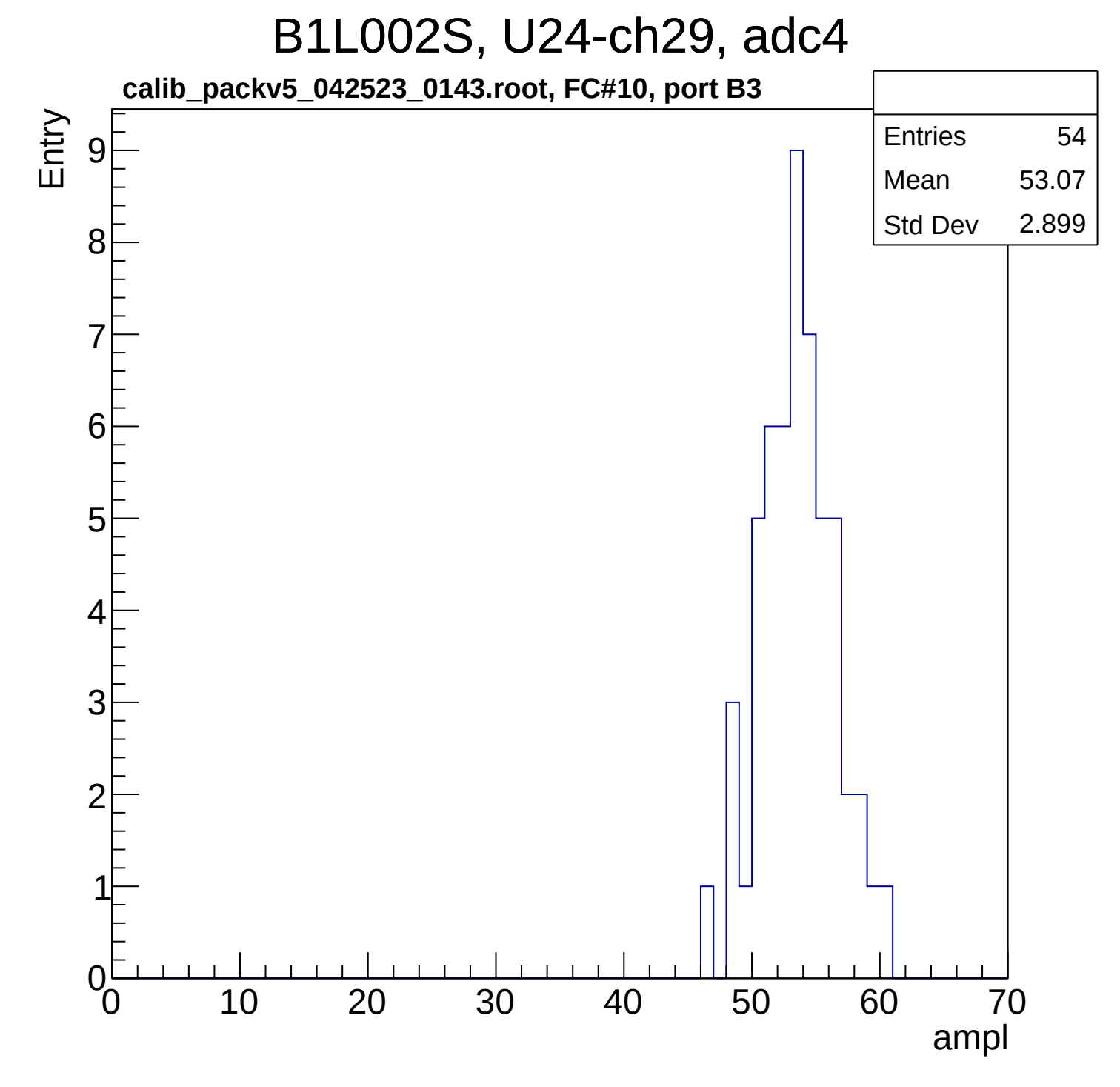
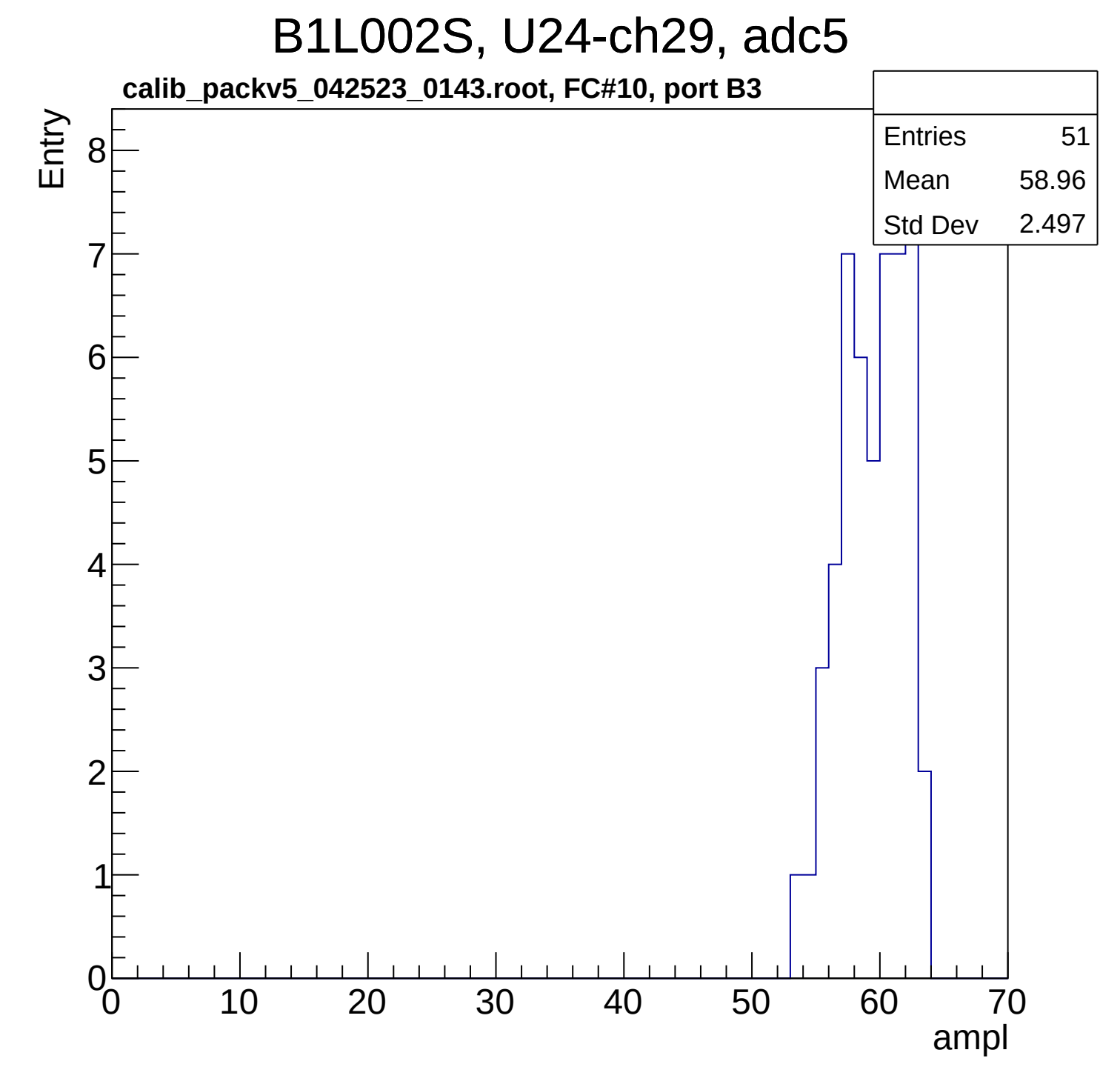
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.07
Std Dev 2.899
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.96
Std Dev 2.497
Entry
ampl
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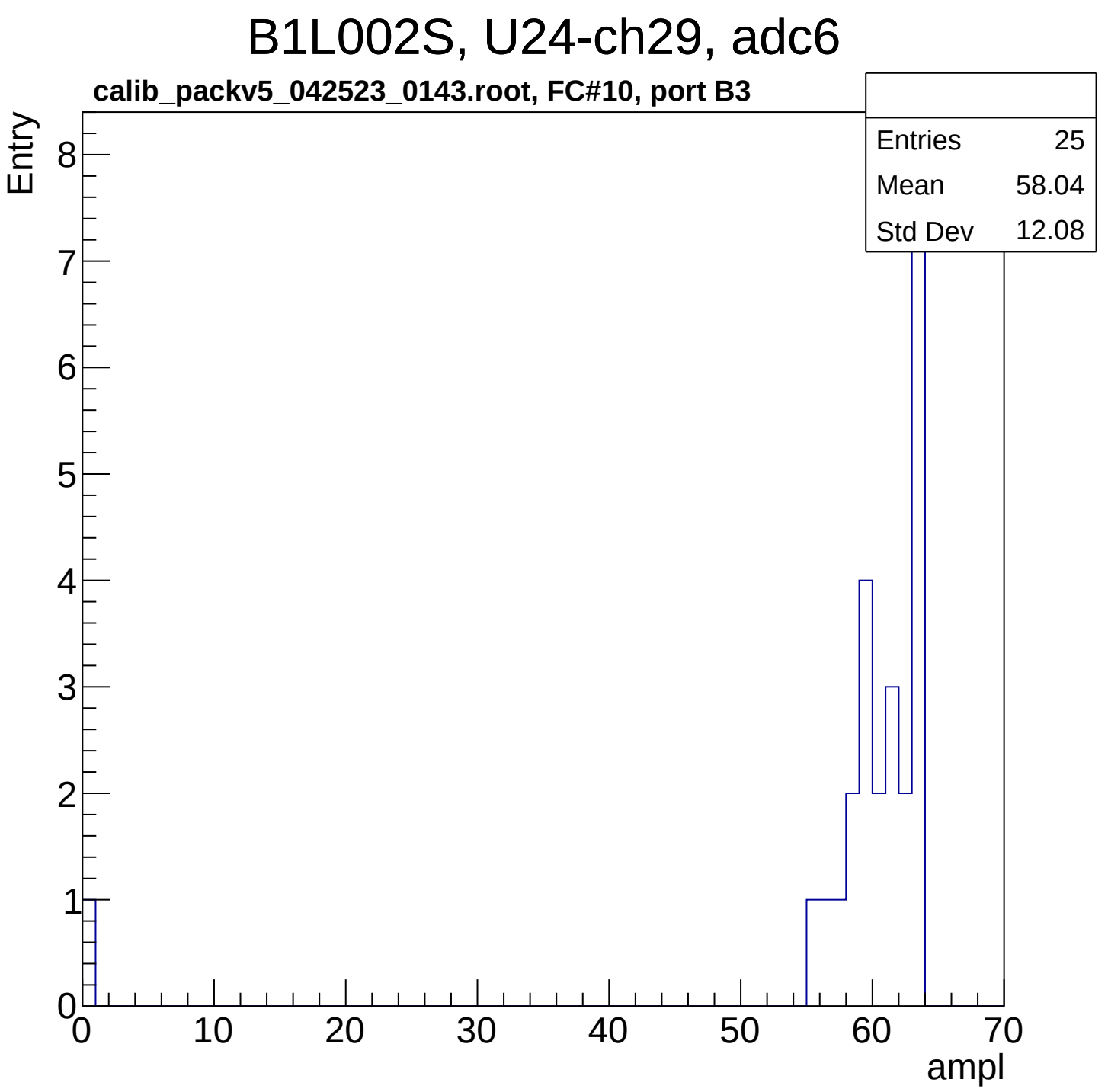
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch29, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.04
Std Dev 12.08
Entry
0
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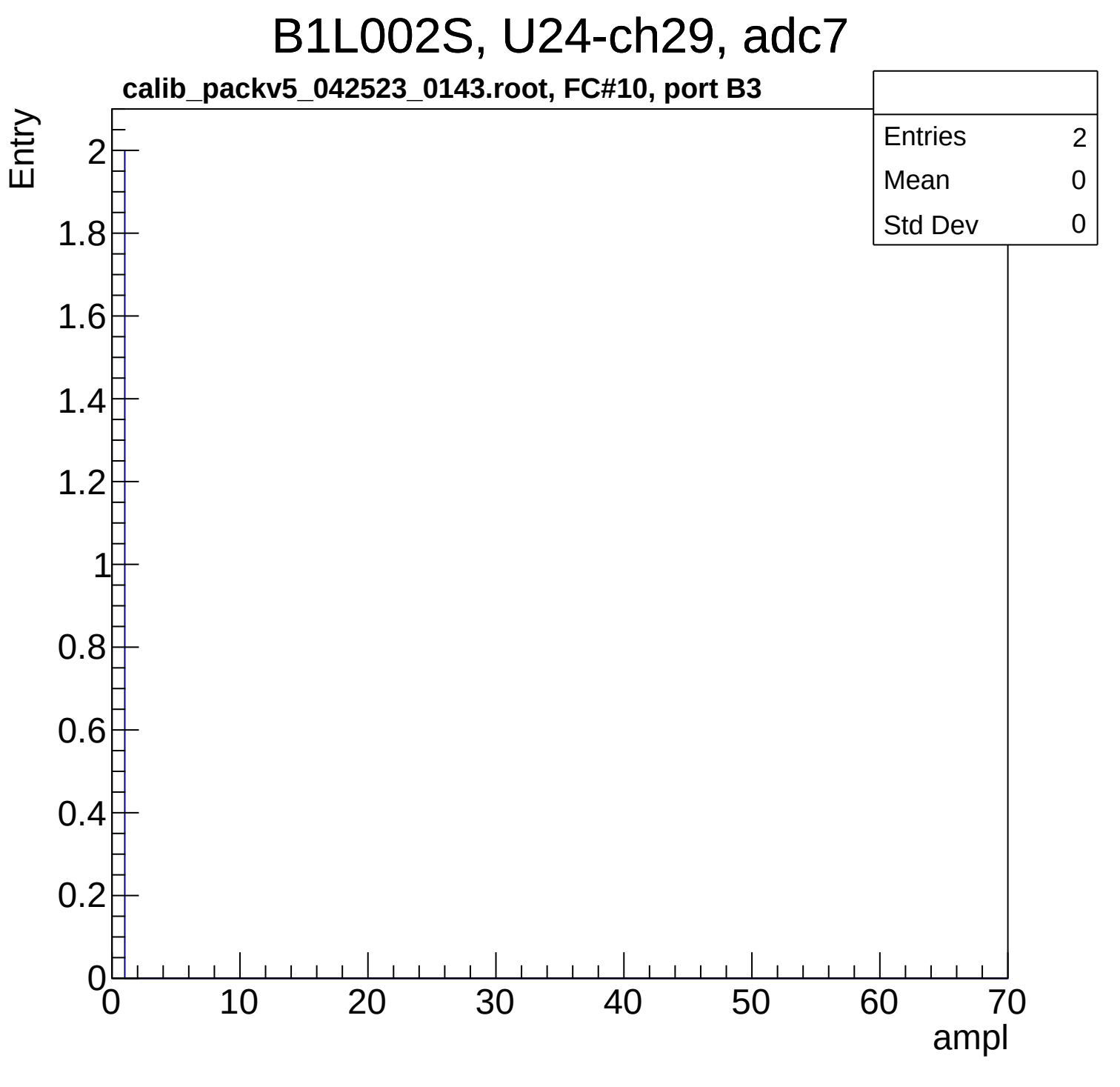
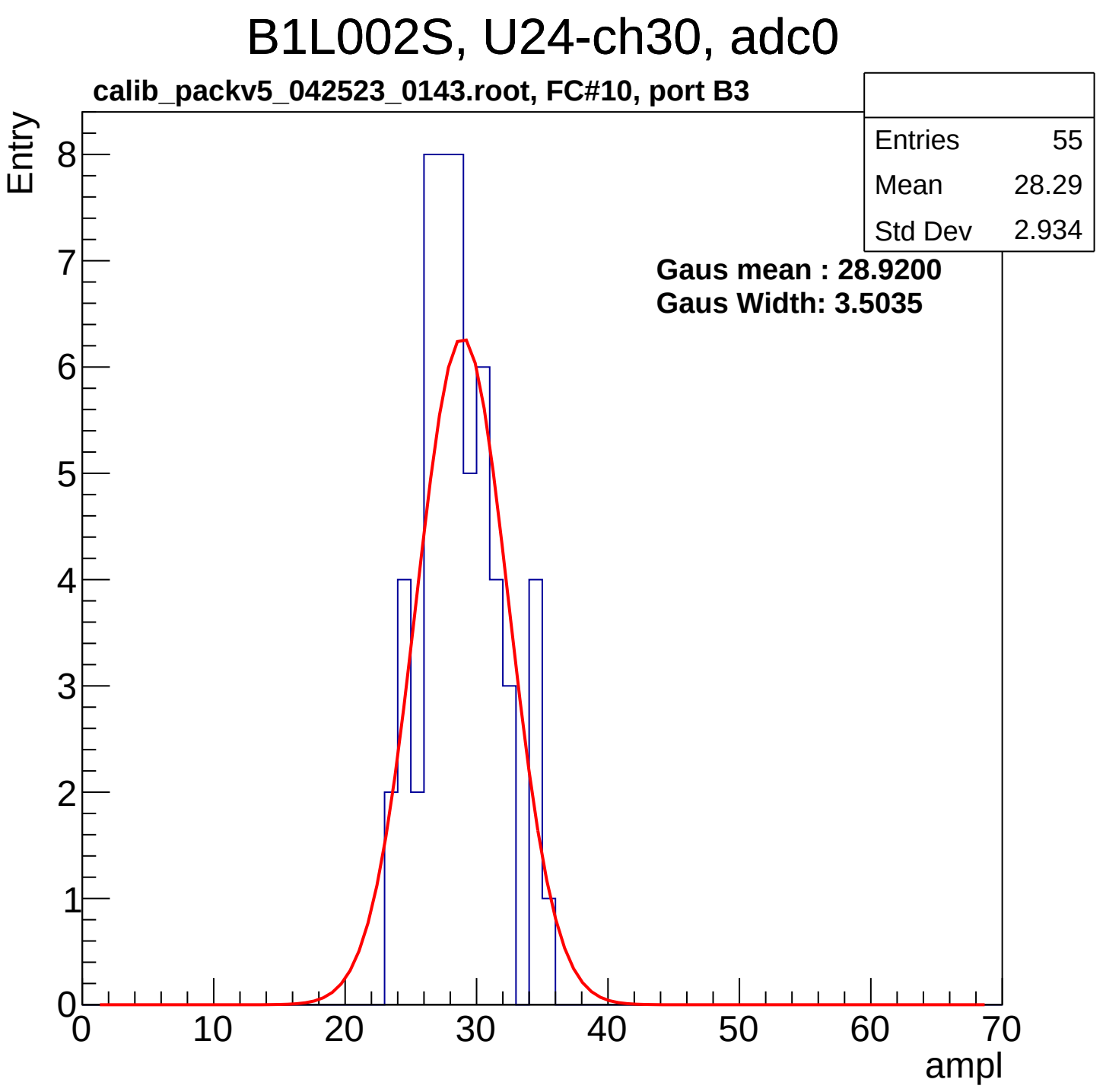
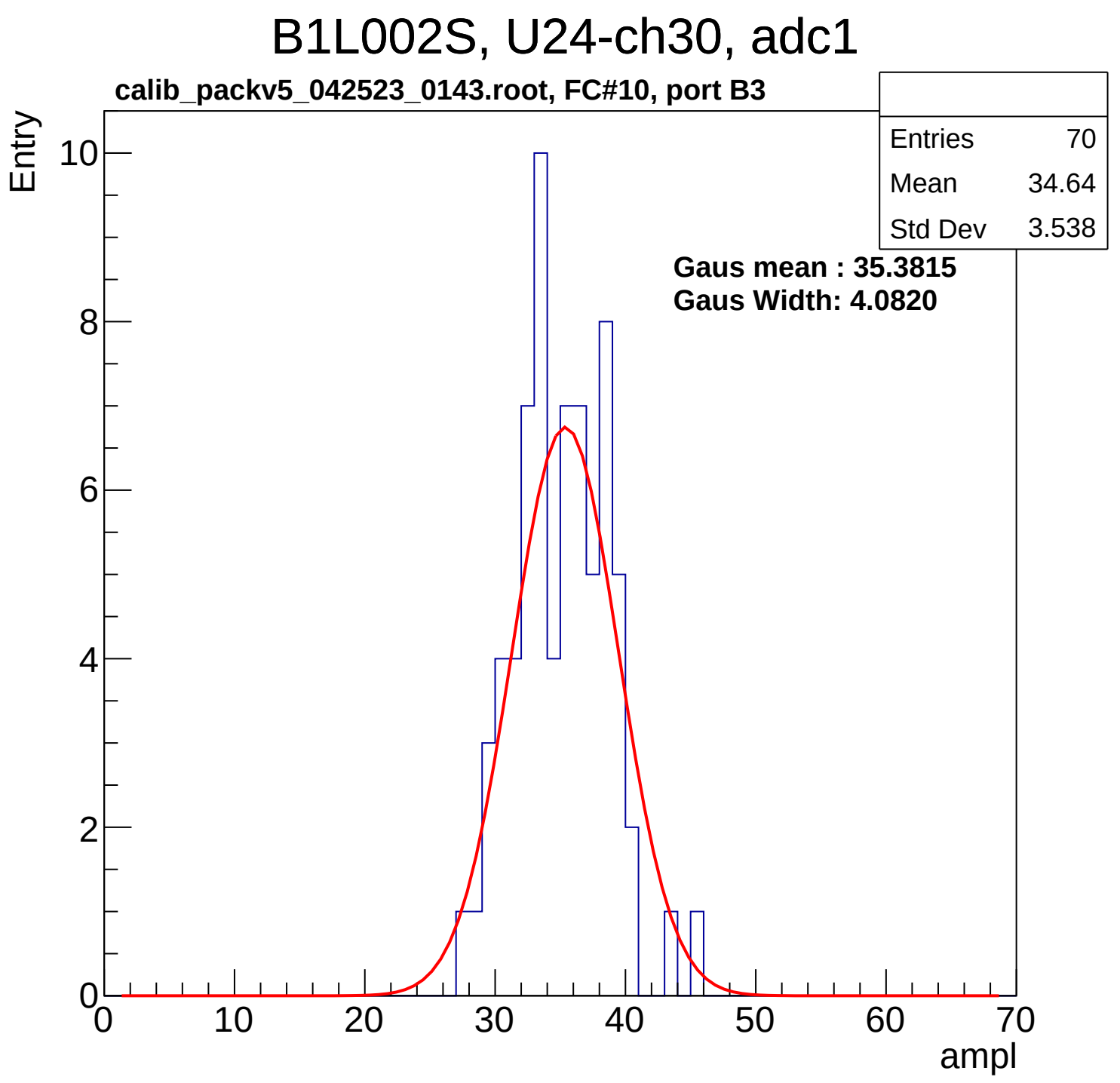
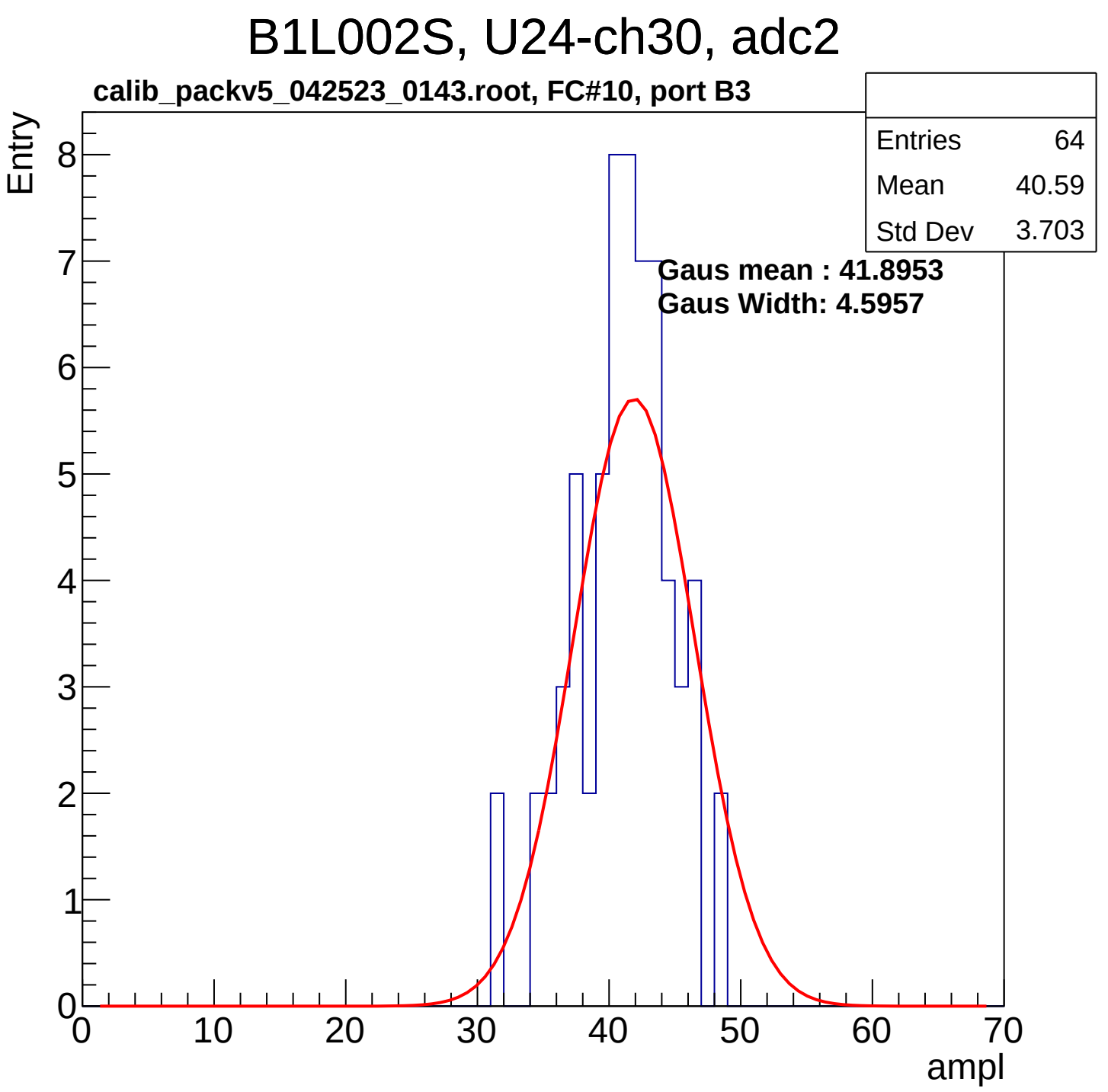
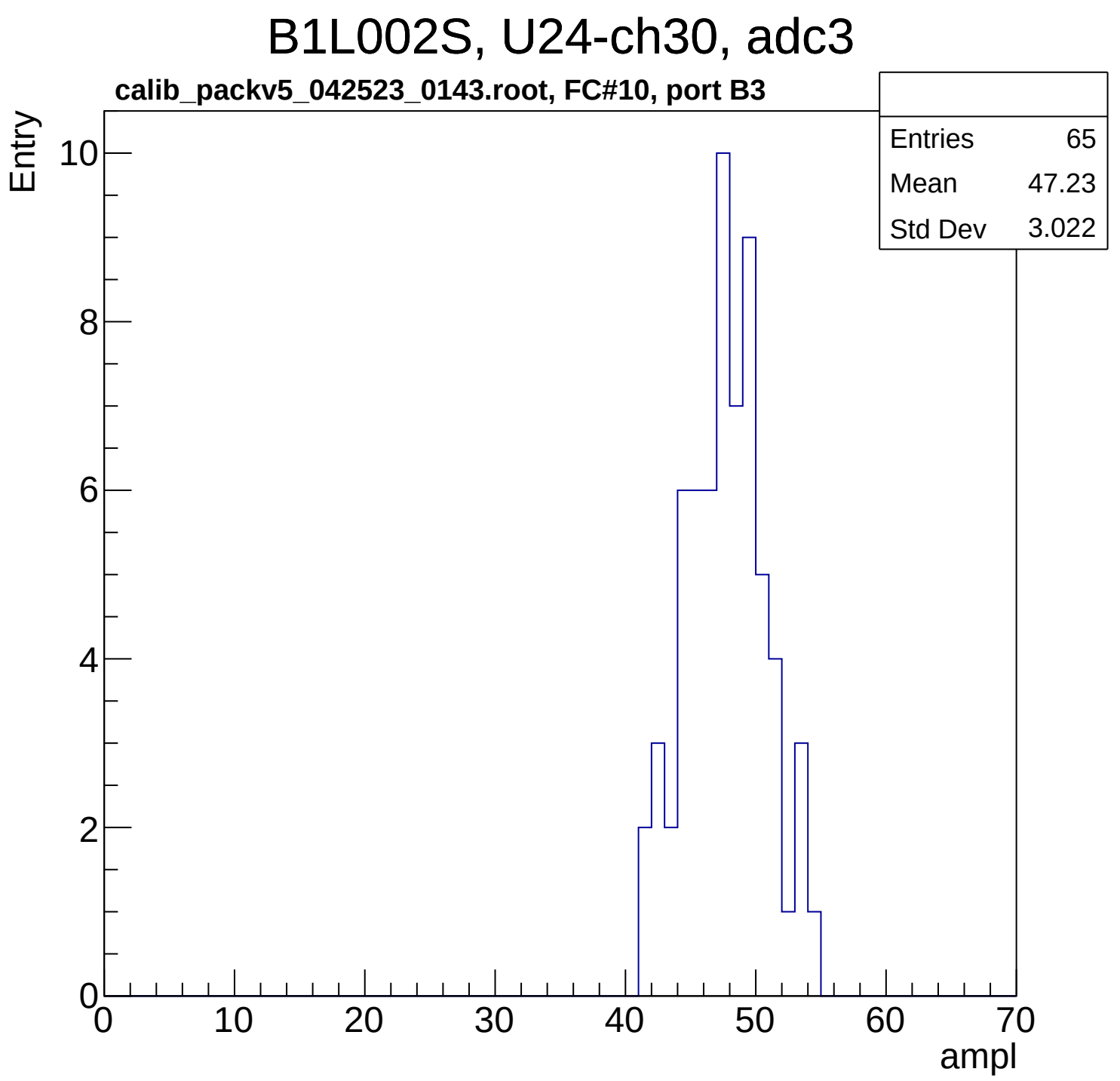
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl

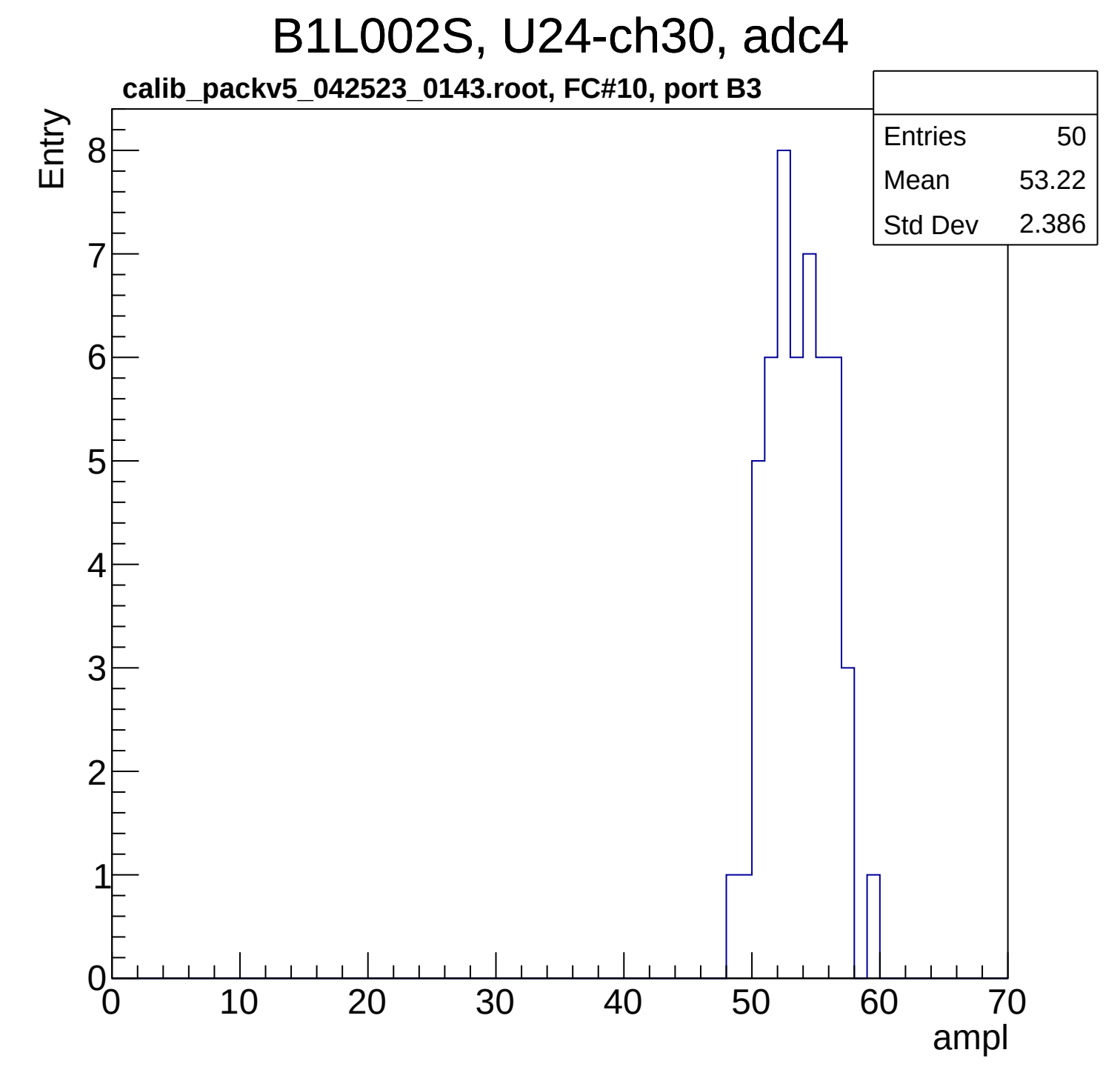

B1L002S, U24-ch29, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch30, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.29
Std Dev 2.934
Gaus mean : 28.9200
Gaus Width: 3.5035
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch30, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.64
Std Dev 3.538
Gaus mean : 35.3815
Gaus Width: 4.0820
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch30, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 40.59
Std Dev 3.703
Gaus mean : 41.8953
Gaus Width: 4.5957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch30, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.23
Std Dev 3.022
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch30, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.22
Std Dev 2.386
Entry
ampl
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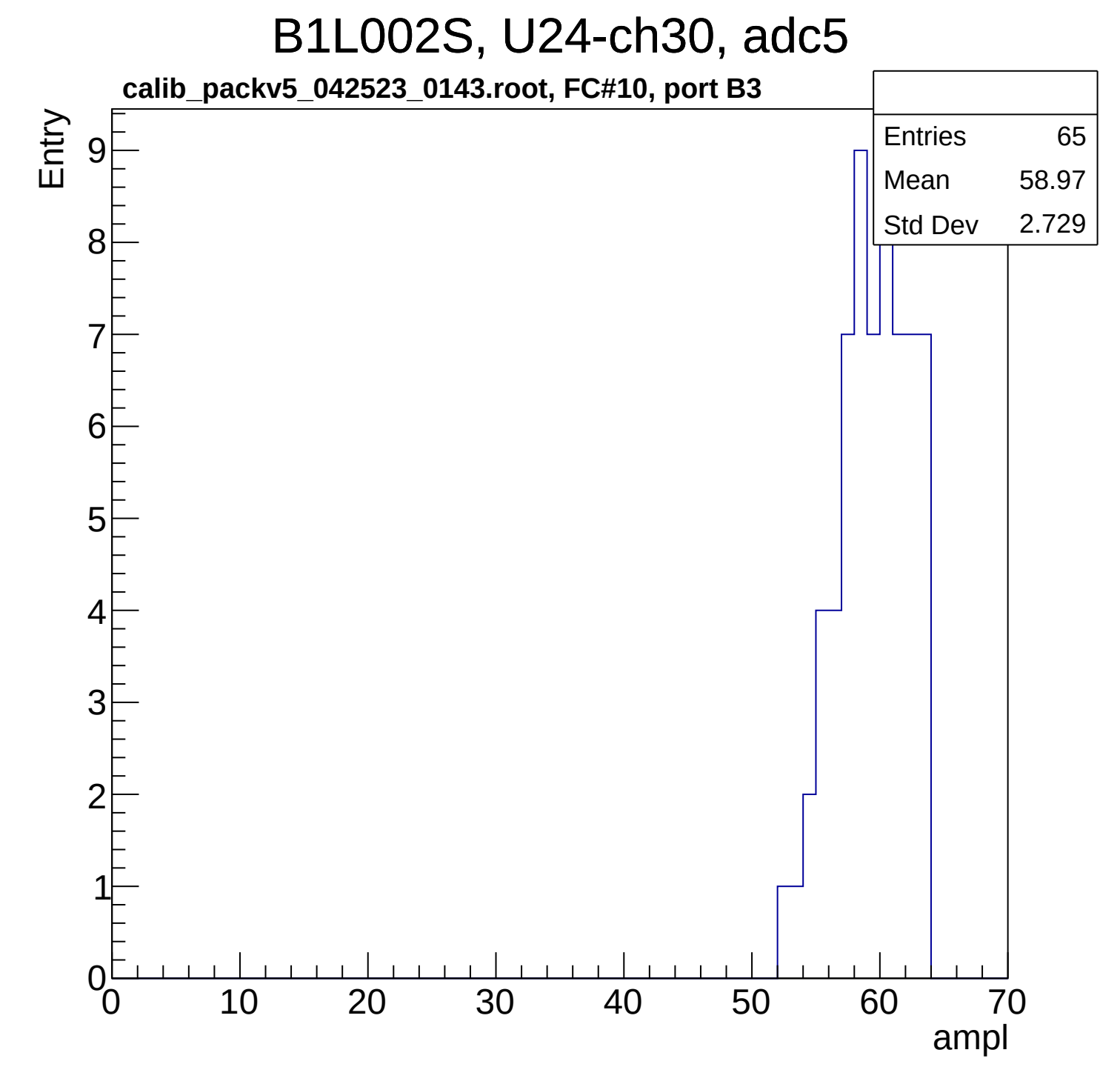
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch30, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.97
Std Dev 2.729
Entry
ampl
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2
3
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5
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40
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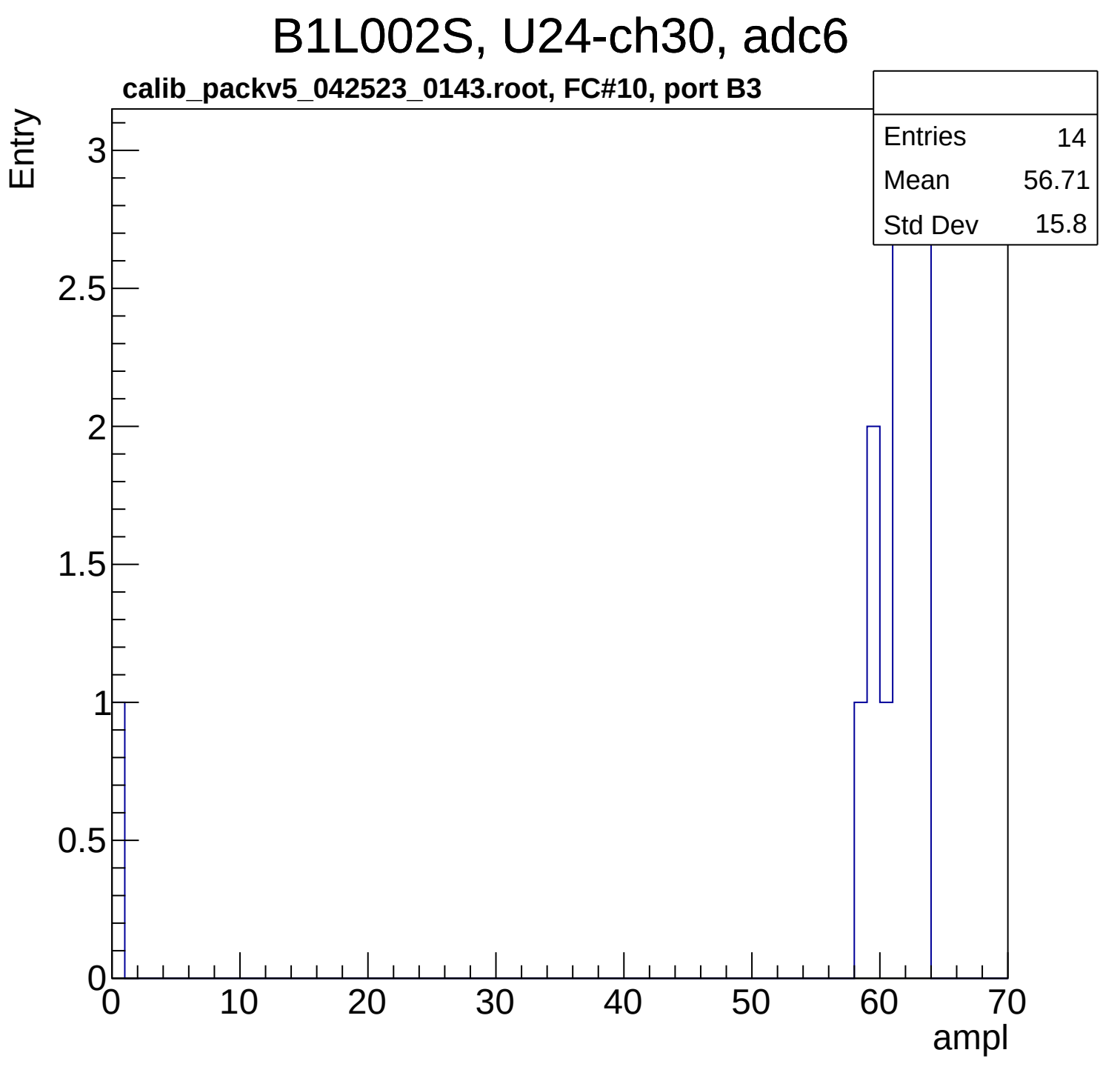
B1L002S, U24-ch30, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 56.71
Std Dev 15.8
Entry
ampl
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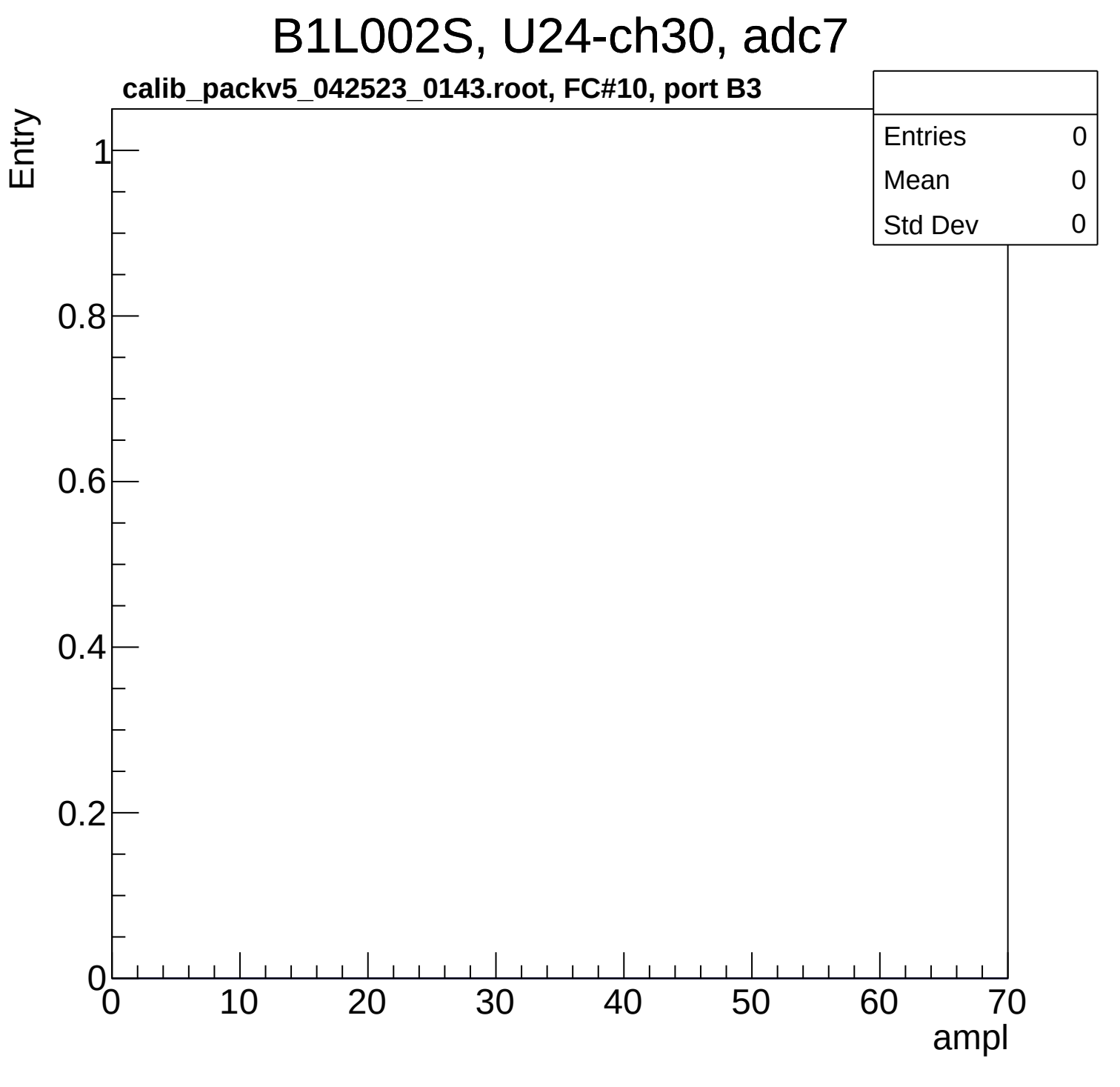
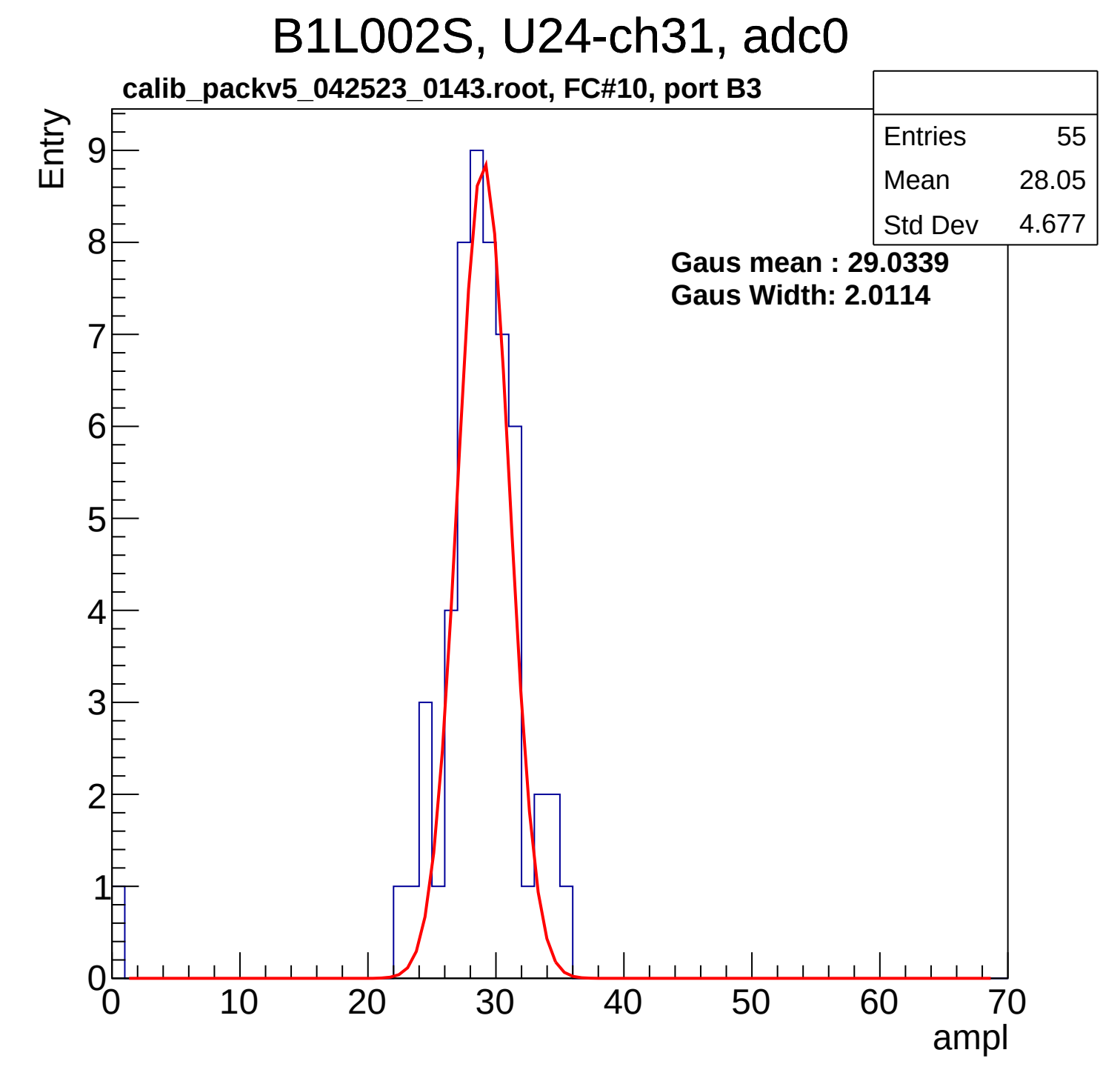
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch30, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch31, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.05
Std Dev 4.677
Gaus mean : 29.0339
Gaus Width: 2.0114
Entry
ampl
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4
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6
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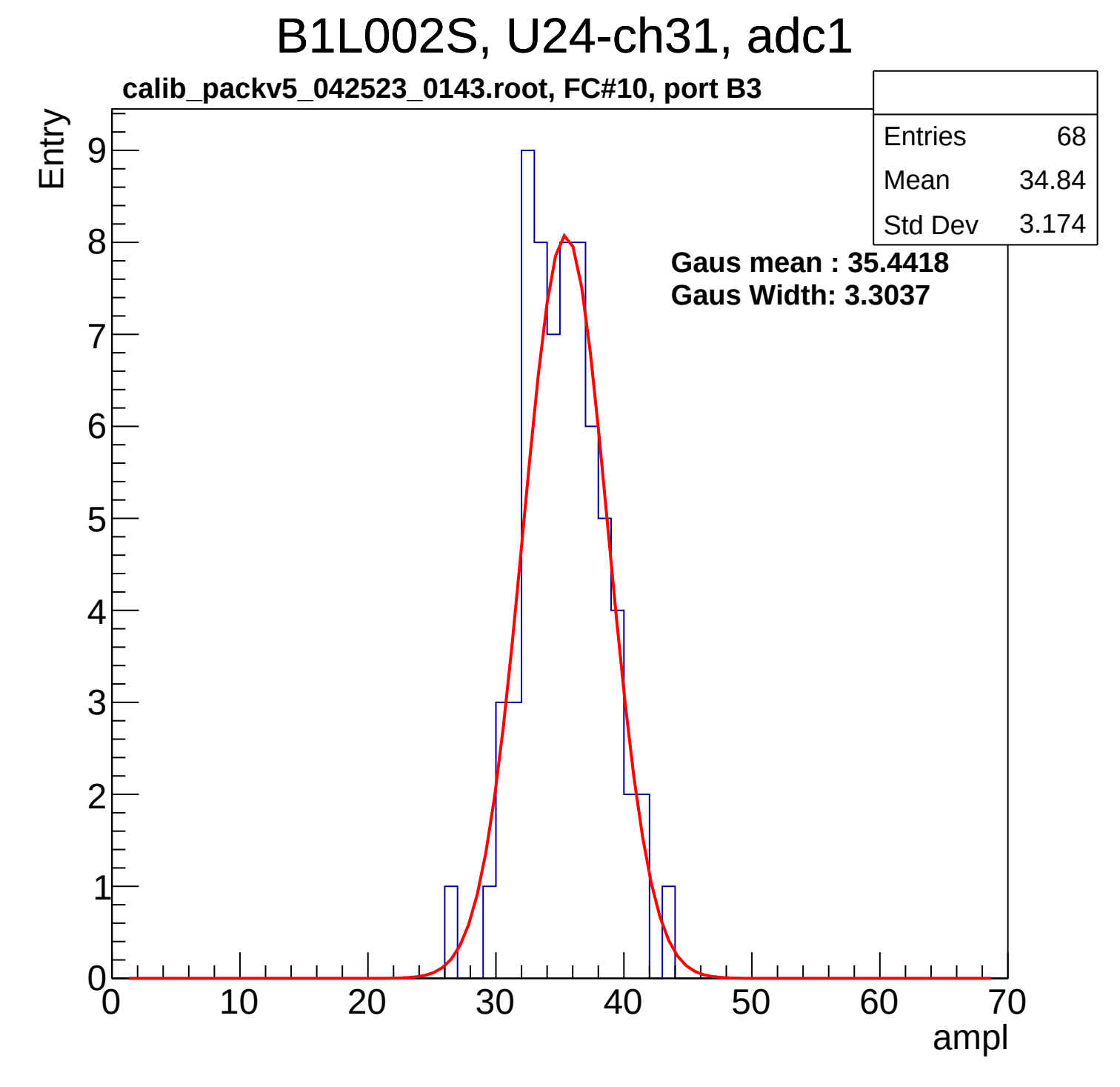
B1L002S, U24-ch31, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.84
Std Dev 3.174
Gaus mean : 35.4418
Gaus Width: 3.3037
Entry
ampl
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2
3
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5
6
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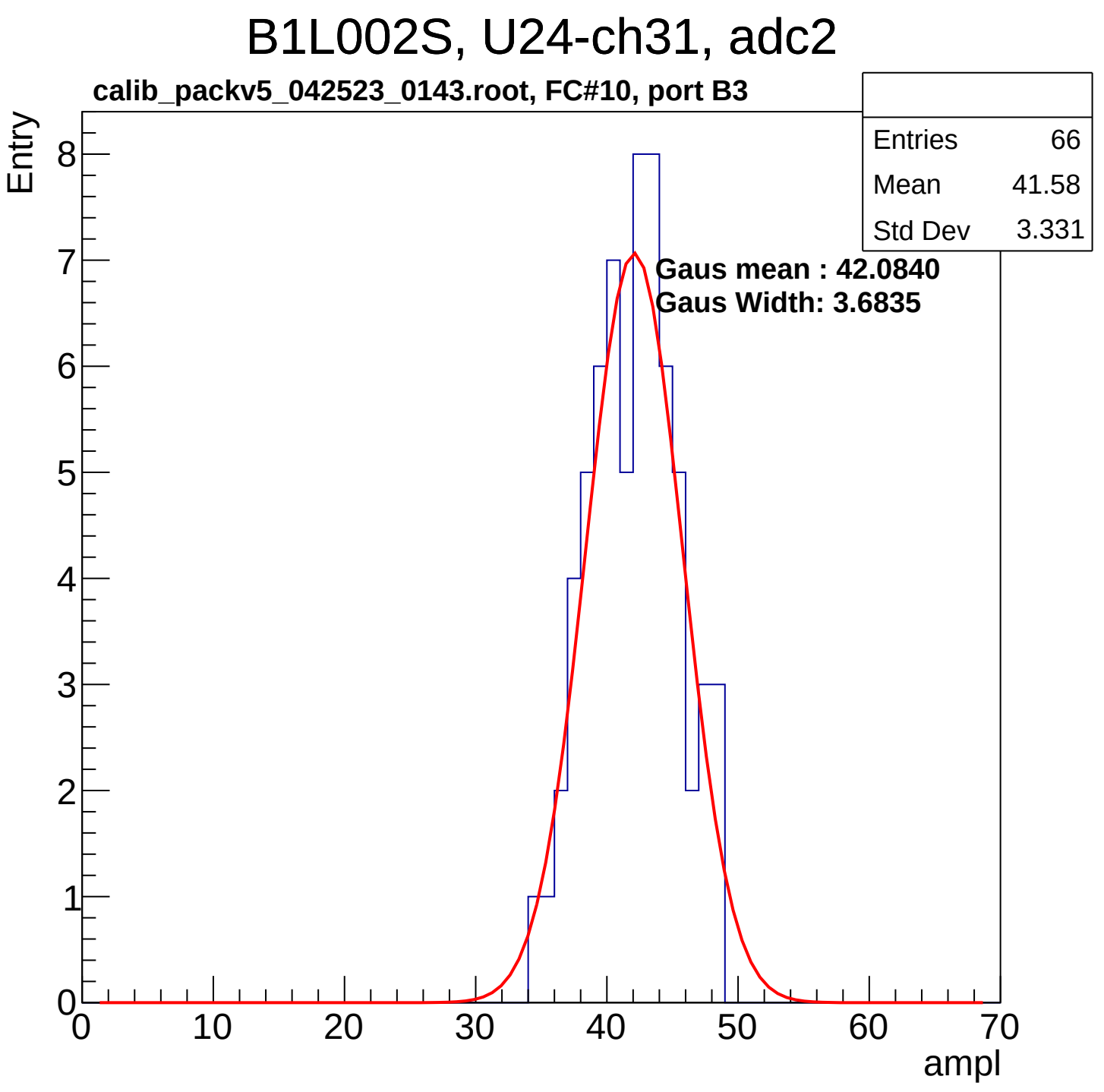
B1L002S, U24-ch31, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 41.58
Std Dev 3.331
Gaus mean : 42.0840
Gaus Width: 3.6835
Entry
ampl
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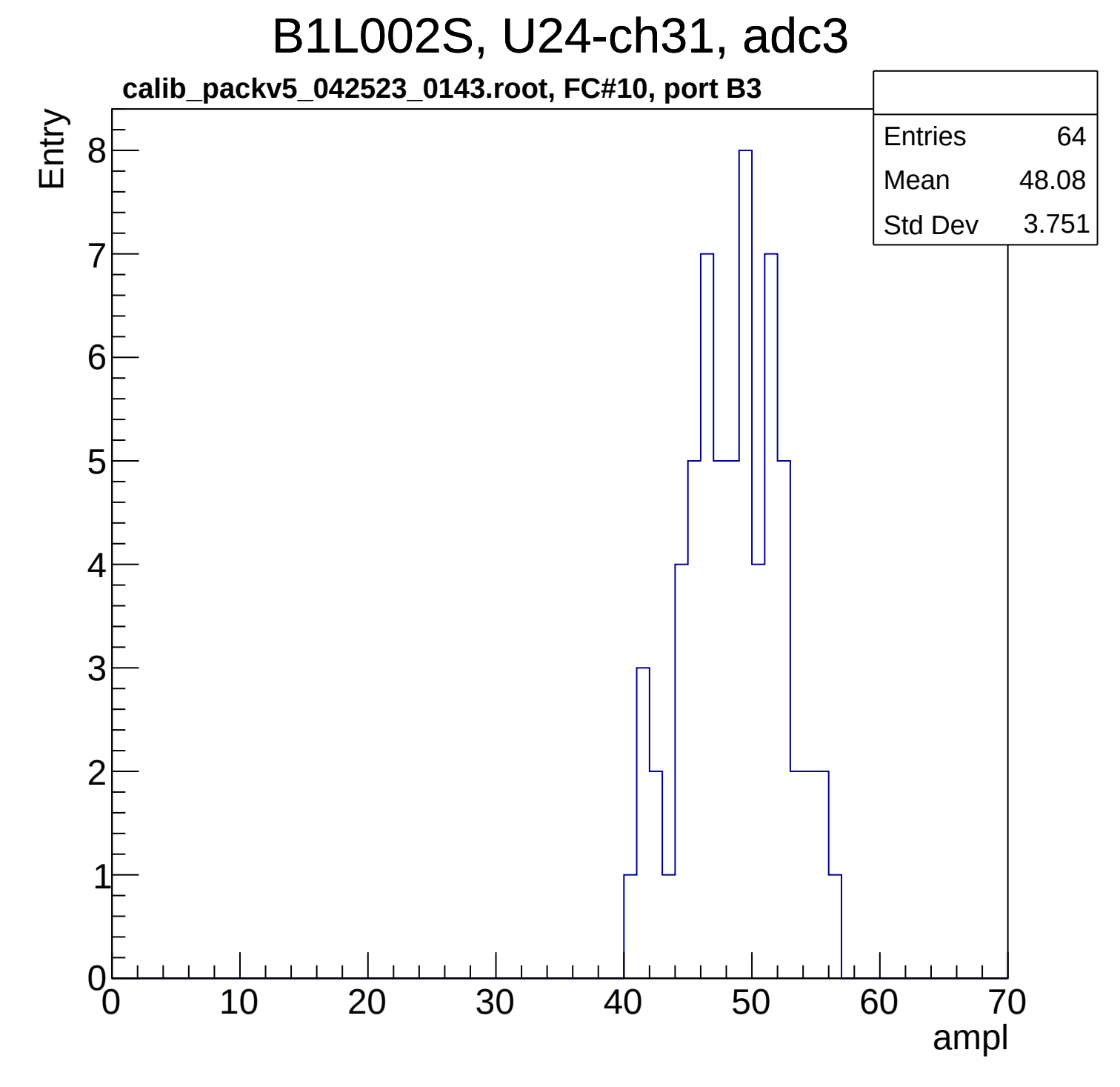
B1L002S, U24-ch31, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 48.08
Std Dev 3.751
Entry
ampl
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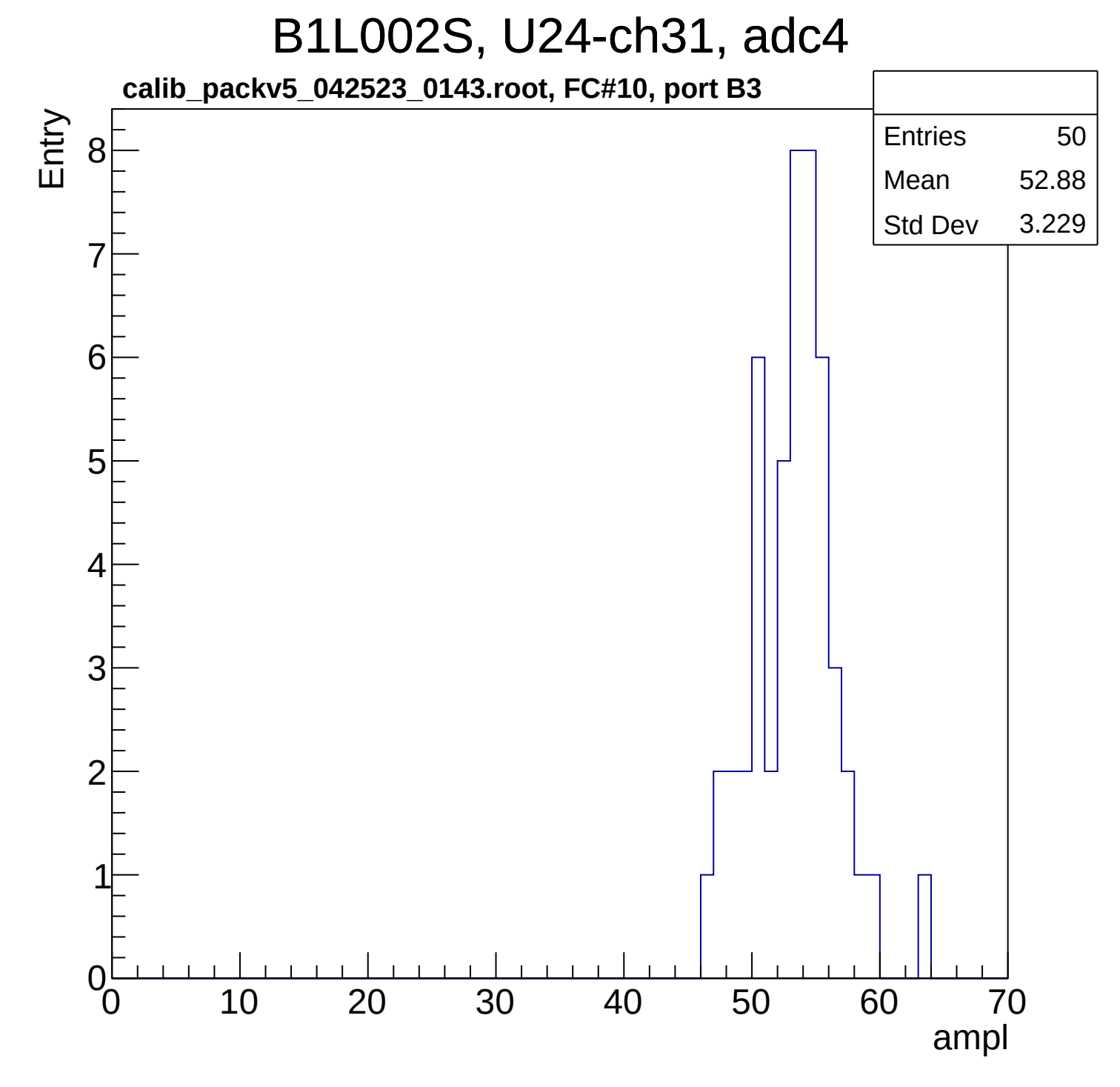
B1L002S, U24-ch31, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 52.88
Std Dev 3.229
Entry
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2
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4
5
6
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8
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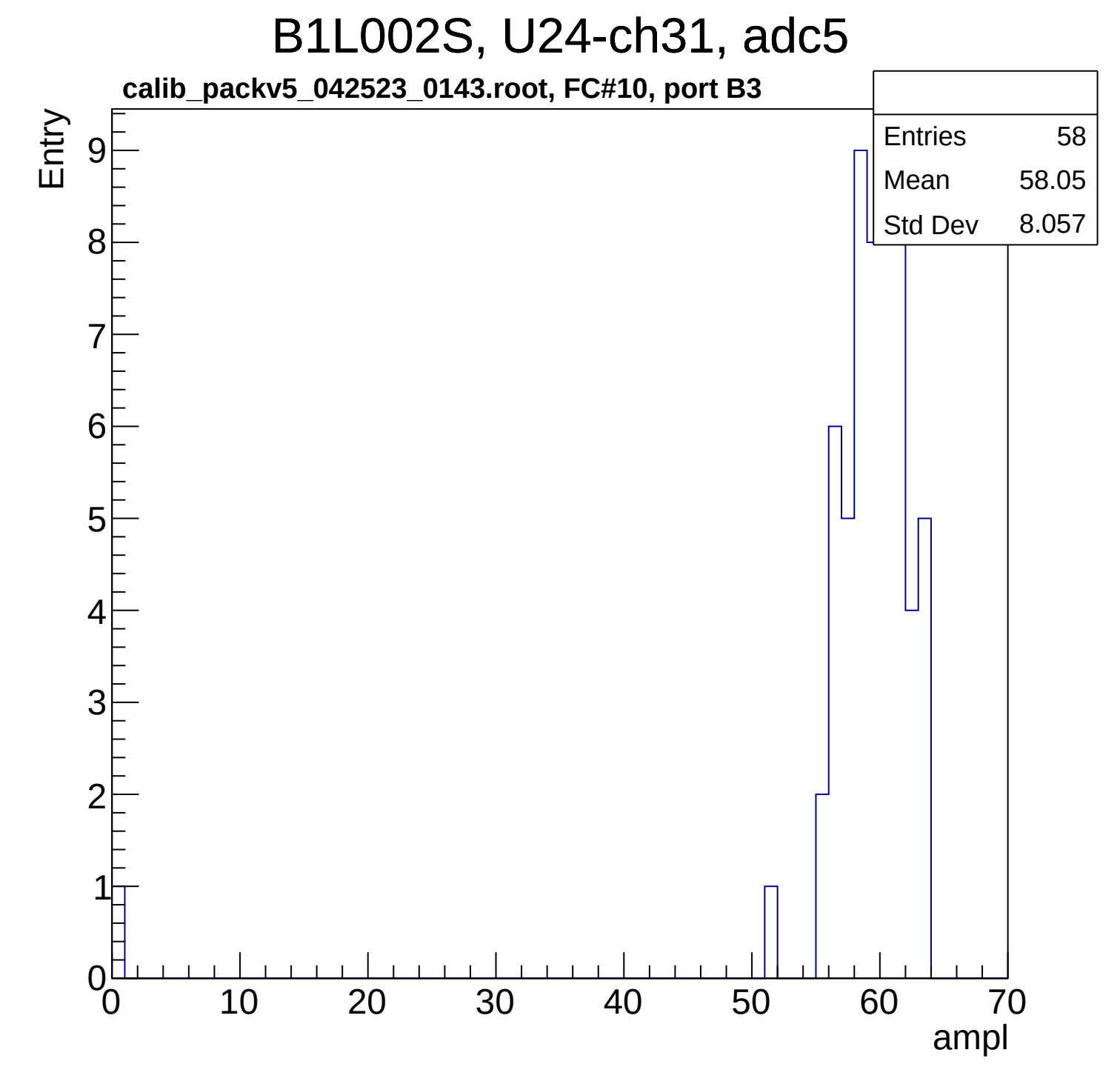
50
60
70
ampl


B1L002S, U24-ch31, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 58.05
Std Dev 8.057
Entry
ampl
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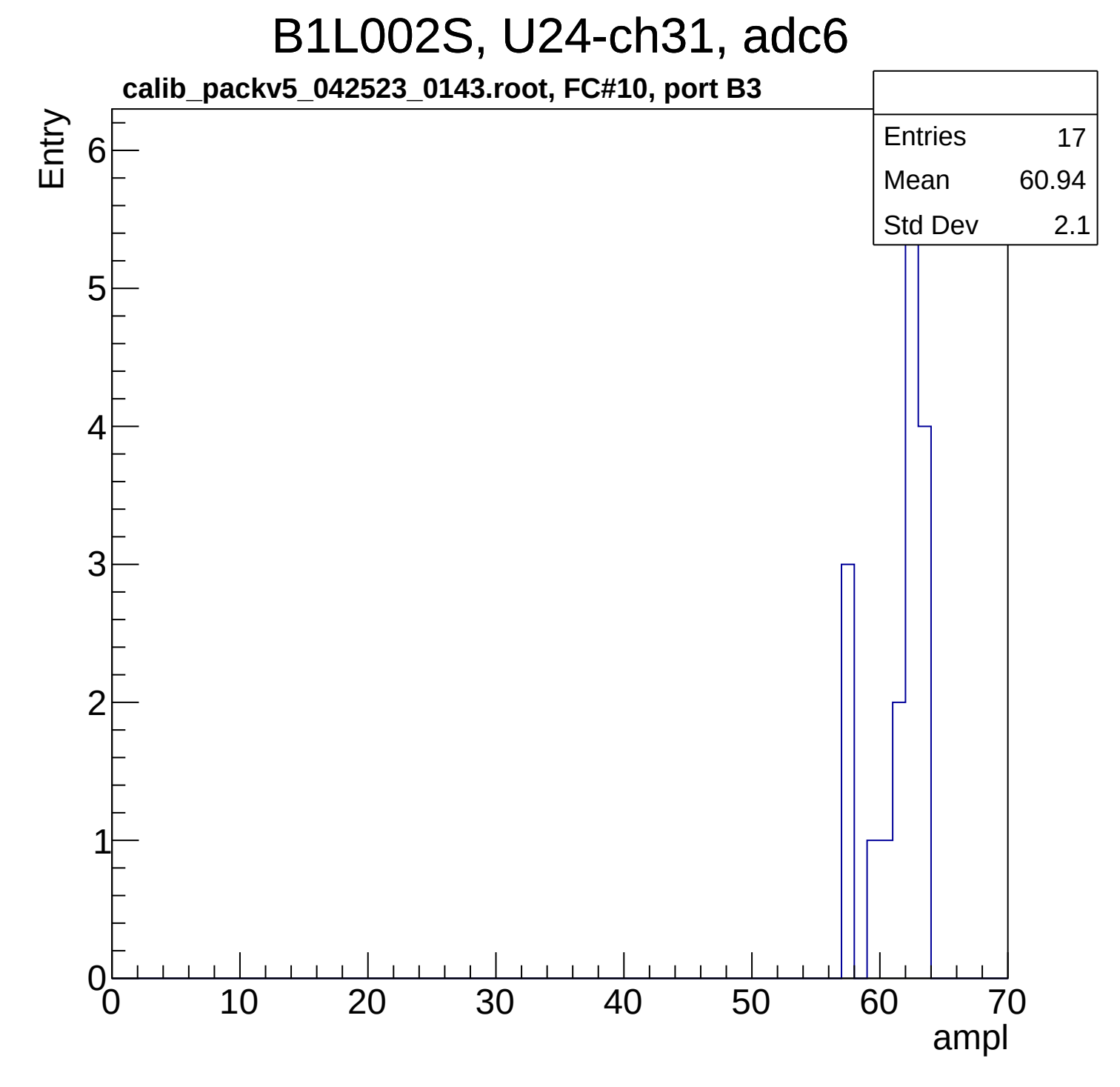
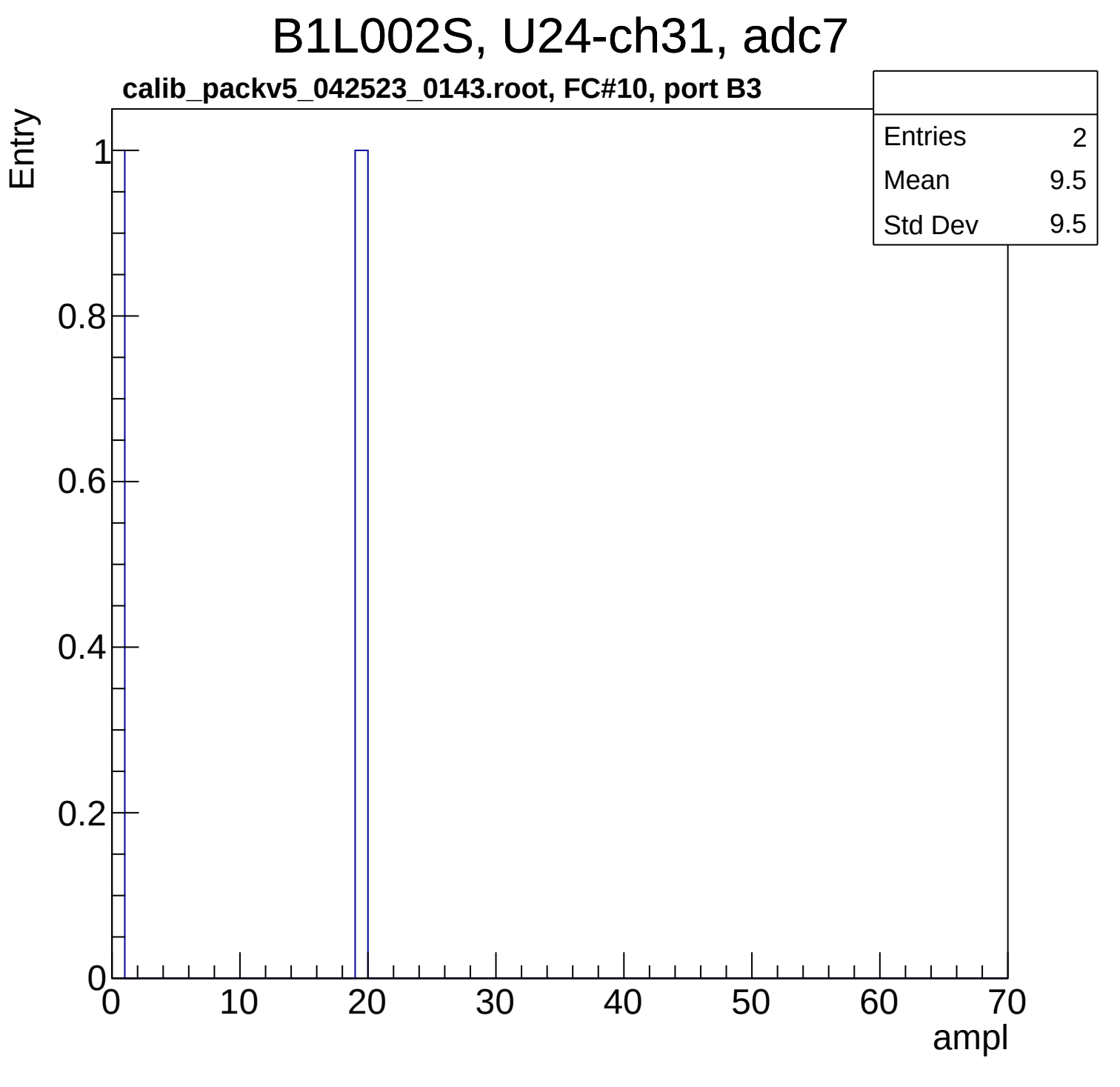
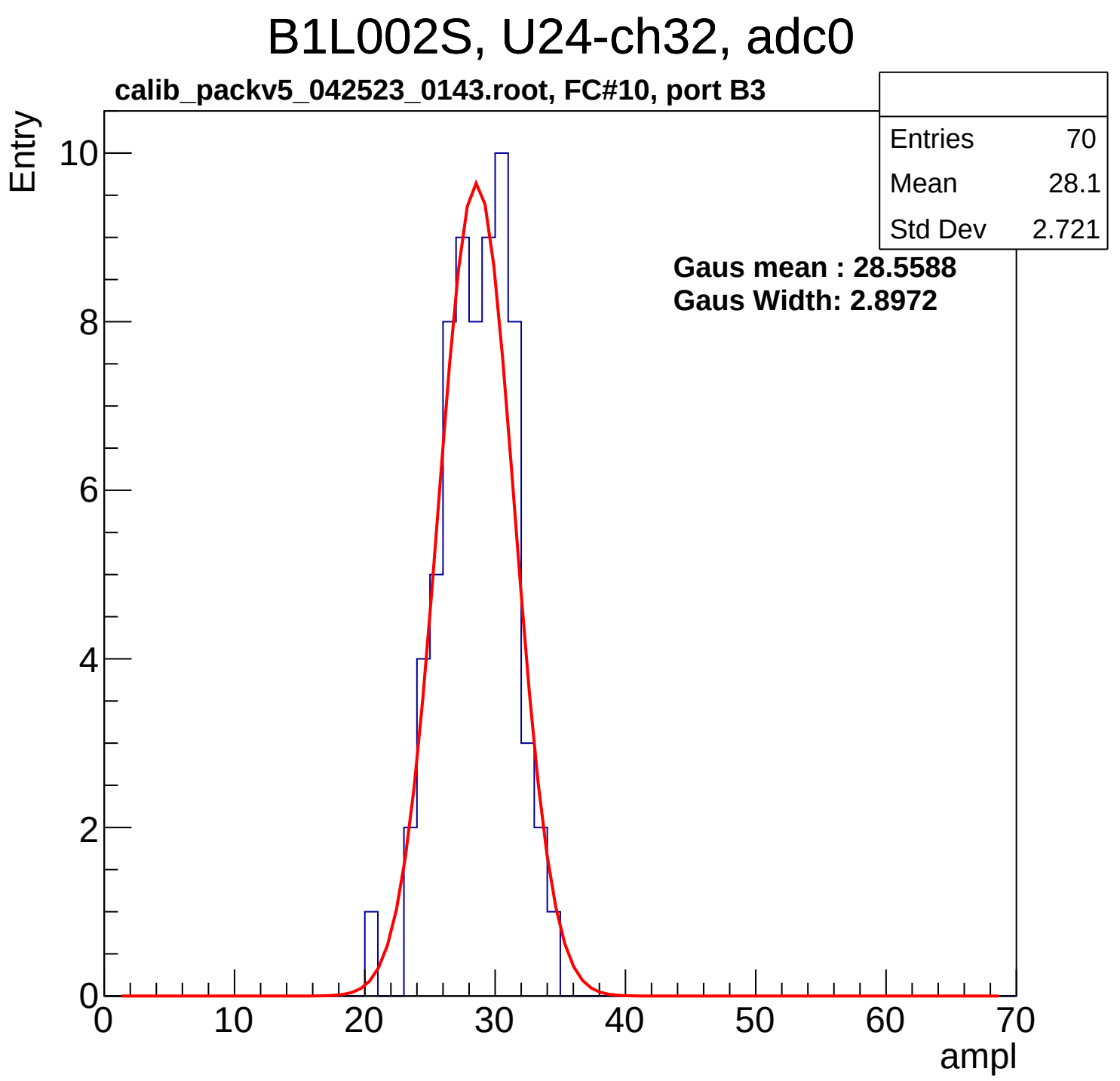

B1L002S, U24-ch31, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 60.94
Std Dev 2.1
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch31, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 9.5
Std Dev 9.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch32, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.1
Std Dev 2.721
Gaus mean : 28.5588
Gaus Width: 2.8972
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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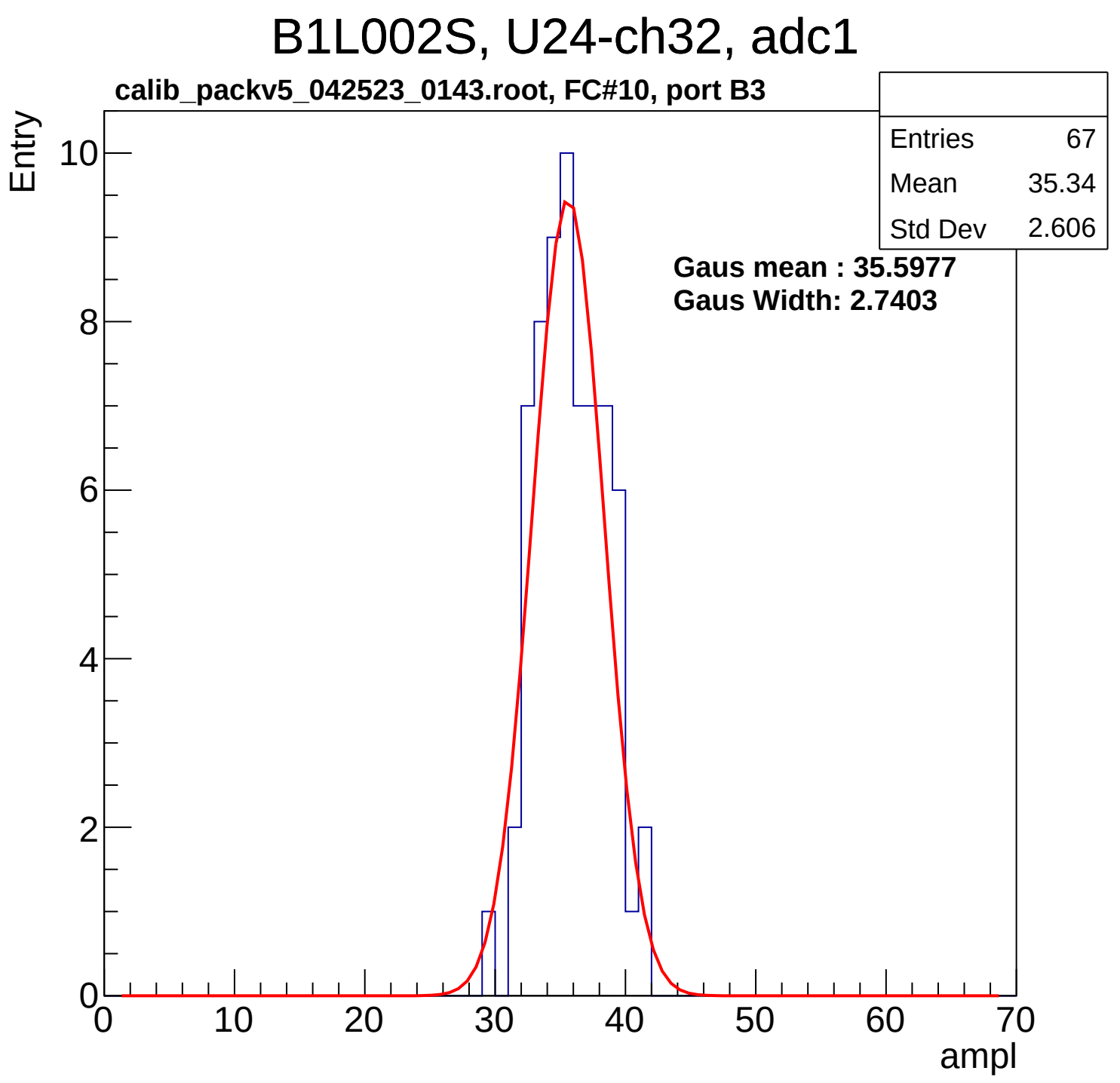
B1L002S, U24-ch32, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 35.34
Std Dev 2.606
Gaus mean : 35.5977
Gaus Width: 2.7403
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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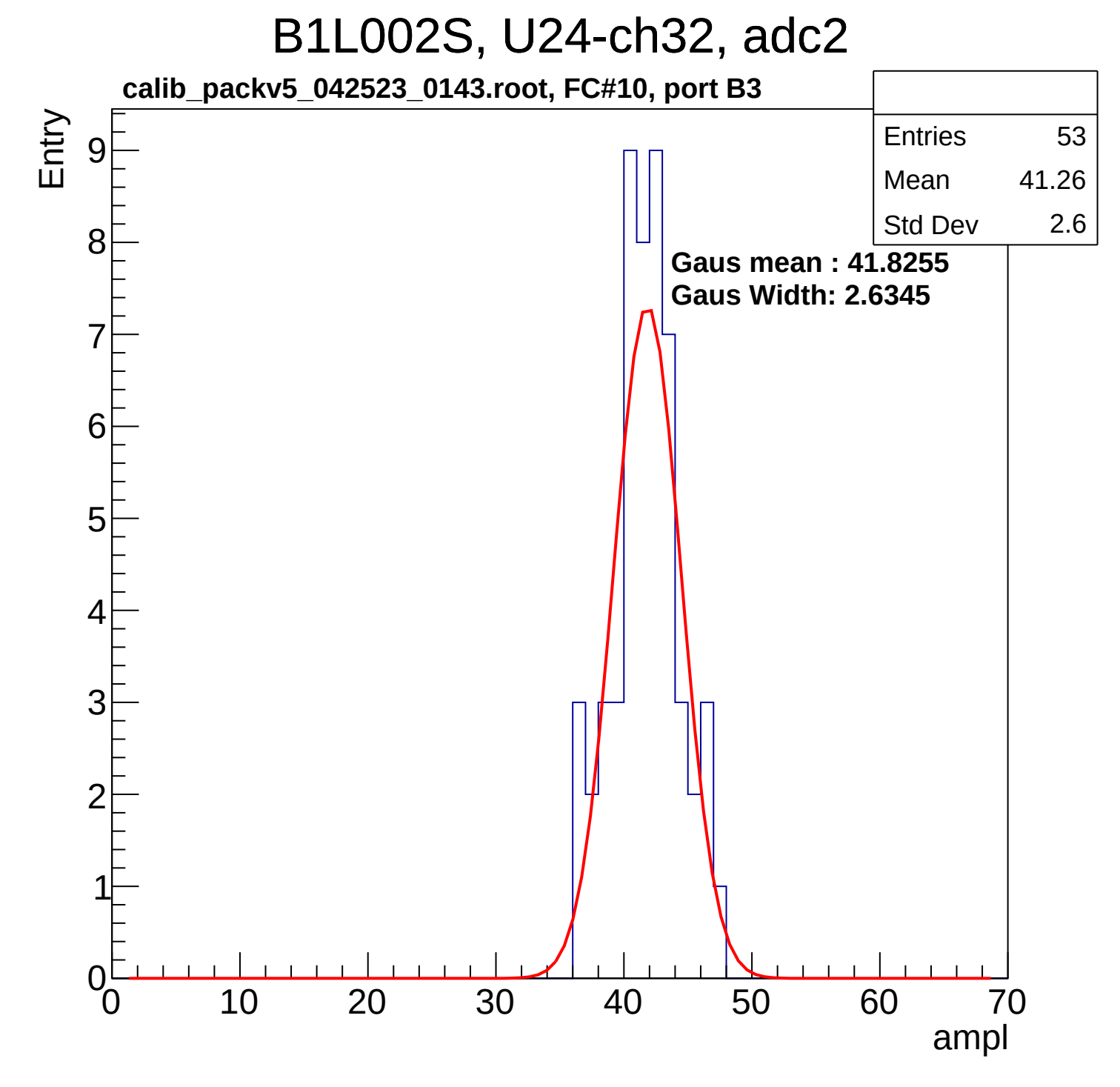
B1L002S, U24-ch32, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 41.26
Std Dev 2.6
Gaus mean : 41.8255
Gaus Width: 2.6345
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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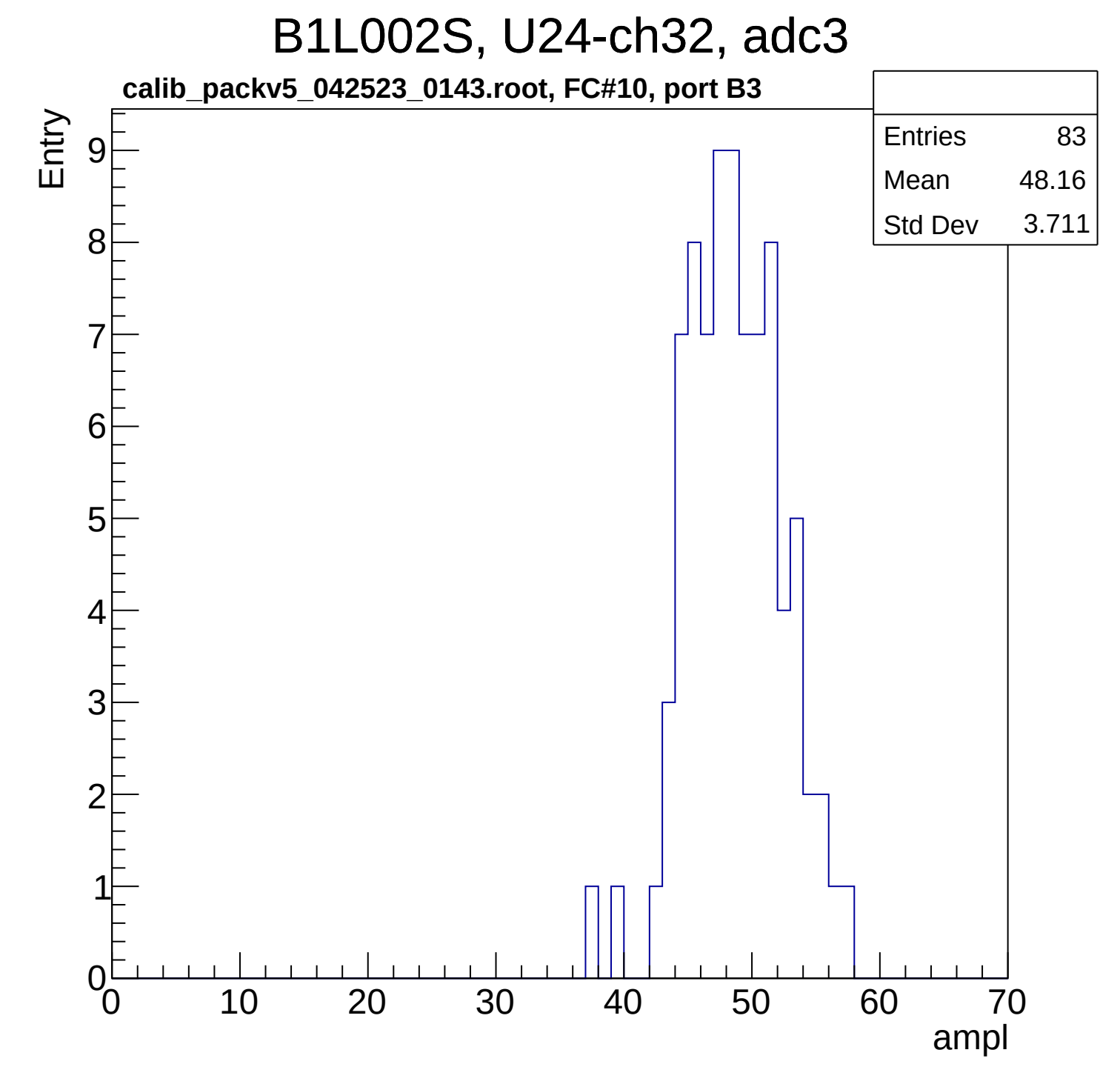
B1L002S, U24-ch32, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 48.16
Std Dev 3.711
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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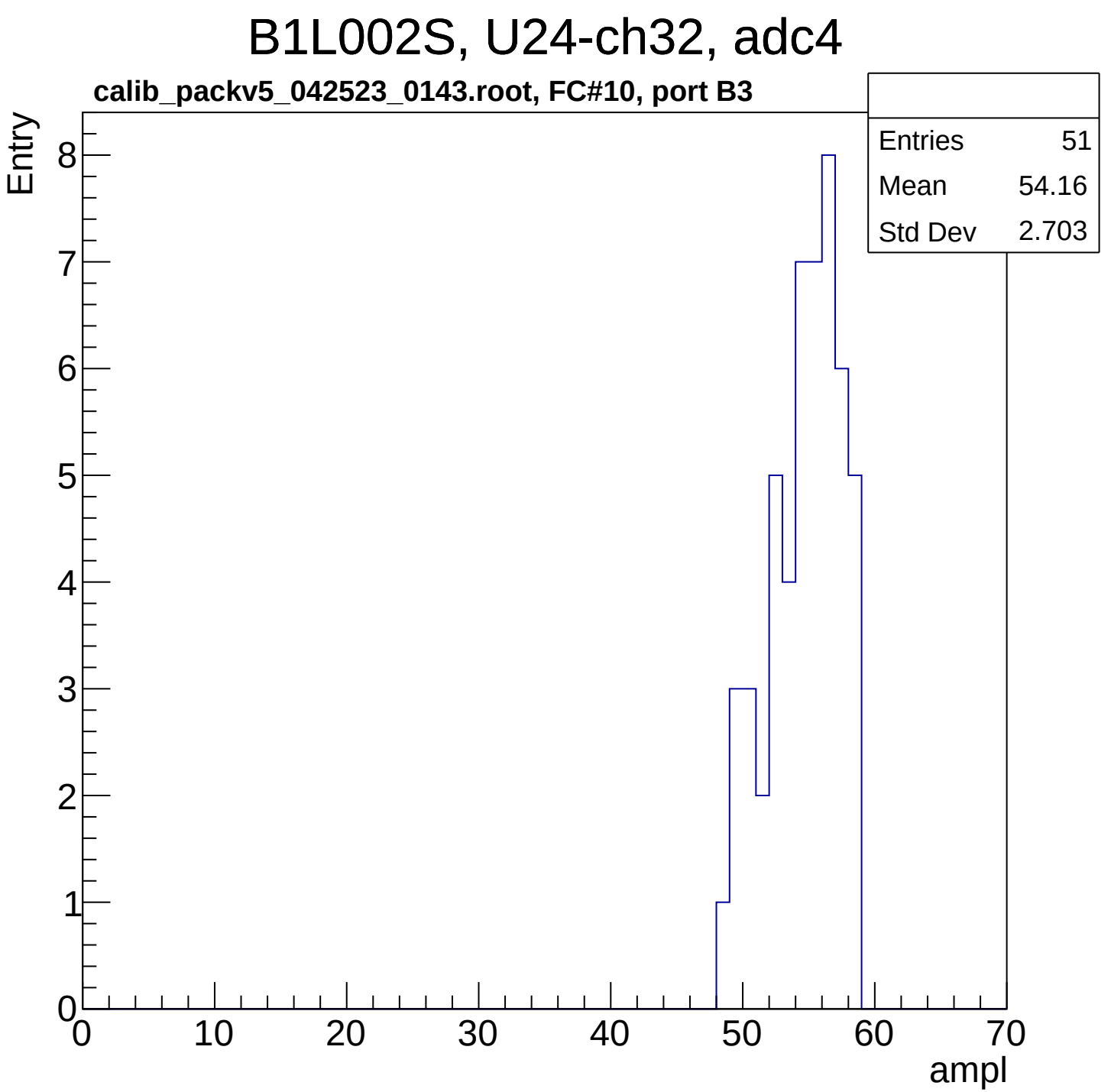
B1L002S, U24-ch32, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 54.16
Std Dev 2.703
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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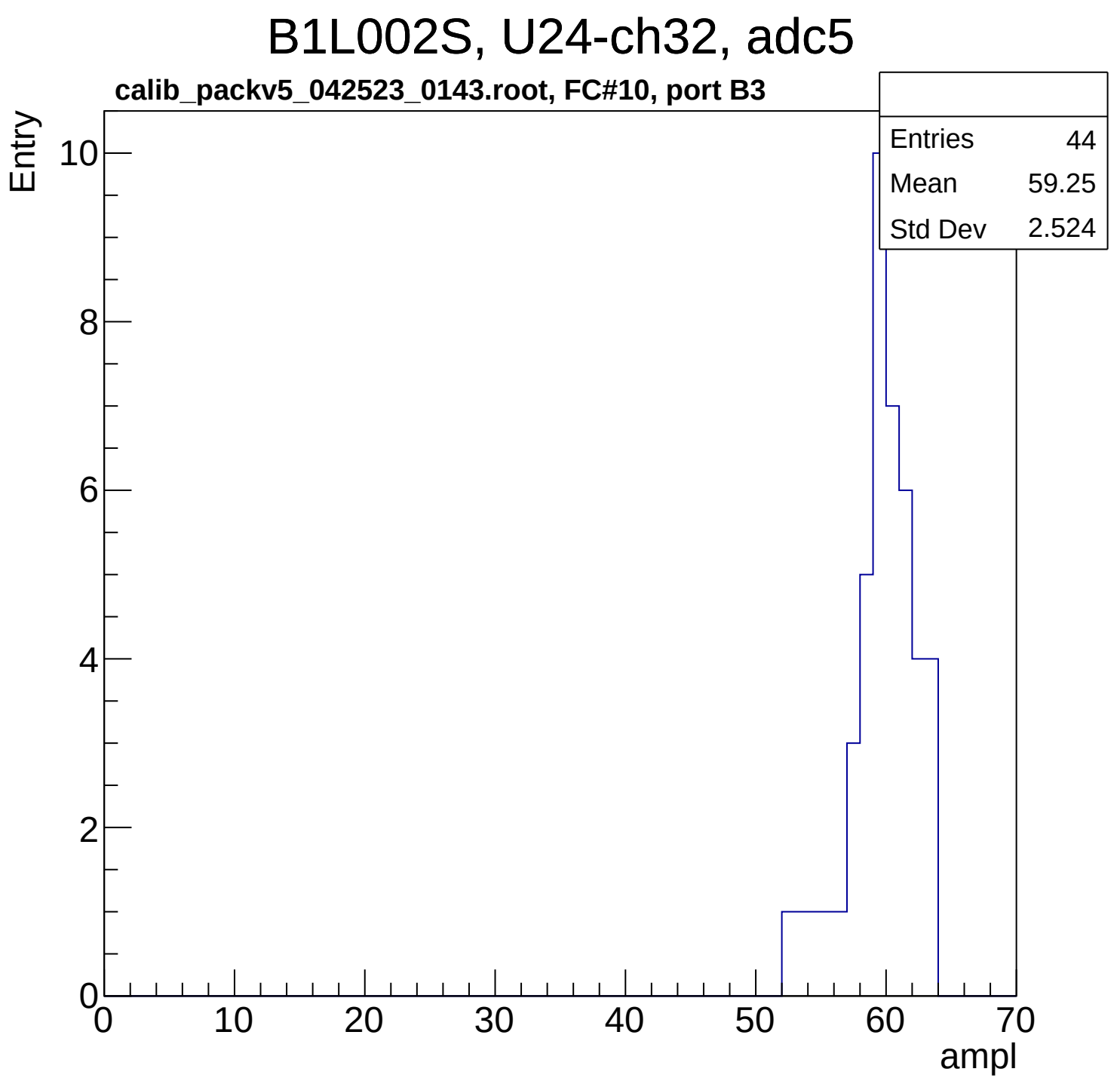
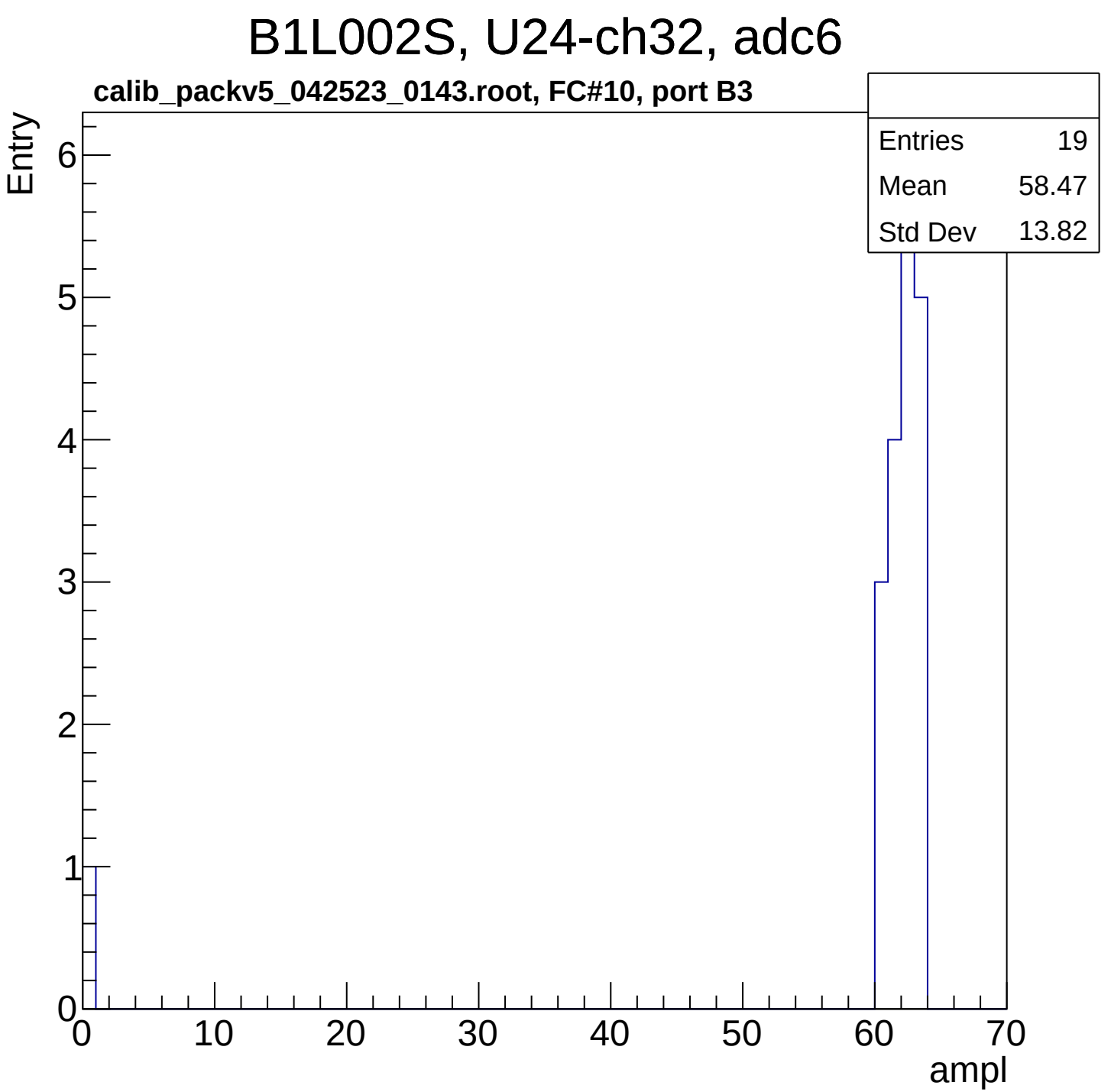
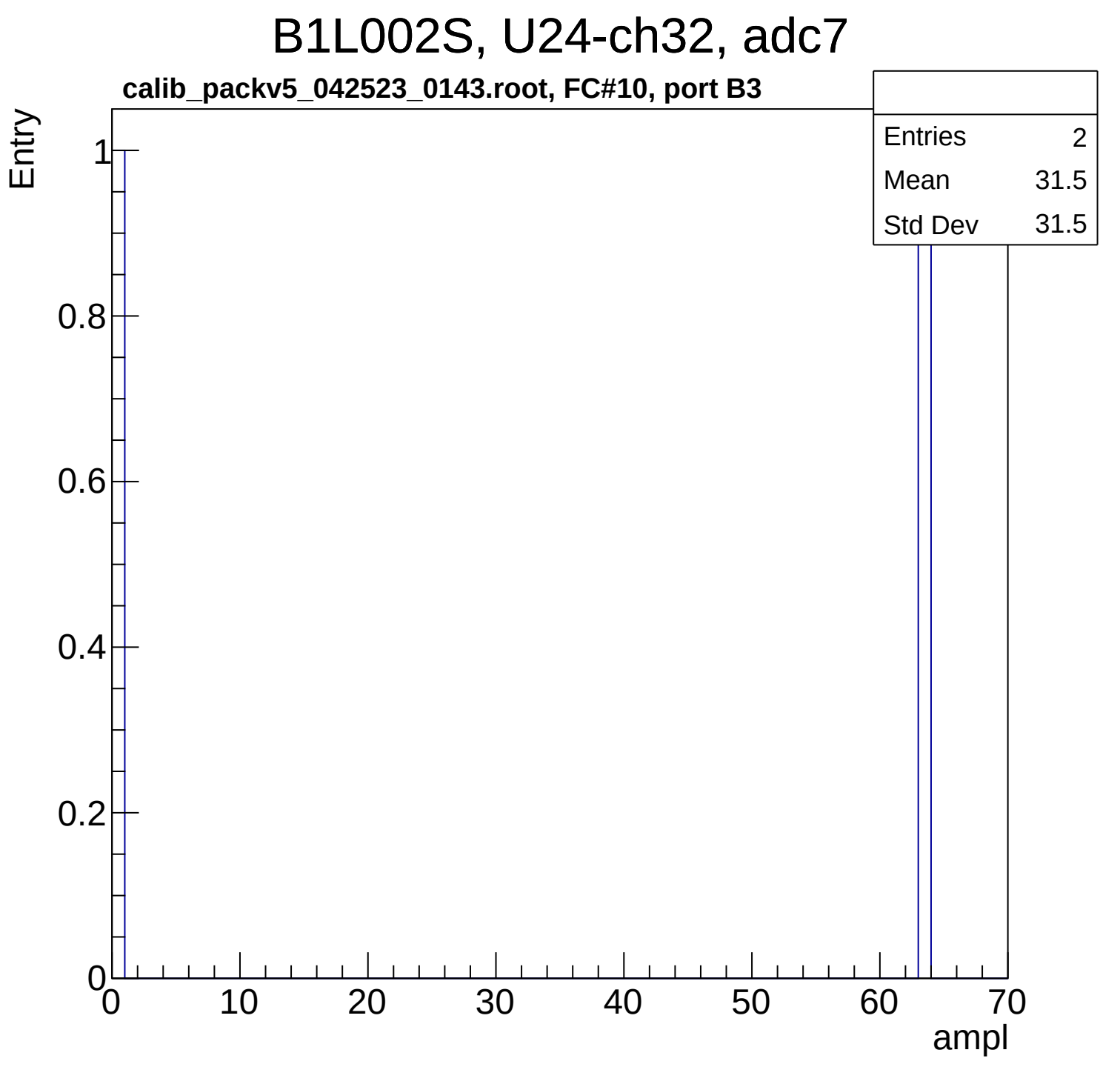

B1L002S, U24-ch32, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 59.25
Std Dev 2.524
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch32, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.47
Std Dev 13.82
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch32, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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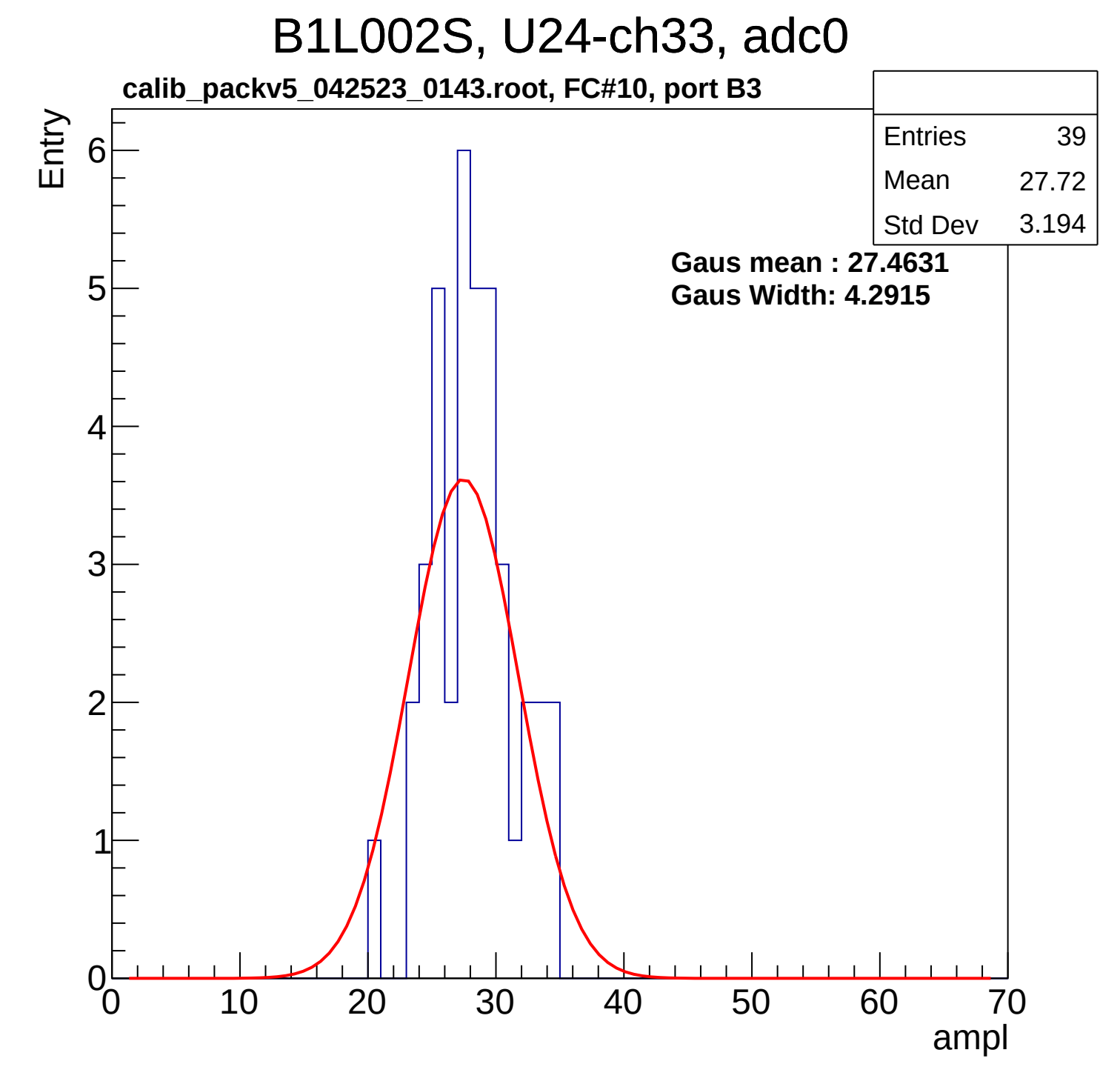
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch33, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 27.72
Std Dev 3.194
Gaus mean : 27.4631
Gaus Width: 4.2915
Entry
ampl
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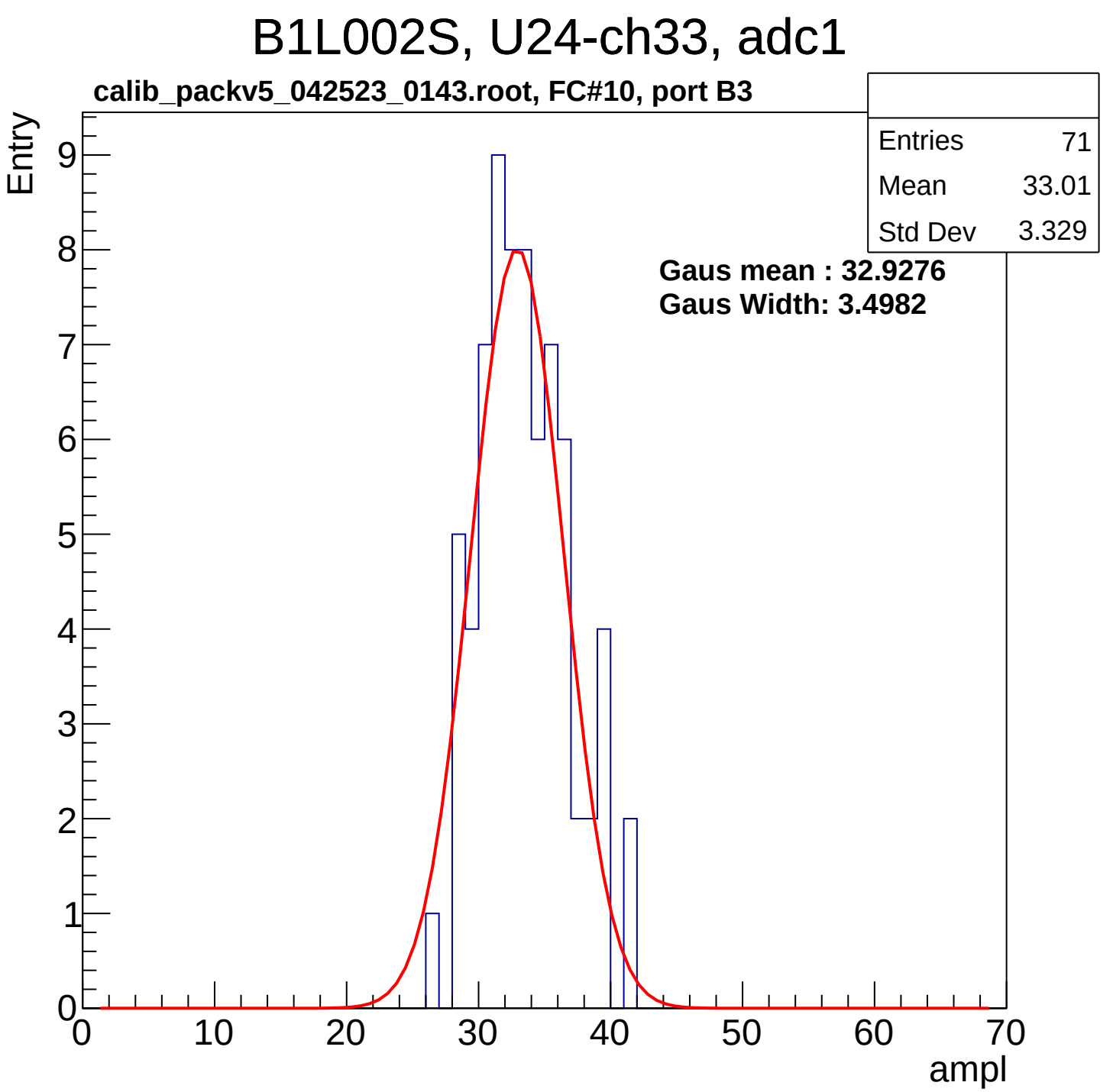
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch33, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.01
Std Dev 3.329
Gaus mean : 32.9276
Gaus Width: 3.4982
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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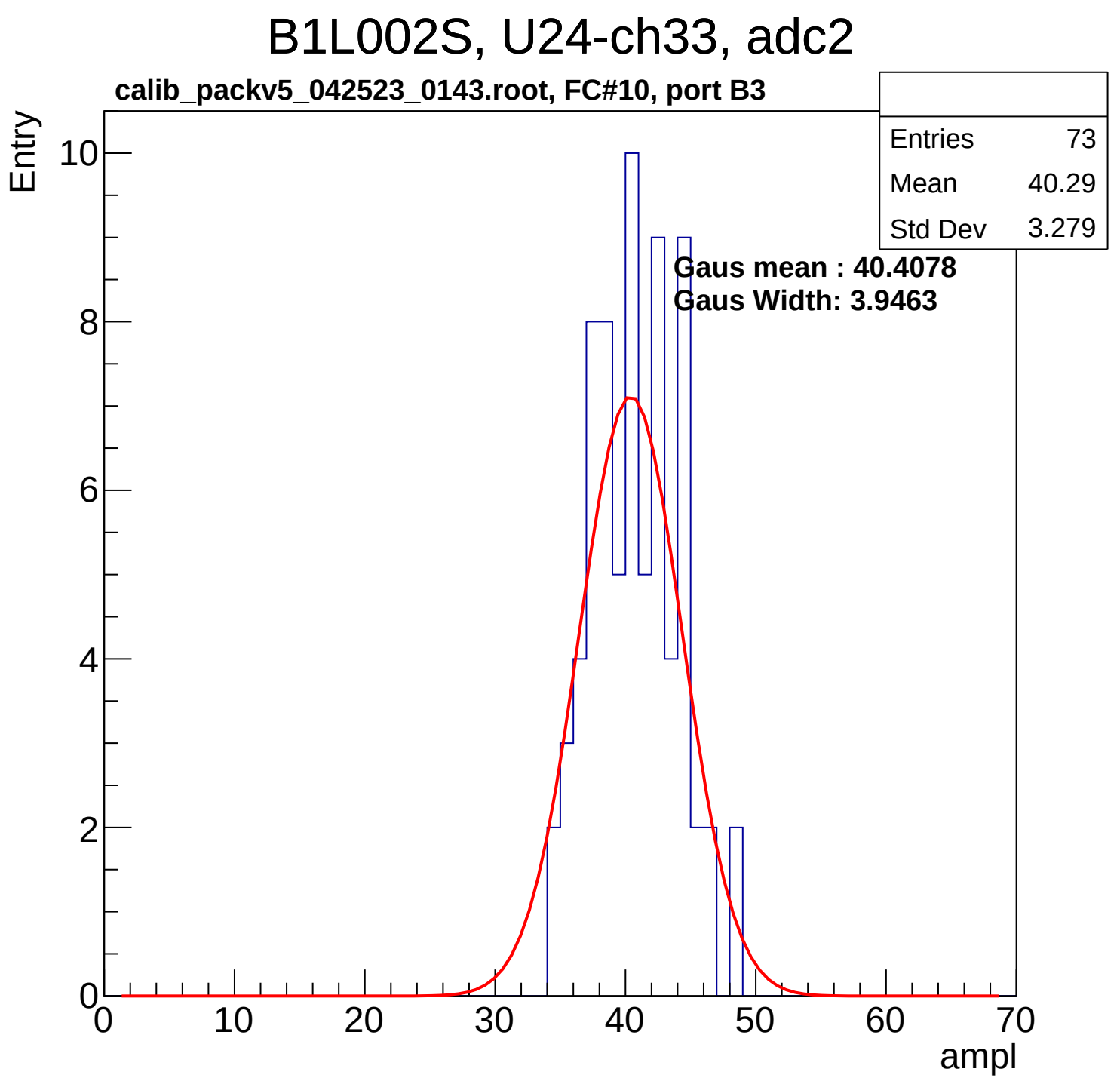
60
70


B1L002S, U24-ch33, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 40.29
Std Dev 3.279
Gaus mean : 40.4078
Gaus Width: 3.9463
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
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60
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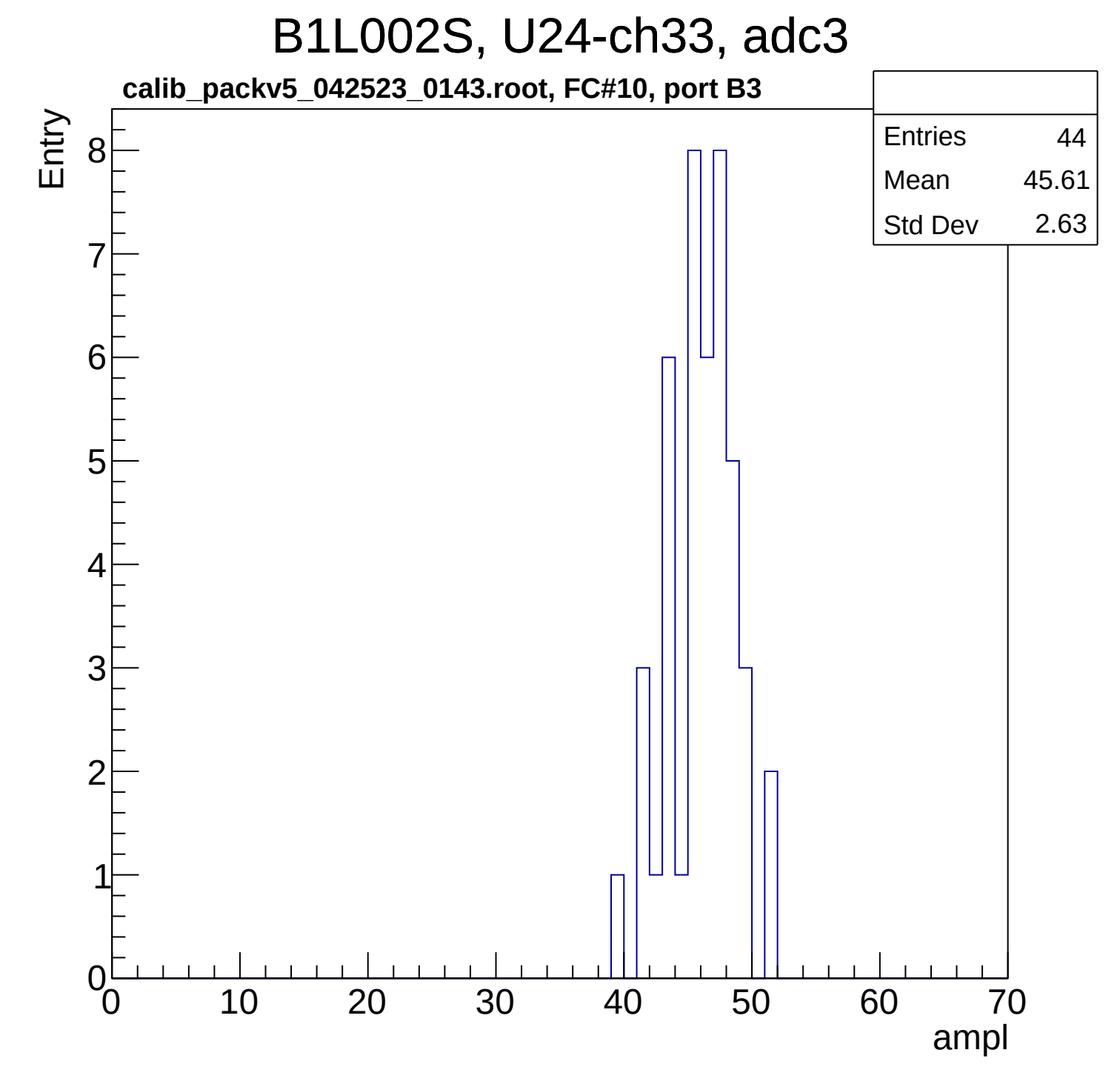
B1L002S, U24-ch33, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 45.61
Std Dev 2.63
Entry
ampl
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0
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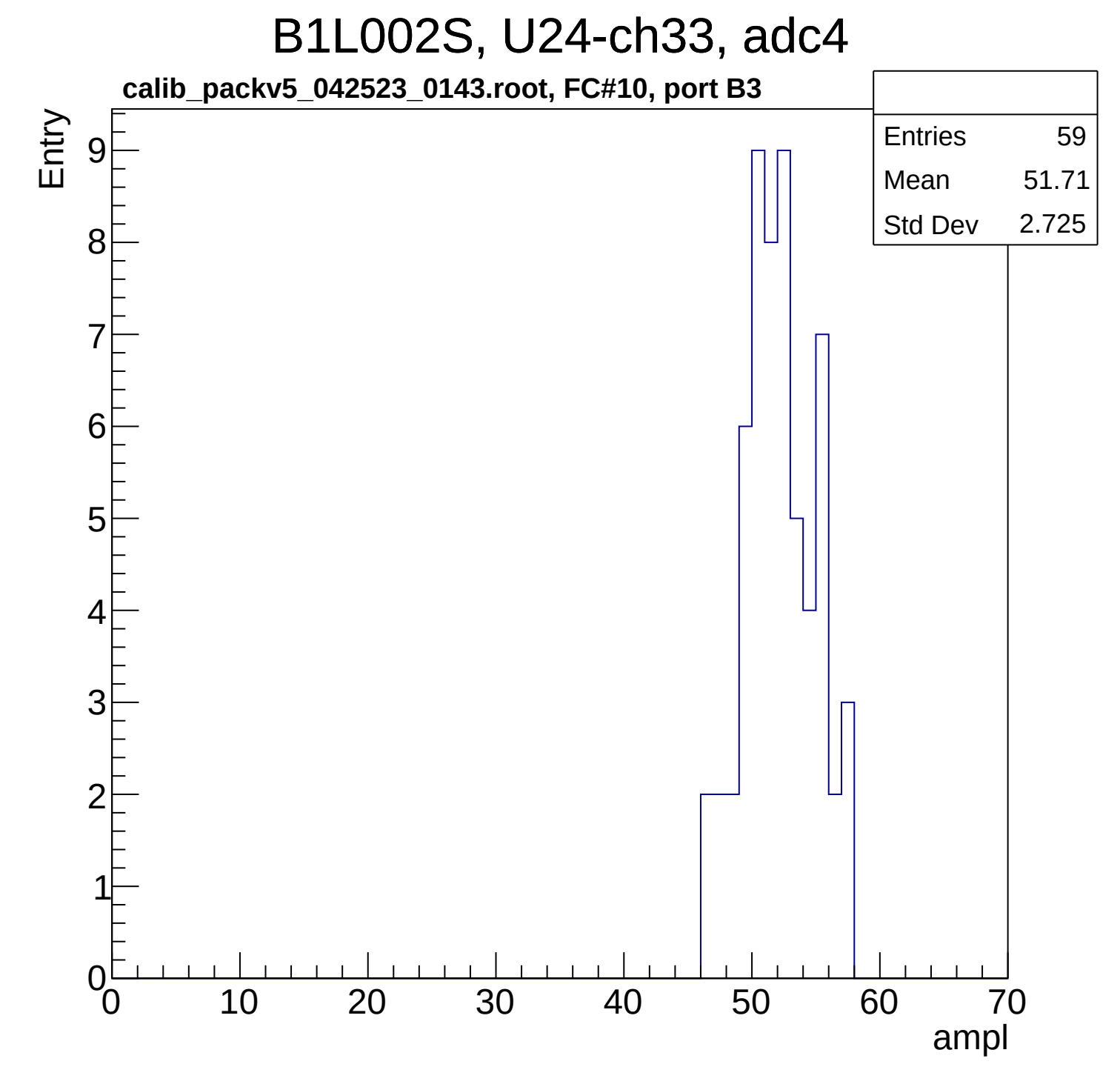
B1L002S, U24-ch33, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 51.71
Std Dev 2.725
Entry
ampl
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3
4
5
6
7
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0
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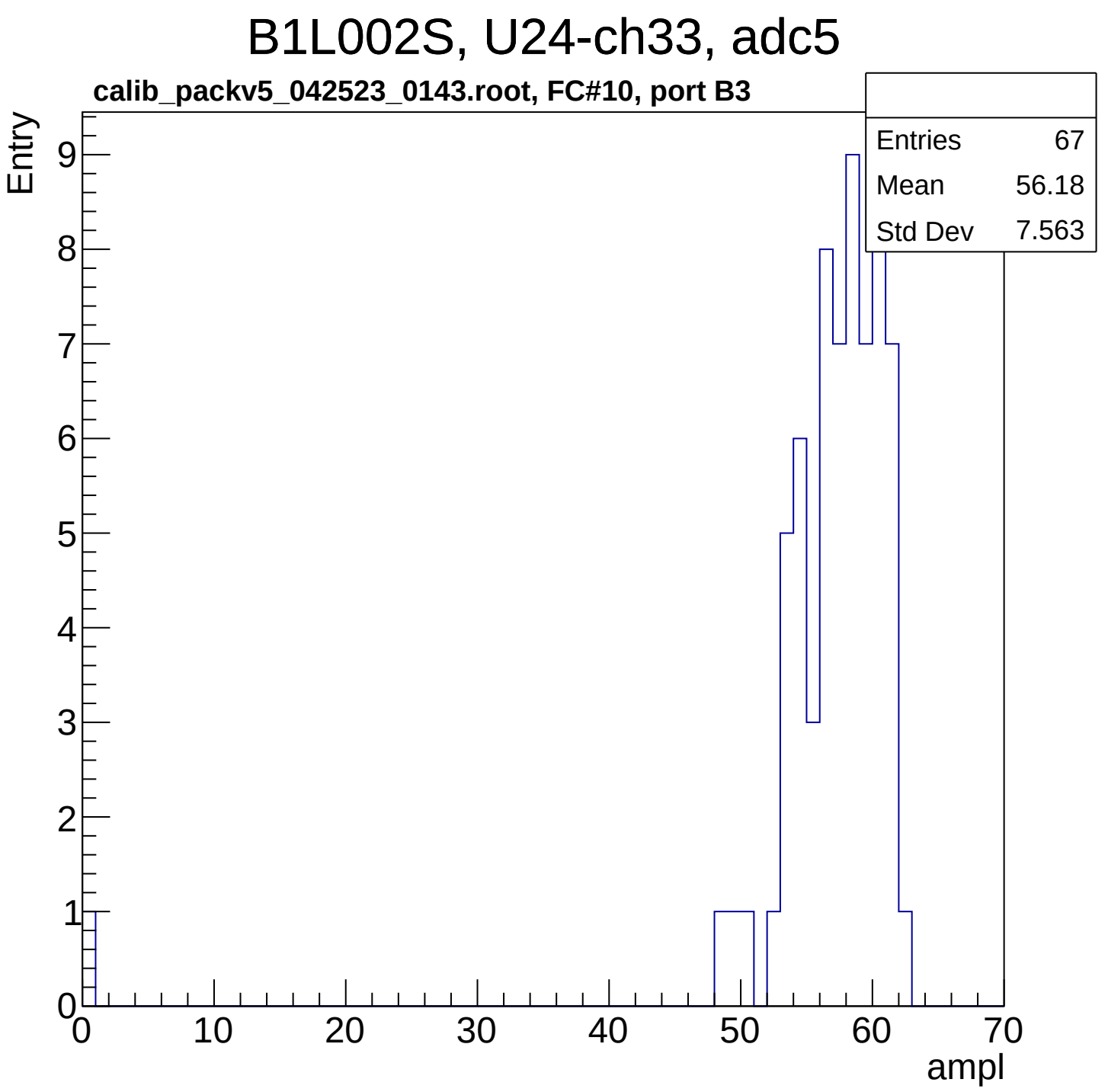
B1L002S, U24-ch33, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 56.18
Std Dev 7.563
Entry
ampl
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3
4
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6
7
8
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0
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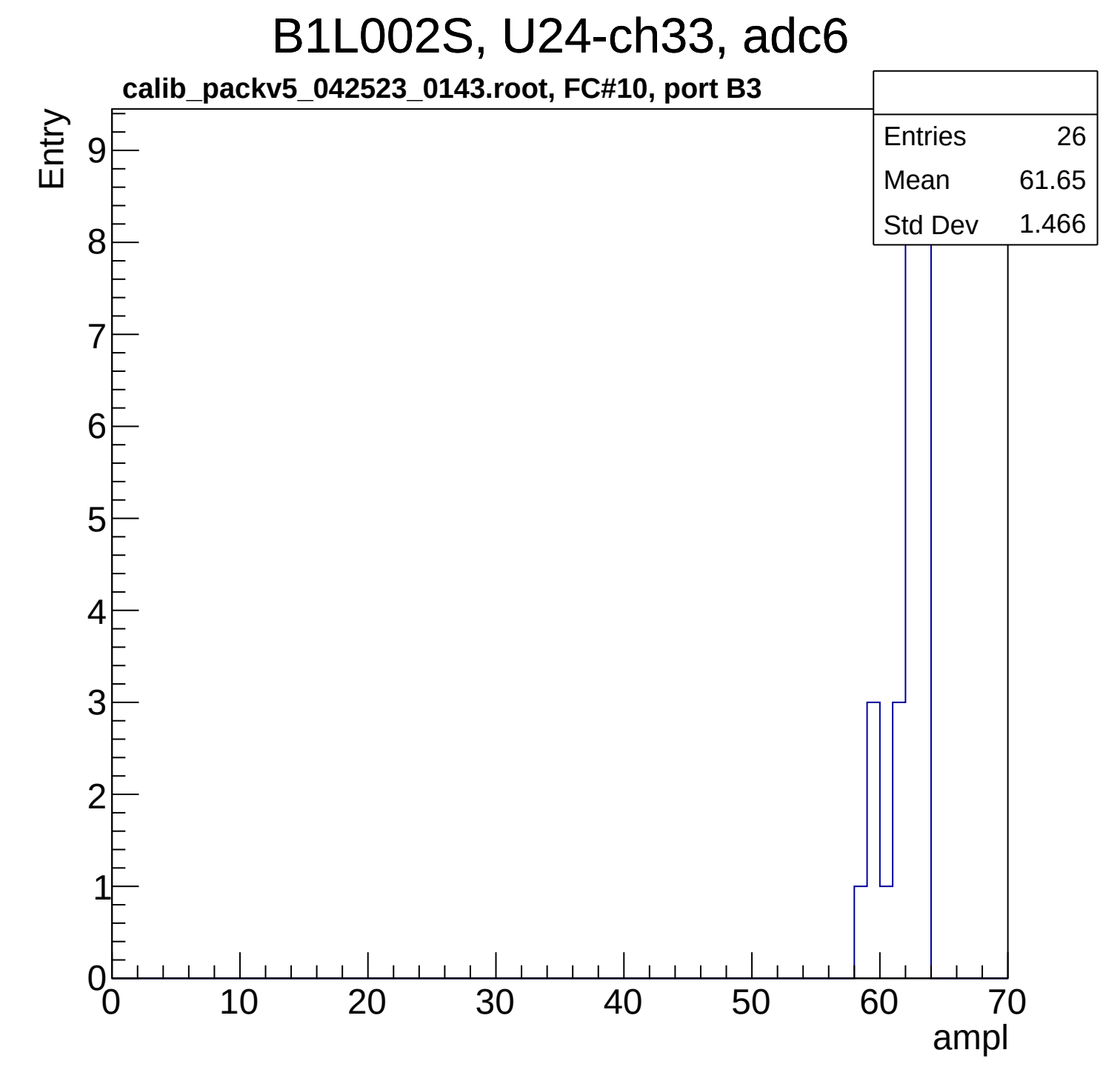
B1L002S, U24-ch33, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 61.65
Std Dev 1.466
Entry
ampl
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4
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6
7
8
9
0
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60
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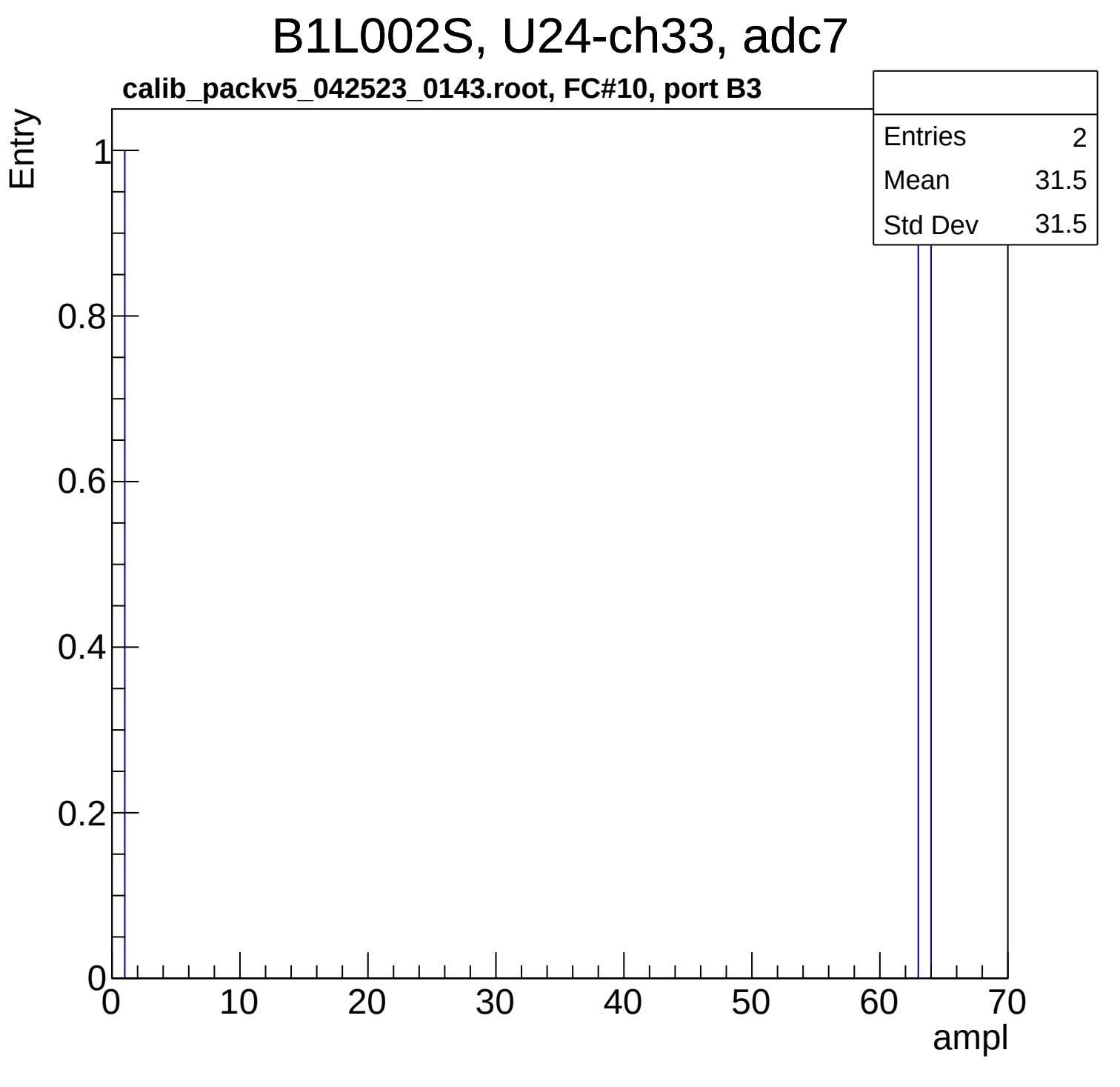
B1L002S, U24-ch33, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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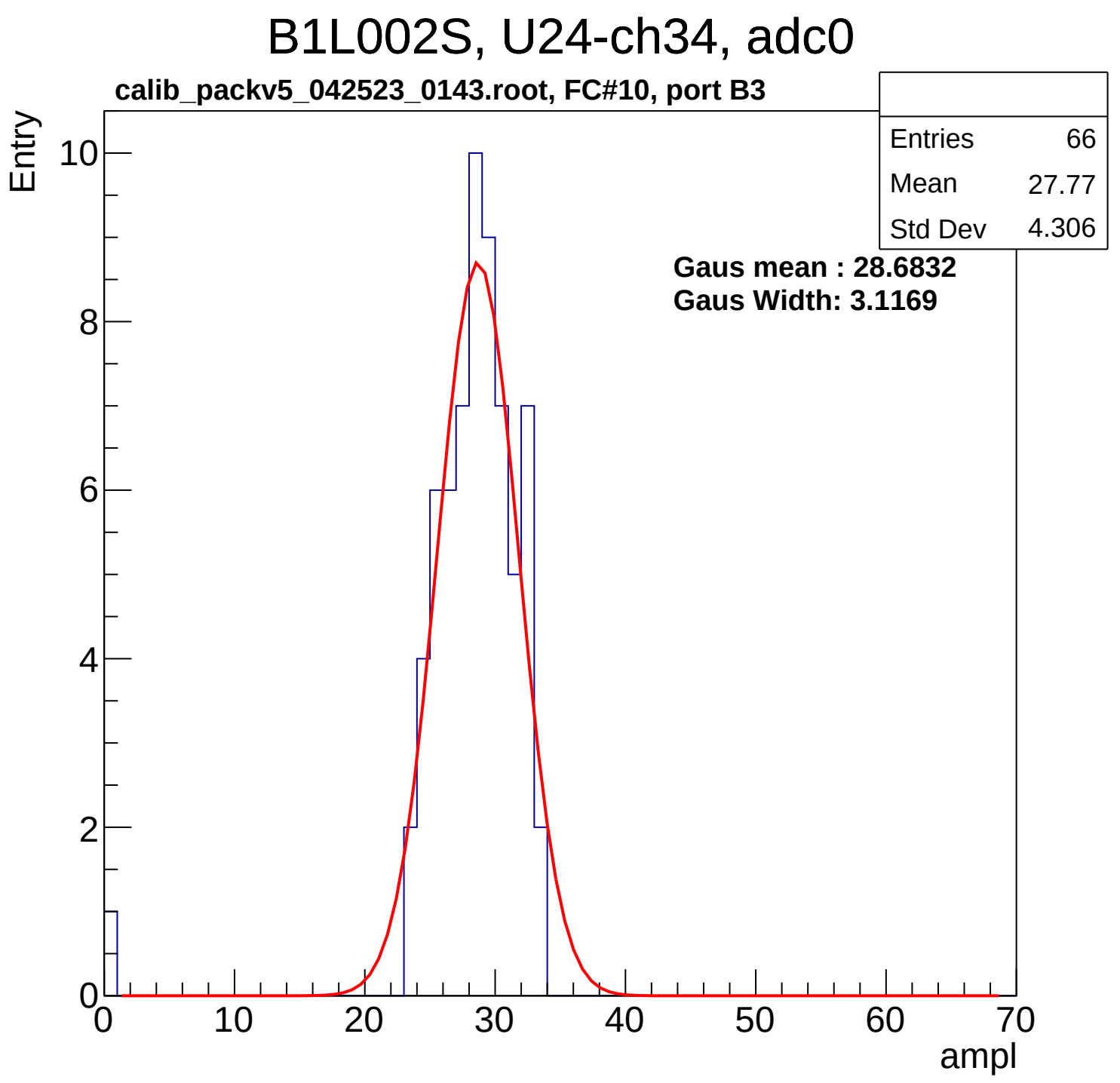
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch34, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.77
Std Dev 4.306
Gaus mean : 28.6832
Gaus Width: 3.1169
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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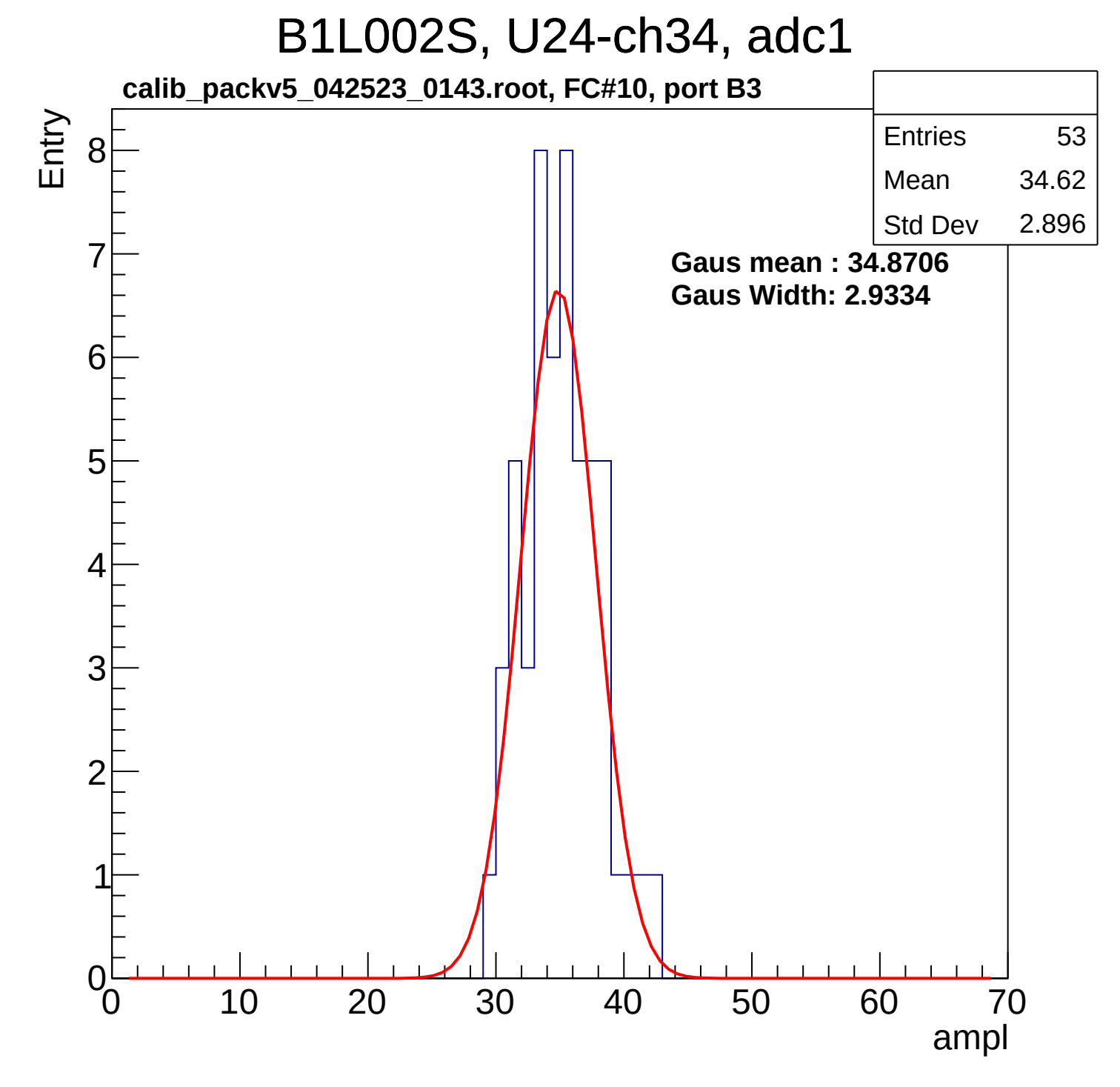
B1L002S, U24-ch34, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 34.62
Std Dev 2.896
Gaus mean : 34.8706
Gaus Width: 2.9334
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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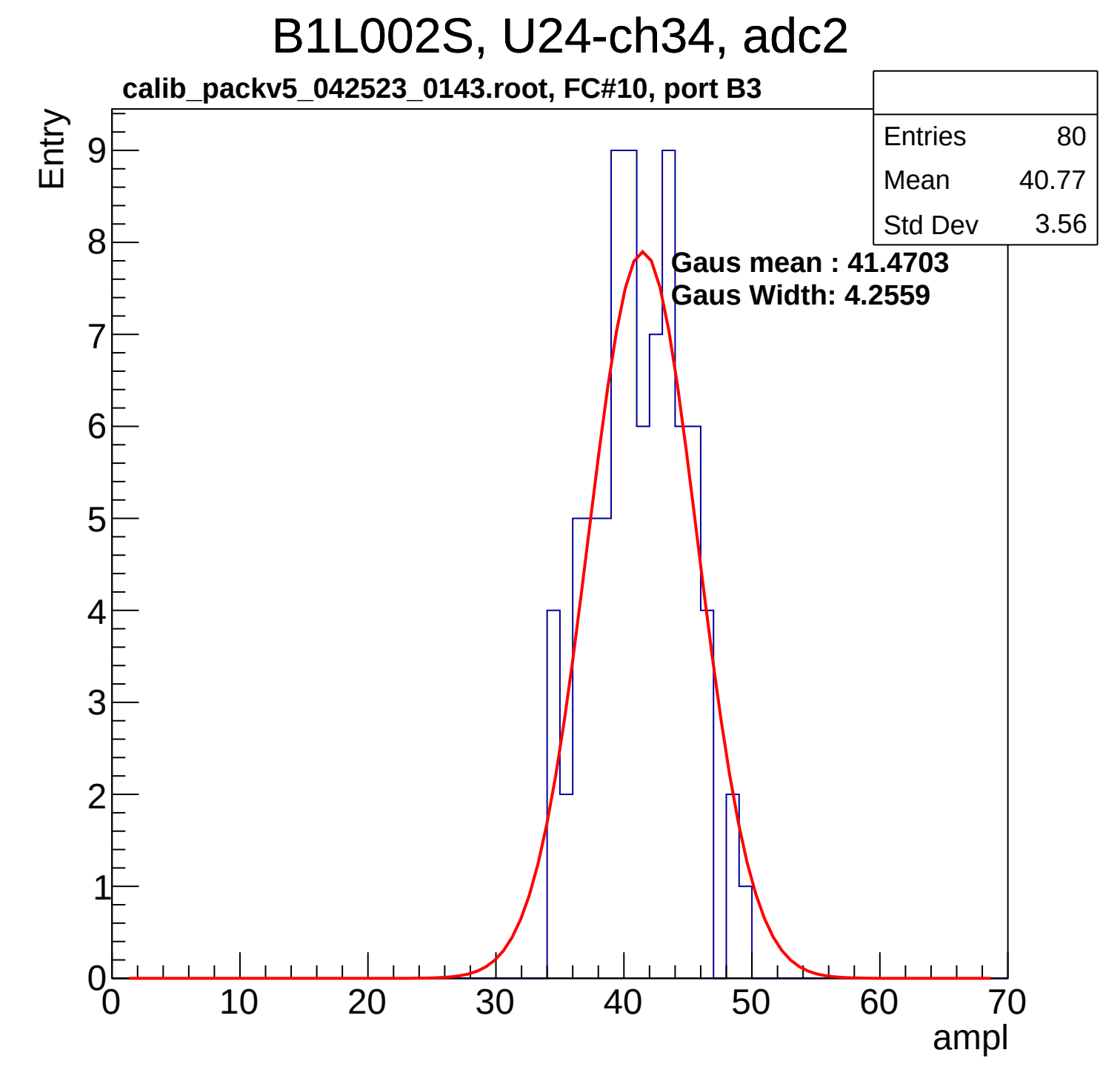
B1L002S, U24-ch34, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 40.77
Std Dev 3.56
Gaus mean : 41.4703
Gaus Width: 4.2559
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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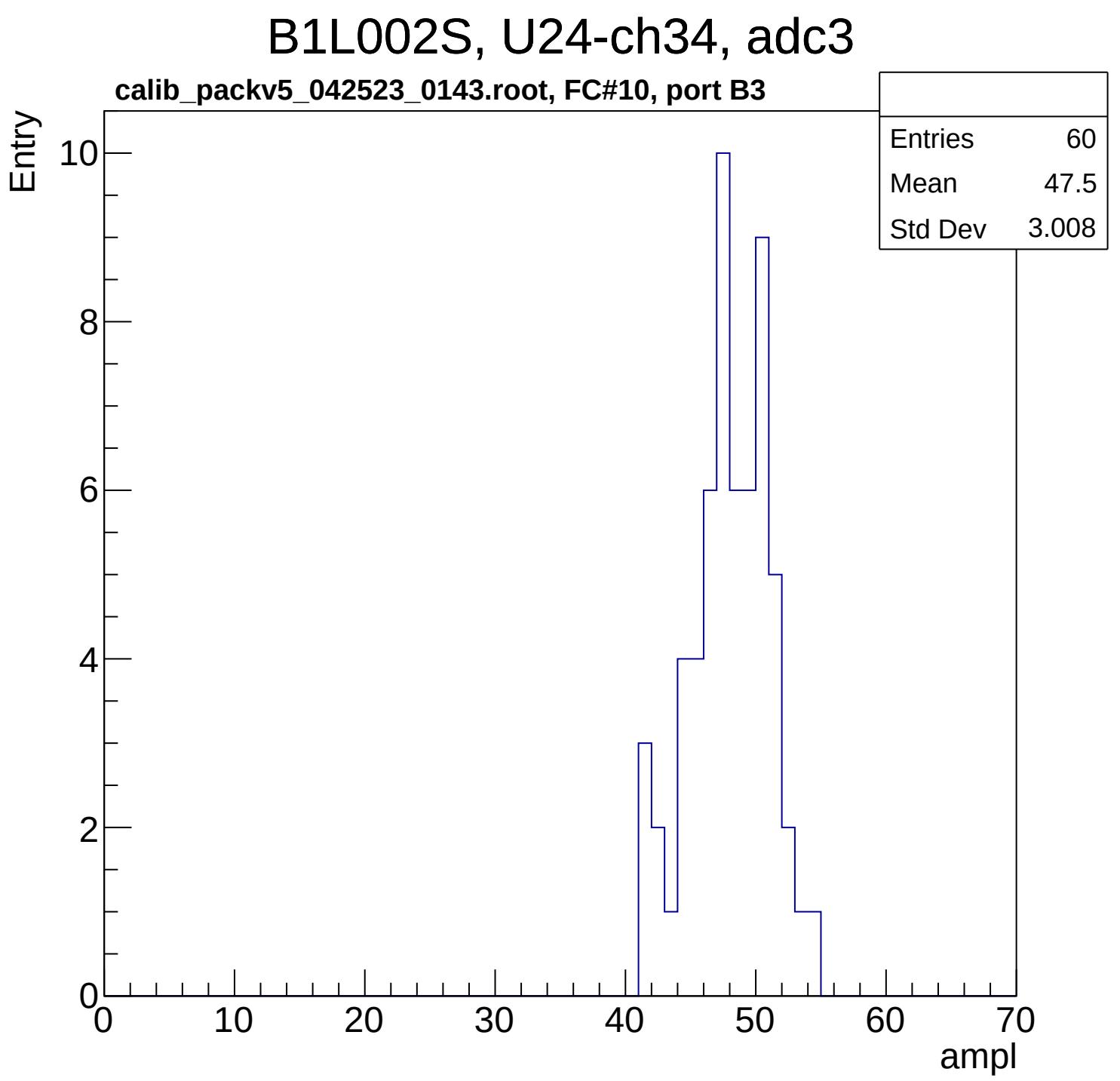
B1L002S, U24-ch34, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.5
Std Dev 3.008
Entry
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2
4
6
8
10
0
10
20
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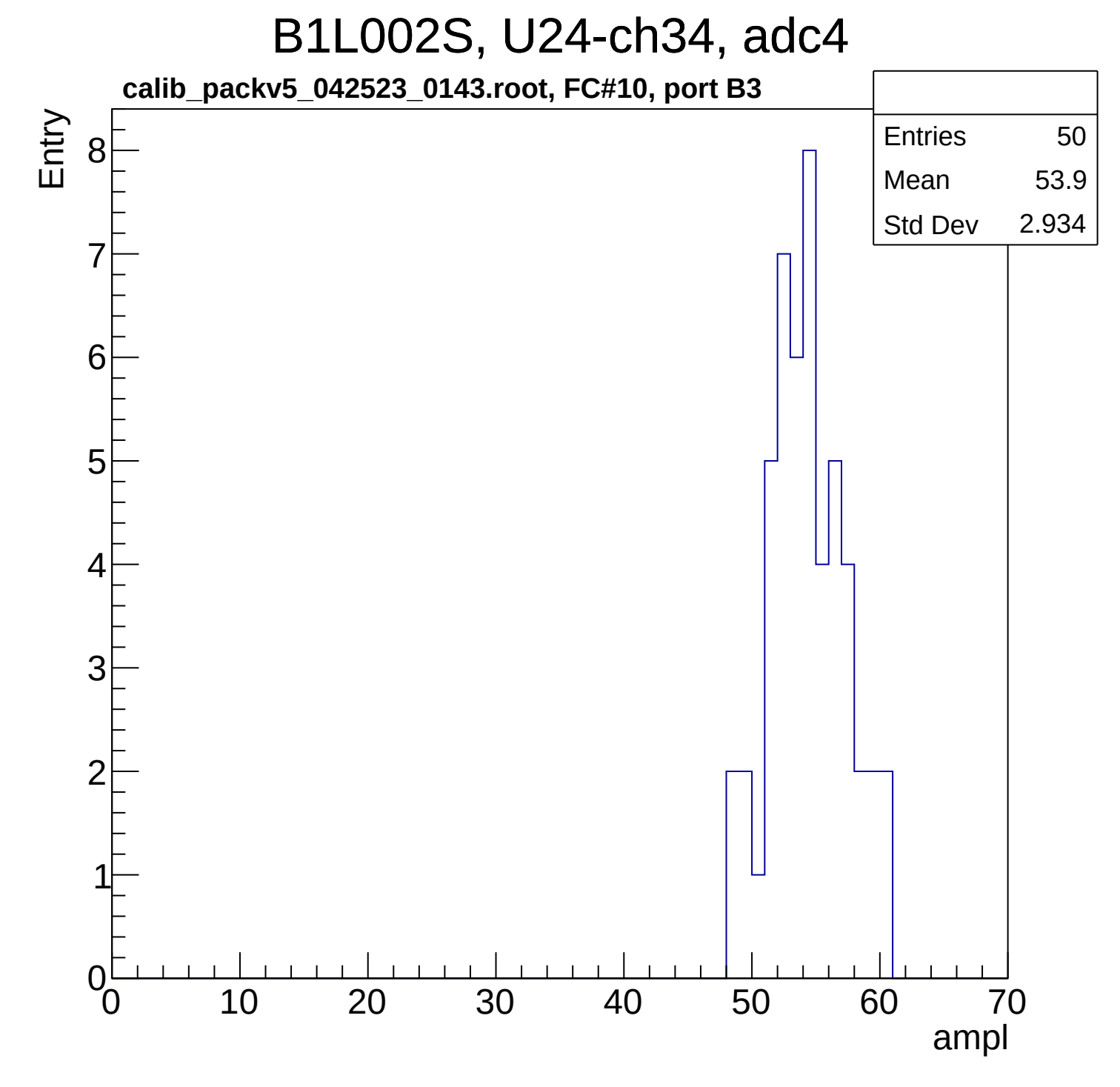
50
60
70
ampl


B1L002S, U24-ch34, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 53.9
Std Dev 2.934
Entry
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10
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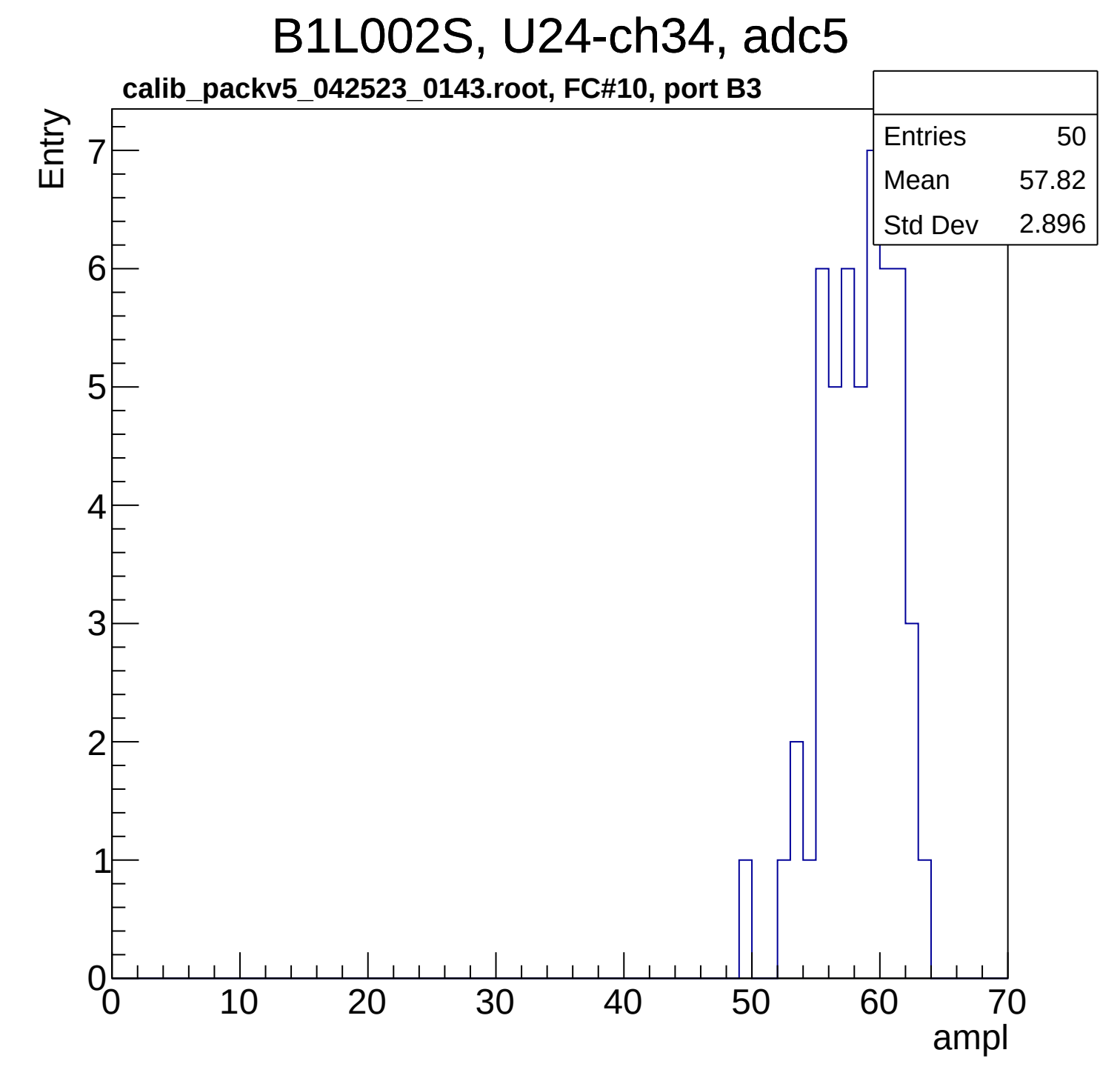
50
60
70
ampl


B1L002S, U24-ch34, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 57.82
Std Dev 2.896
Entry
ampl
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2
3
4
5
6
7
0
10
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30
40
50
60
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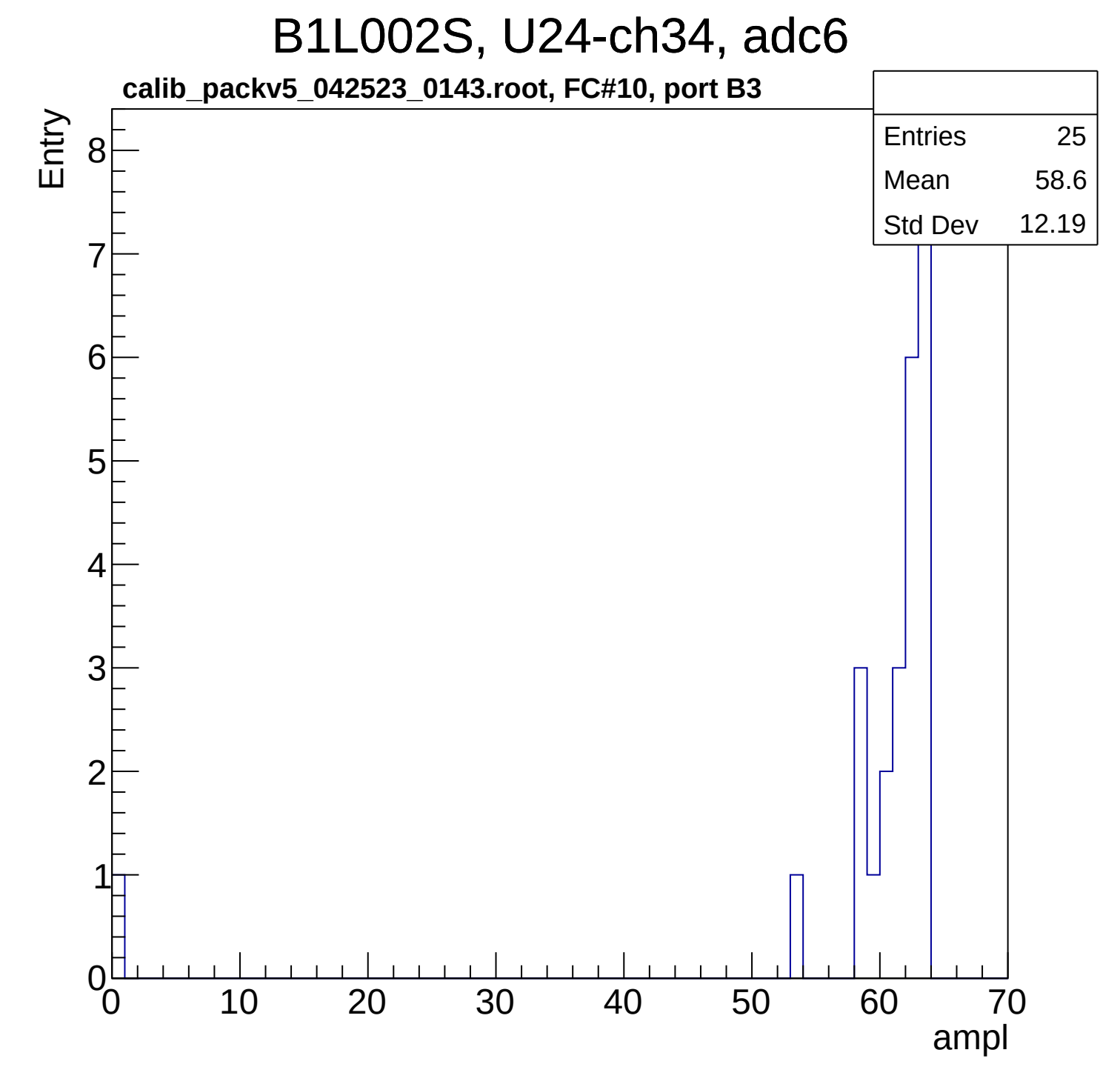
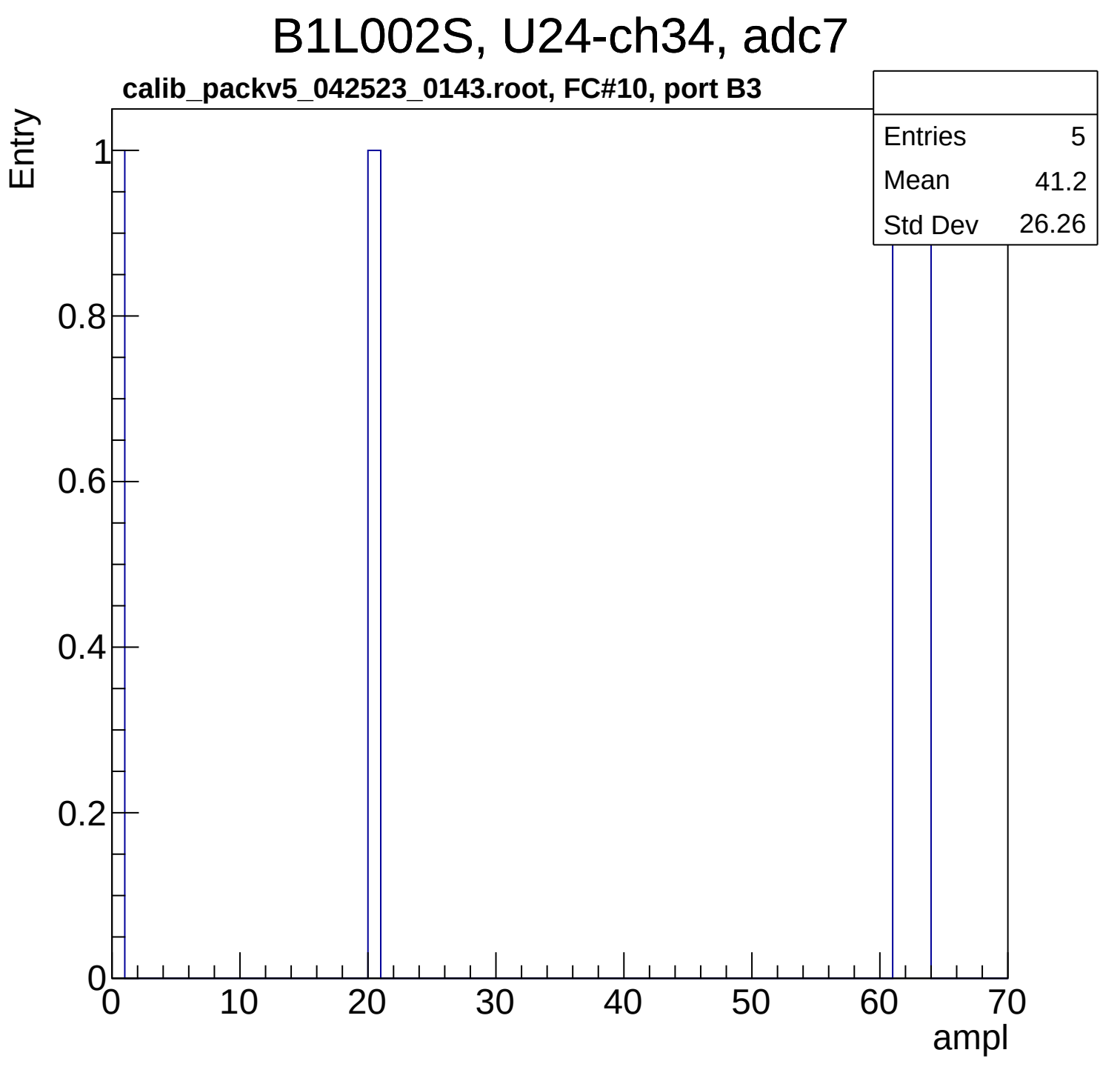

B1L002S, U24-ch34, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 25
Mean 58.6
Std Dev 12.19
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch34, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 41.2
Std Dev 26.26
Entry
ampl
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0.2
0.4
0.6
0.8
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0
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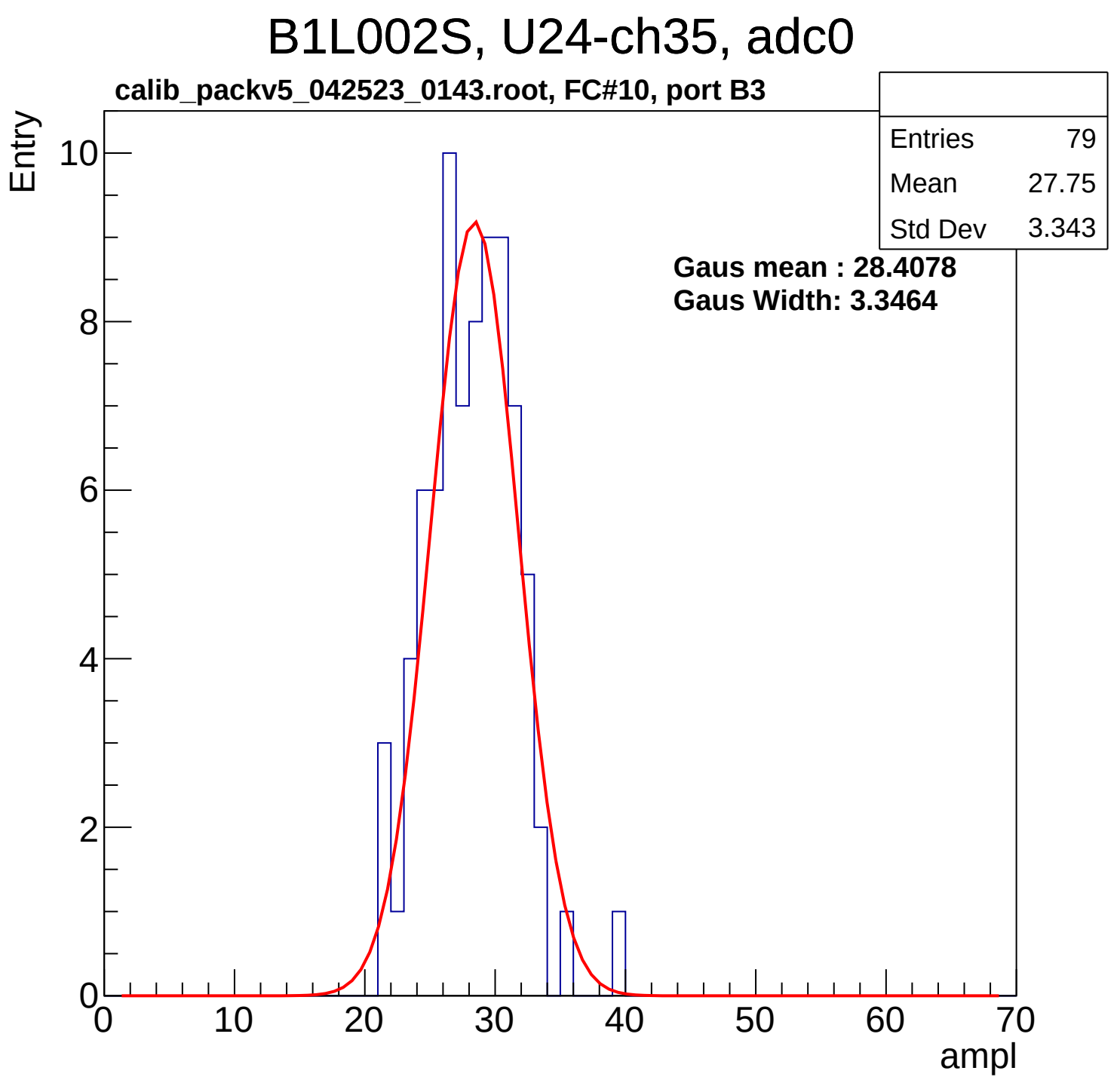
B1L002S, U24-ch35, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.75
Std Dev 3.343
Gaus mean : 28.4078
Gaus Width: 3.3464
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
50
60
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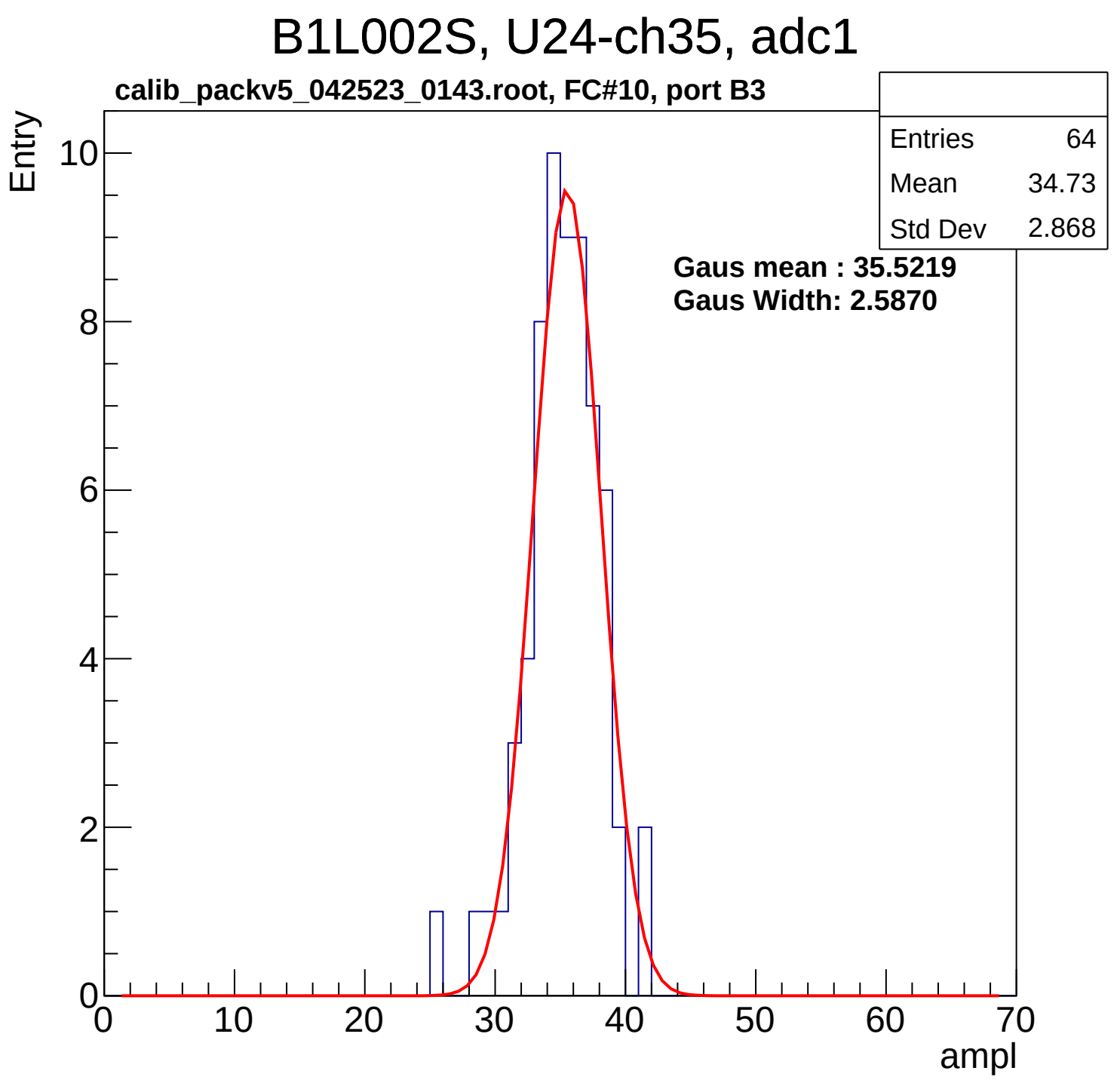
B1L002S, U24-ch35, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 34.73
Std Dev 2.868
Gaus mean : 35.5219
Gaus Width: 2.5870
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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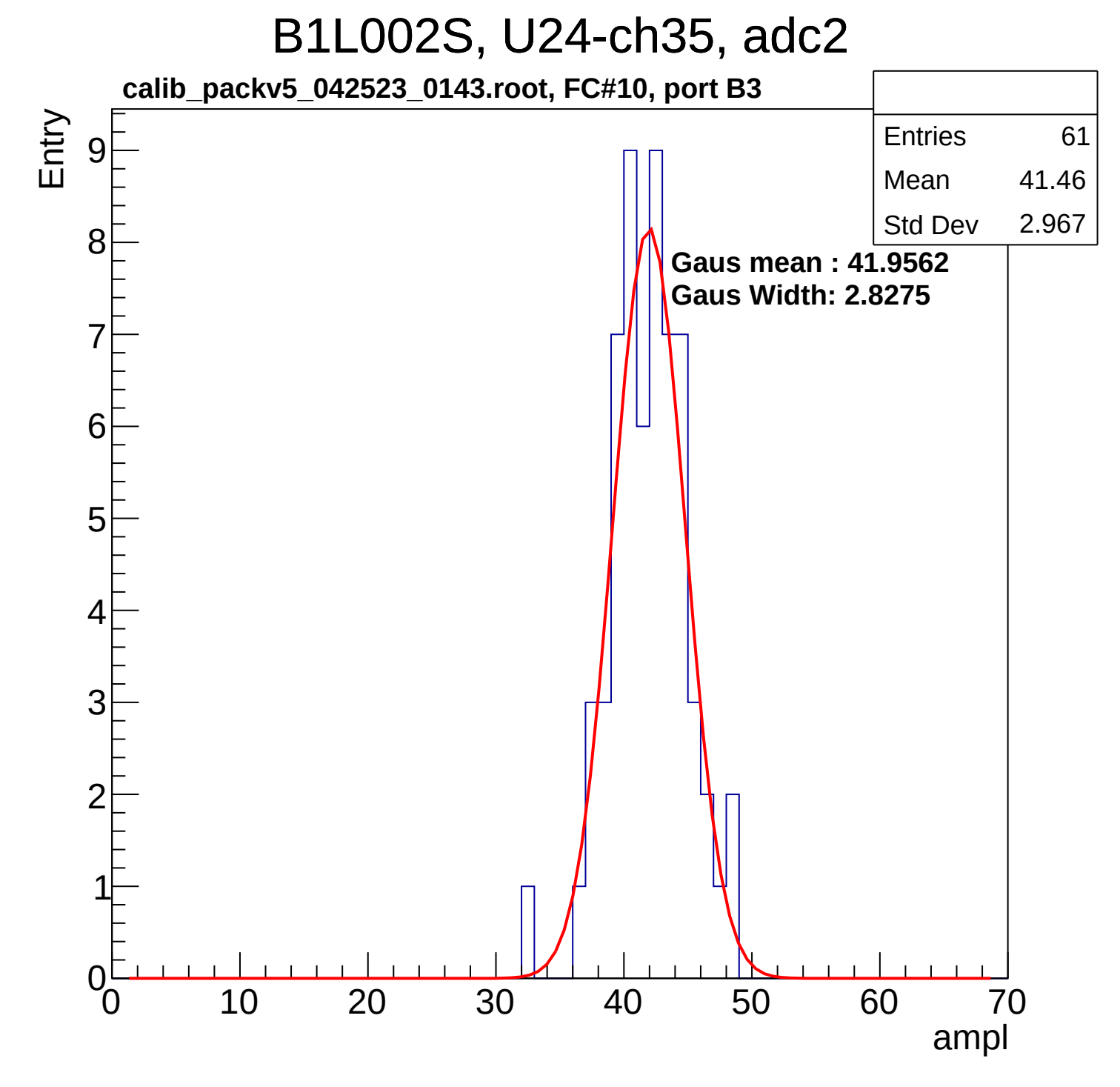
B1L002S, U24-ch35, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.46
Std Dev 2.967
Gaus mean : 41.9562
Gaus Width: 2.8275
Entry
ampl
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2
3
4
5
6
7
8
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0
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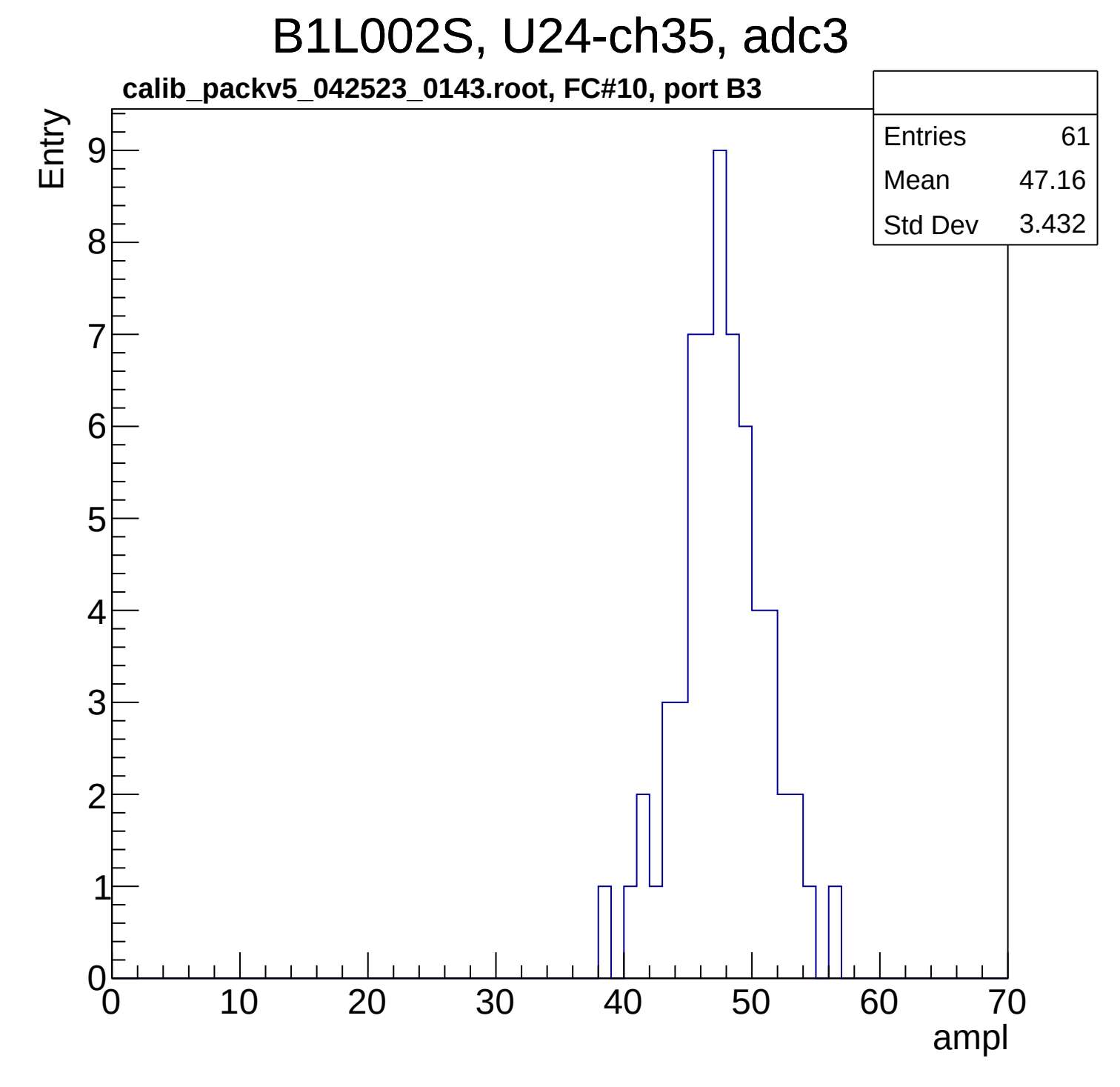
B1L002S, U24-ch35, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.16
Std Dev 3.432
Entry
ampl
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2
3
4
5
6
7
8
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0
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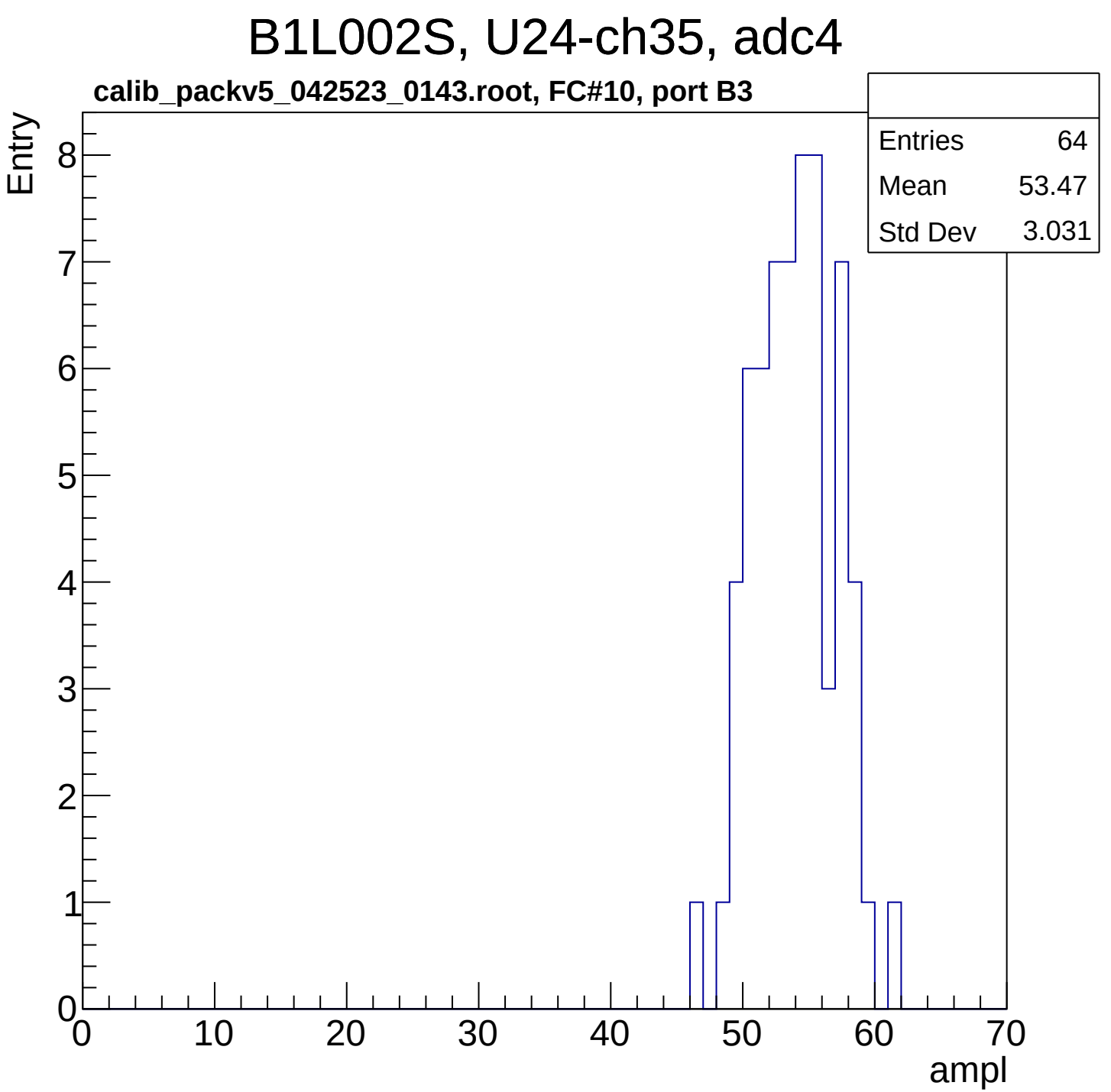
B1L002S, U24-ch35, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.47
Std Dev 3.031
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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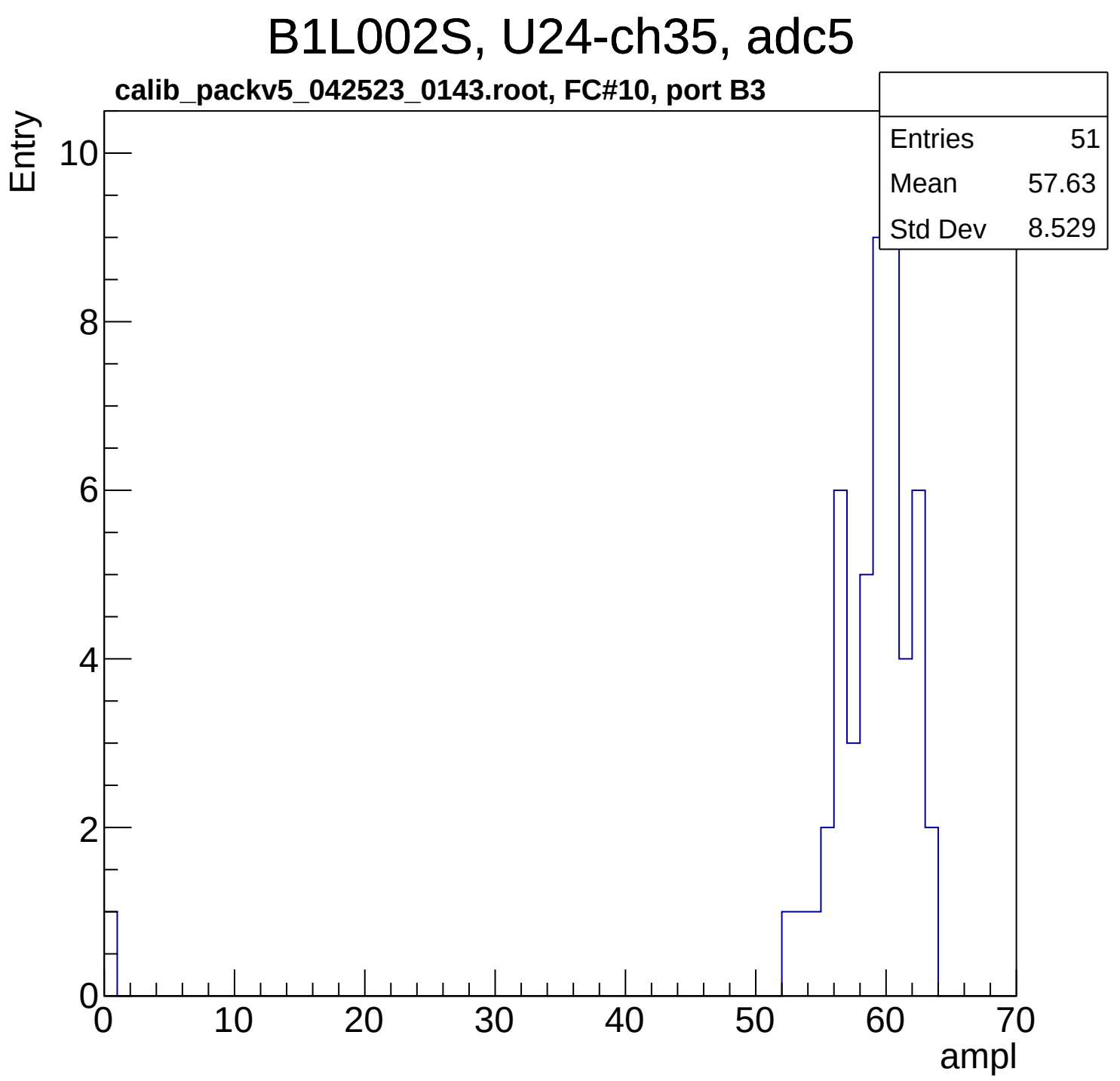
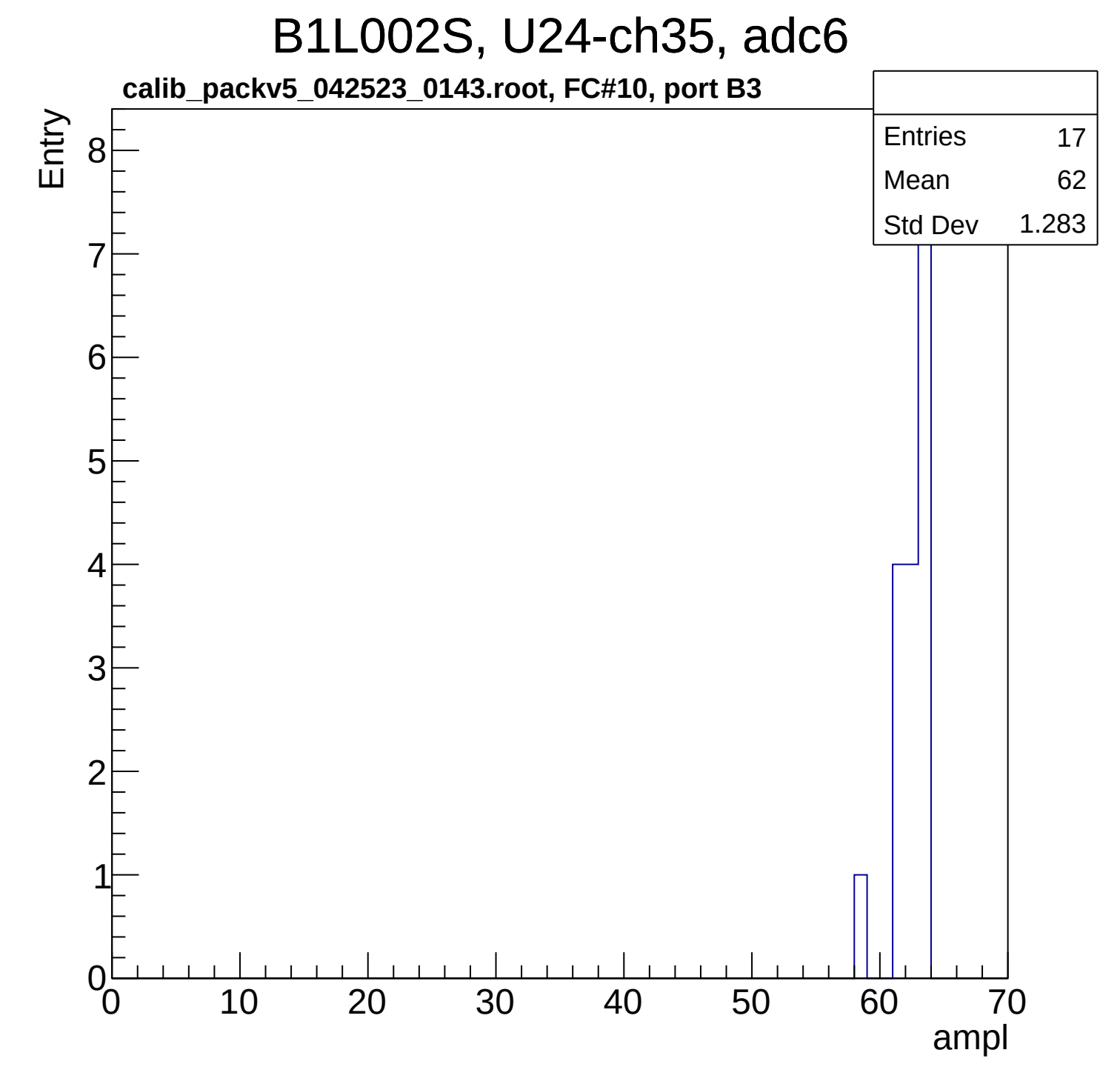

B1L002S, U24-ch35, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.63
Std Dev 8.529
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch35, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 62
Std Dev 1.283
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
50
60
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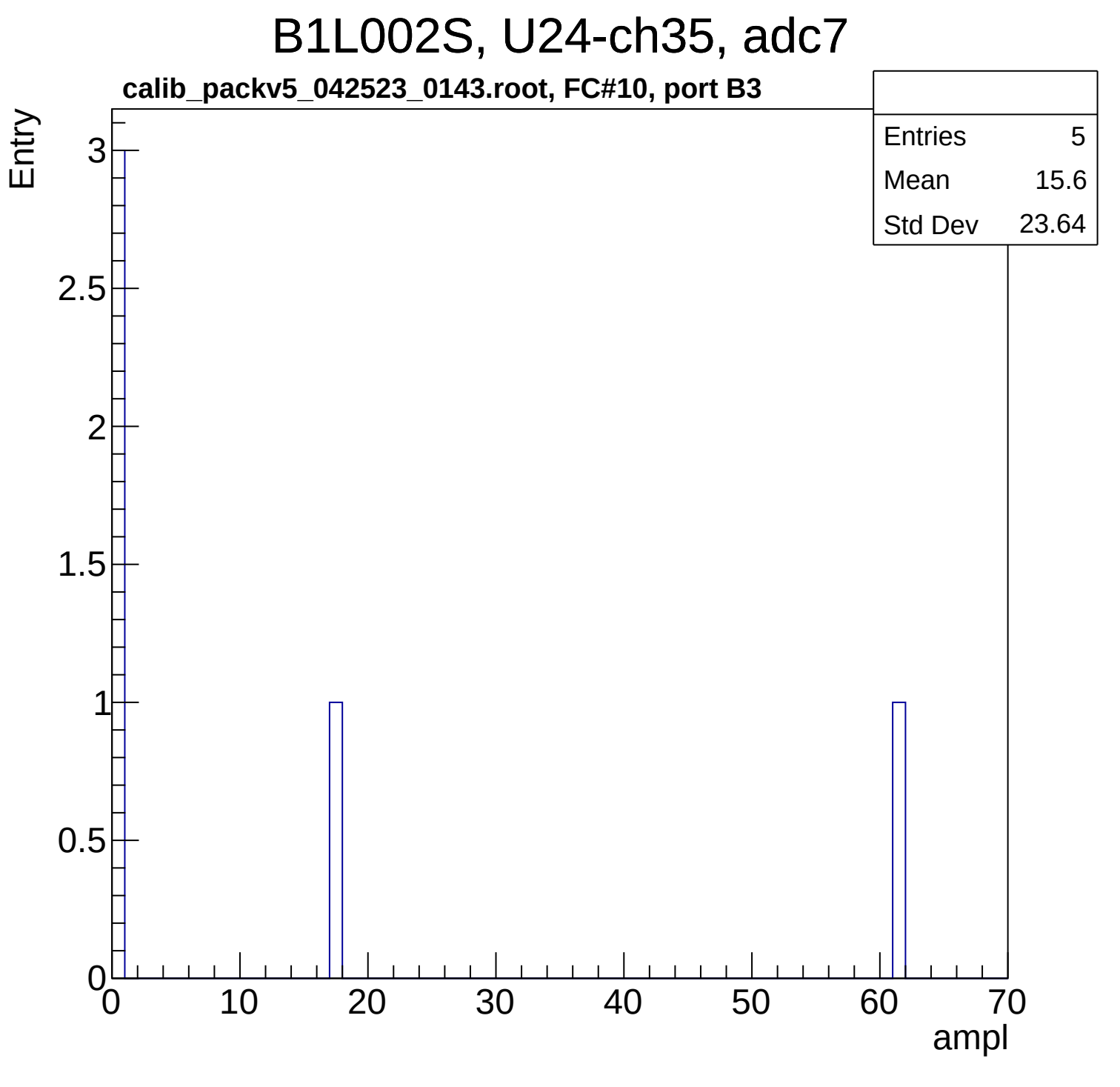
B1L002S, U24-ch35, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 15.6
Std Dev 23.64
Entry
ampl
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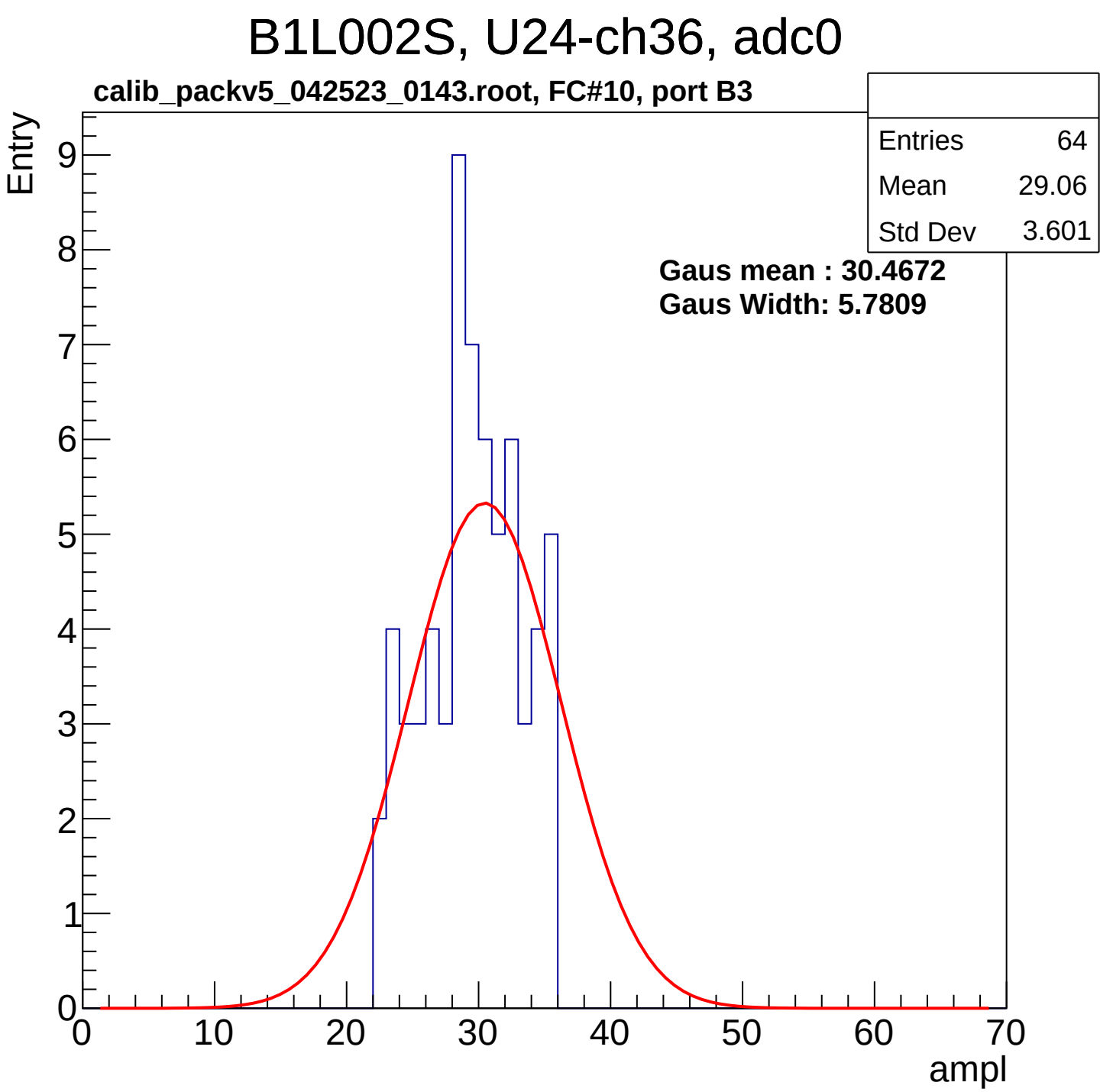
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch36, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 29.06
Std Dev 3.601
Gaus mean : 30.4672
Gaus Width: 5.7809
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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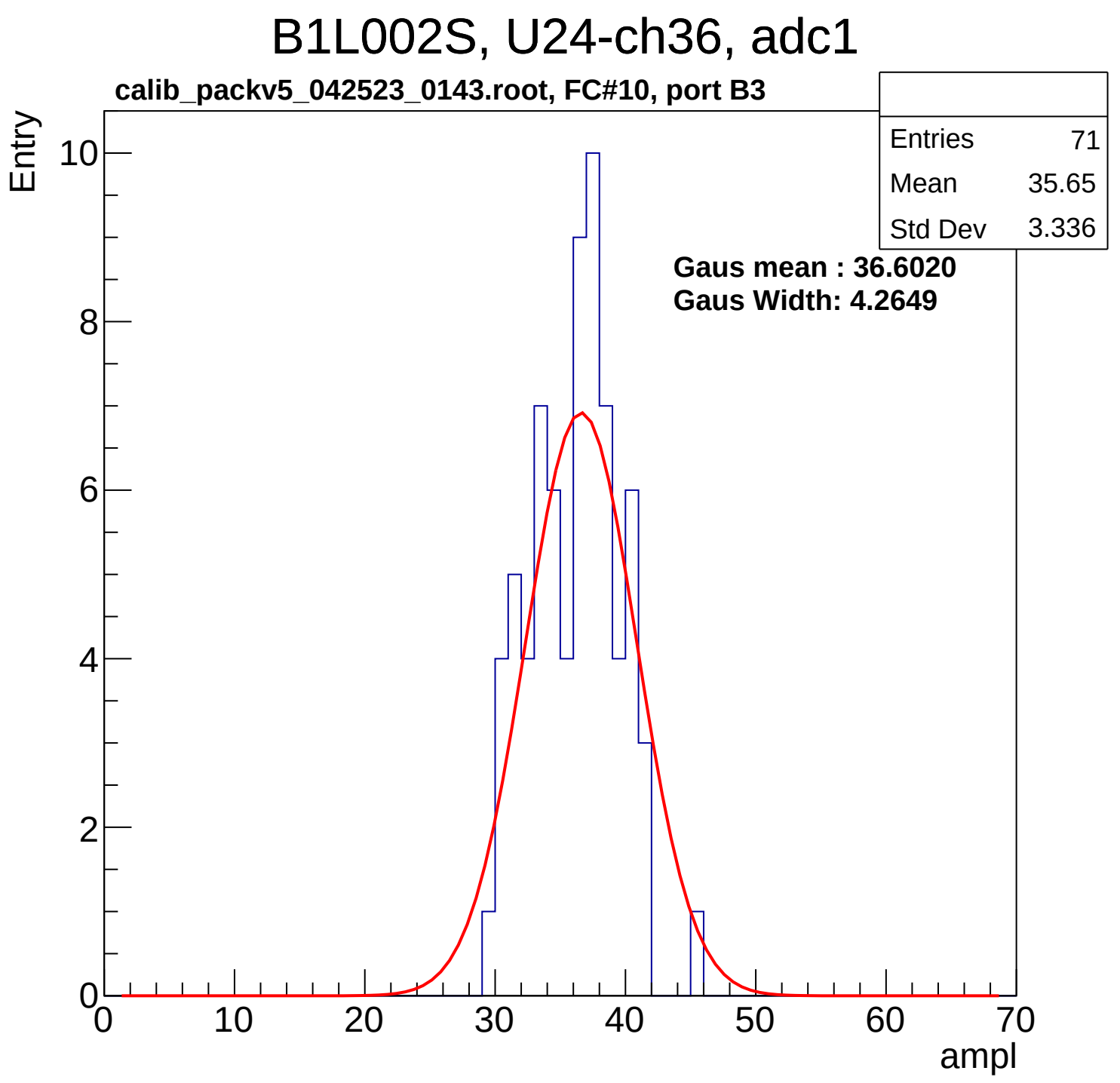
B1L002S, U24-ch36, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.65
Std Dev 3.336
Gaus mean : 36.6020
Gaus Width: 4.2649
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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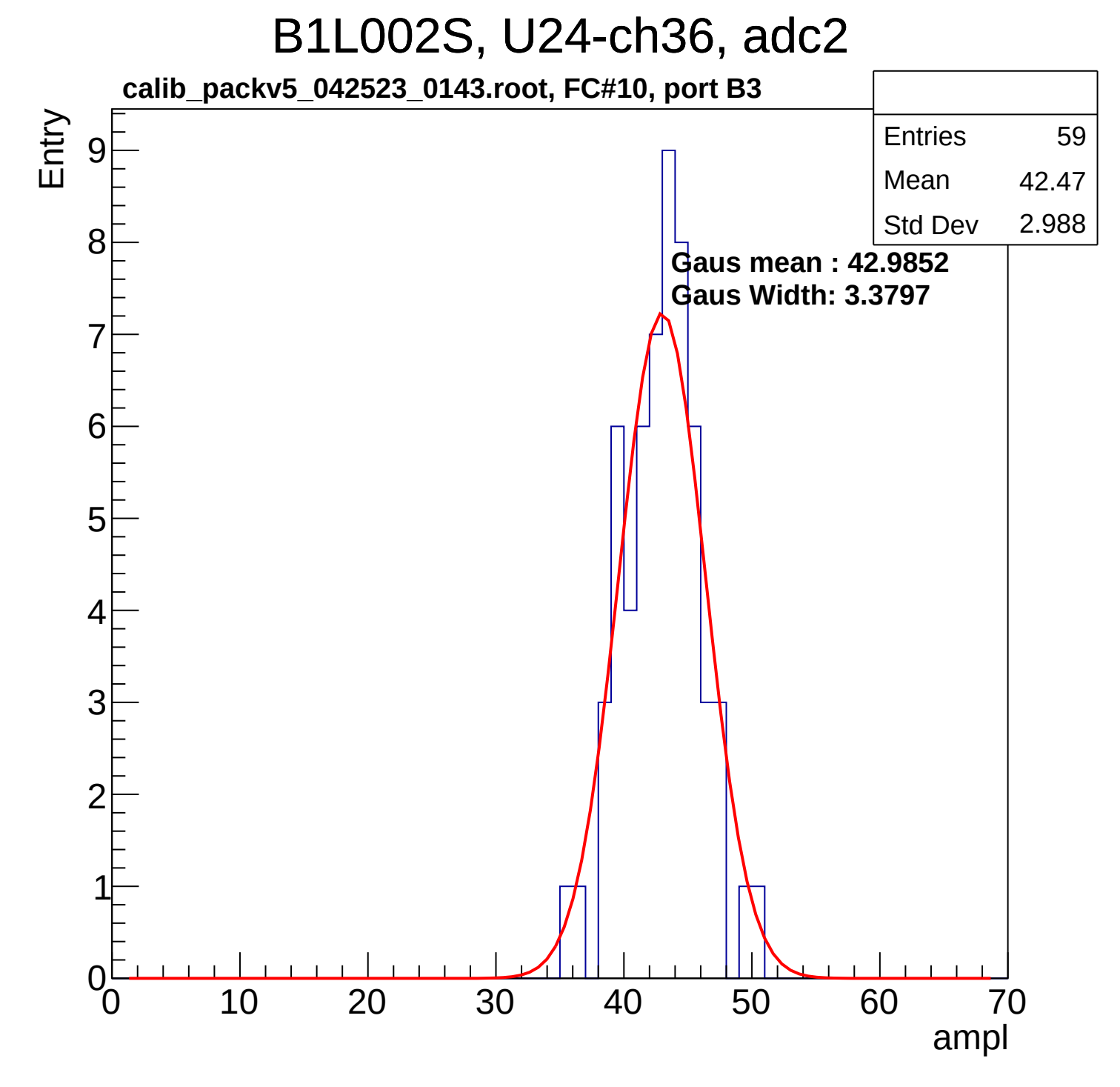
B1L002S, U24-ch36, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 42.47
Std Dev 2.988
Gaus mean : 42.9852
Gaus Width: 3.3797
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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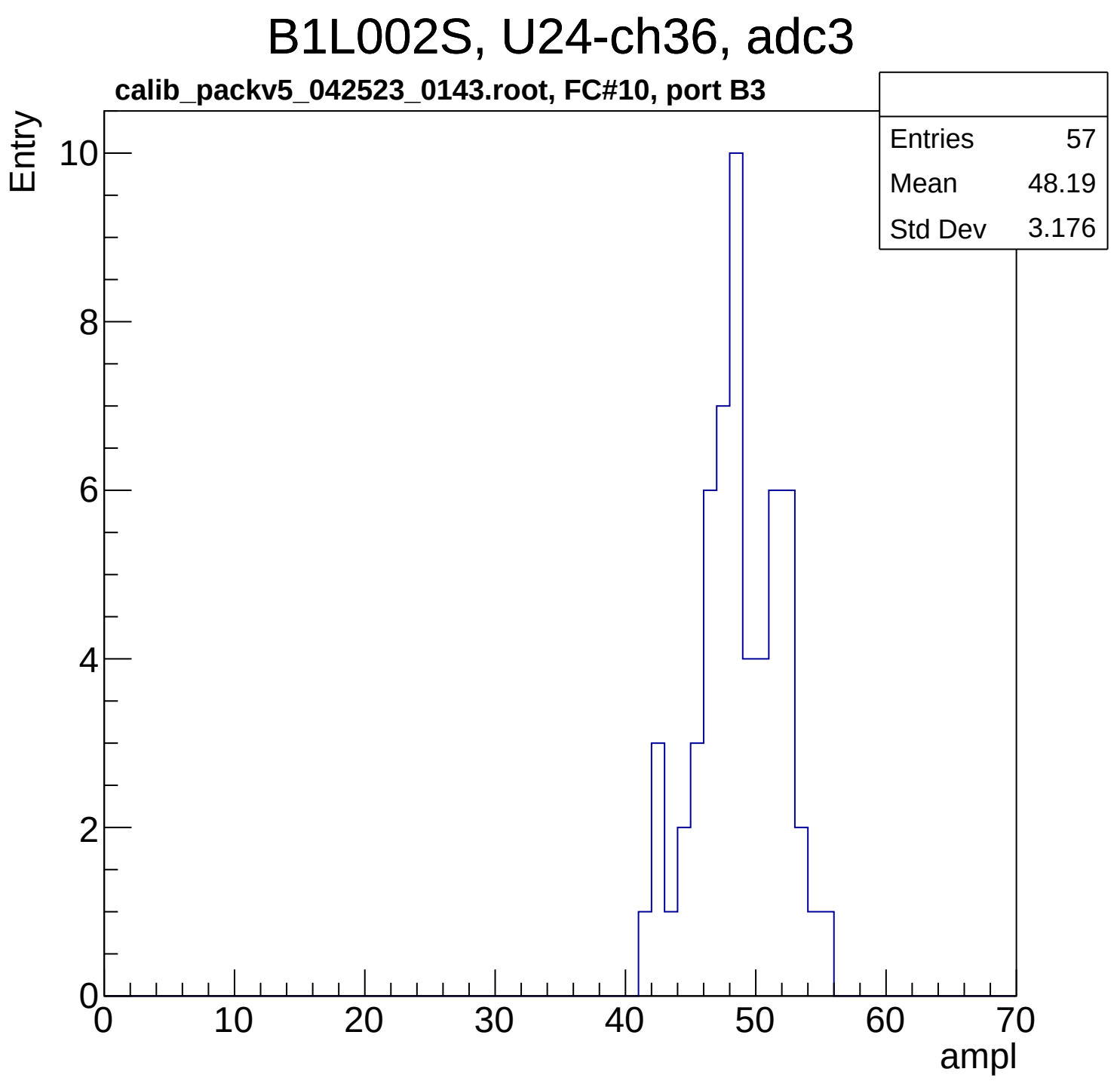
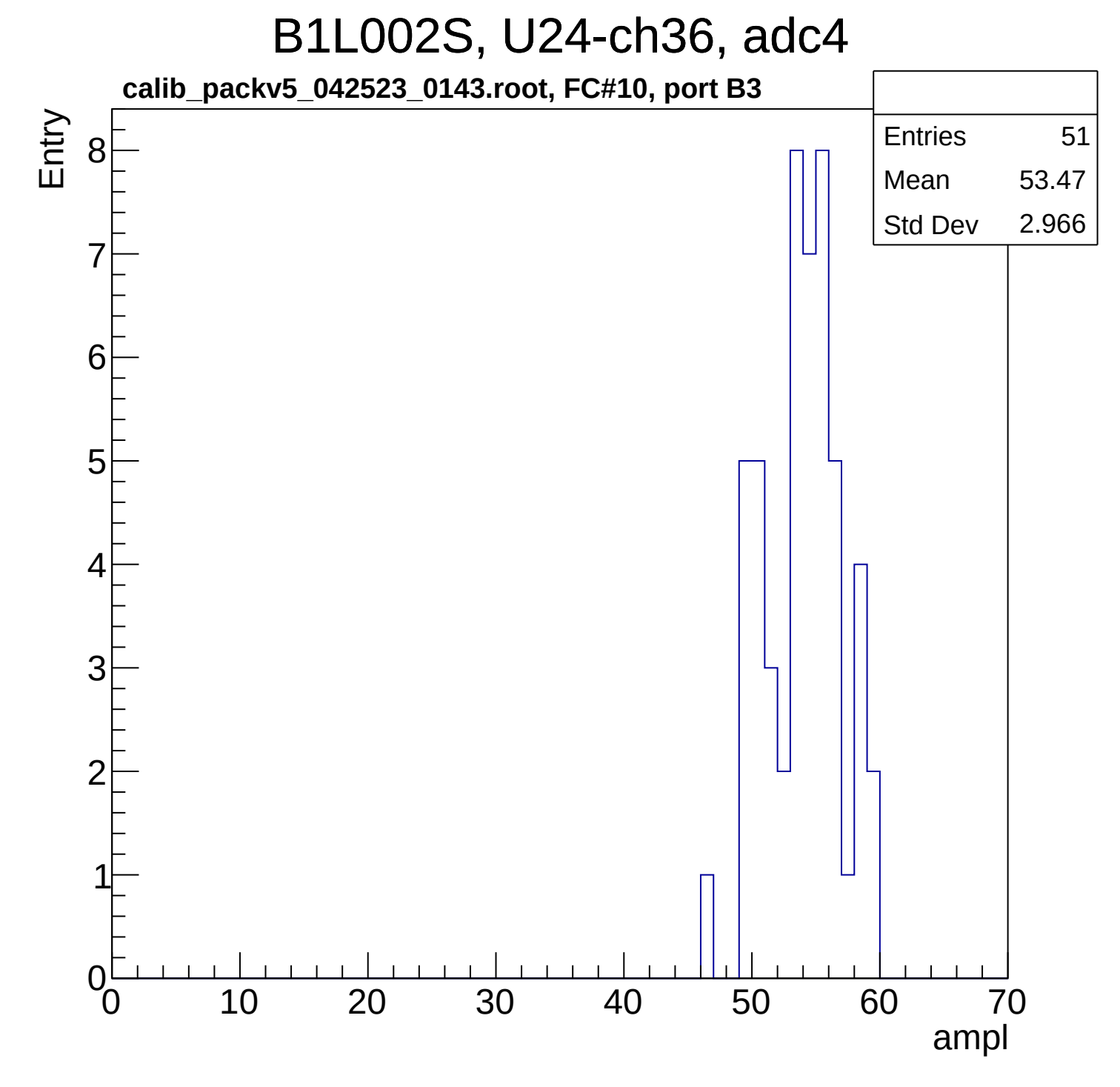

B1L002S, U24-ch36, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 48.19
Std Dev 3.176
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch36, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.47
Std Dev 2.966
Entry
ampl
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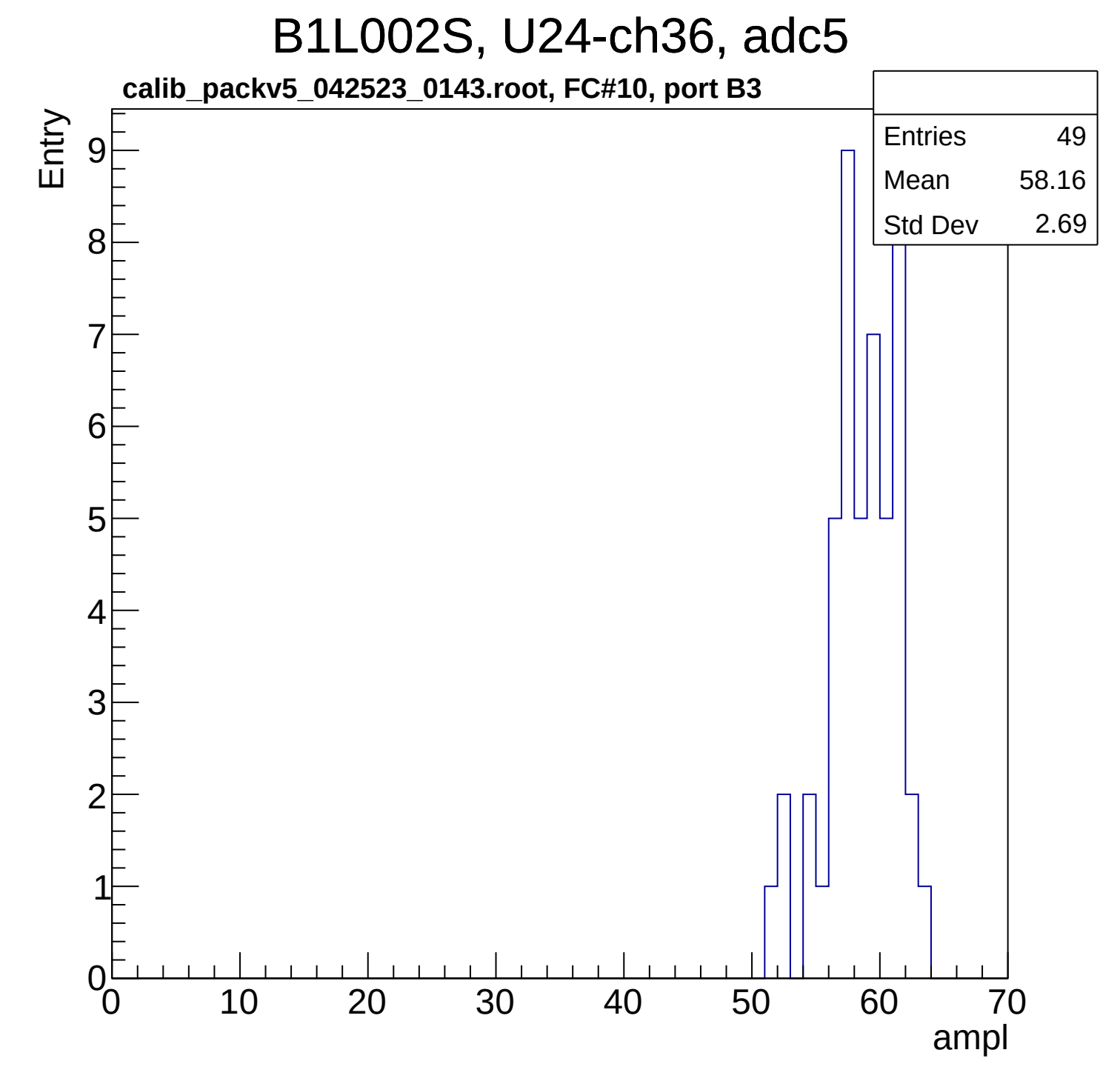
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch36, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 58.16
Std Dev 2.69
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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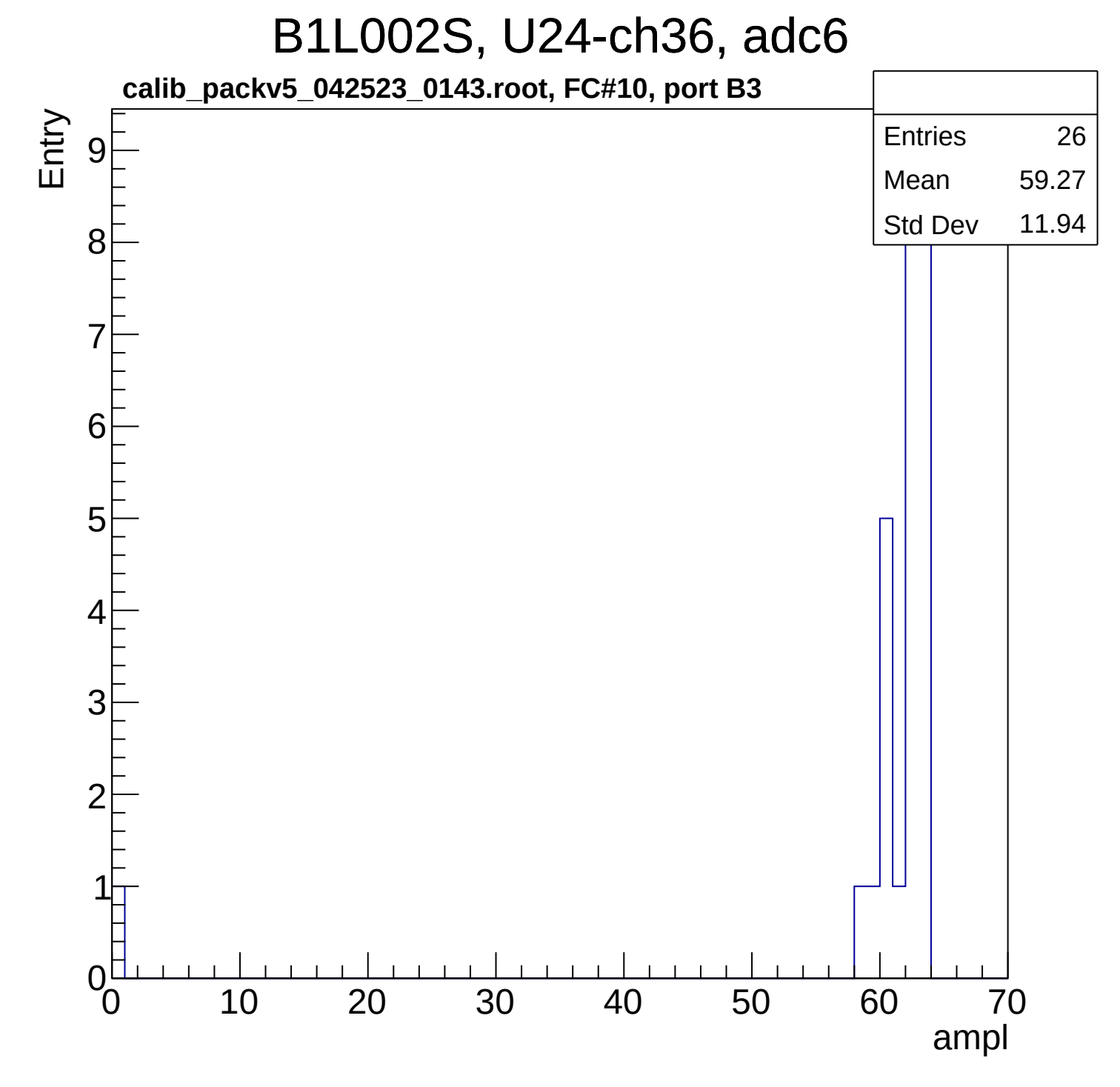
B1L002S, U24-ch36, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 59.27
Std Dev 11.94
Entry
0
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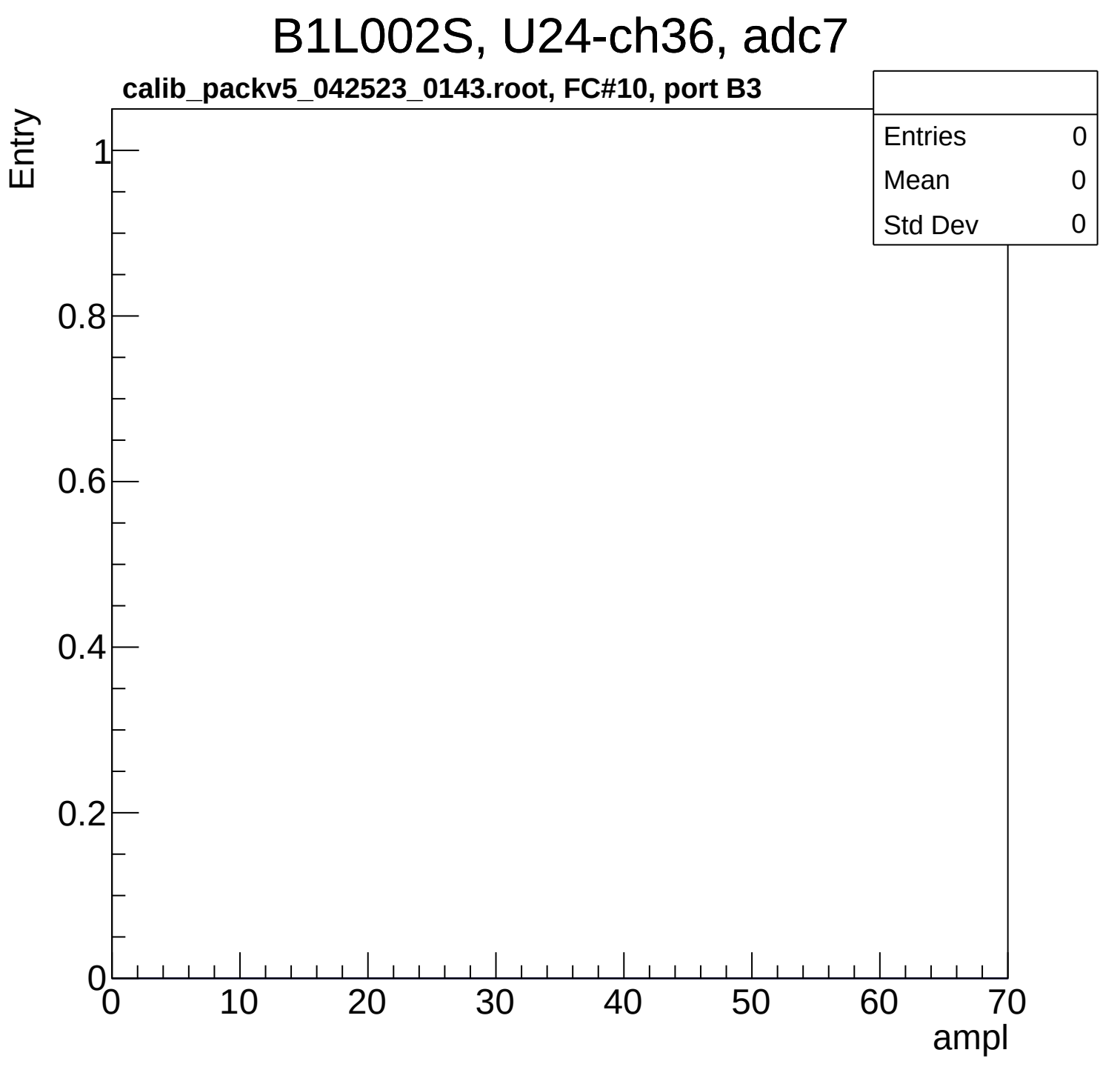
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch36, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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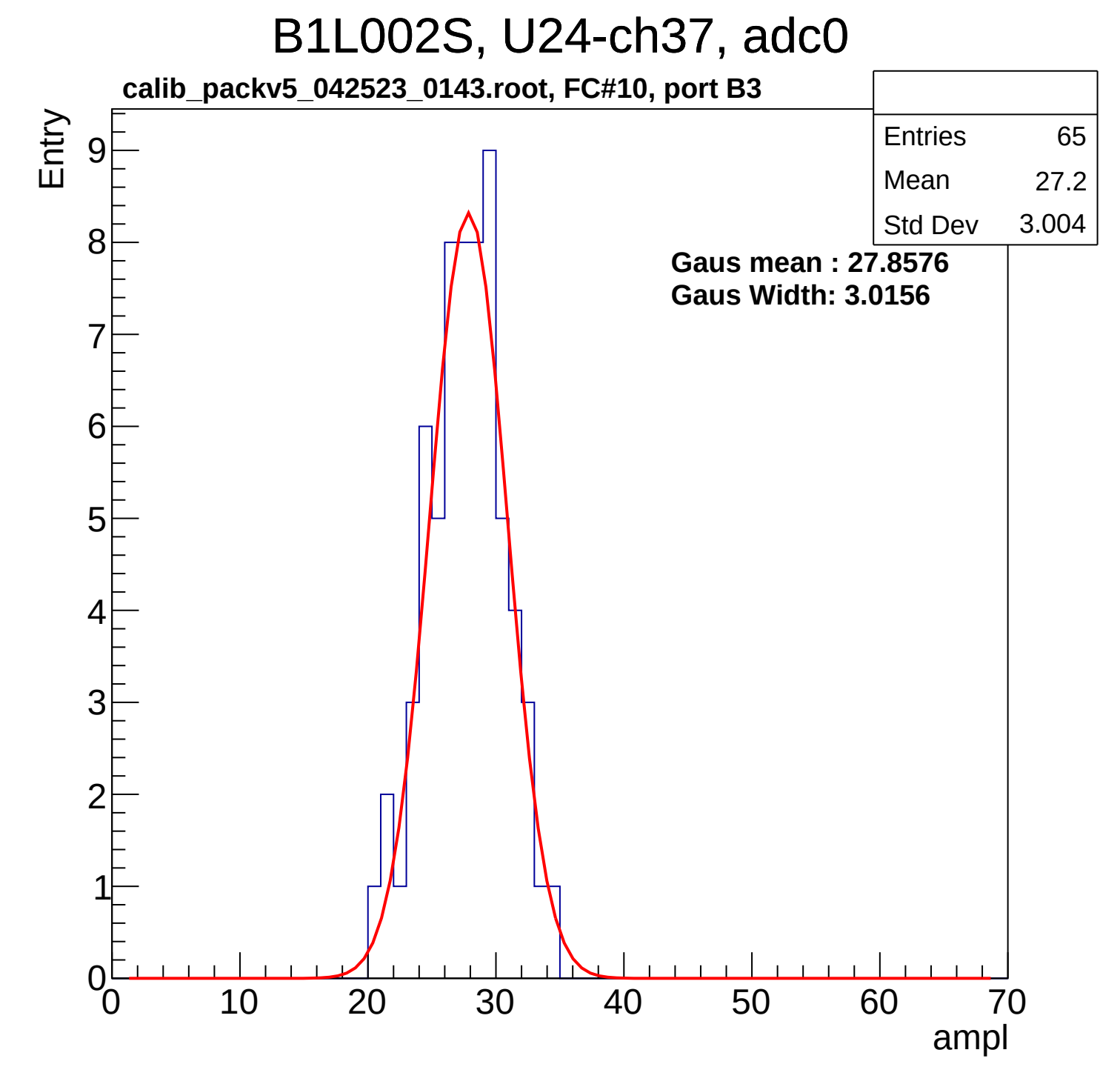
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch37, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.2
Std Dev 3.004
Gaus mean : 27.8576
Gaus Width: 3.0156
Entry
ampl
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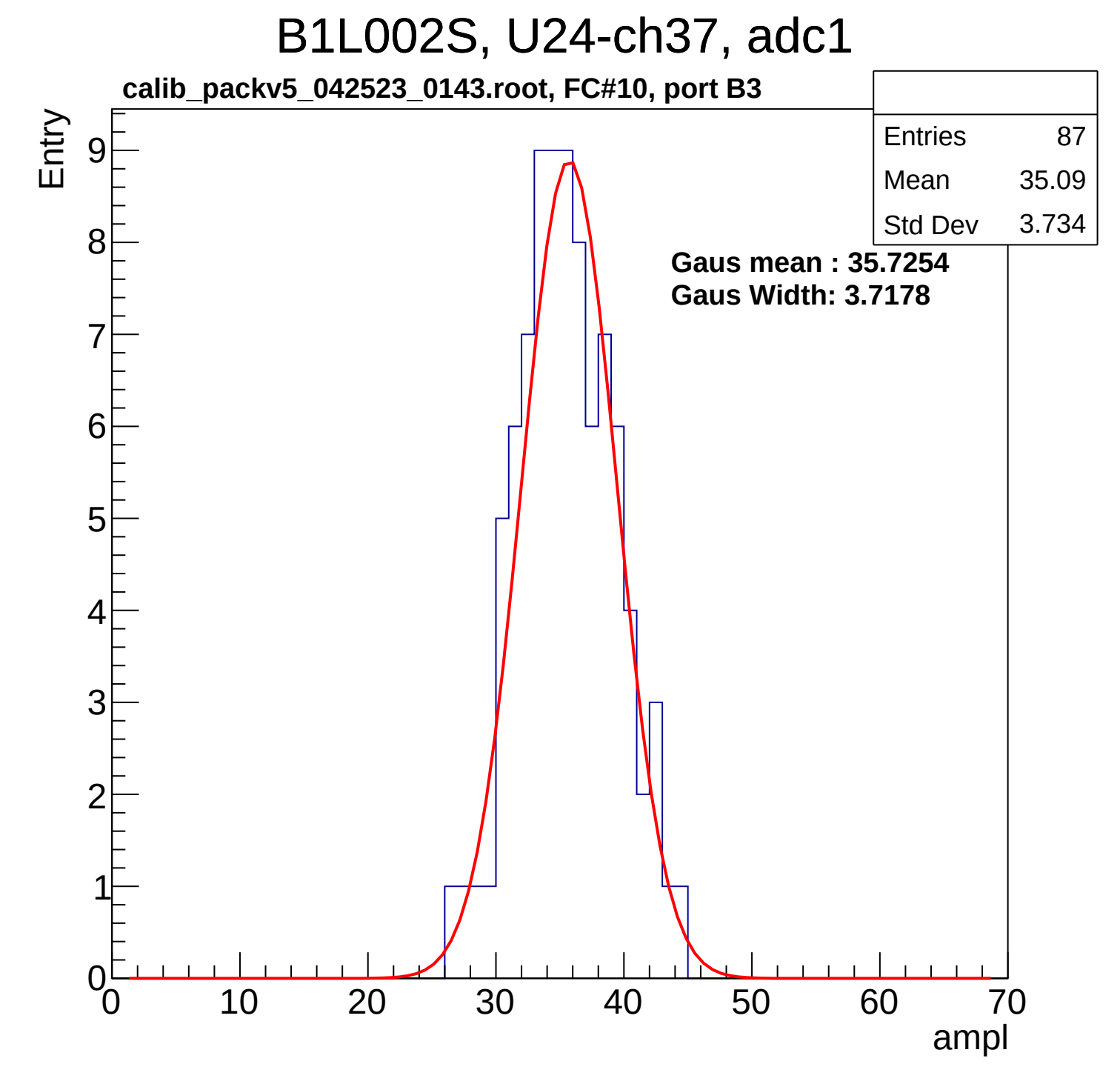
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch37, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 35.09
Std Dev 3.734
Gaus mean : 35.7254
Gaus Width: 3.7178
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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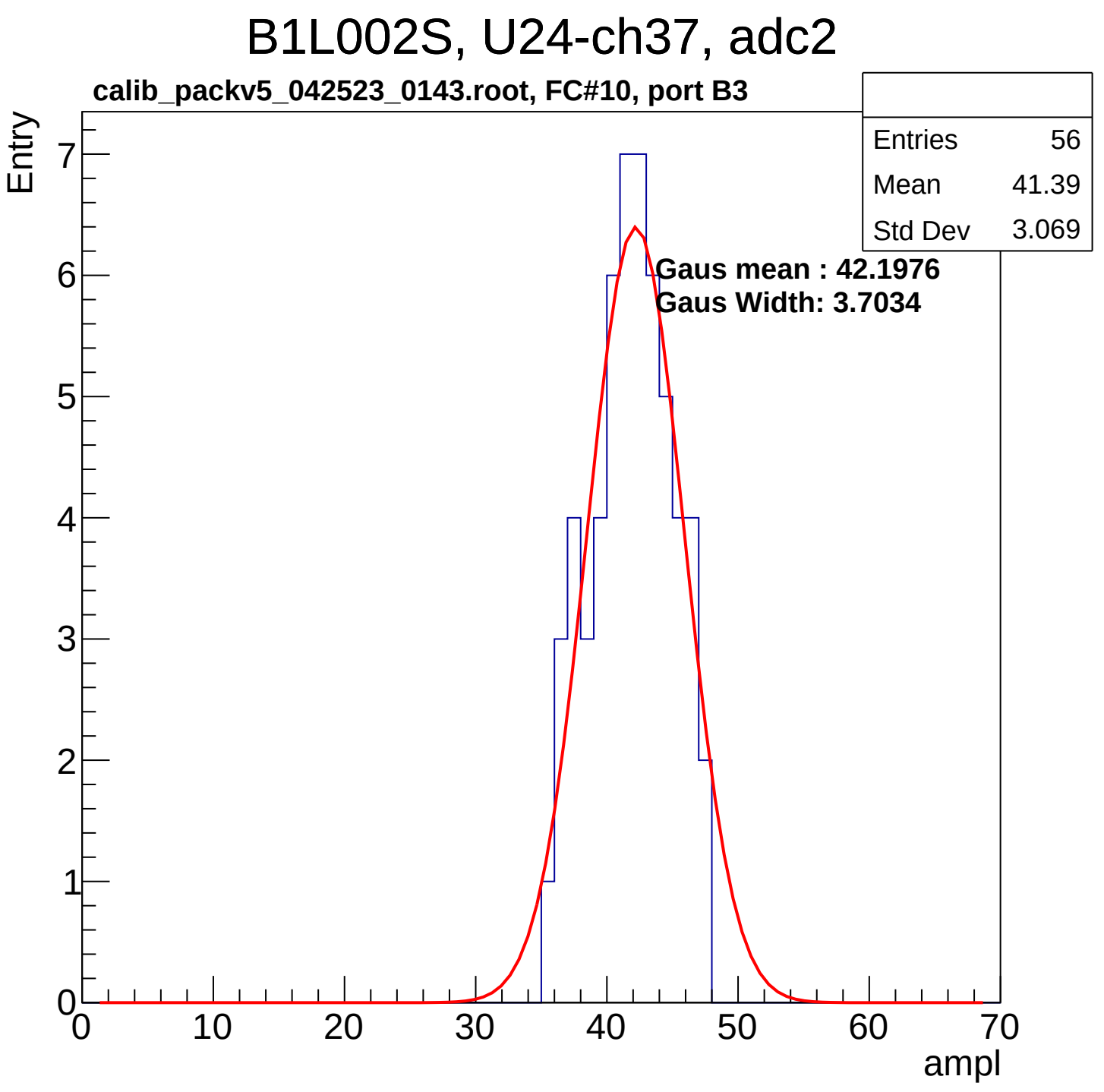
B1L002S, U24-ch37, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.39
Std Dev 3.069
Gaus mean : 42.1976
Gaus Width: 3.7034
Entry
ampl
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2
3
4
5
6
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0
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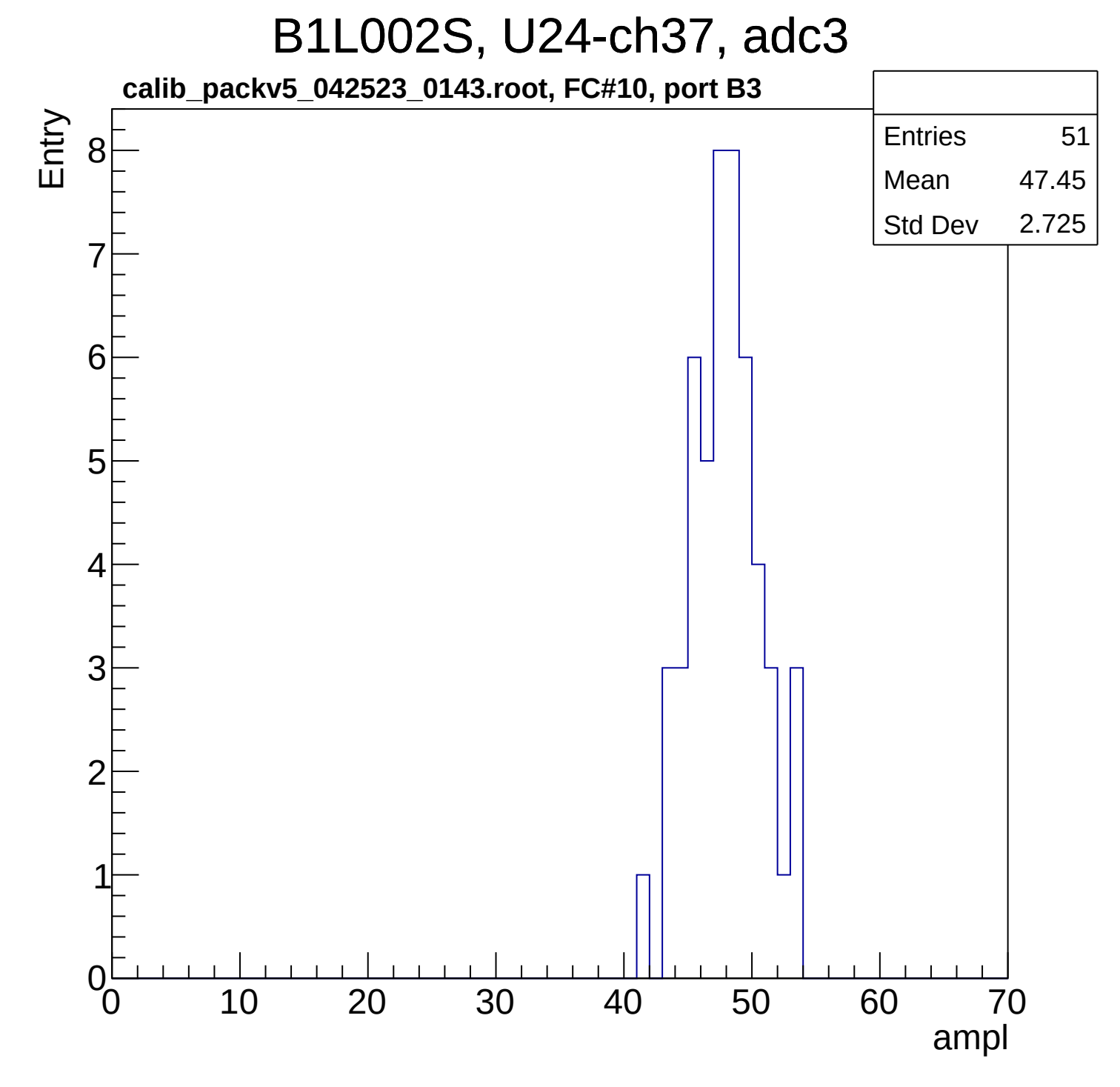
B1L002S, U24-ch37, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.45
Std Dev 2.725
Entry
ampl
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2
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0
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60
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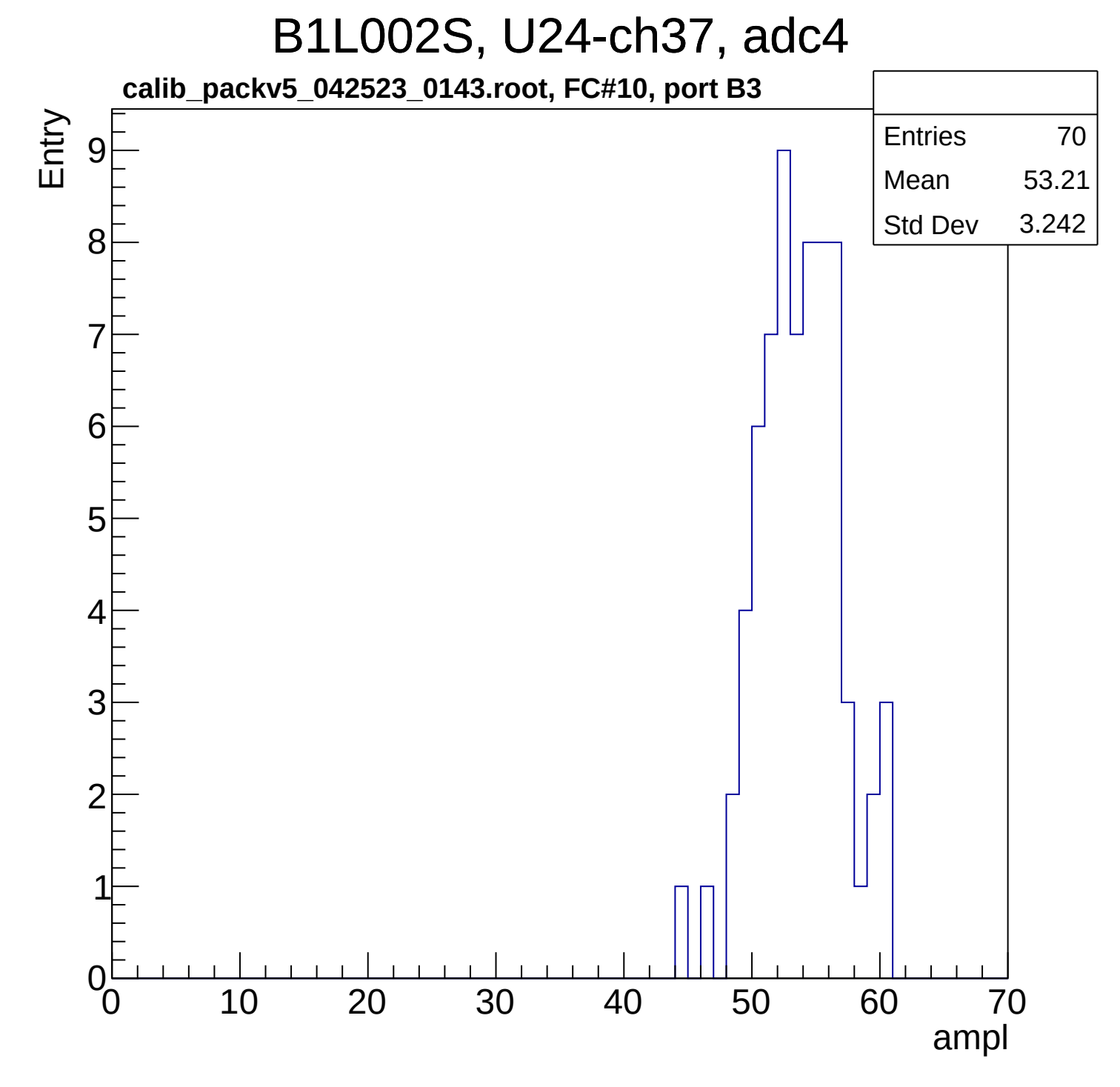
B1L002S, U24-ch37, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 53.21
Std Dev 3.242
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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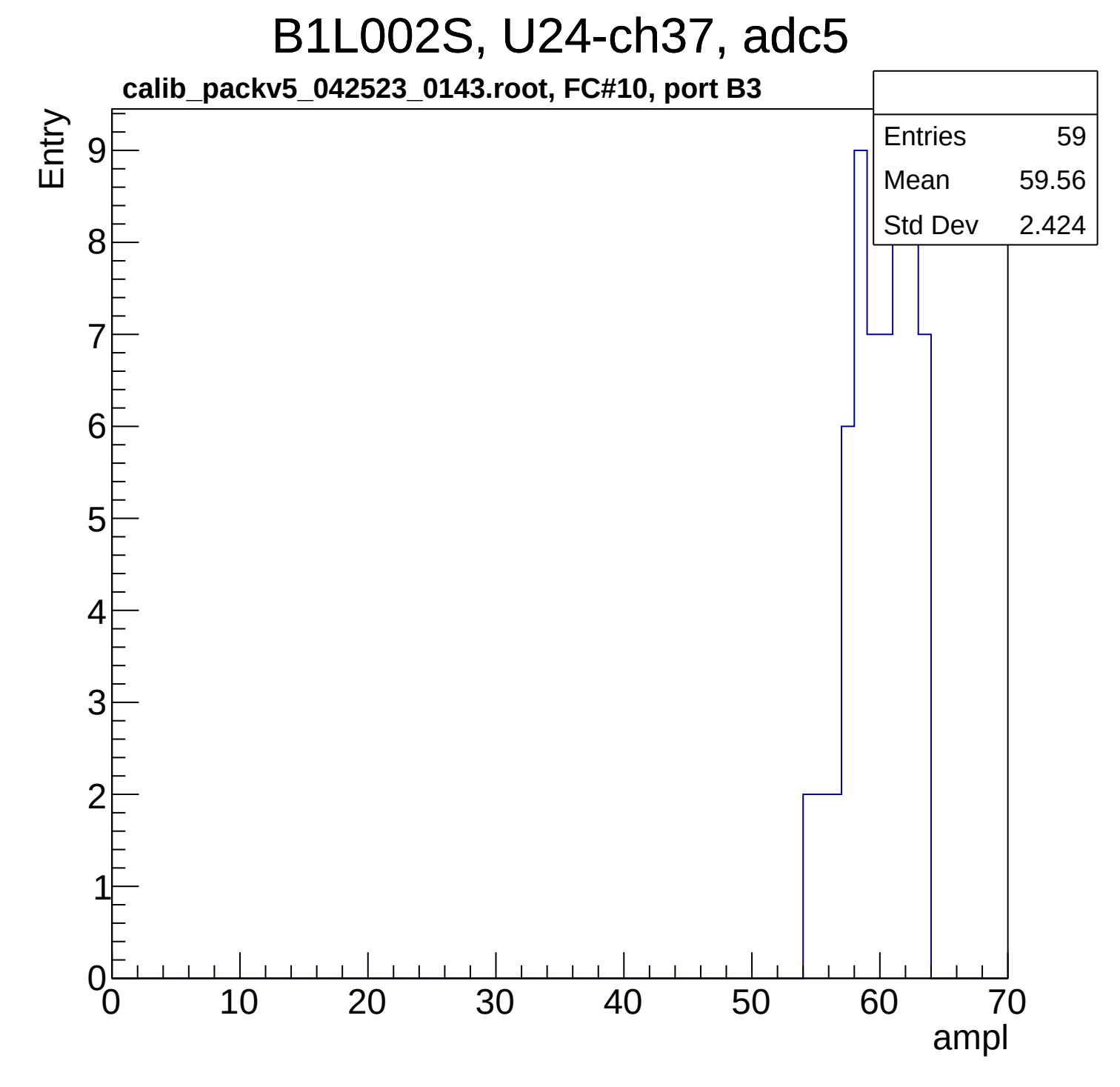
B1L002S, U24-ch37, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.56
Std Dev 2.424
Entry
ampl
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0
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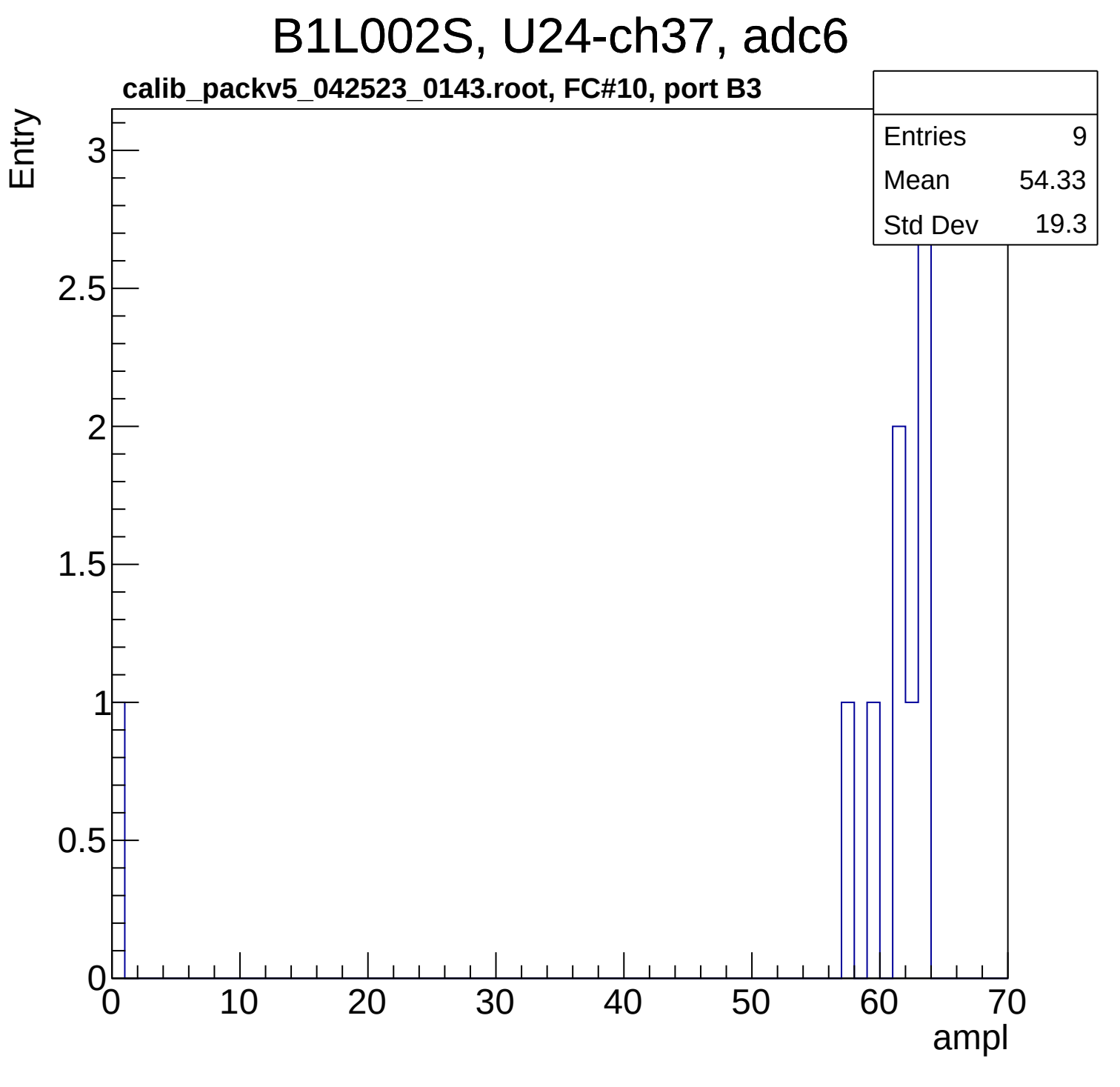
B1L002S, U24-ch37, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 54.33
Std Dev 19.3
Entry
ampl
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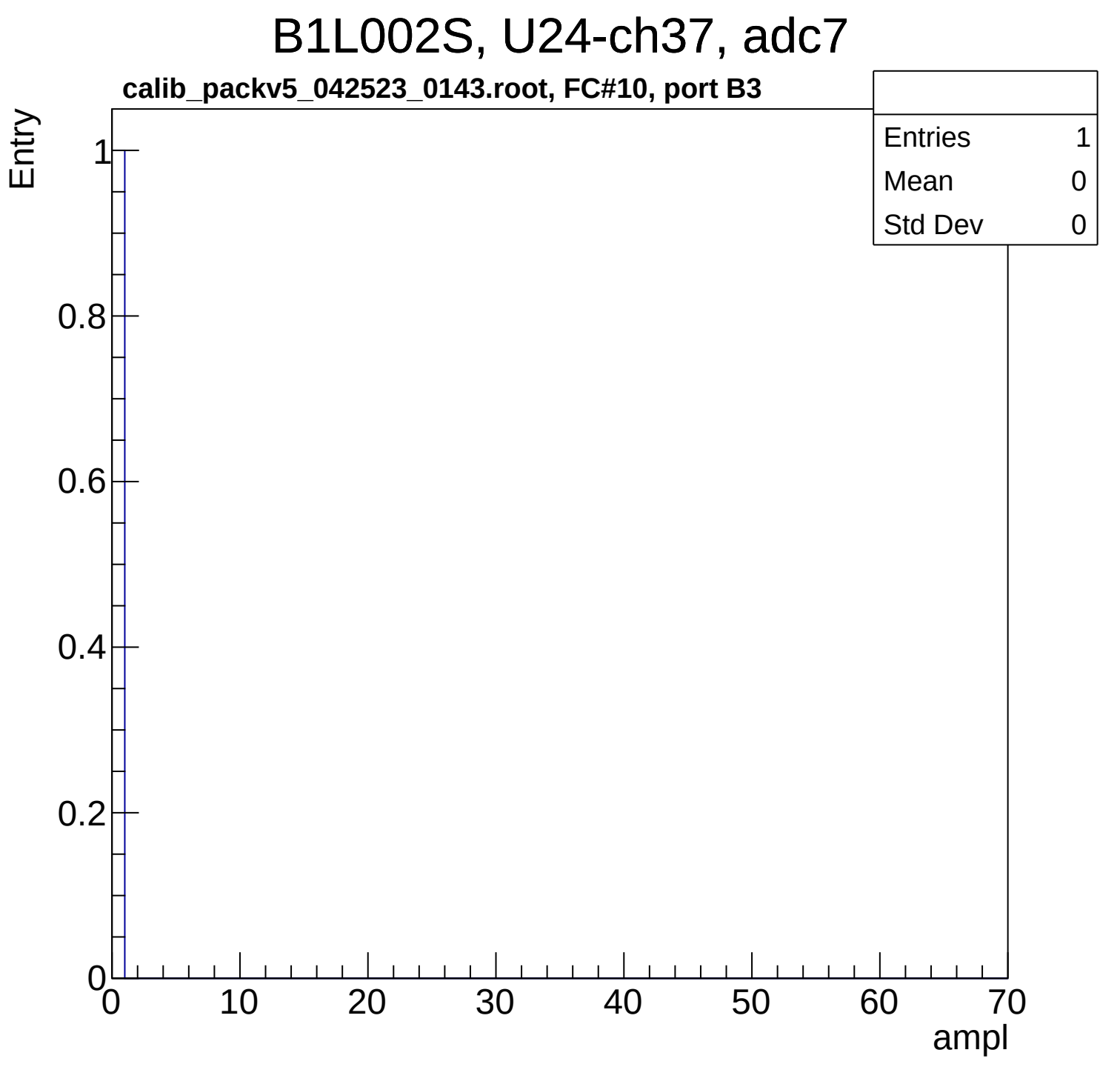
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch37, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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10
20
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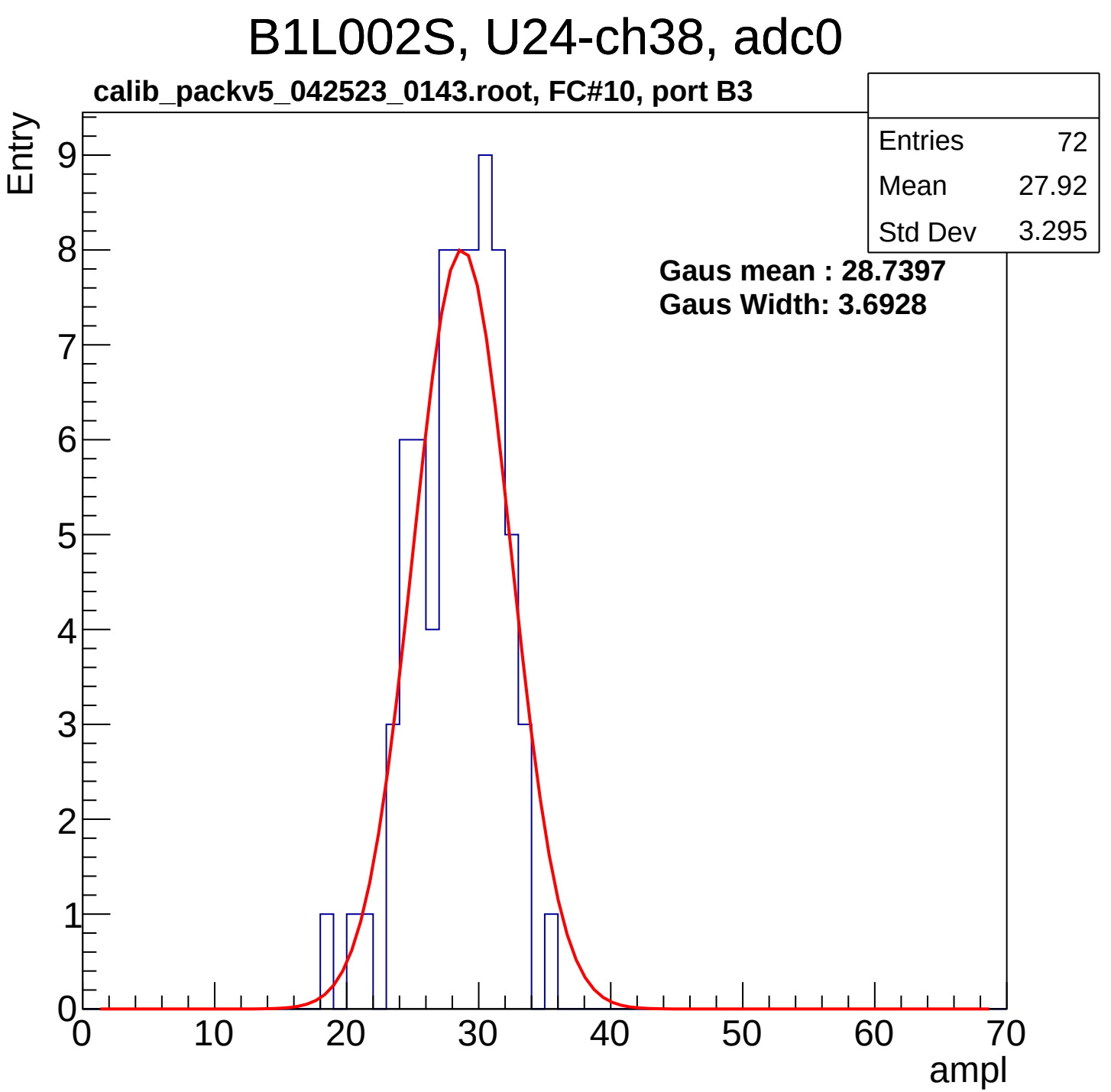
50
60
70
ampl


B1L002S, U24-ch38, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 27.92
Std Dev 3.295
Gaus mean : 28.7397
Gaus Width: 3.6928
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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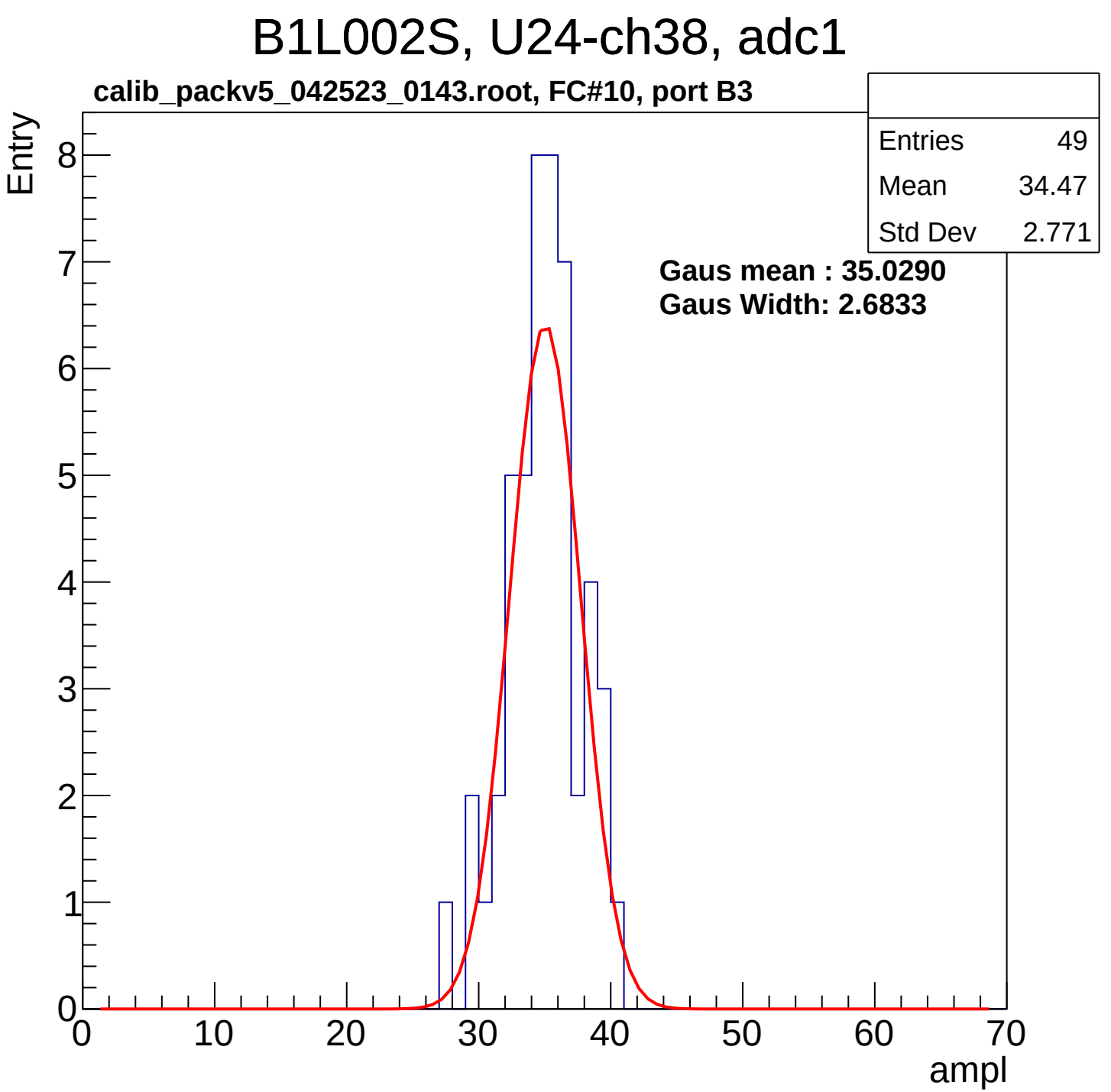
B1L002S, U24-ch38, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 34.47
Std Dev 2.771
Gaus mean : 35.0290
Gaus Width: 2.6833
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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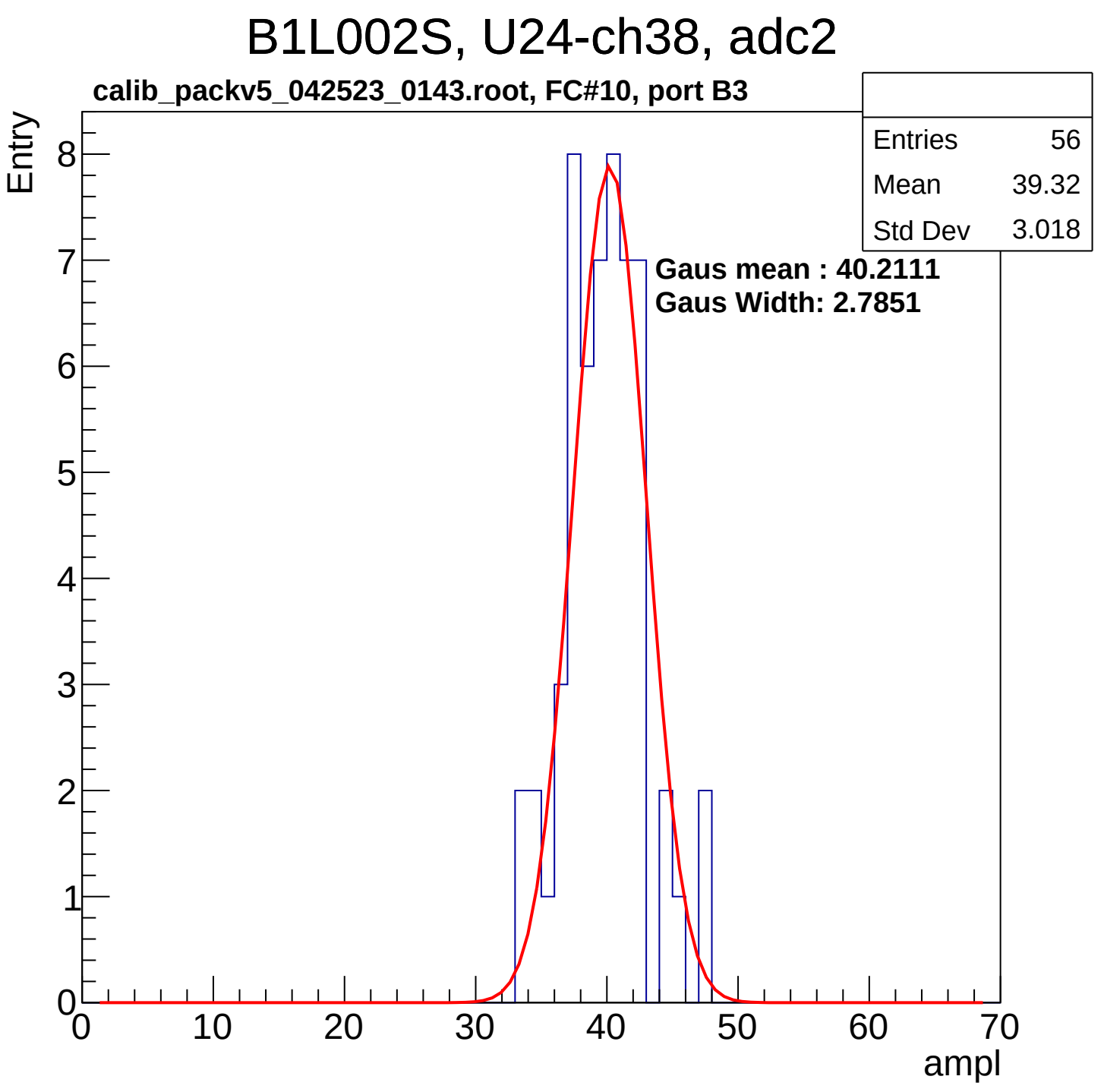
B1L002S, U24-ch38, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.32
Std Dev 3.018
Gaus mean : 40.2111
Gaus Width: 2.7851
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
50
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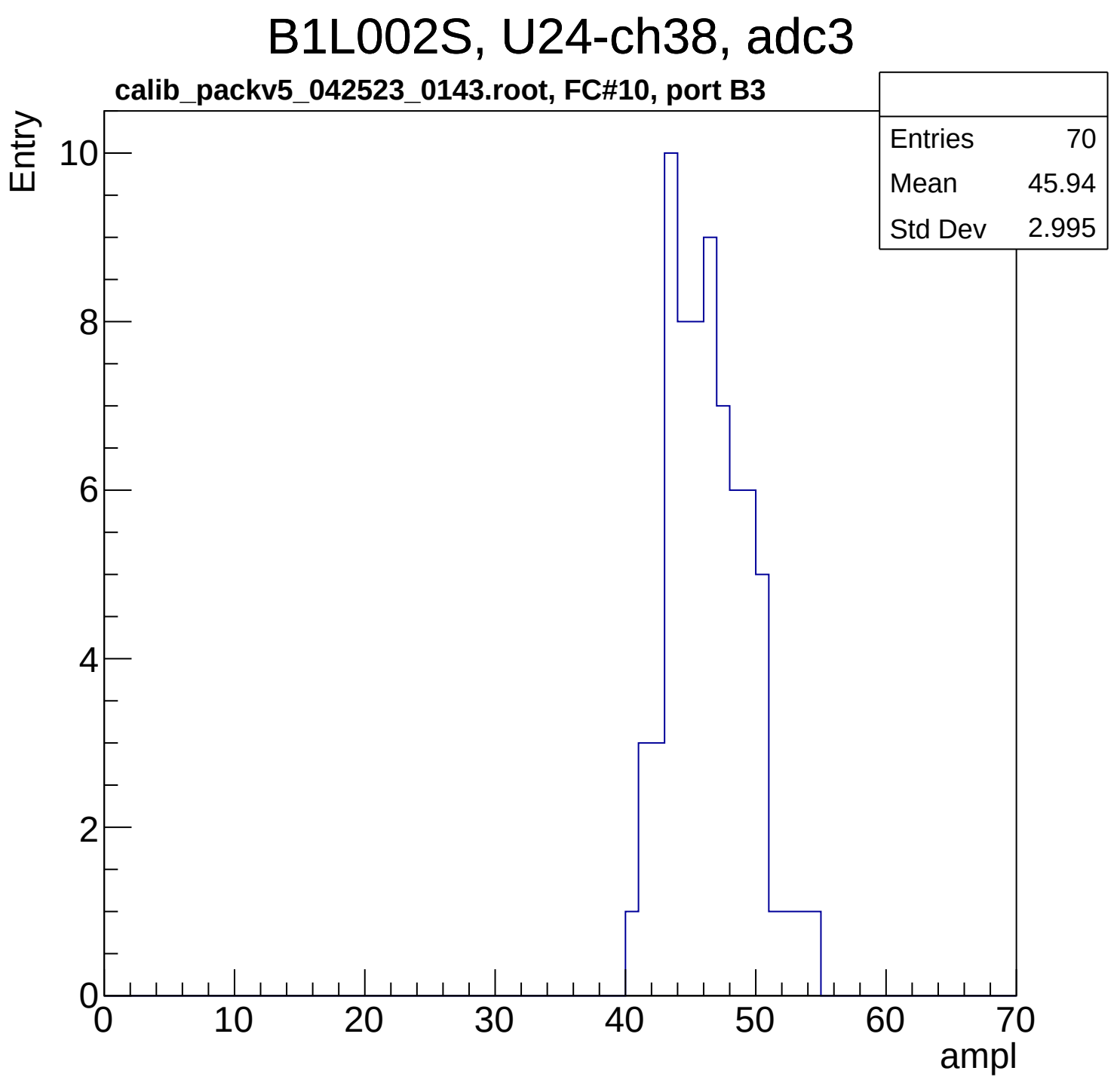
B1L002S, U24-ch38, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 45.94
Std Dev 2.995
Entry
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4
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10
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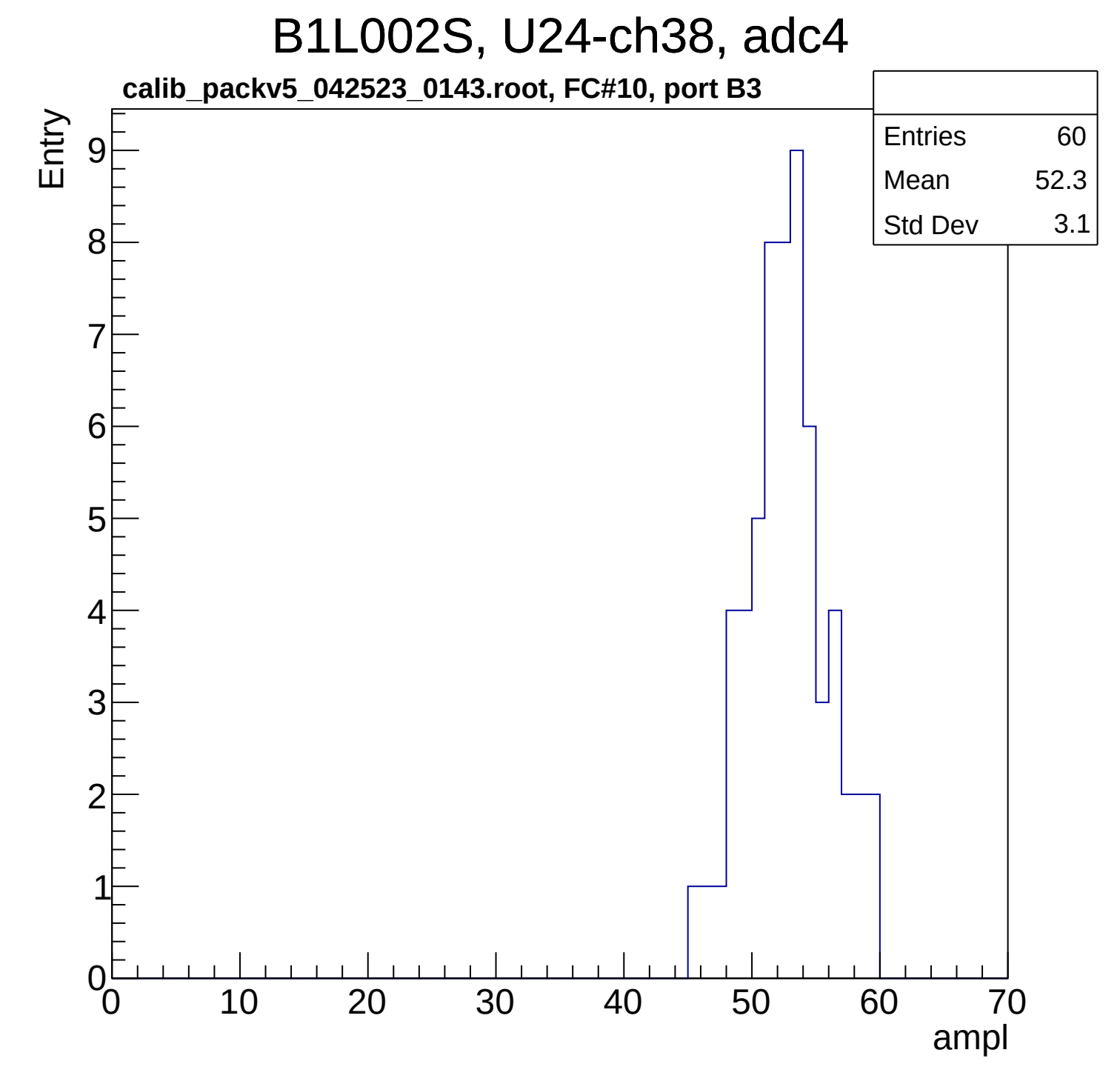
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch38, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.3
Std Dev 3.1
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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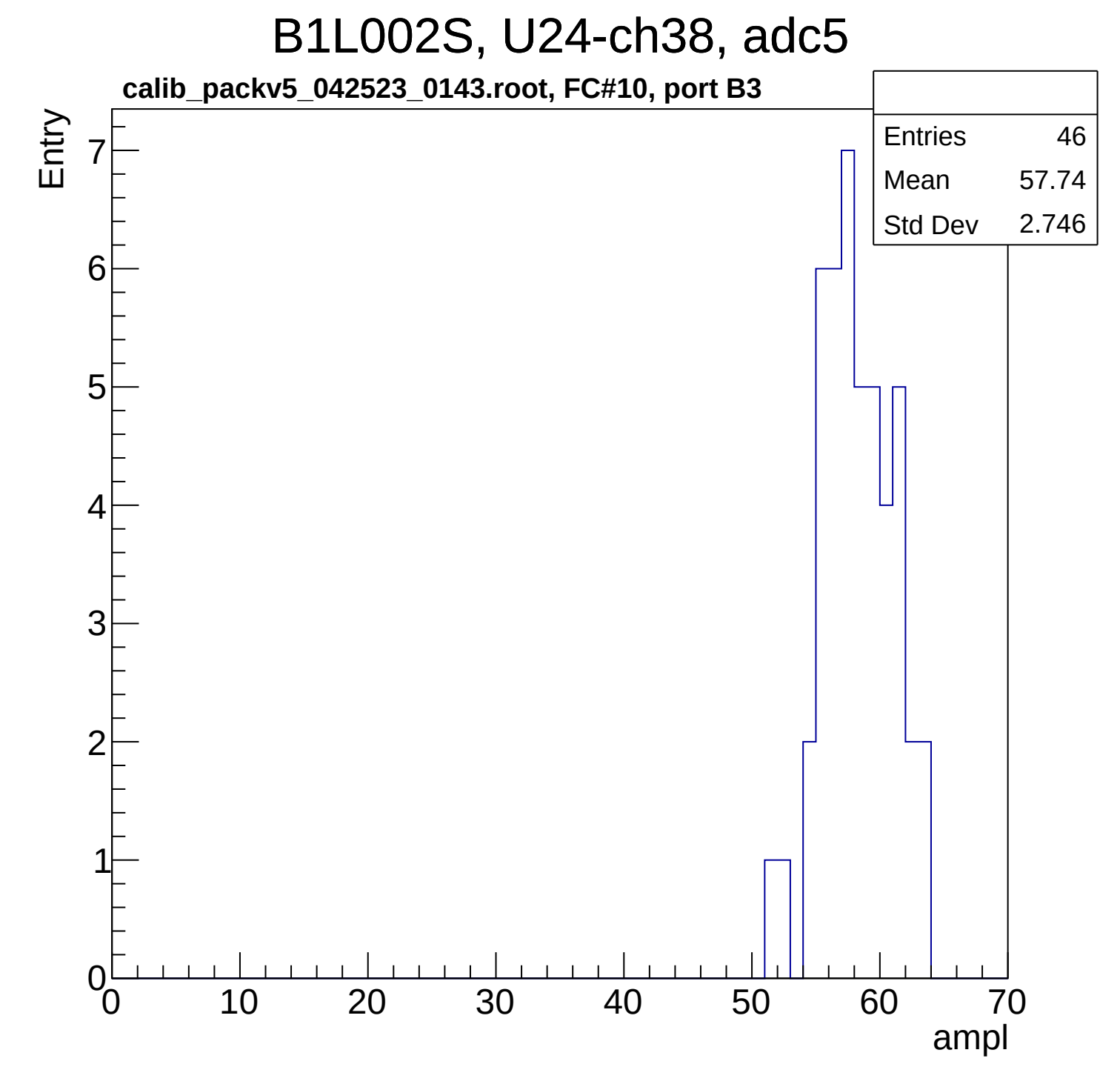
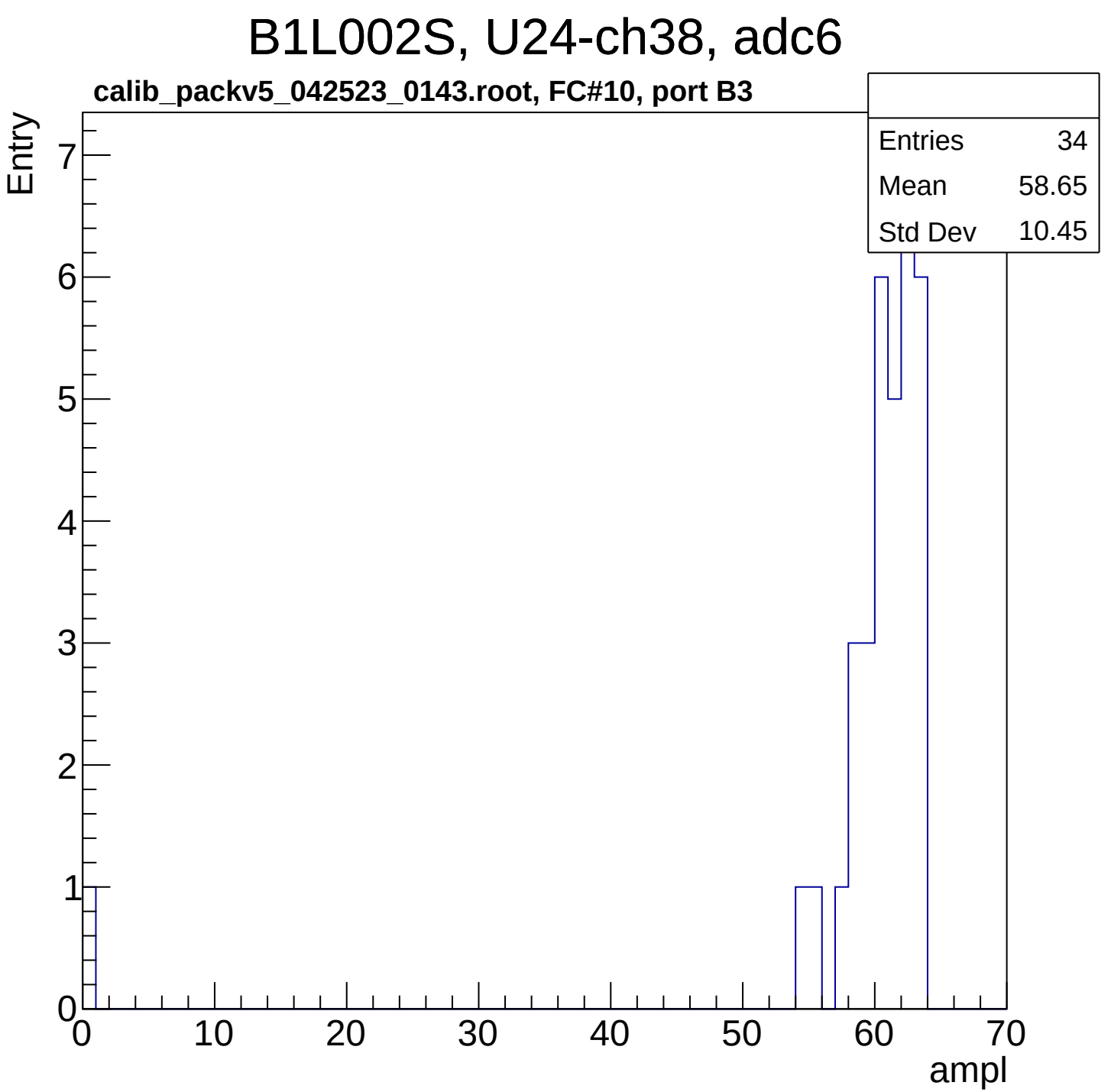

B1L002S, U24-ch38, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.74
Std Dev 2.746
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch38, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.65
Std Dev 10.45
Entry
ampl
0
1
2
3
4
5
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0
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60
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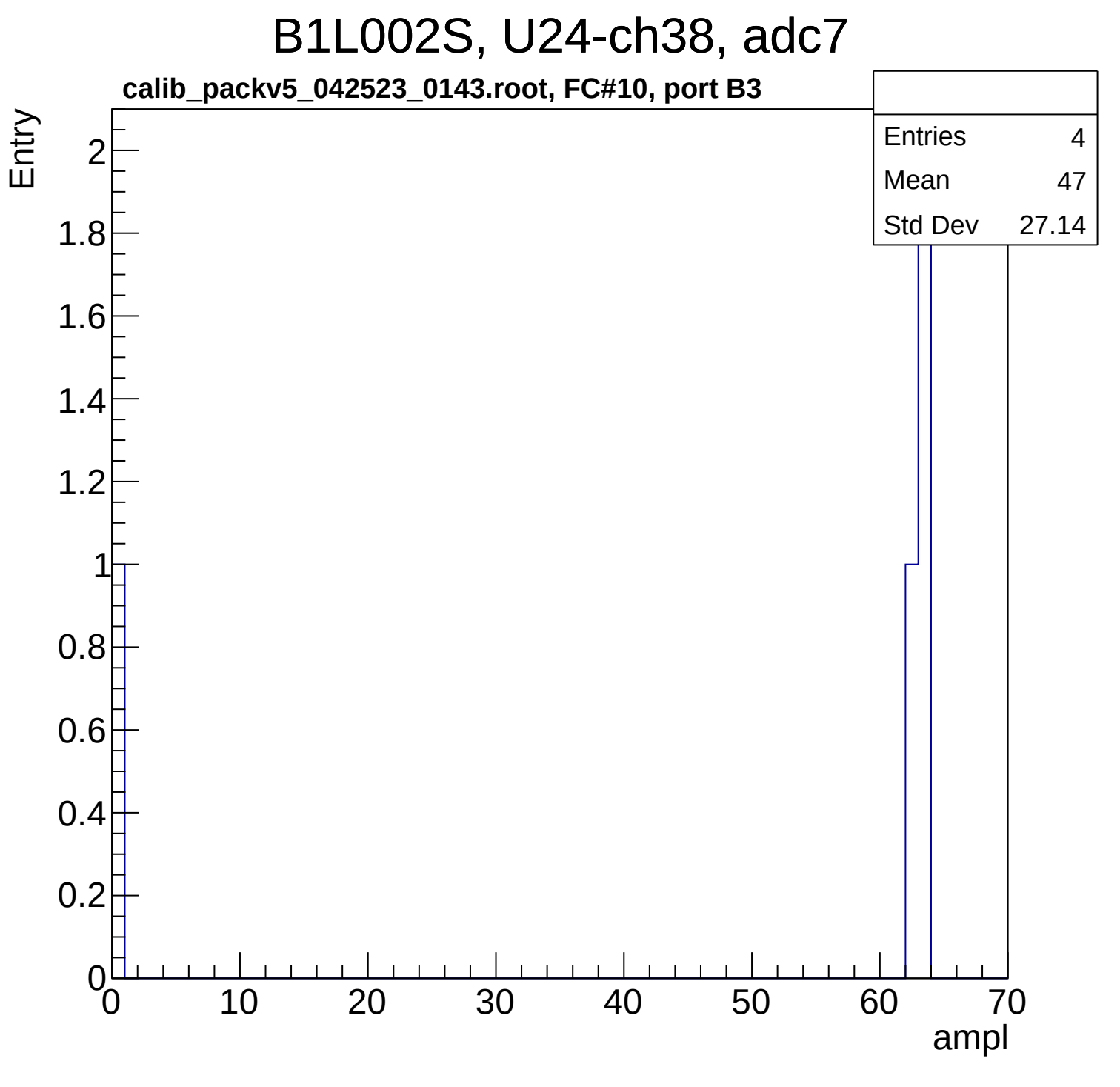
B1L002S, U24-ch38, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 47
Std Dev 27.14
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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60
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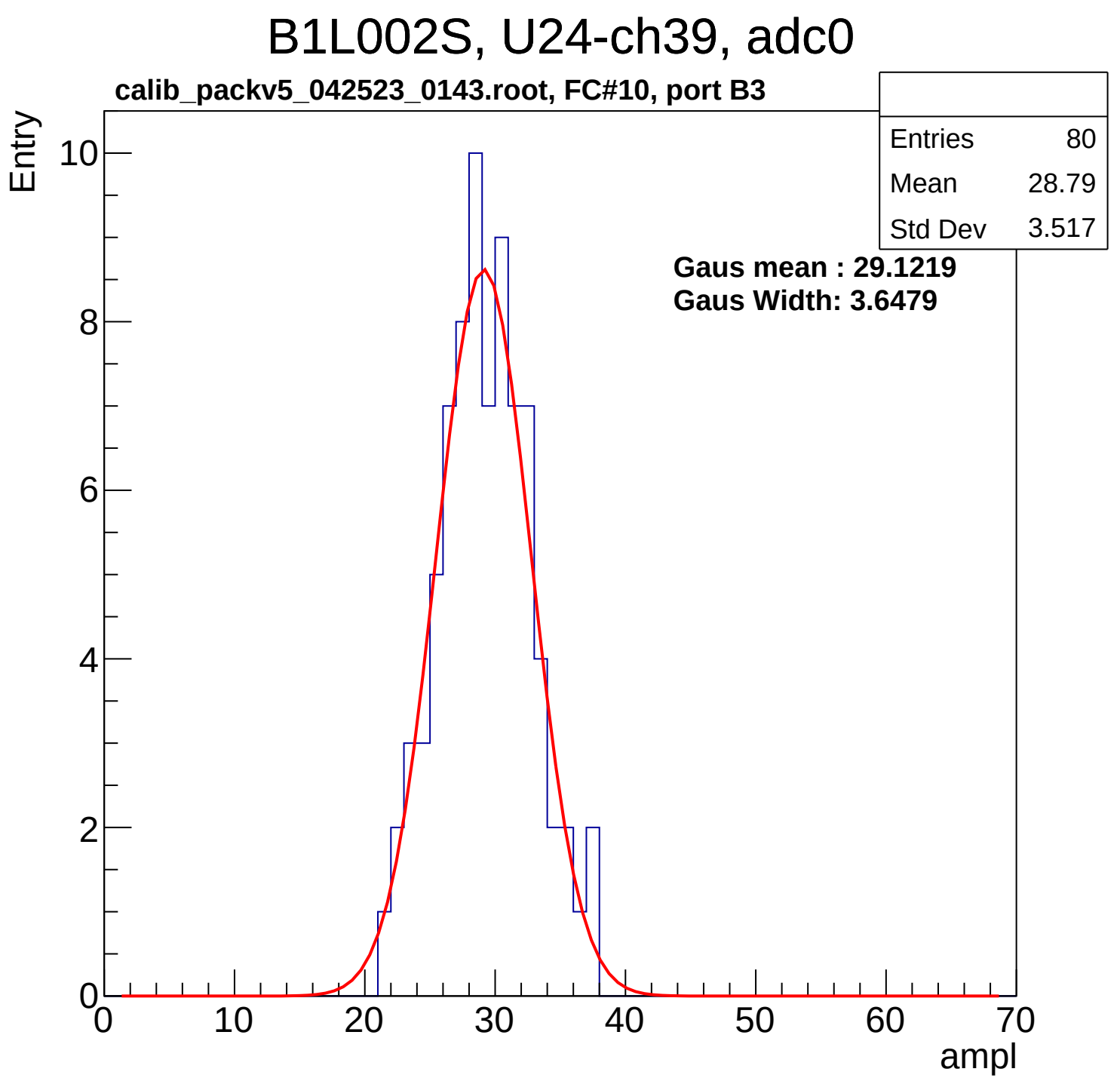
B1L002S, U24-ch39, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 28.79
Std Dev 3.517
Gaus mean : 29.1219
Gaus Width: 3.6479
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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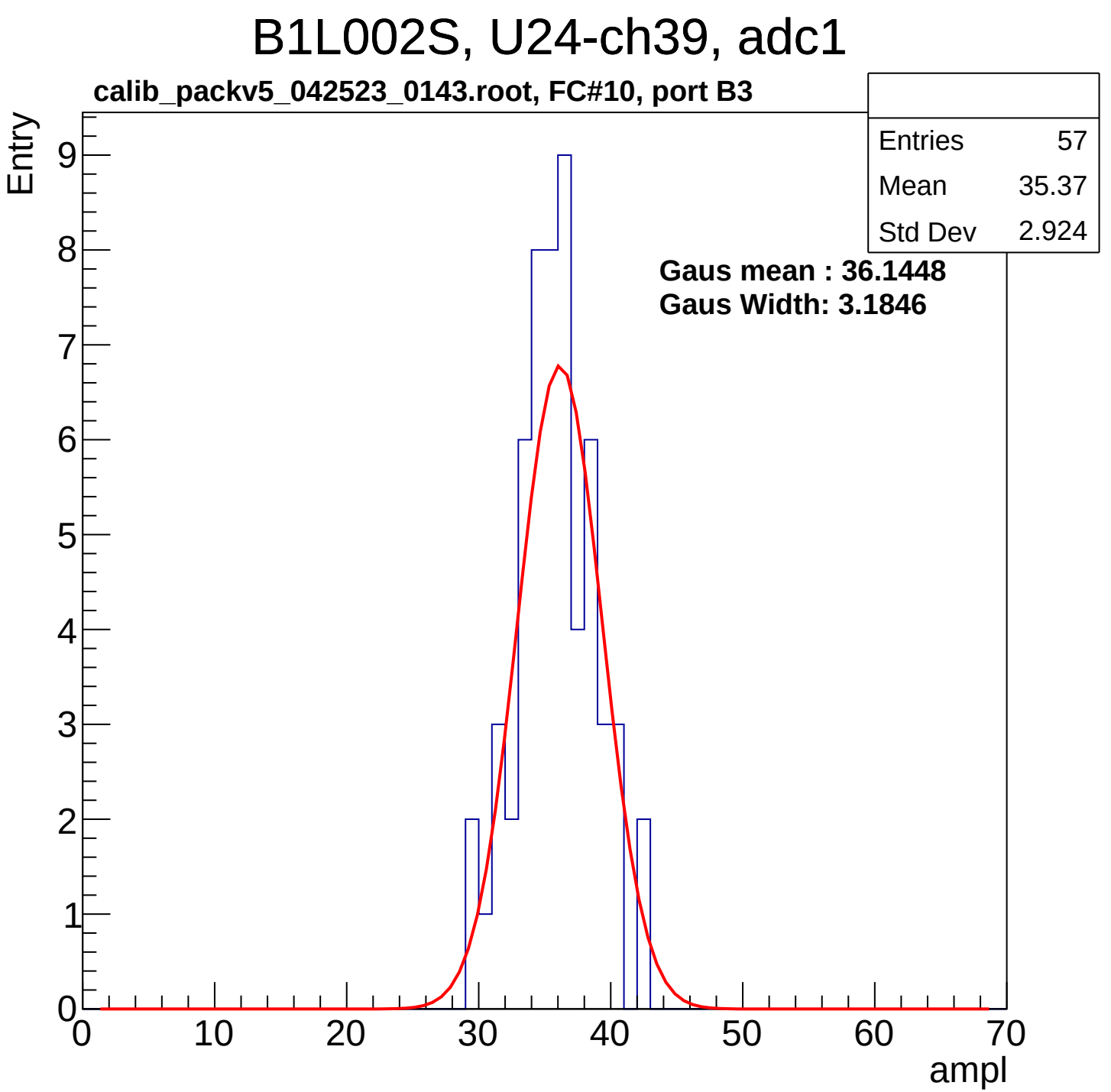
B1L002S, U24-ch39, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 35.37
Std Dev 2.924
Gaus mean : 36.1448
Gaus Width: 3.1846
Entry
ampl
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2
3
4
5
6
7
8
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0
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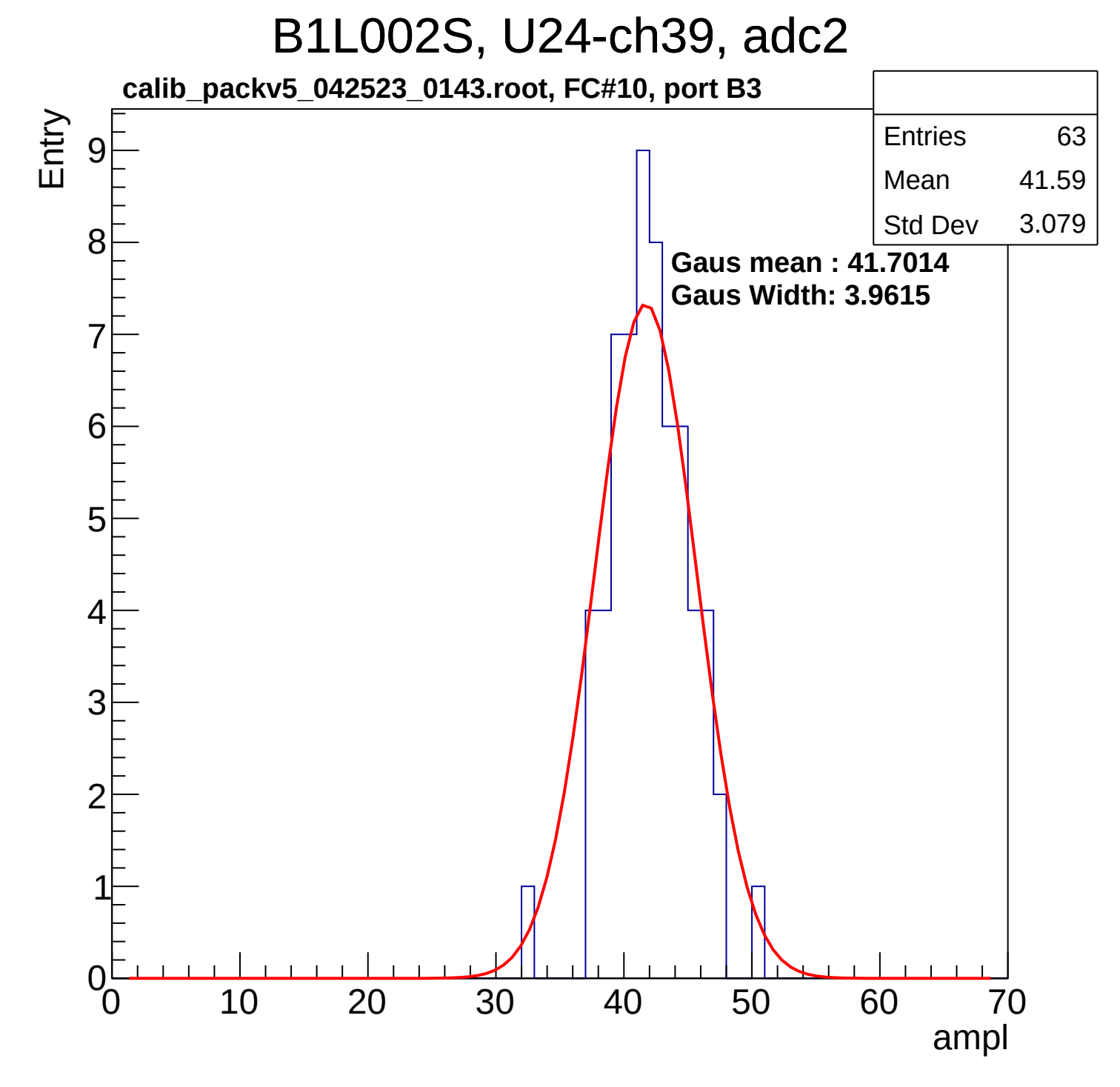
B1L002S, U24-ch39, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 41.59
Std Dev 3.079
Gaus mean : 41.7014
Gaus Width: 3.9615
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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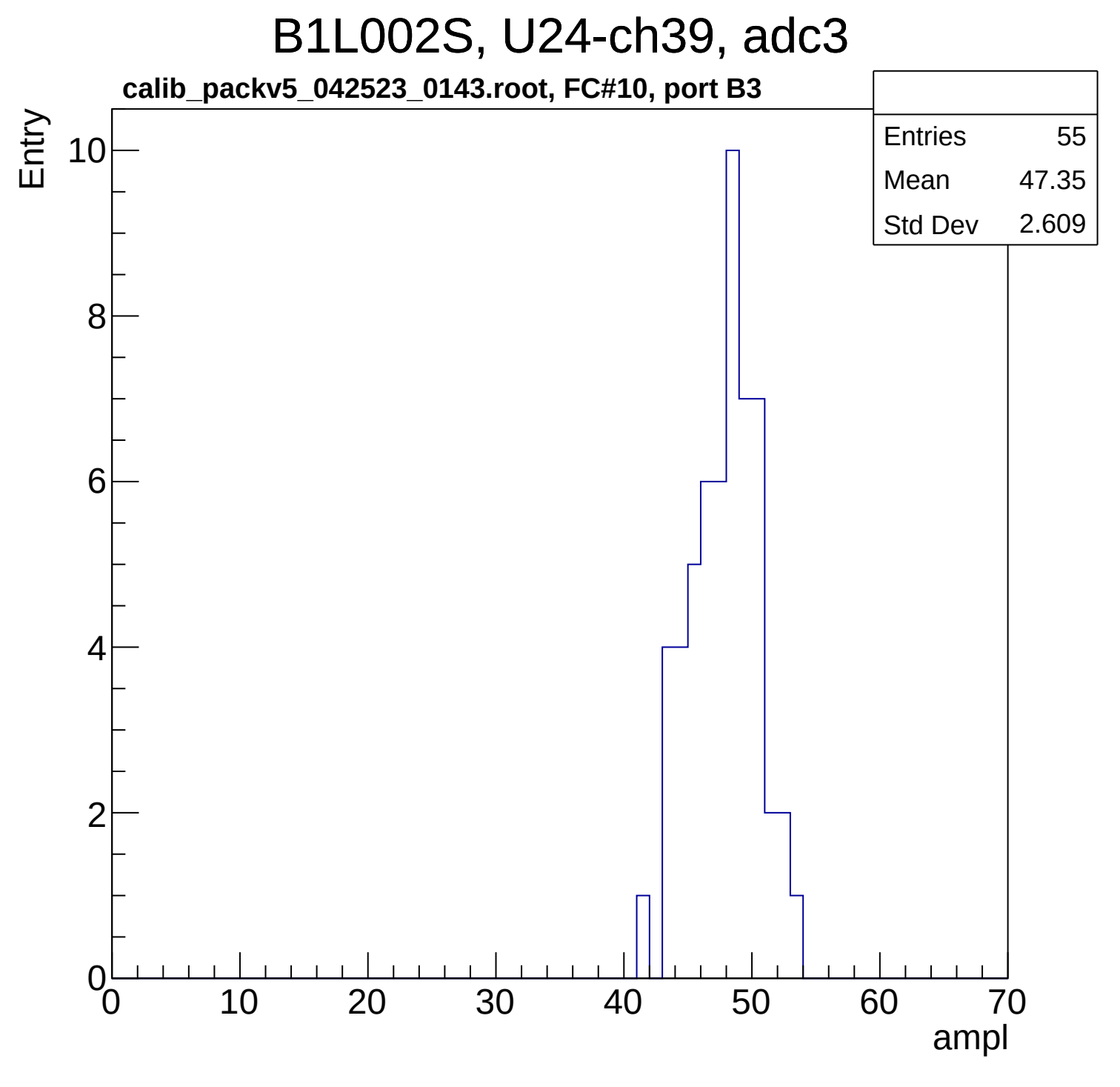
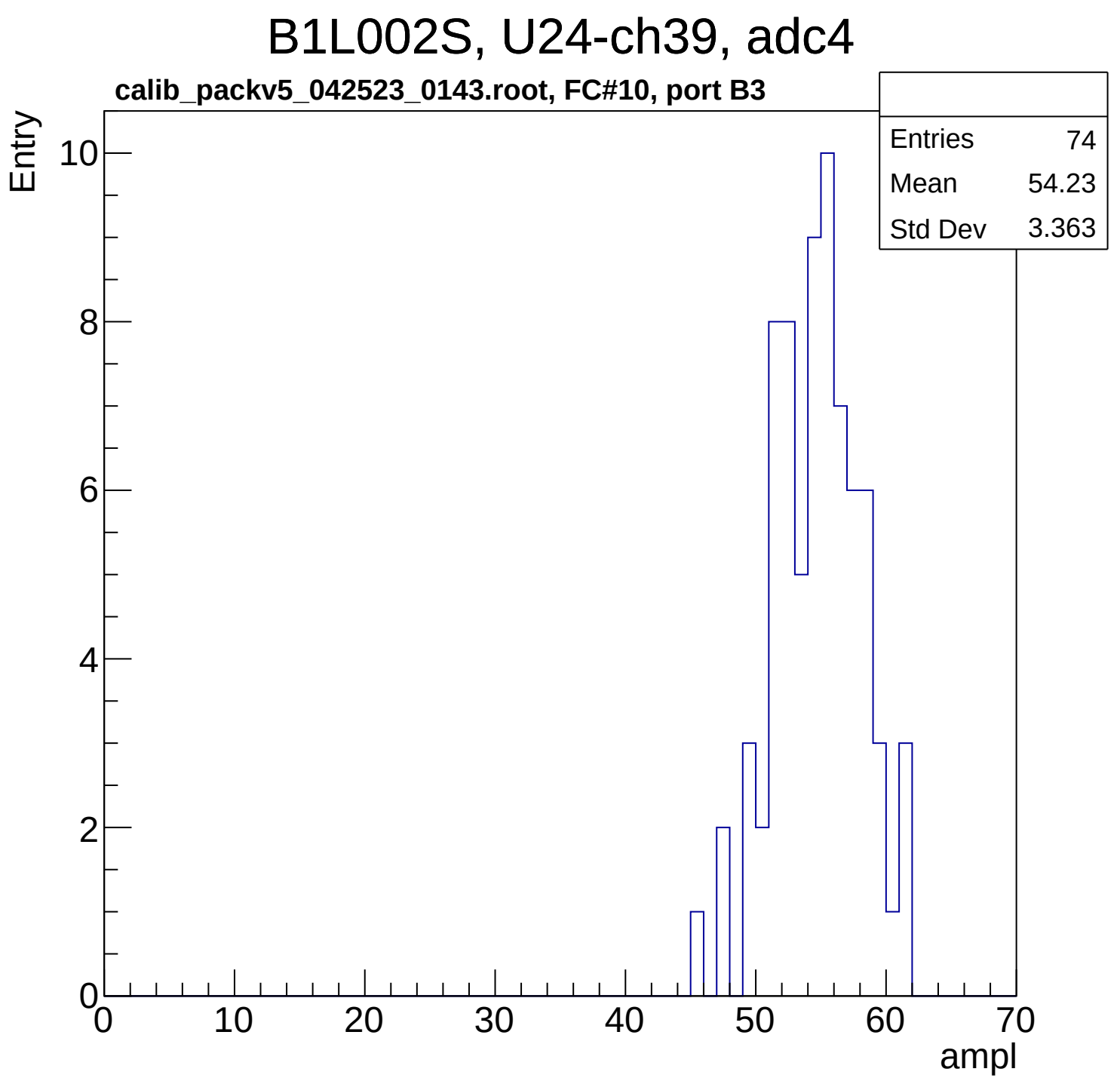

B1L002S, U24-ch39, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 47.35
Std Dev 2.609
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch39, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 54.23
Std Dev 3.363
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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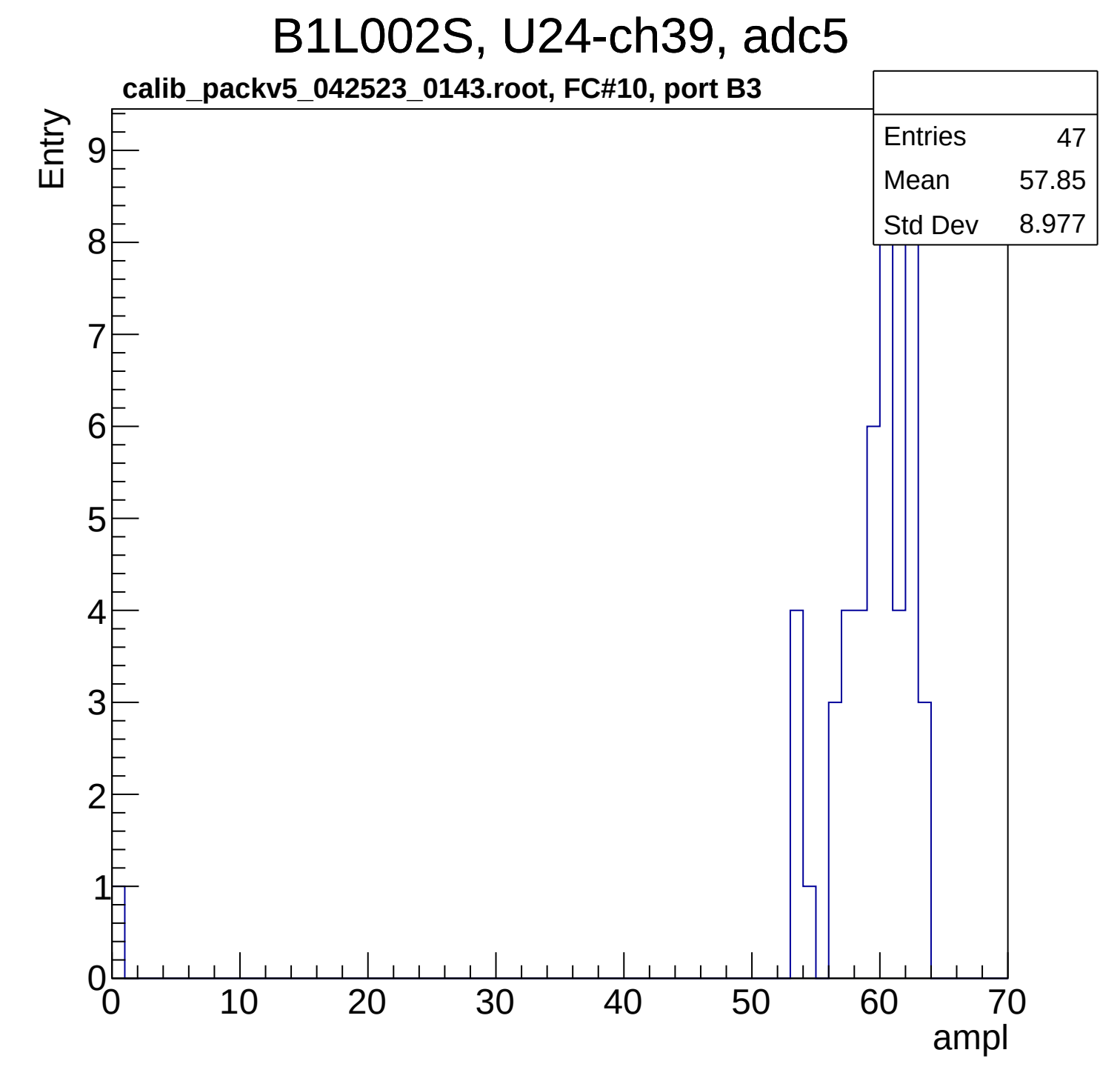
B1L002S, U24-ch39, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 57.85
Std Dev 8.977
Entry
ampl
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2
3
4
5
6
7
8
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0
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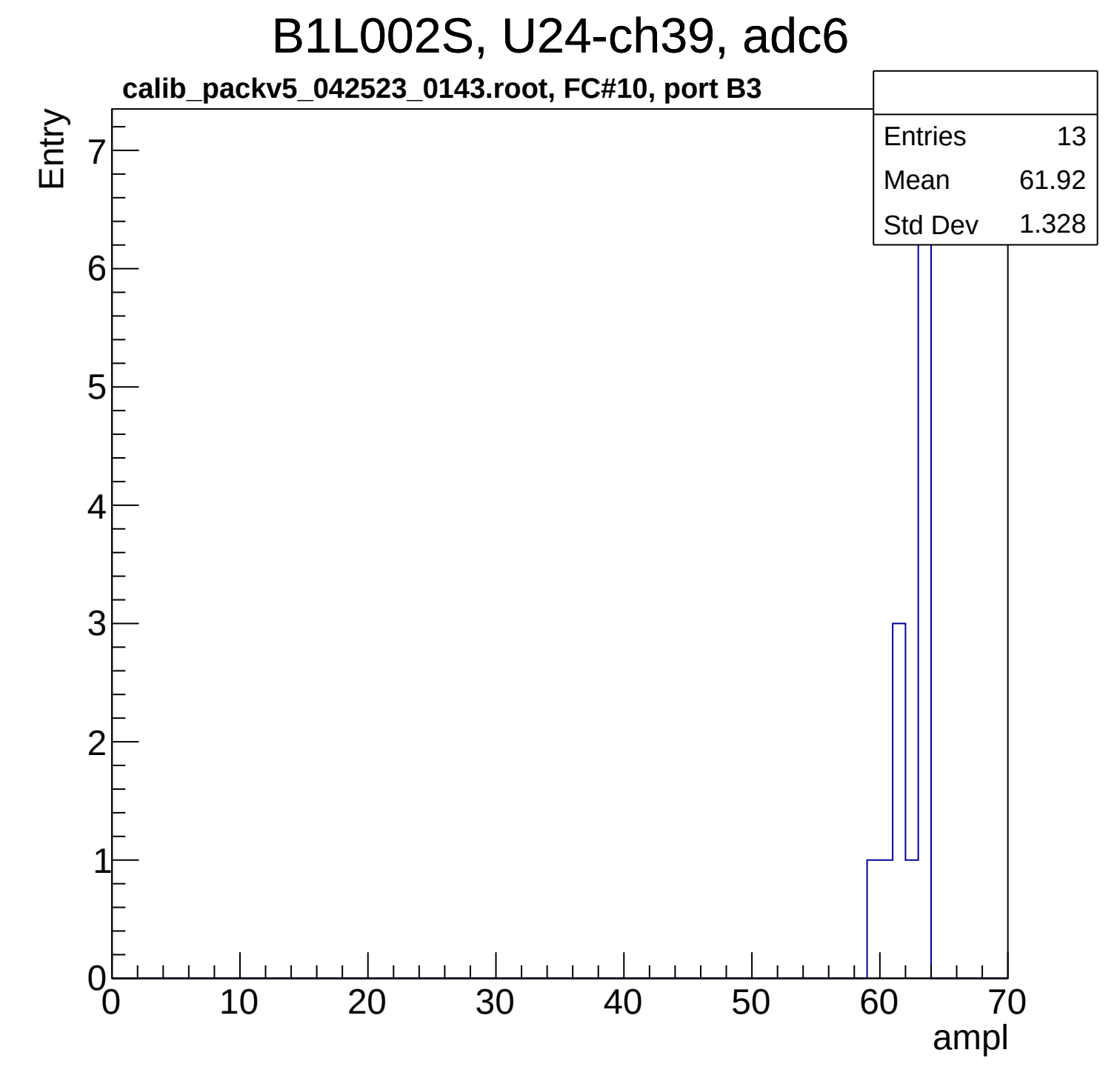
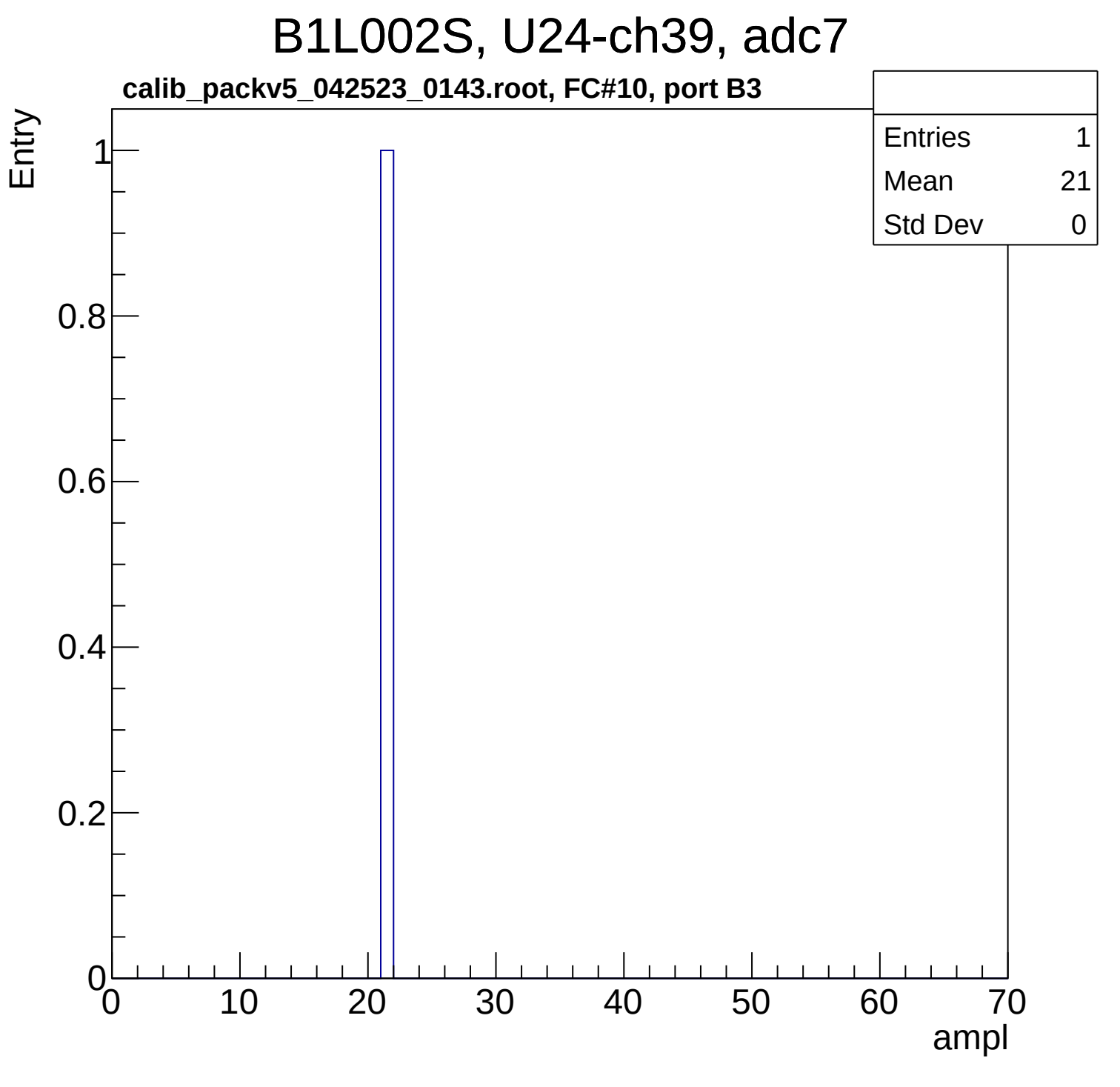
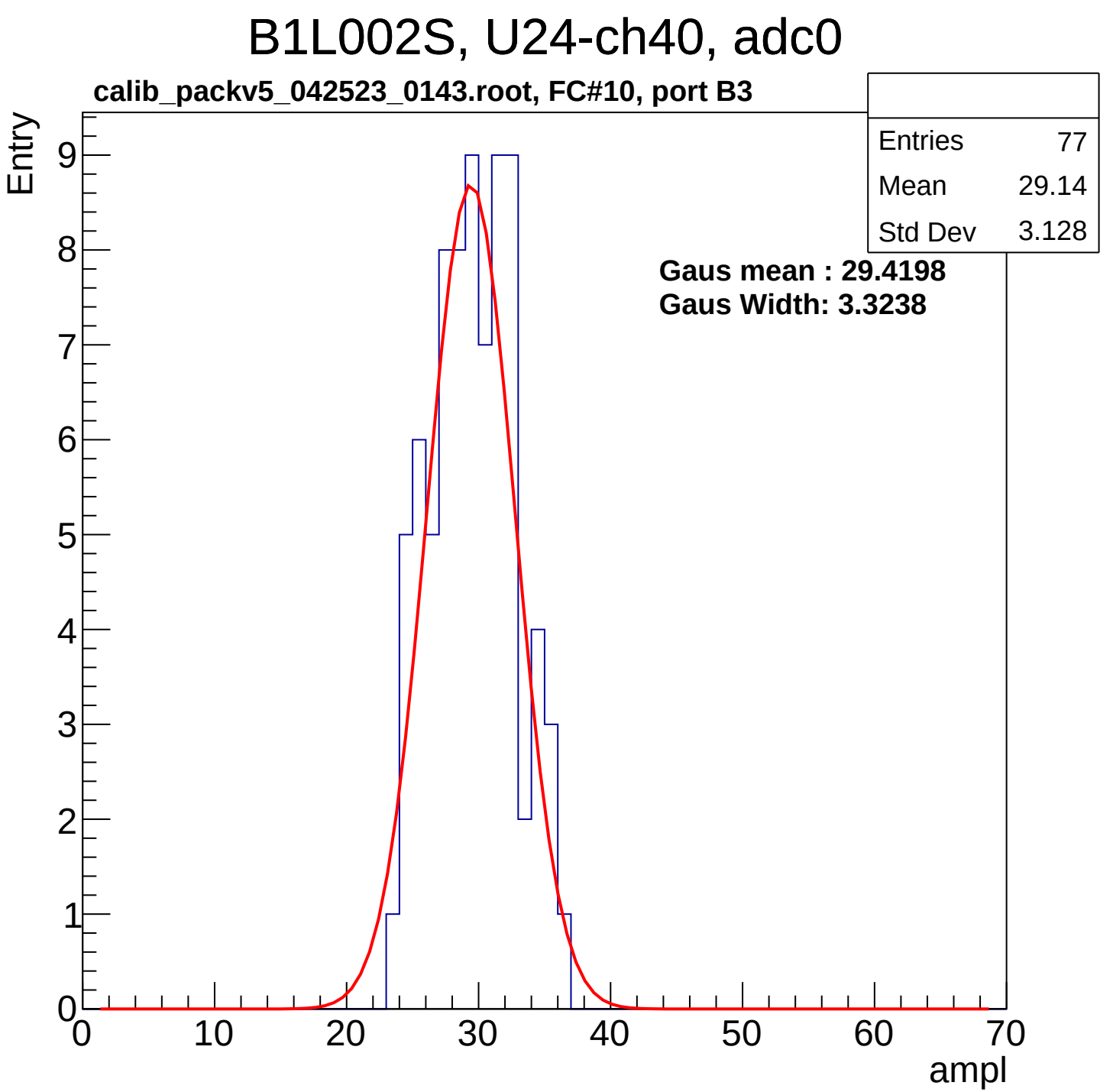
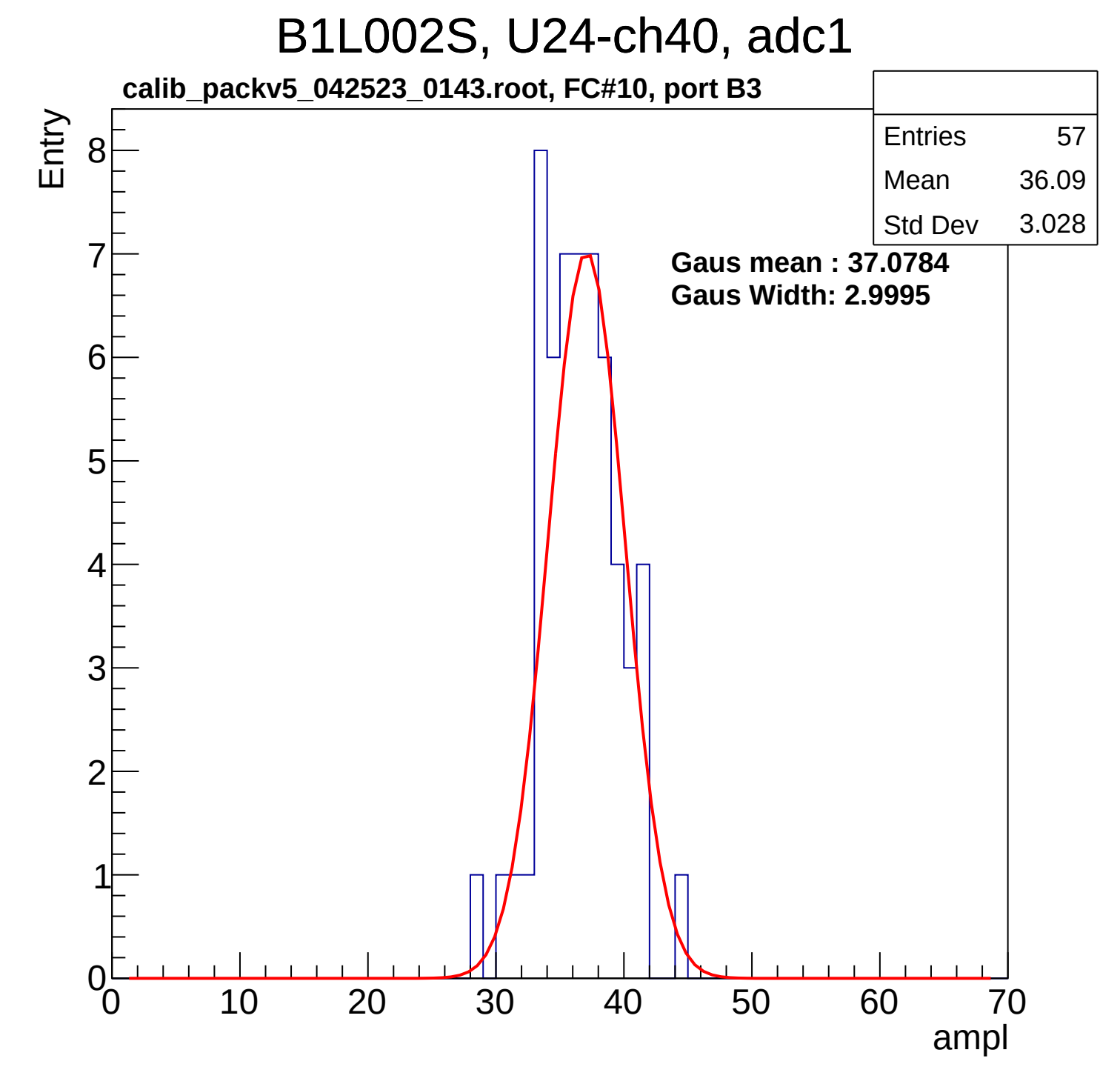

B1L002S, U24-ch39, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 13
Mean 61.92
Std Dev 1.328
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch39, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch40, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 29.14
Std Dev 3.128
Gaus mean : 29.4198
Gaus Width: 3.3238
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch40, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 36.09
Std Dev 3.028
Gaus mean : 37.0784
Gaus Width: 2.9995
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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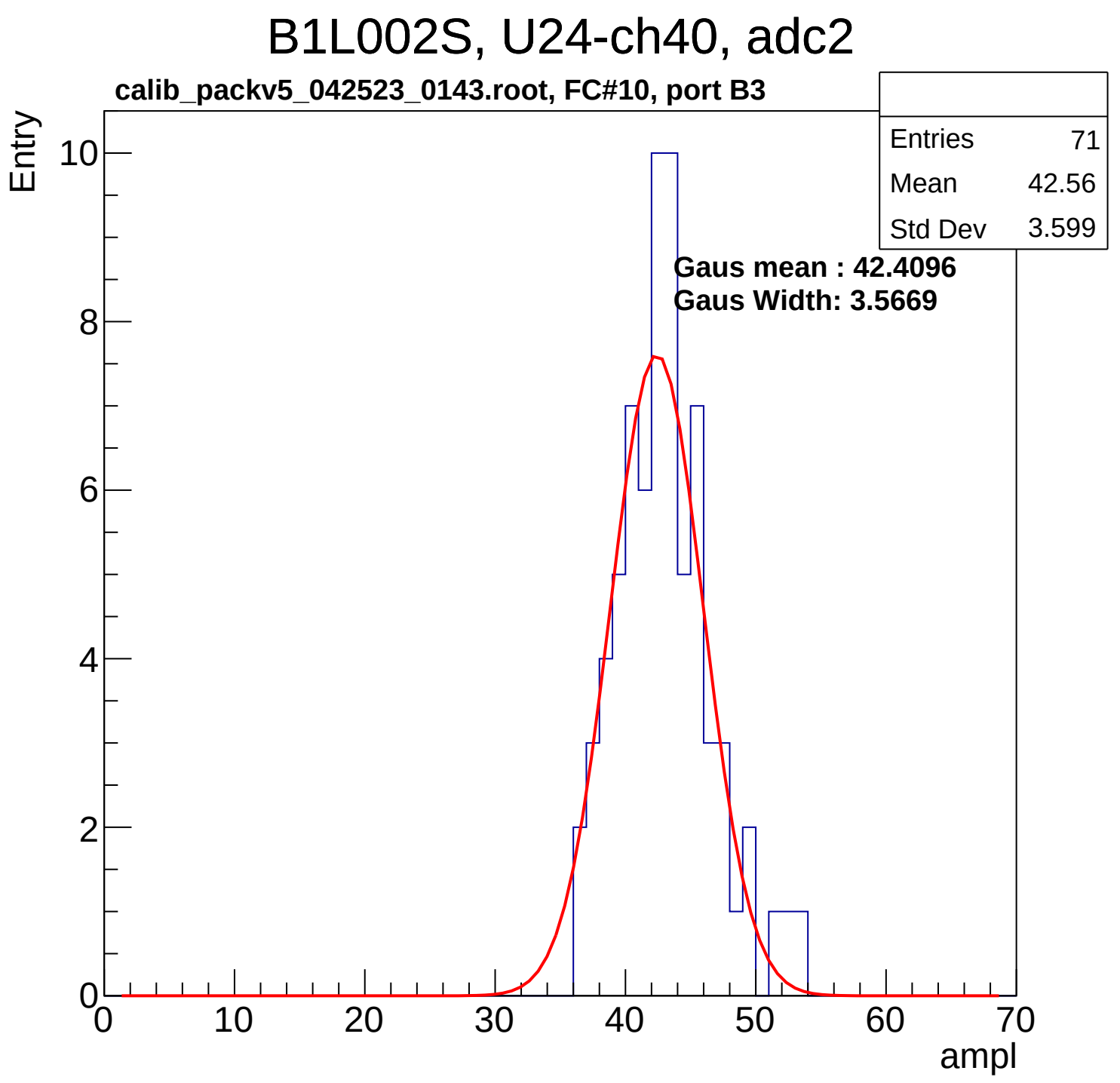
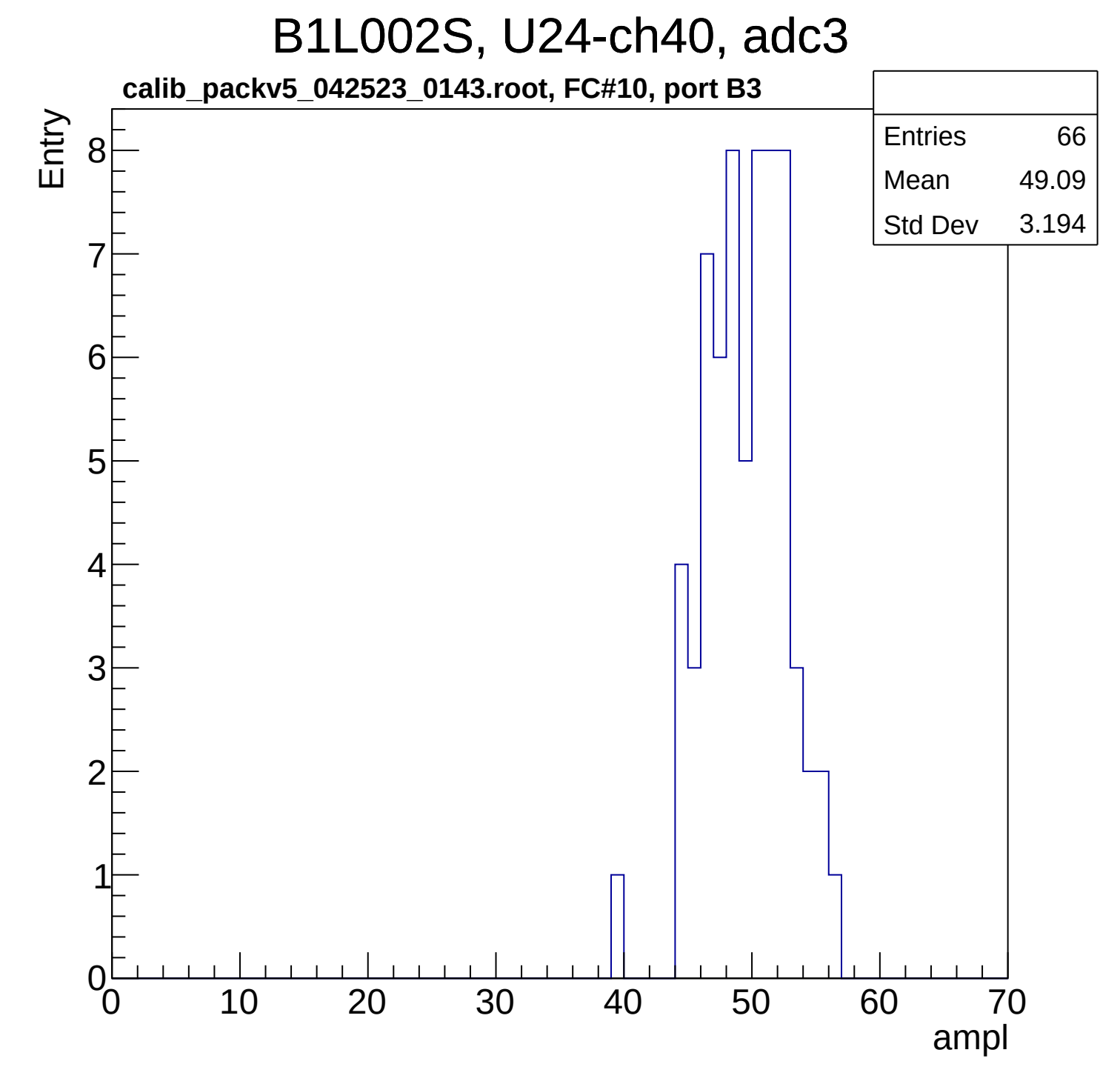
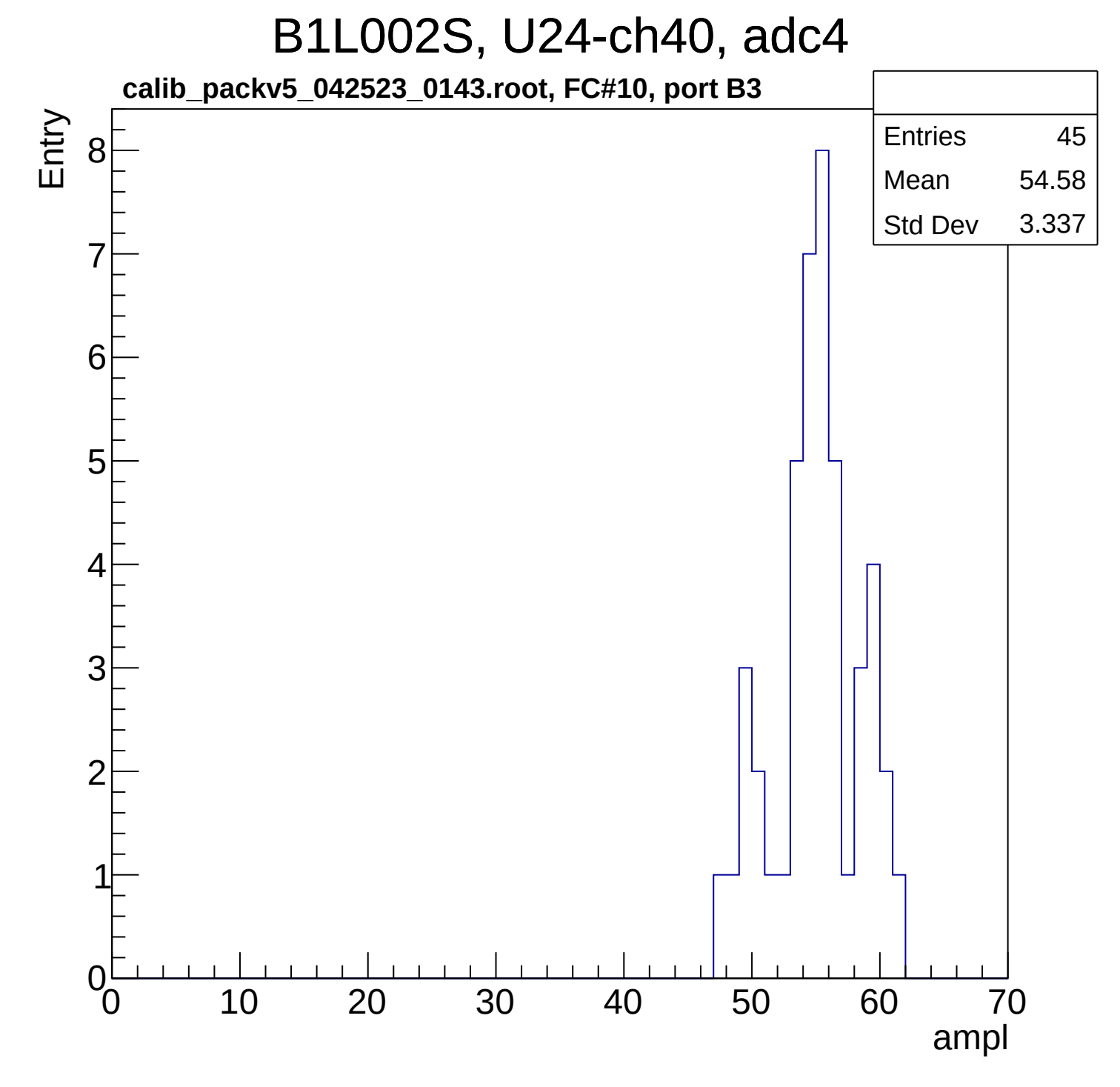

B1L002S, U24-ch40, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 42.56
Std Dev 3.599
Gaus mean : 42.4096
Gaus Width: 3.5669
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch40, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 49.09
Std Dev 3.194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch40, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 54.58
Std Dev 3.337
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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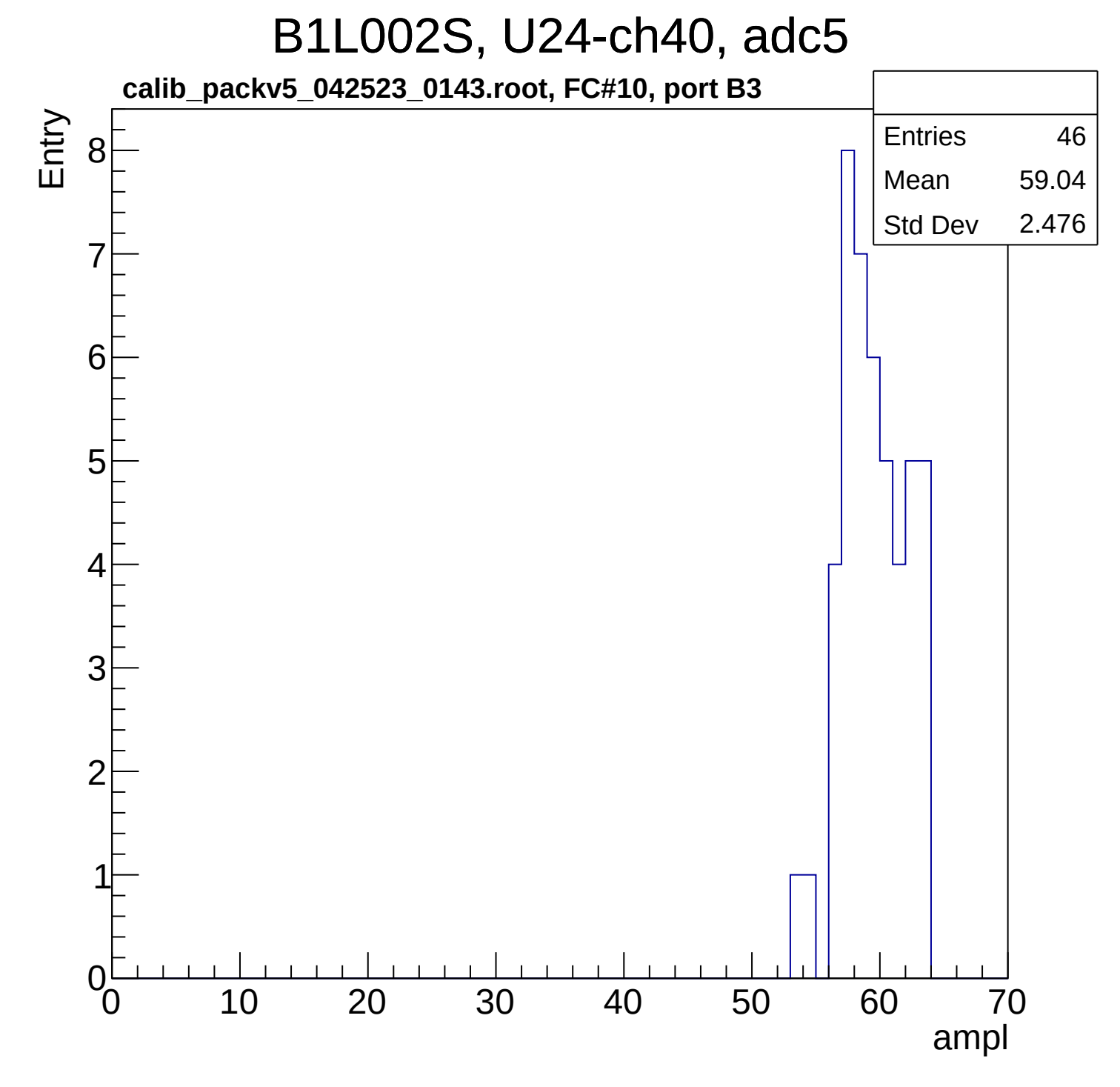
B1L002S, U24-ch40, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 59.04
Std Dev 2.476
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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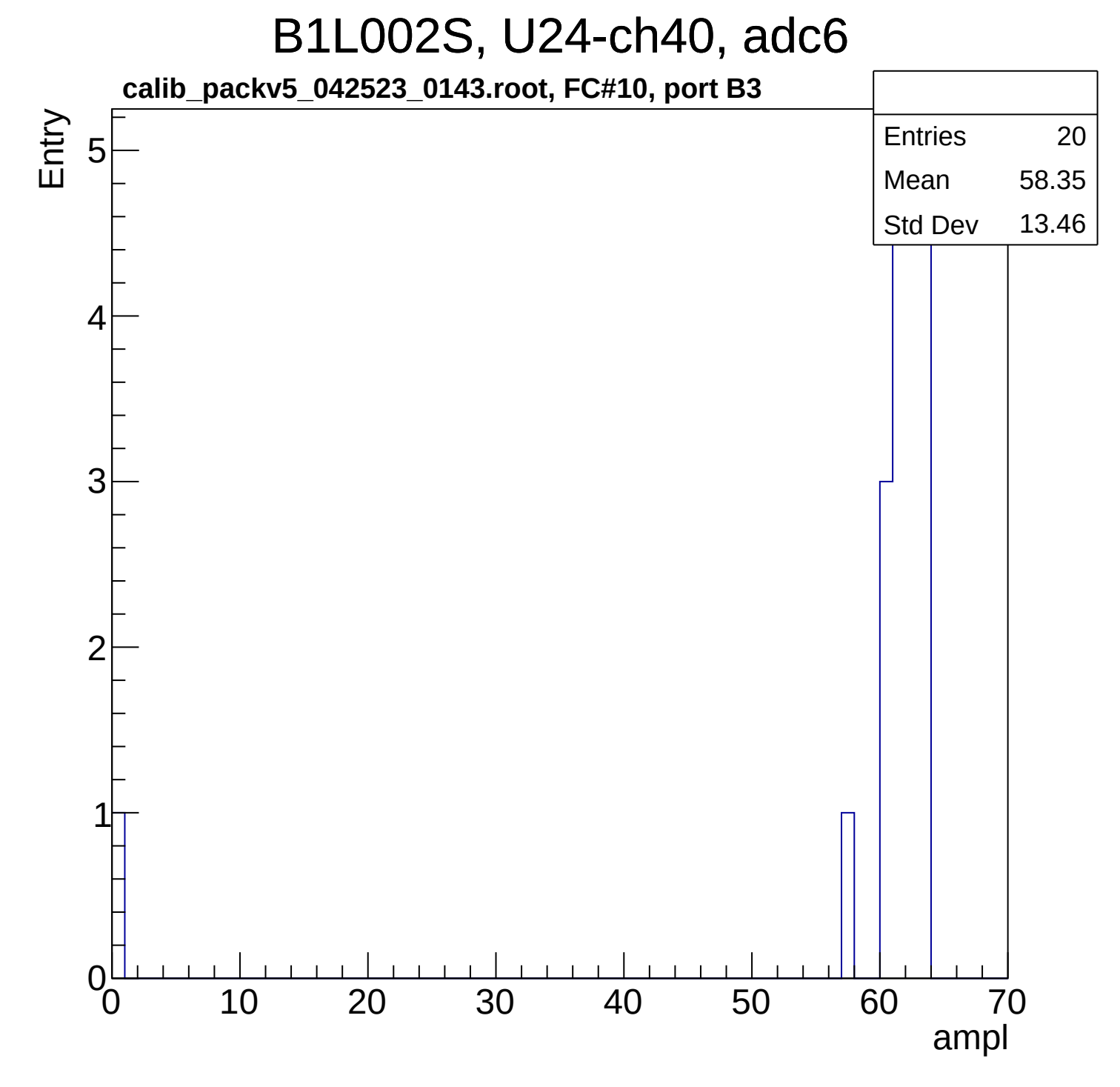
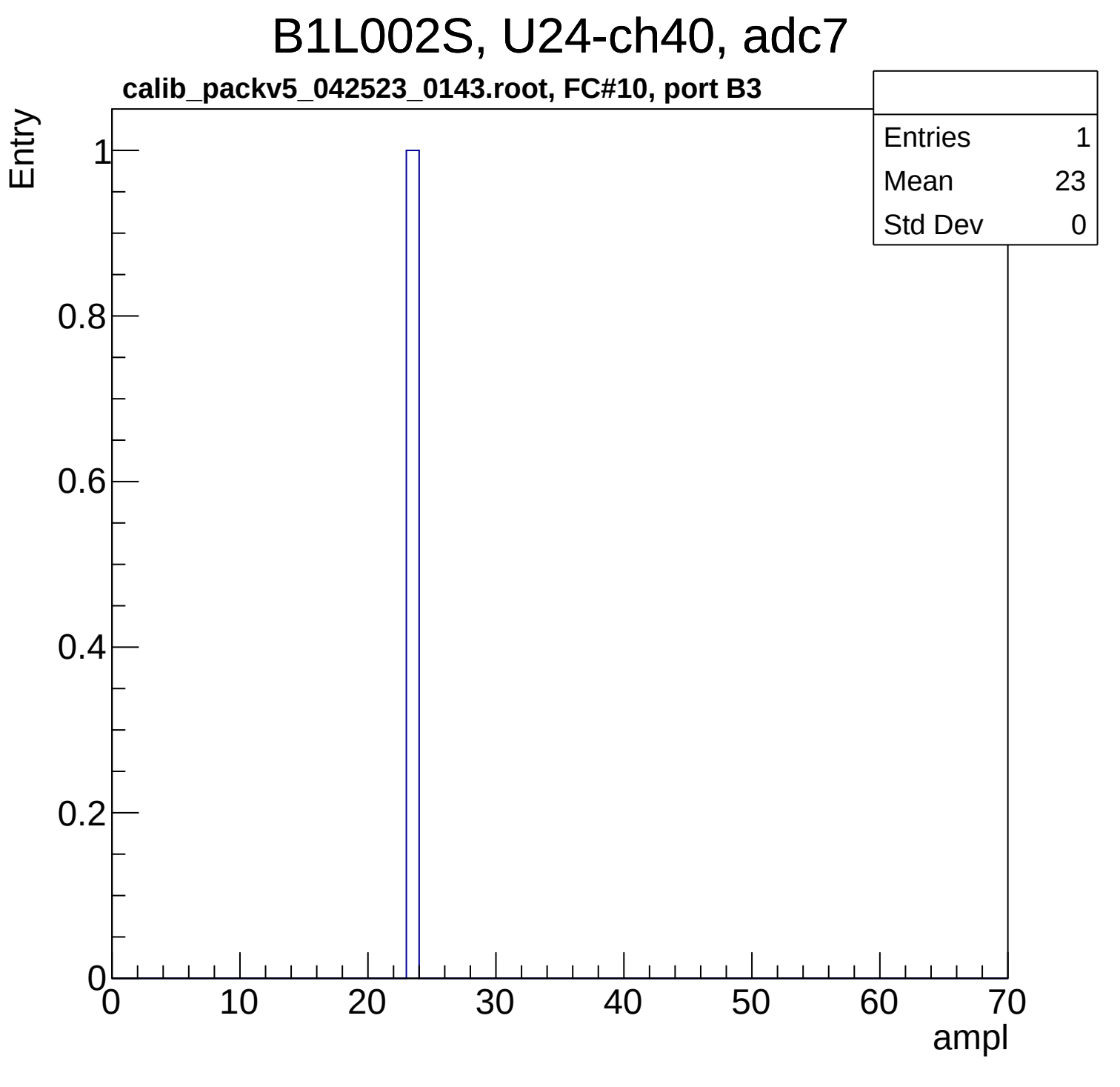
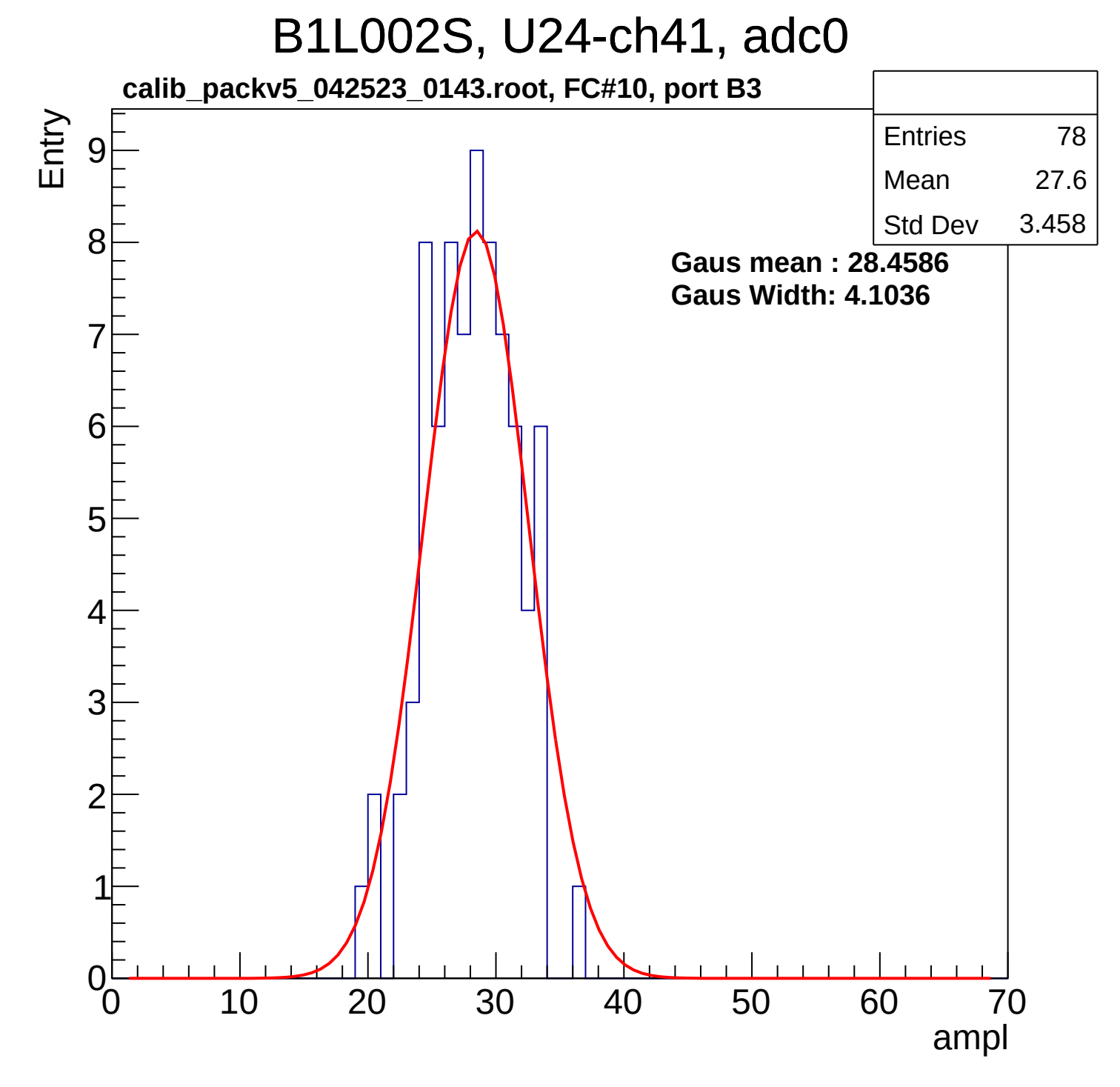

B1L002S, U24-ch40, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.35
Std Dev 13.46
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch40, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch41, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 27.6
Std Dev 3.458
Gaus mean : 28.4586
Gaus Width: 4.1036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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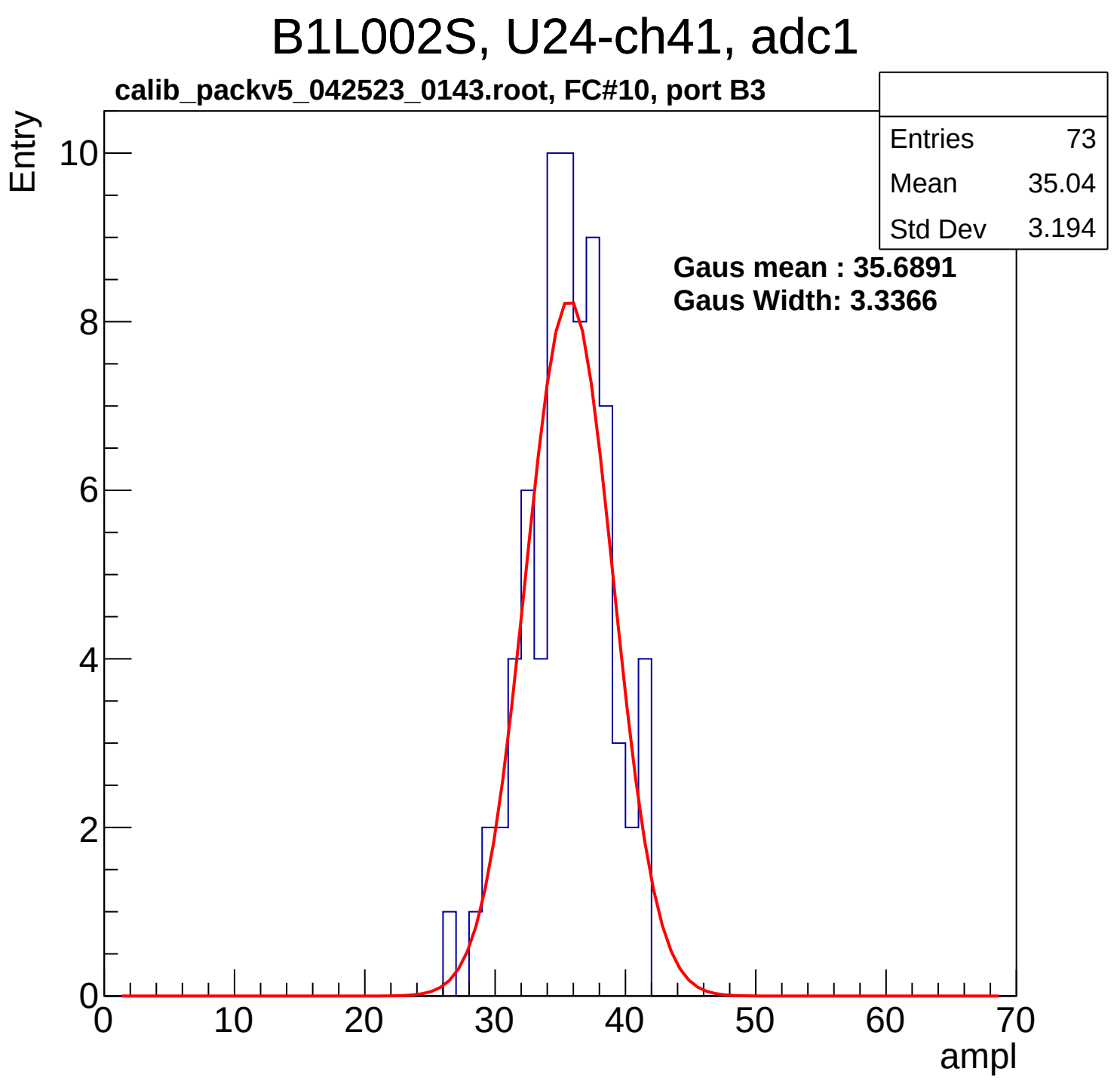
B1L002S, U24-ch41, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 35.04
Std Dev 3.194
Gaus mean : 35.6891
Gaus Width: 3.3366
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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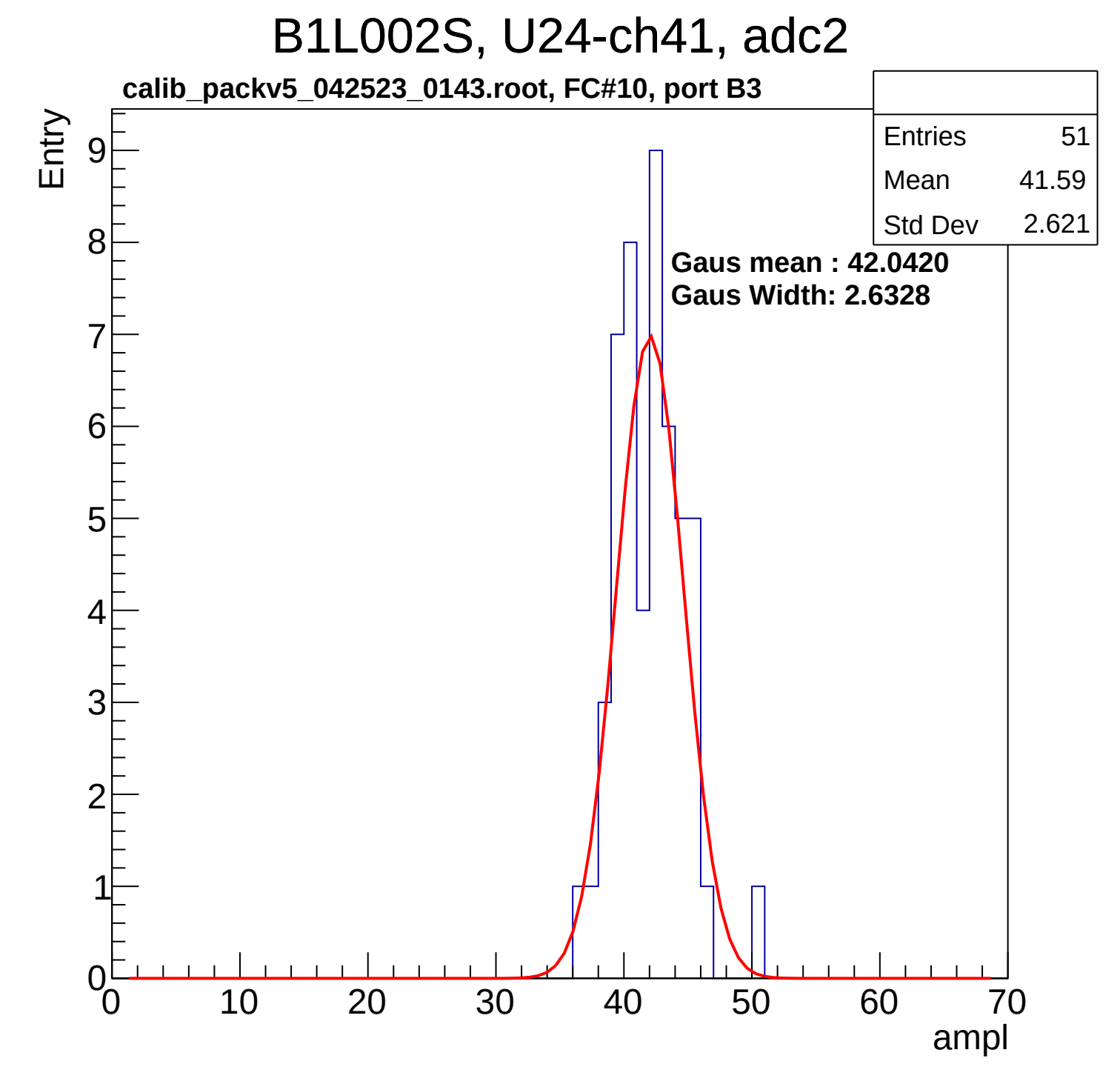
B1L002S, U24-ch41, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 41.59
Std Dev 2.621
Gaus mean : 42.0420
Gaus Width: 2.6328
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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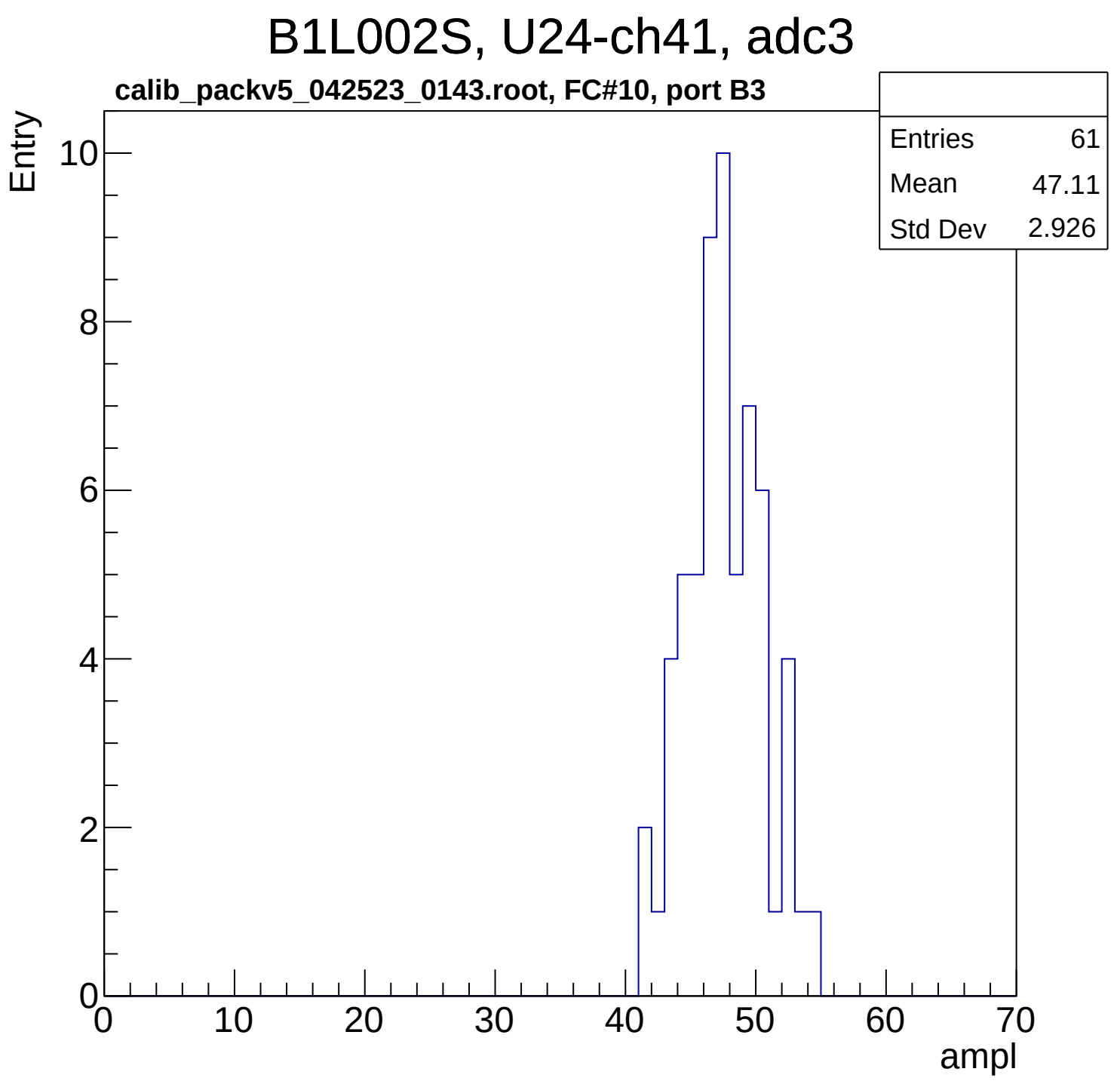
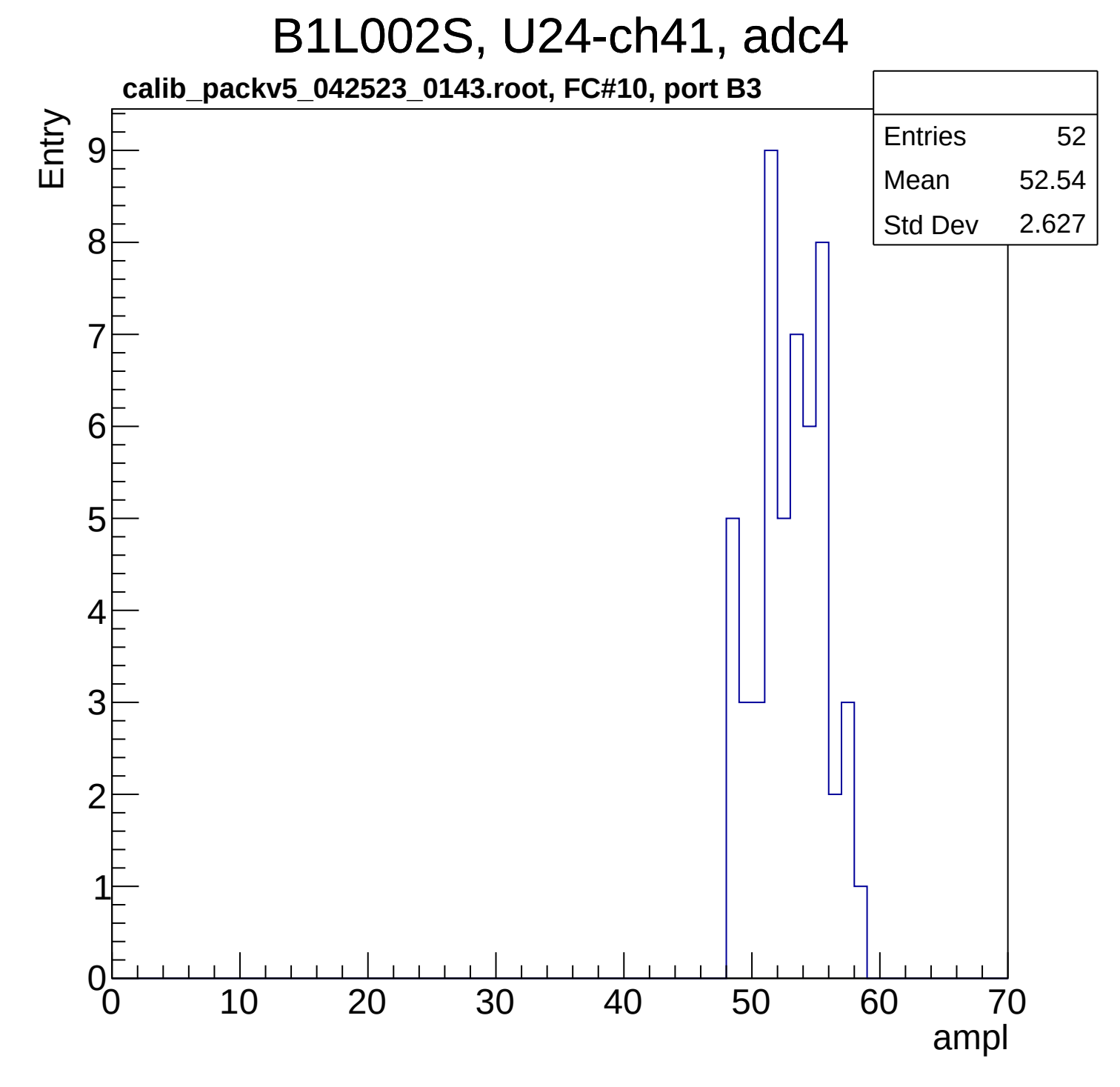

B1L002S, U24-ch41, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 47.11
Std Dev 2.926
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch41, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 52.54
Std Dev 2.627
Entry
ampl
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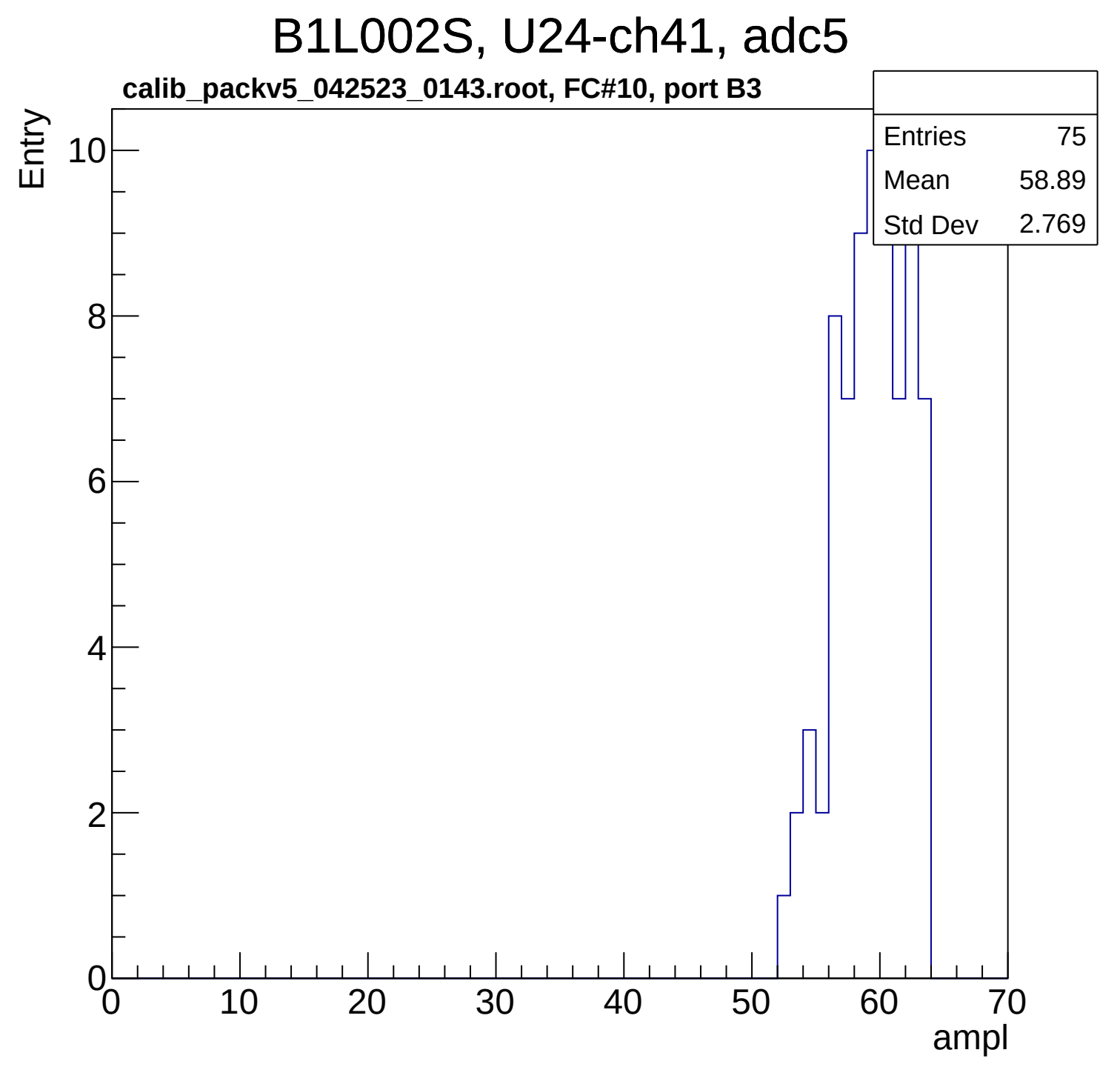
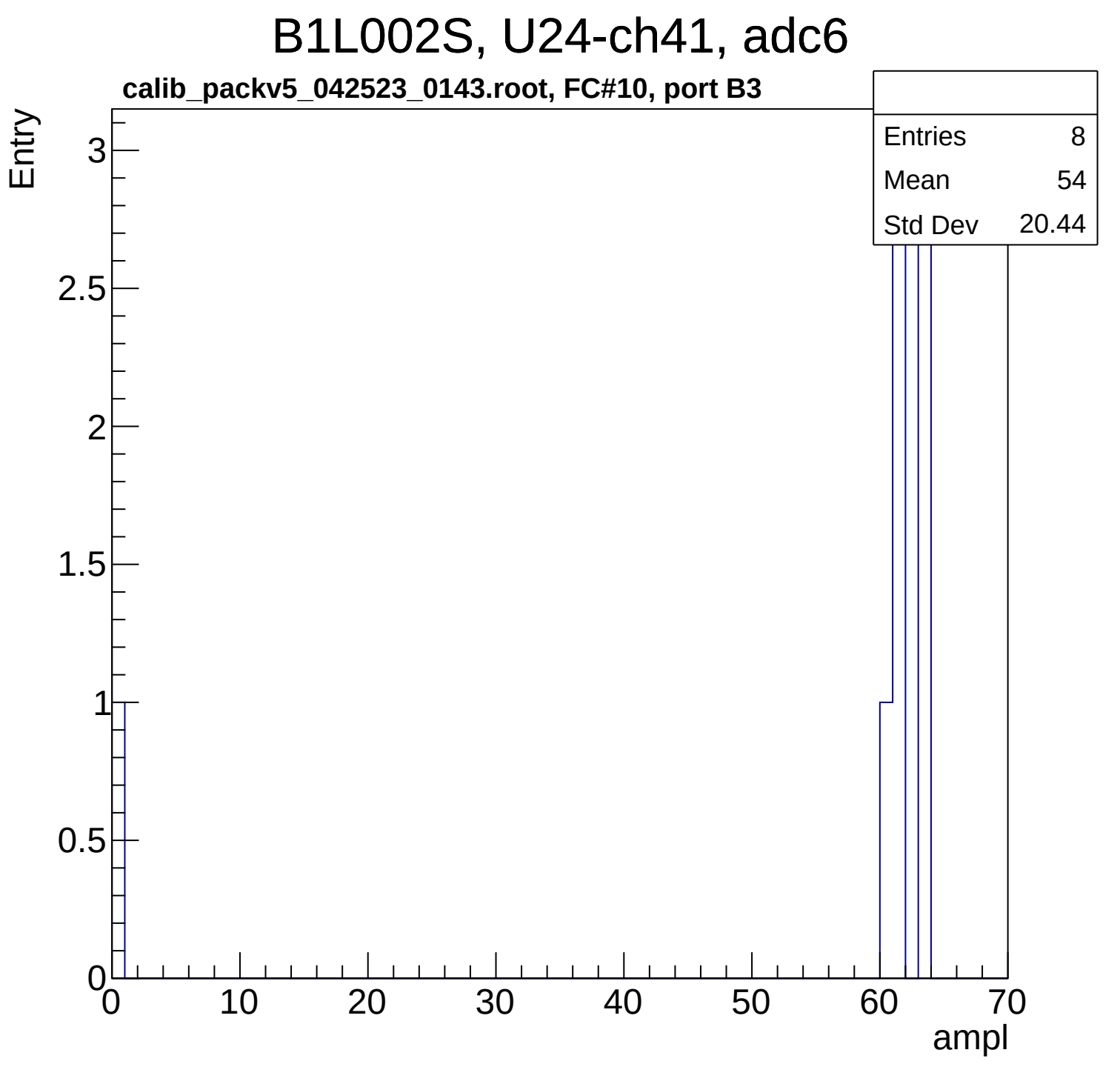
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch41, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 58.89
Std Dev 2.769
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch41, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 54
Std Dev 20.44
Entry
ampl
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0.5
1
1.5
2
2.5
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0
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20
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40
50
60
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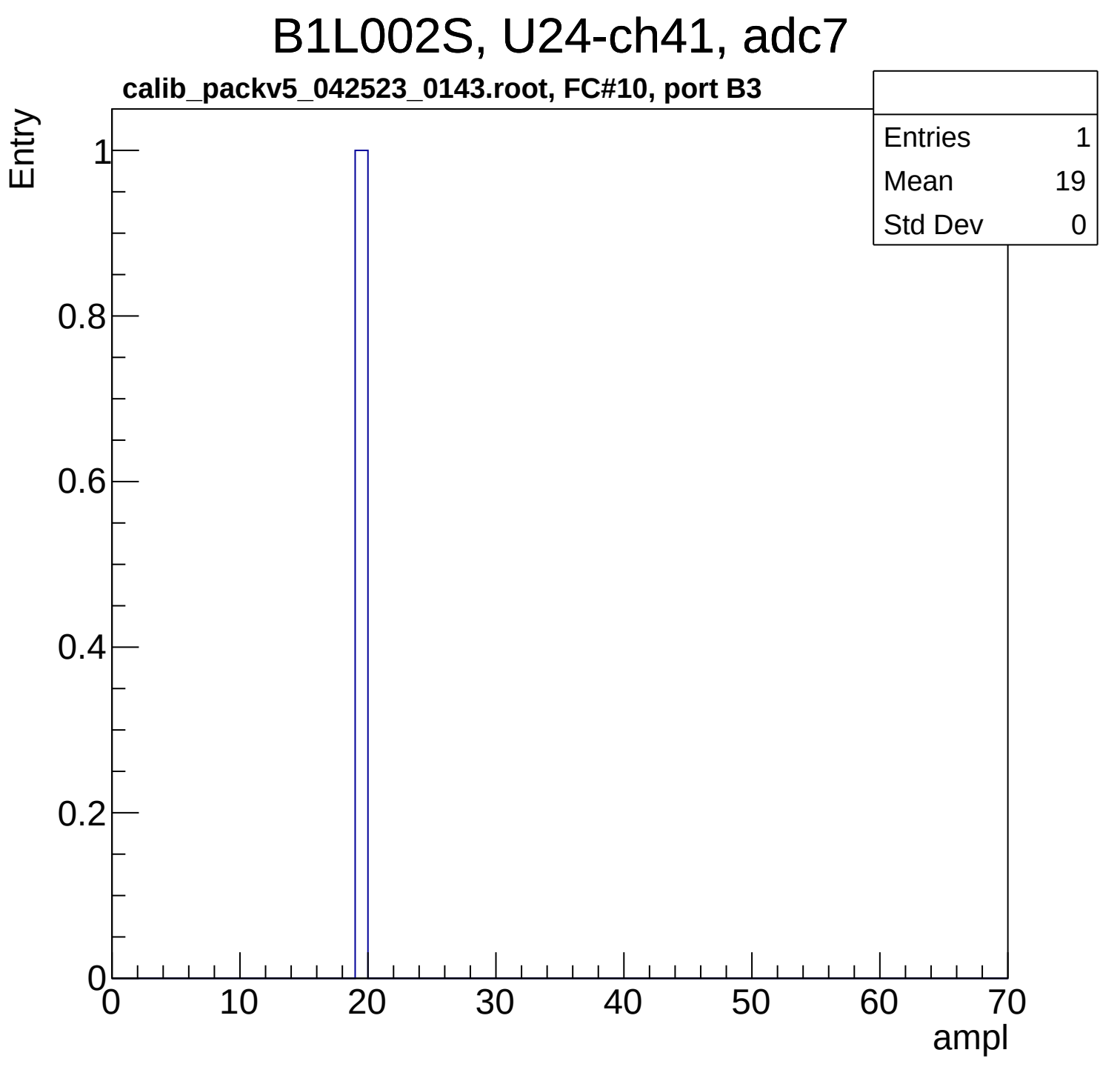
B1L002S, U24-ch41, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 19
Std Dev 0
Entry
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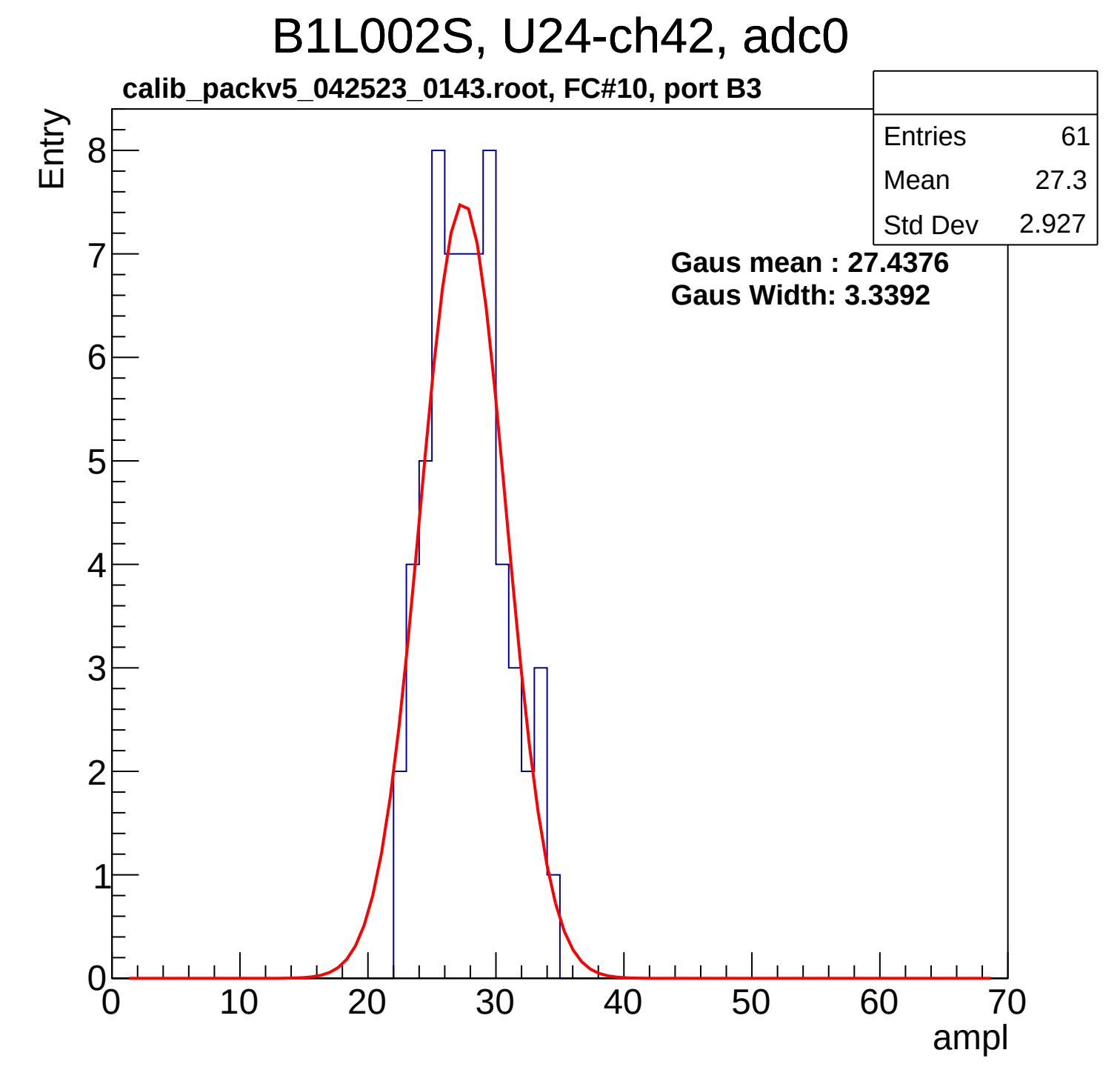
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch42, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 27.3
Std Dev 2.927
Gaus mean : 27.4376
Gaus Width: 3.3392
Entry
ampl
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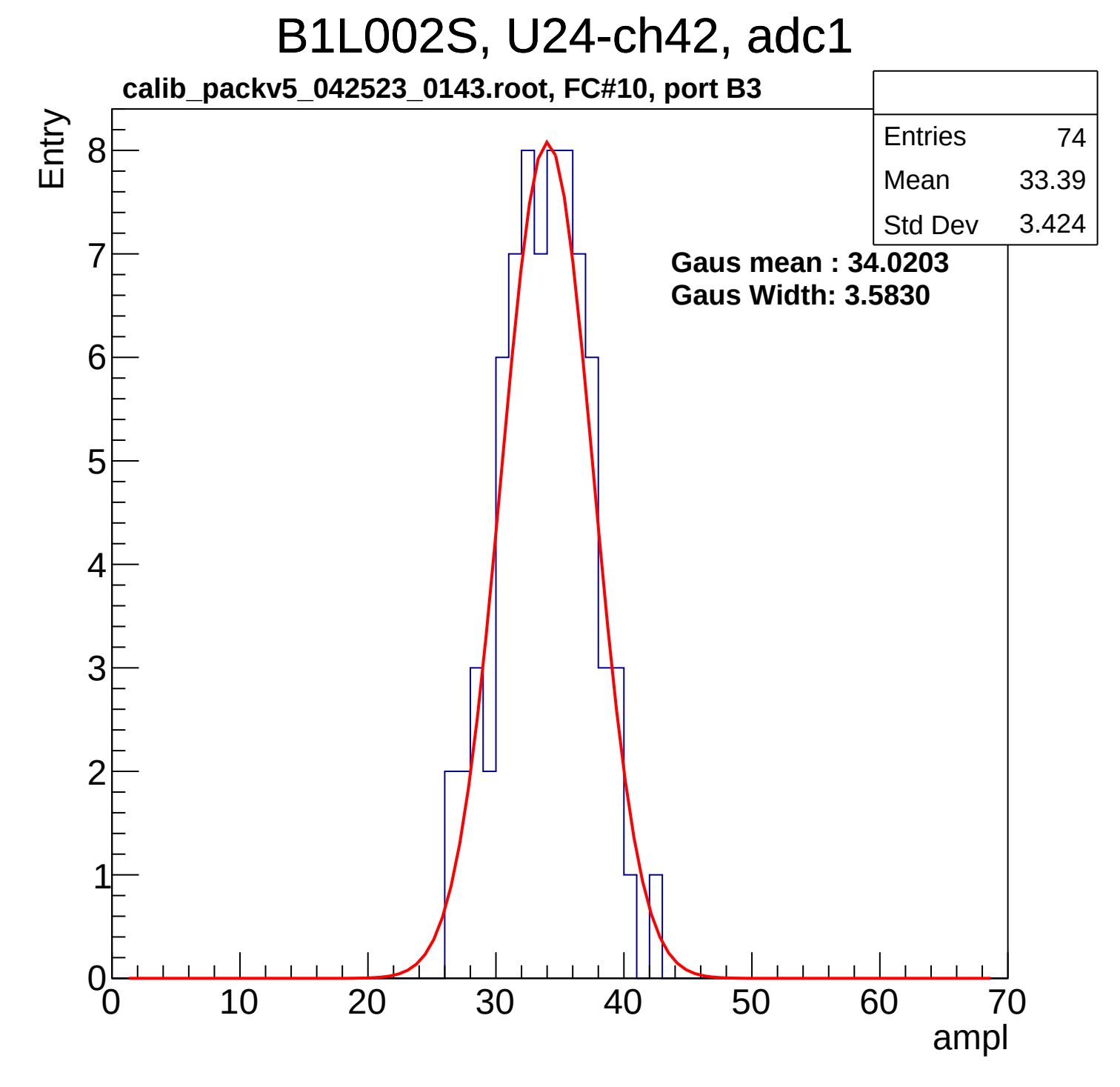
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch42, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 33.39
Std Dev 3.424
Gaus mean : 34.0203
Gaus Width: 3.5830
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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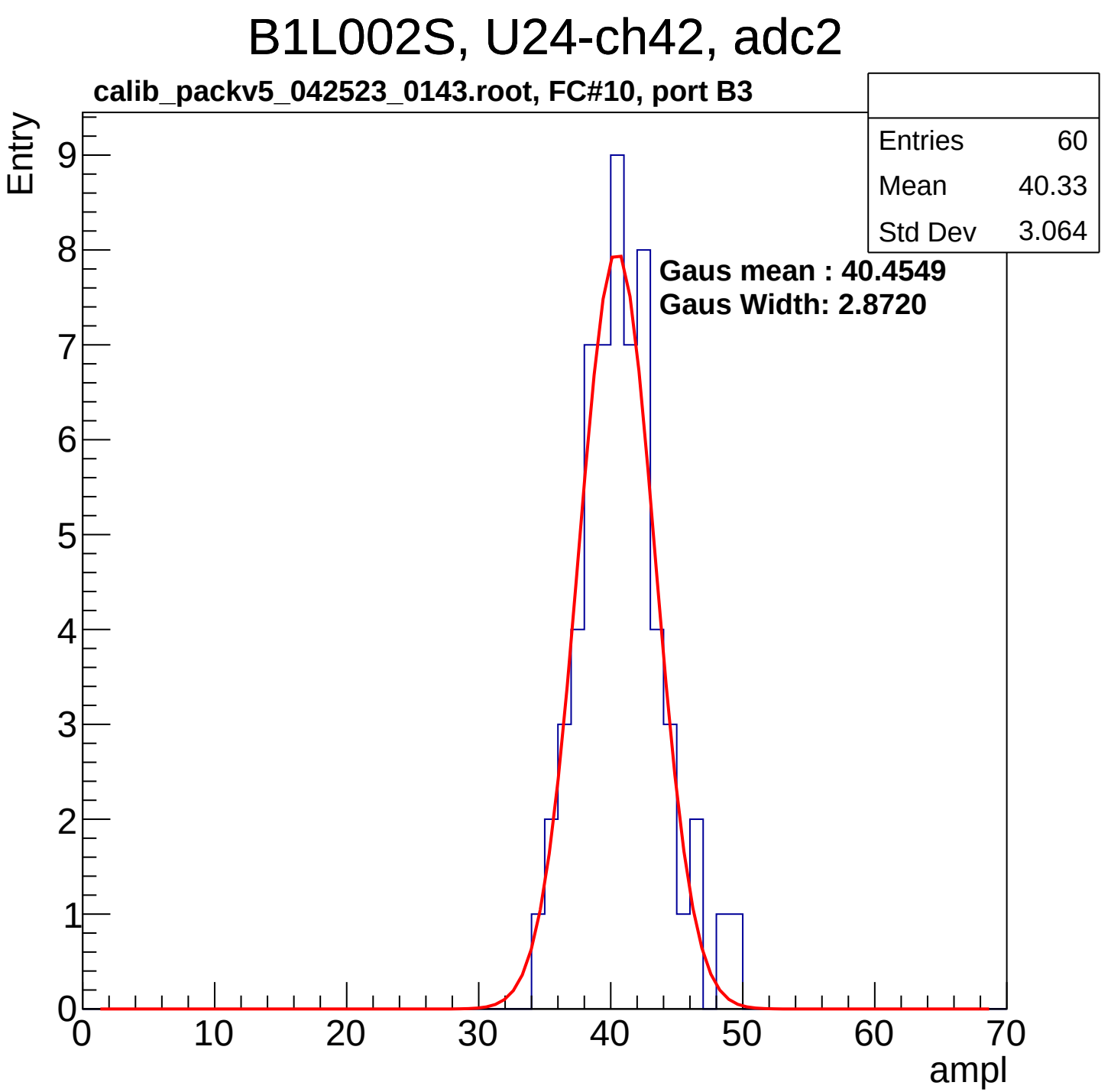
B1L002S, U24-ch42, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.33
Std Dev 3.064
Gaus mean : 40.4549
Gaus Width: 2.8720
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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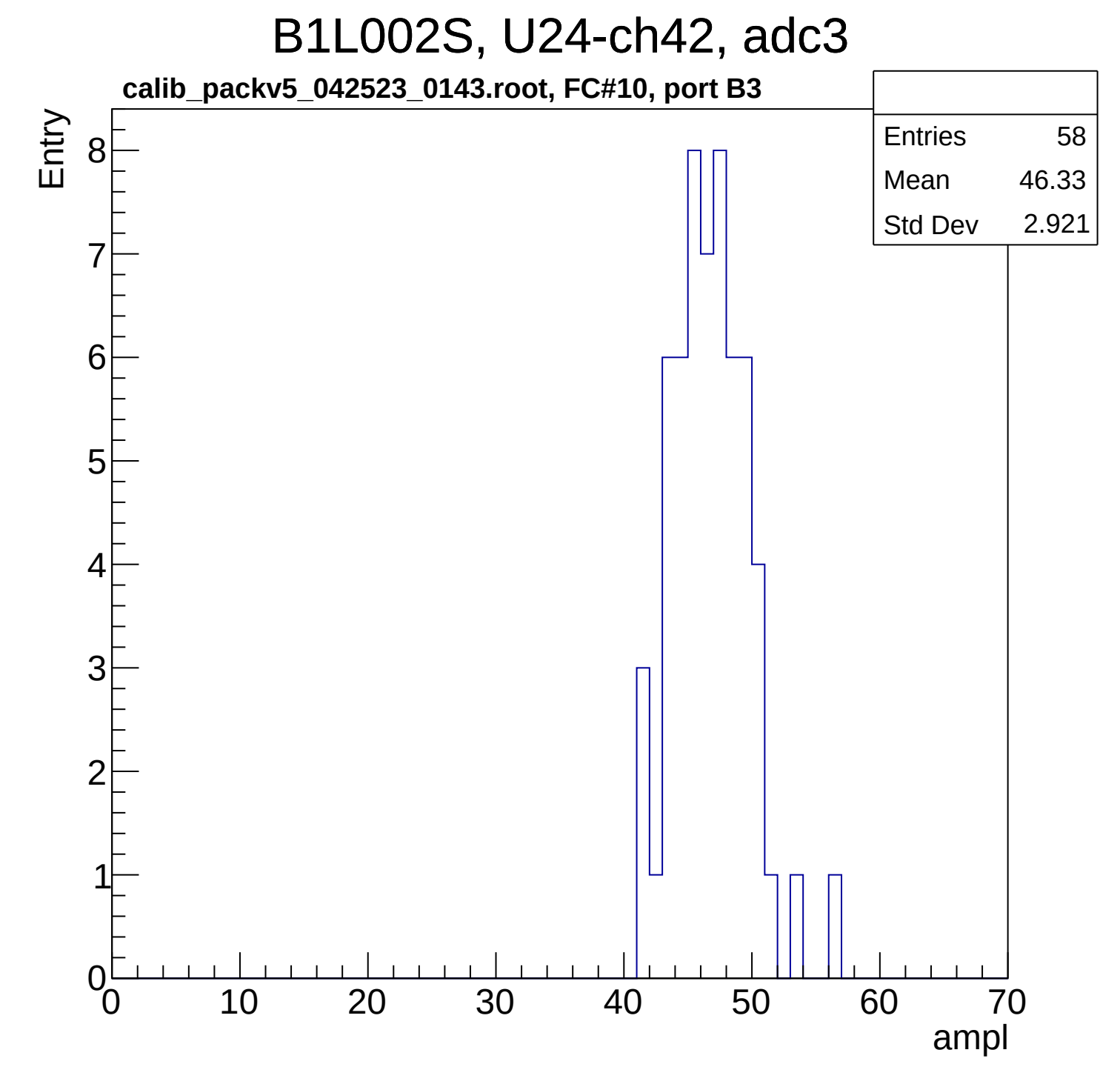
B1L002S, U24-ch42, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.33
Std Dev 2.921
Entry
ampl
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3
4
5
6
7
8
0
10
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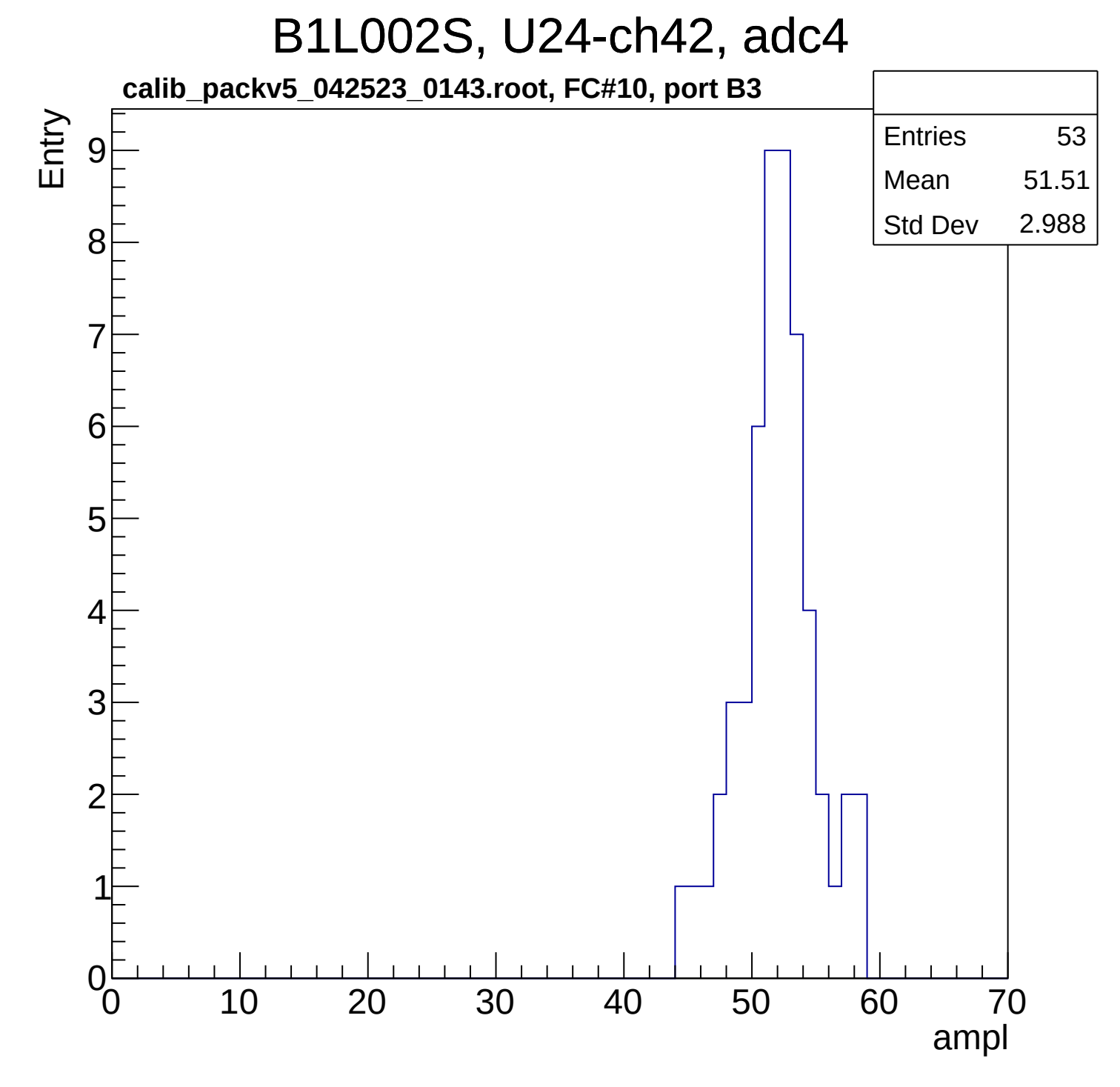
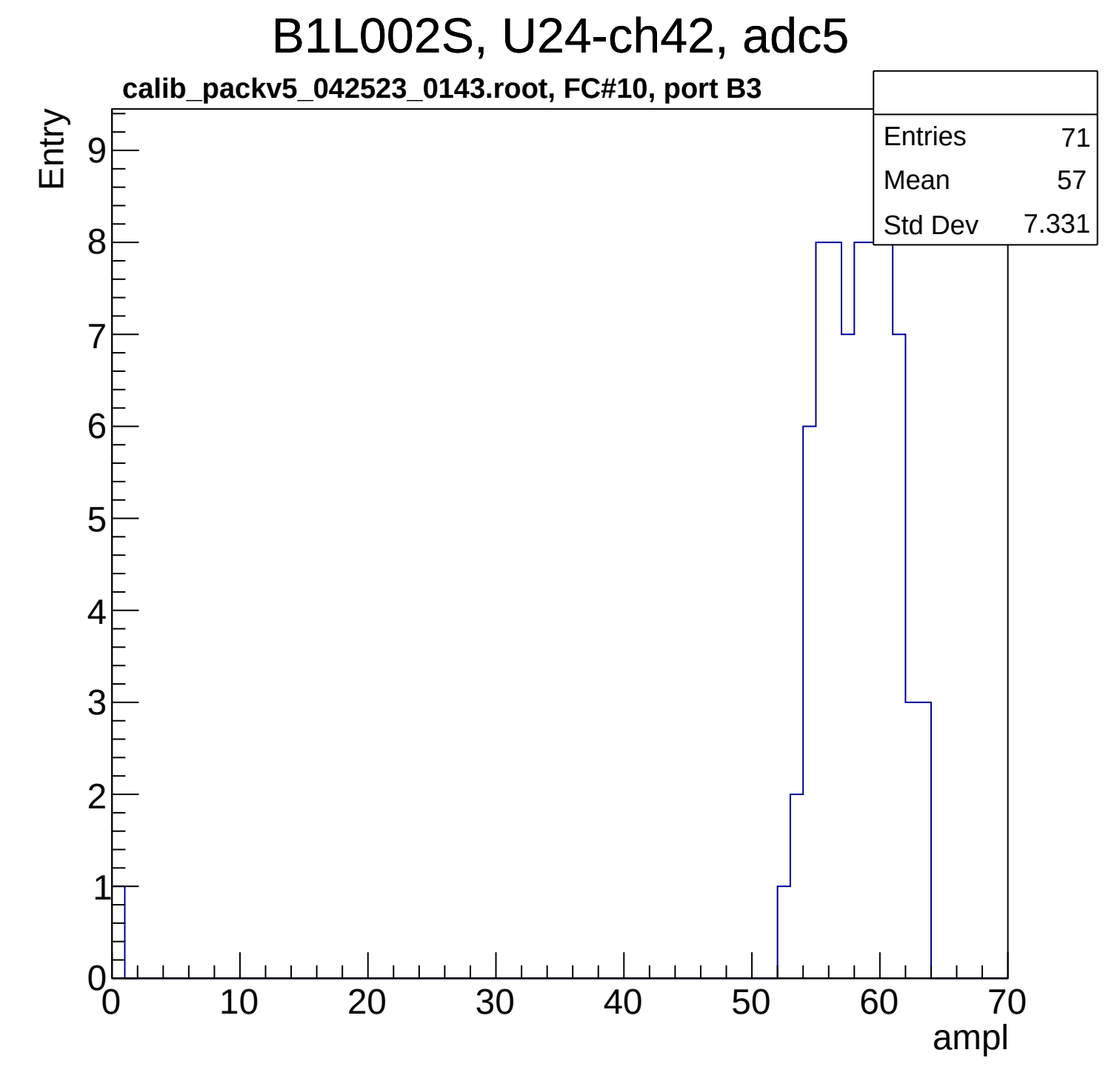

B1L002S, U24-ch42, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.51
Std Dev 2.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch42, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 57
Std Dev 7.331
Entry
ampl
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1
2
3
4
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6
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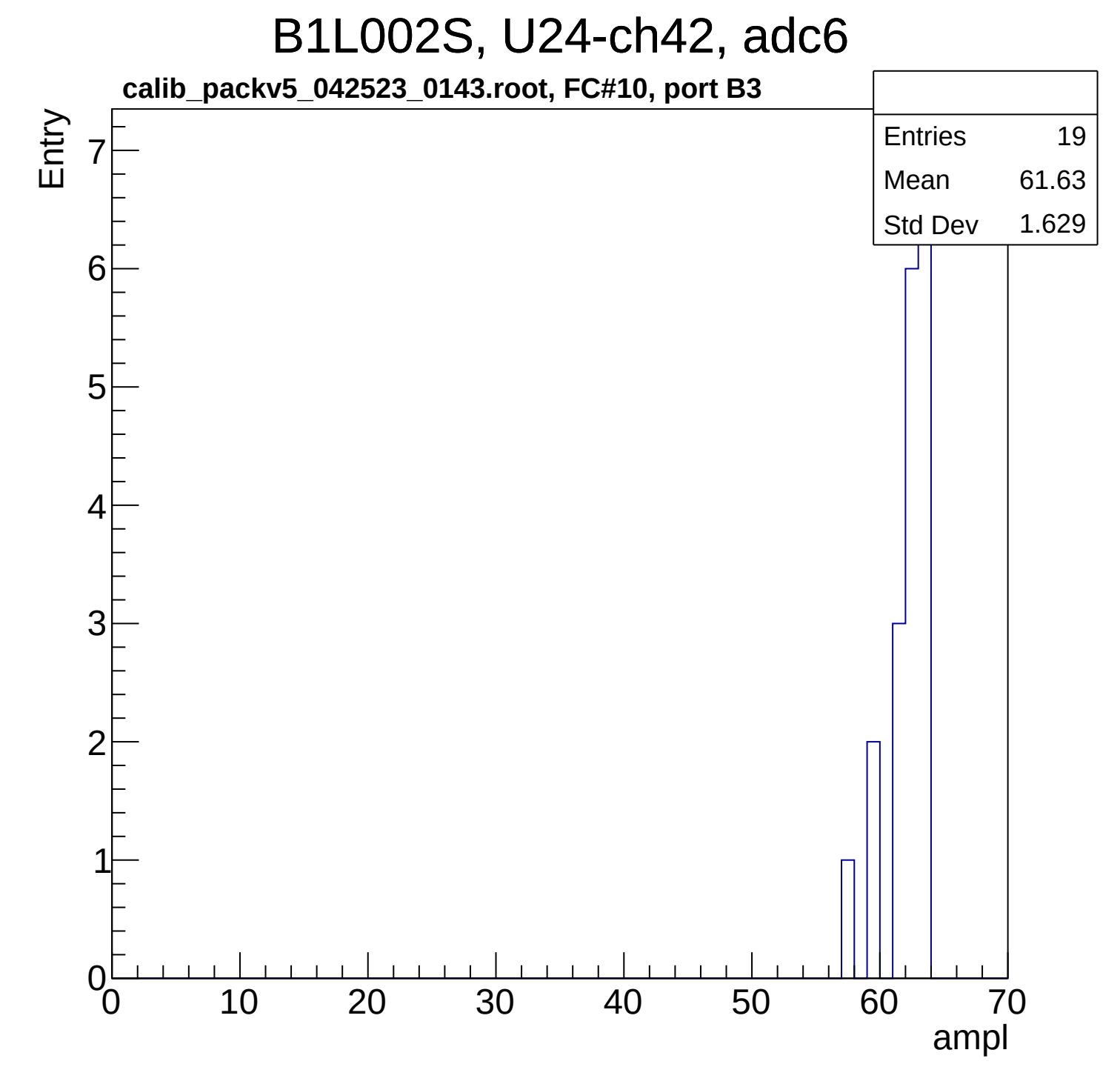
B1L002S, U24-ch42, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.63
Std Dev 1.629
Entry
ampl
0
1
2
3
4
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6
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60
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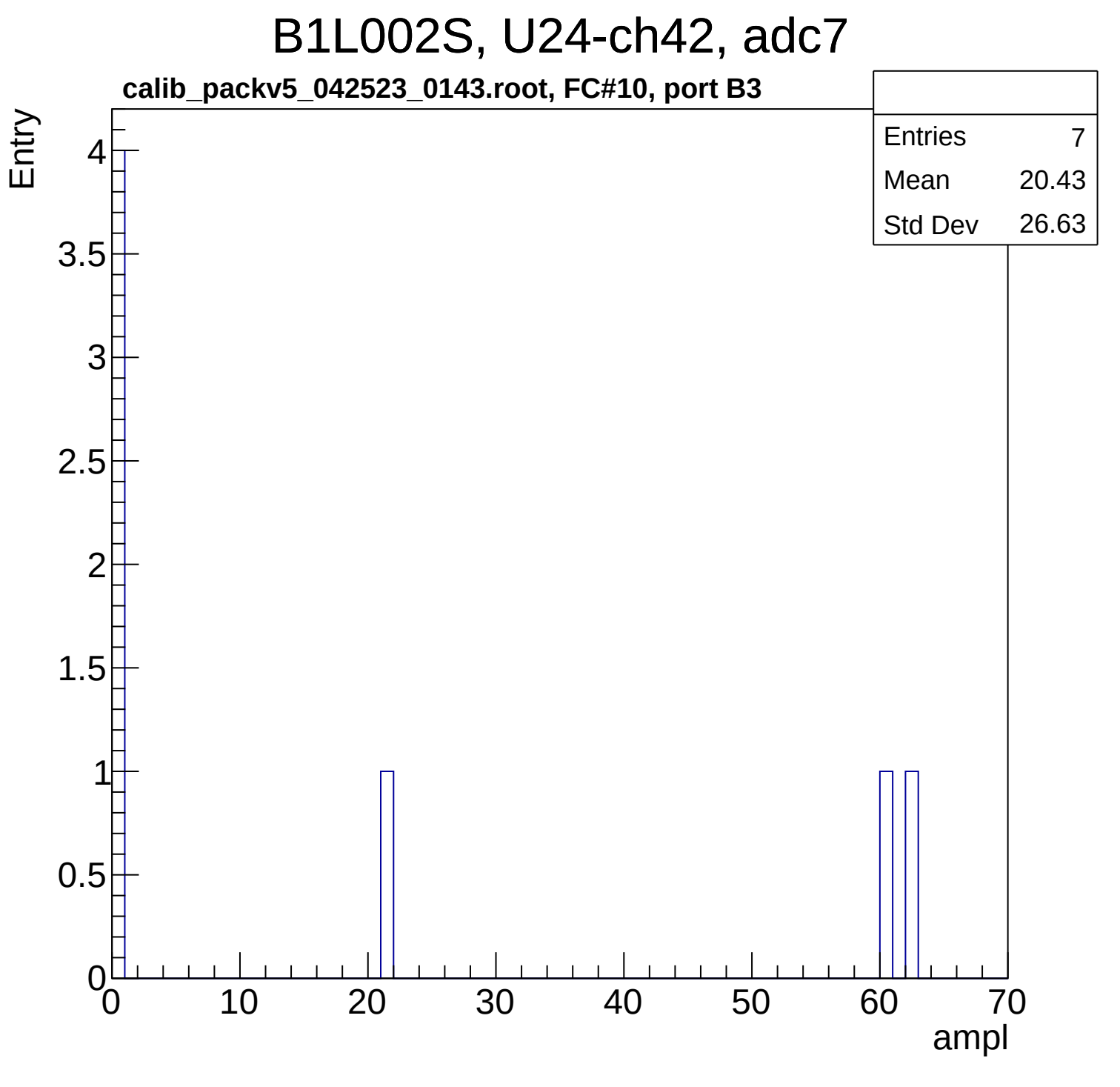
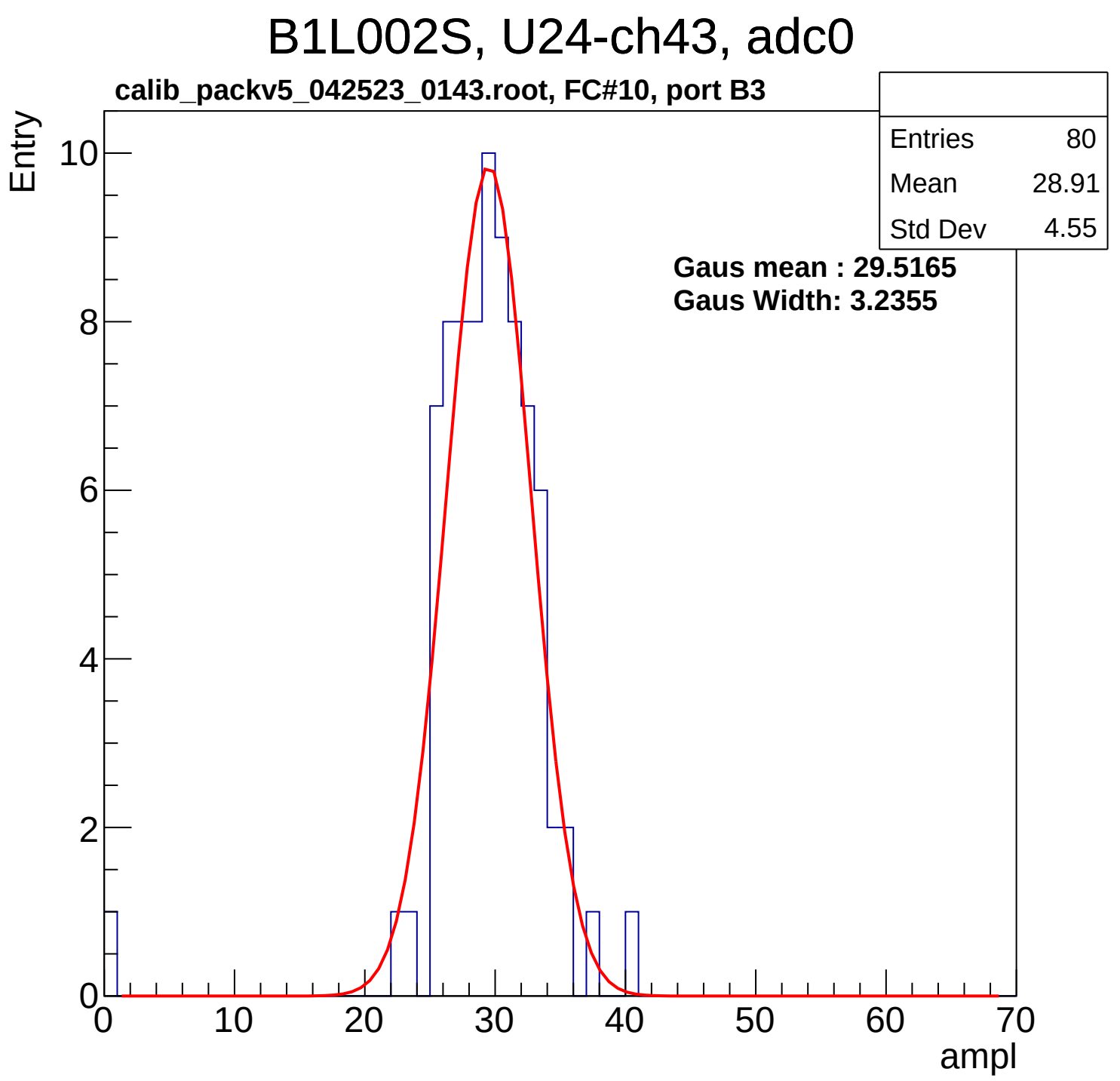

B1L002S, U24-ch42, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 20.43
Std Dev 26.63
Entry
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch43, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 28.91
Std Dev 4.55
Gaus mean : 29.5165
Gaus Width: 3.2355
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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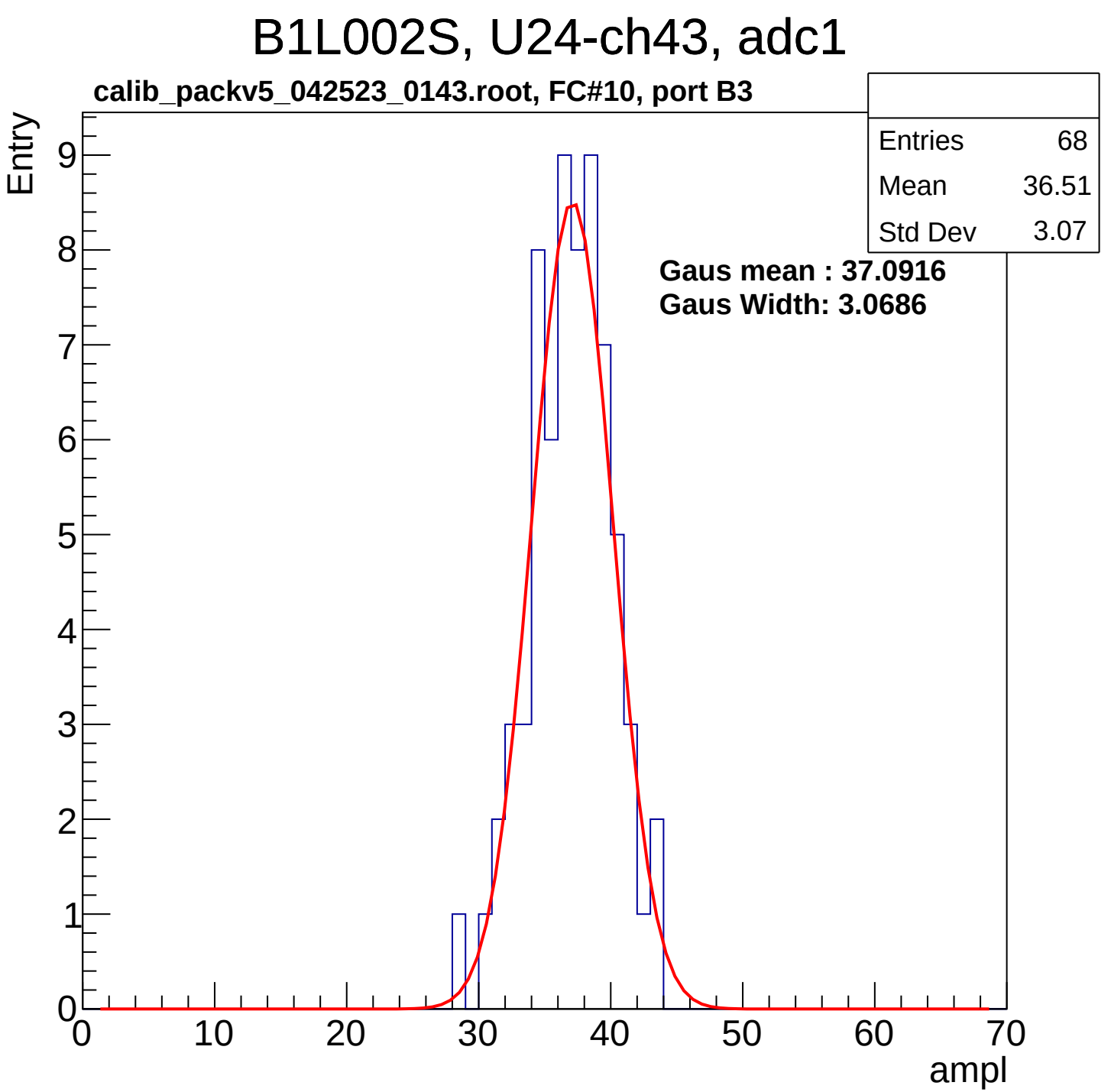
B1L002S, U24-ch43, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 36.51
Std Dev 3.07
Gaus mean : 37.0916
Gaus Width: 3.0686
Entry
ampl
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2
3
4
5
6
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8
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0
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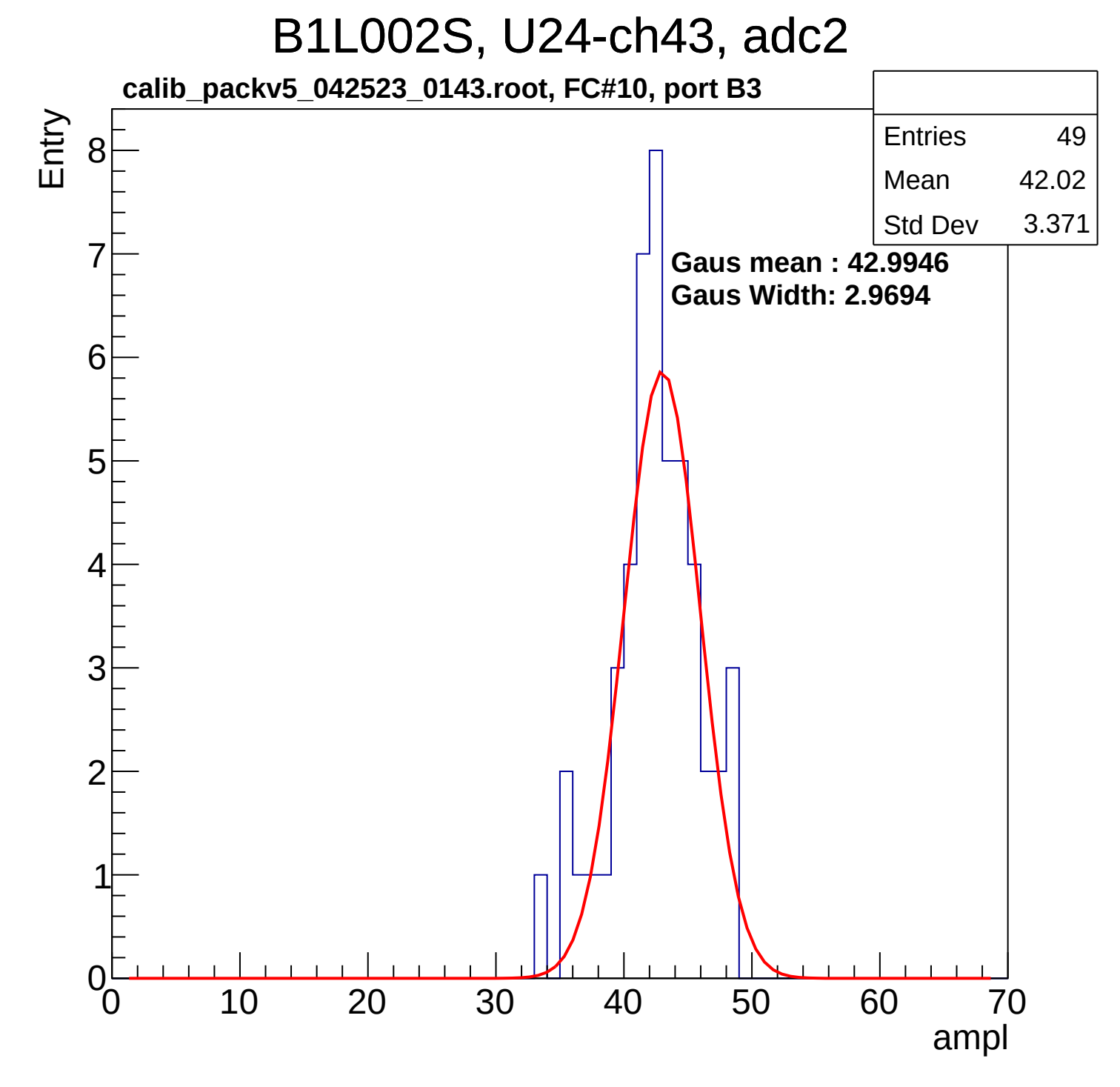
B1L002S, U24-ch43, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 42.02
Std Dev 3.371
Gaus mean : 42.9946
Gaus Width: 2.9694
Entry
ampl
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1
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4
5
6
7
8
0
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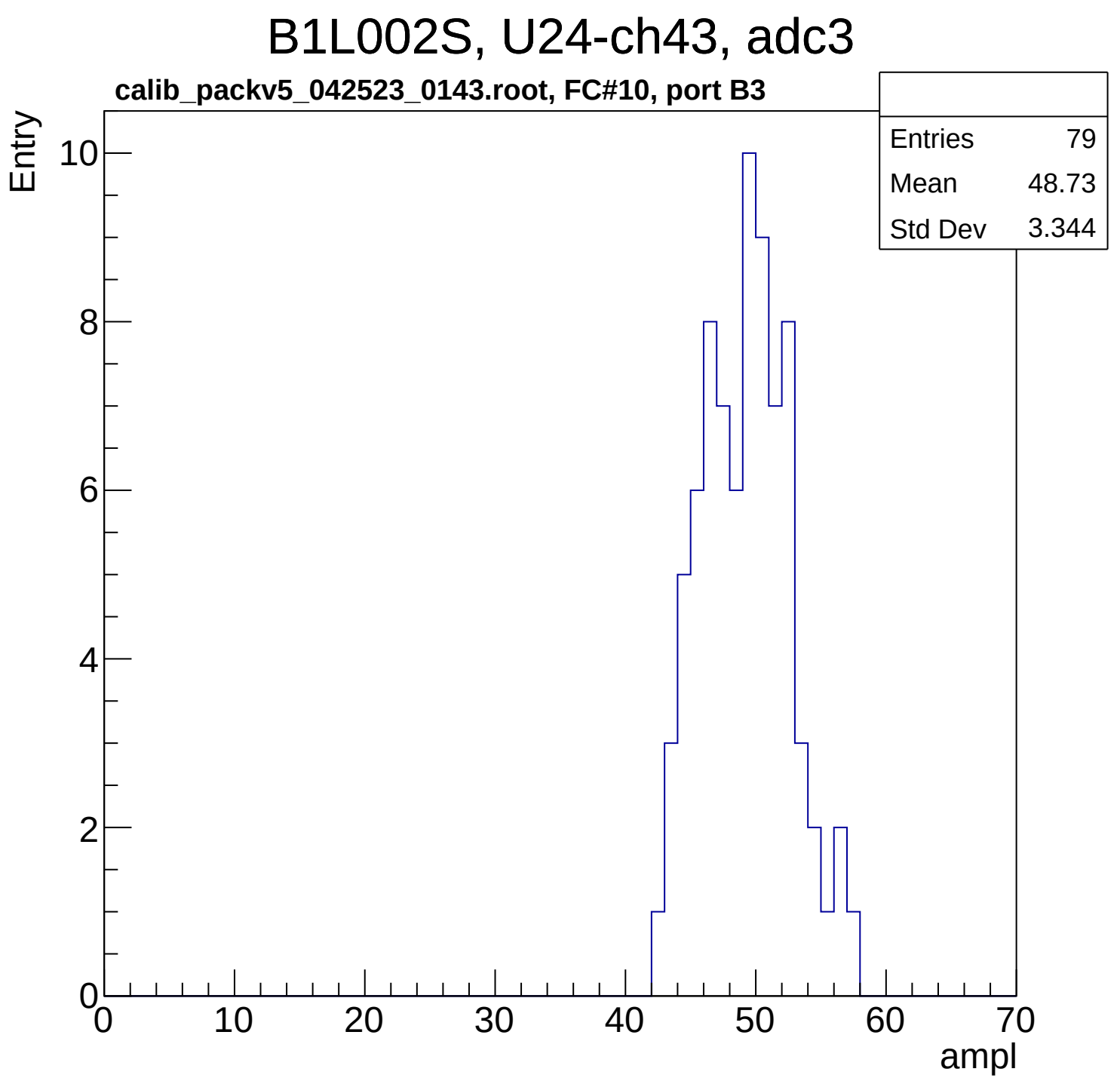
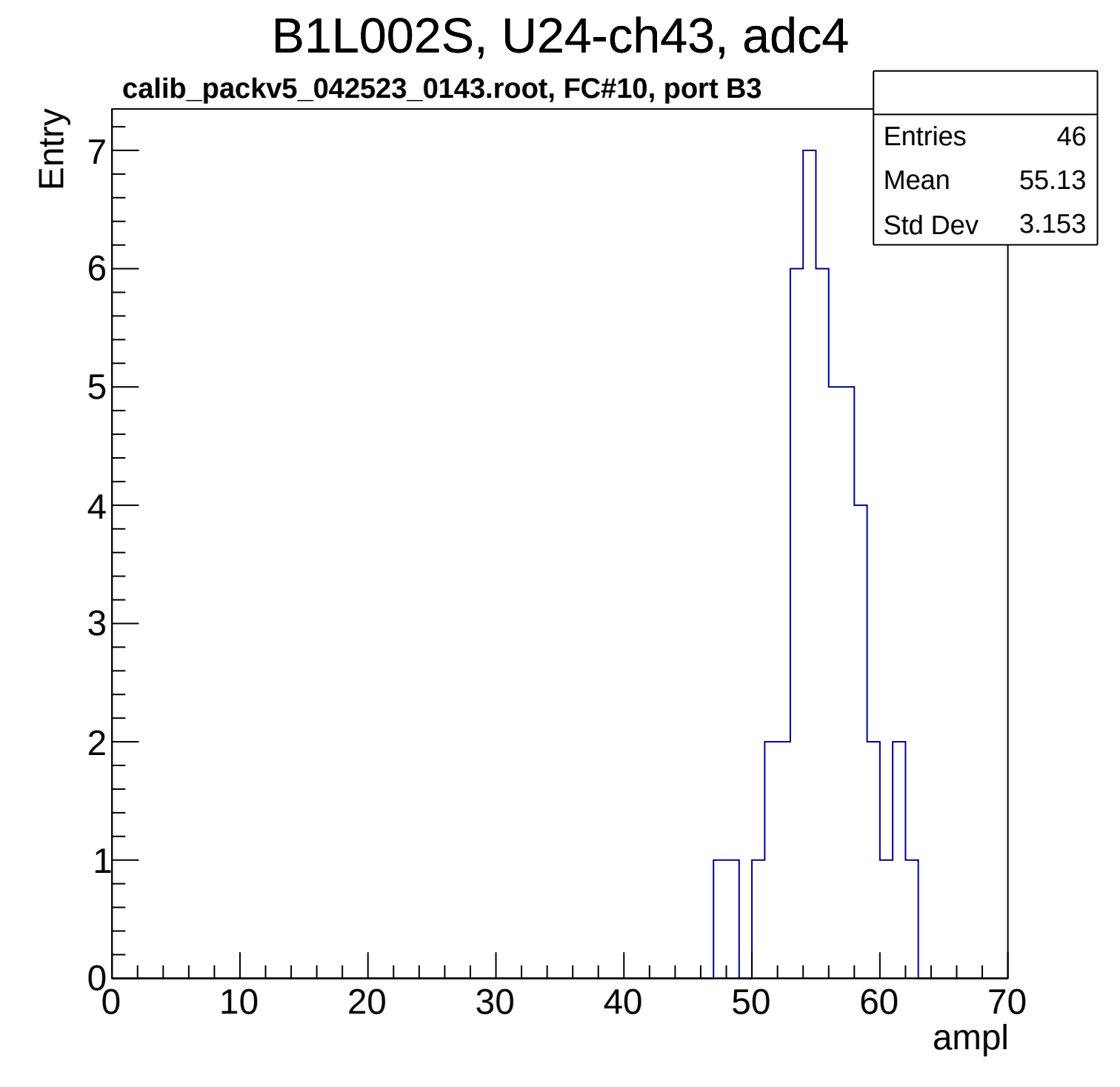

B1L002S, U24-ch43, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 48.73
Std Dev 3.344
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch43, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 55.13
Std Dev 3.153
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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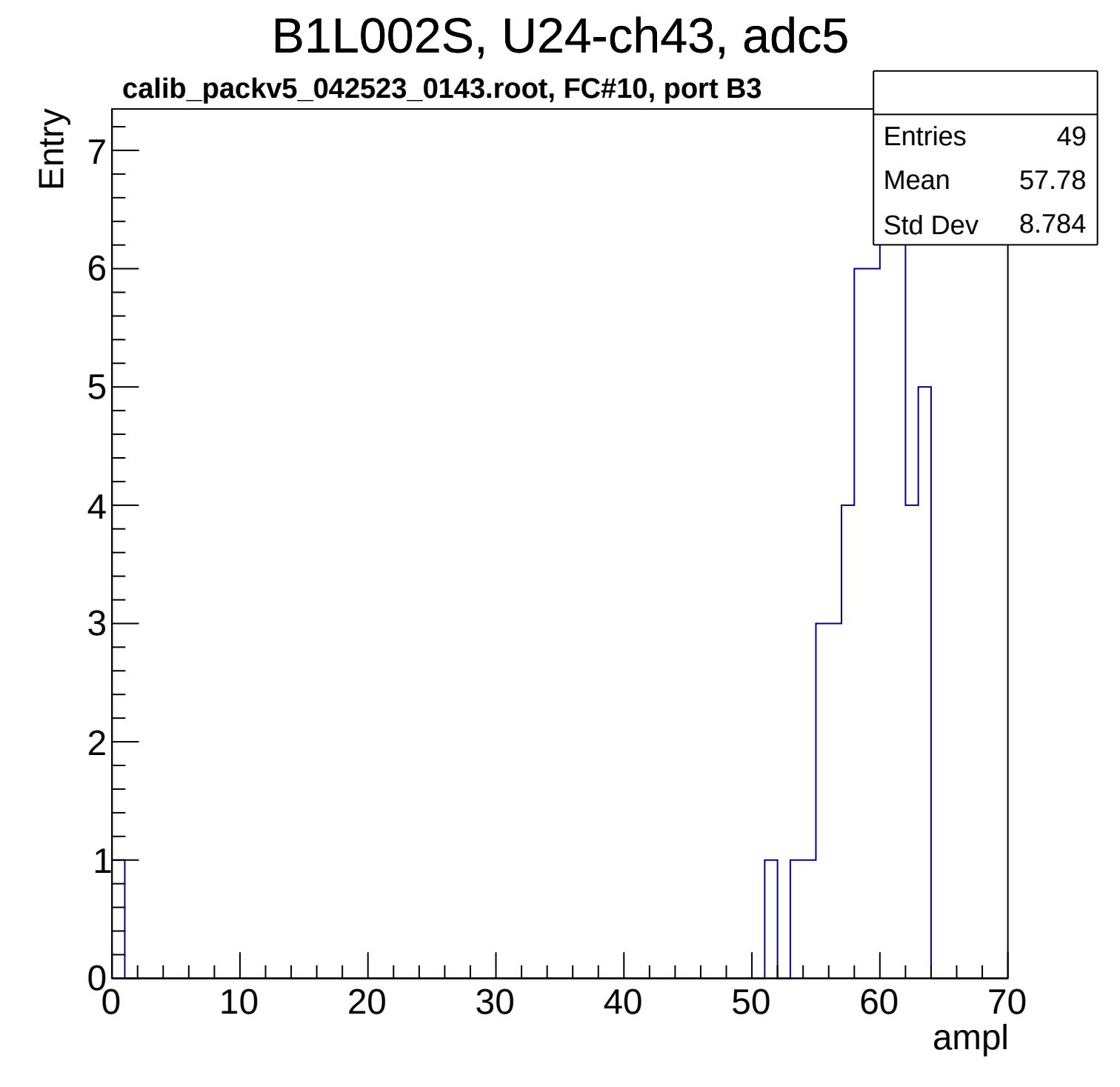
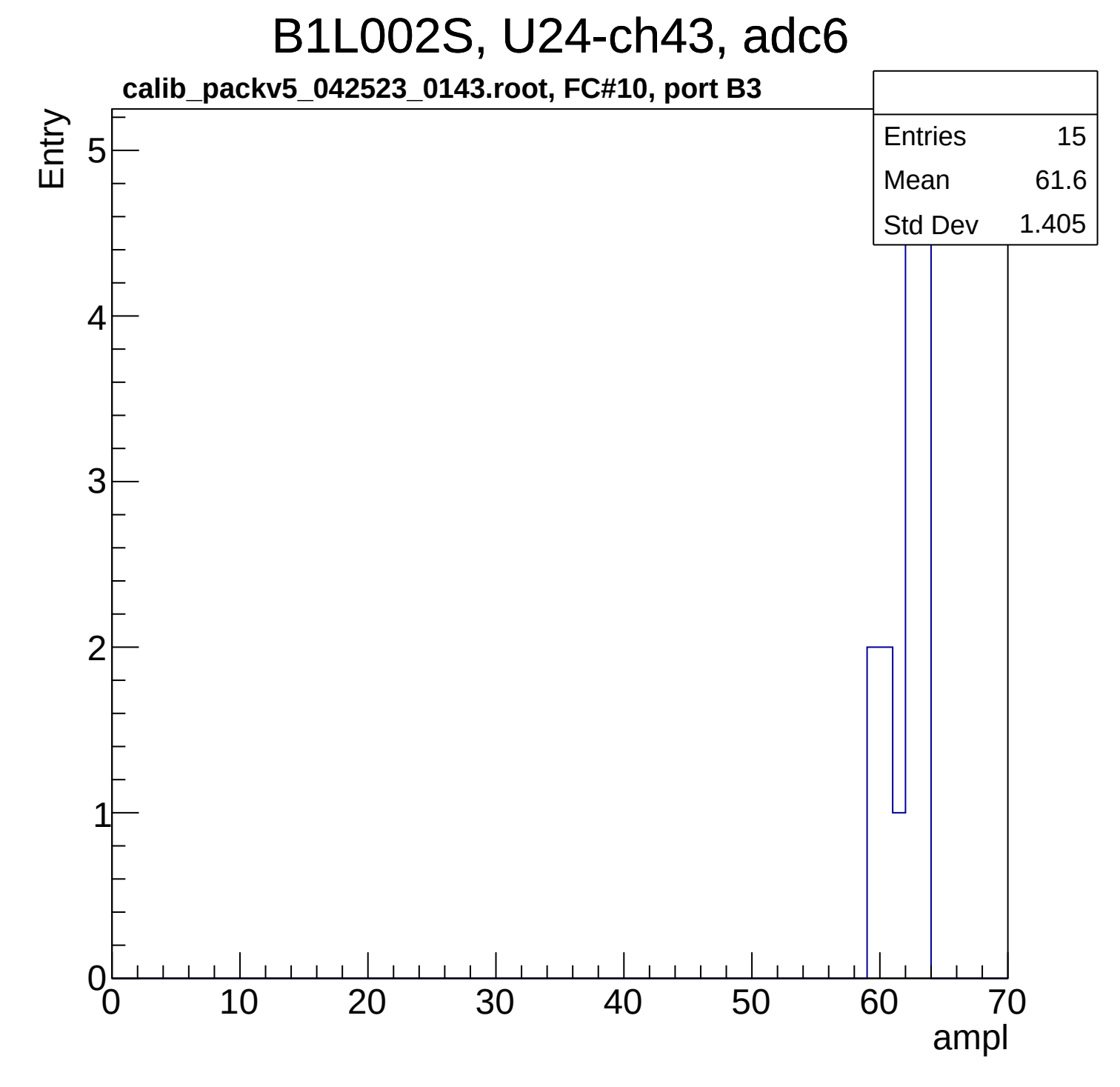
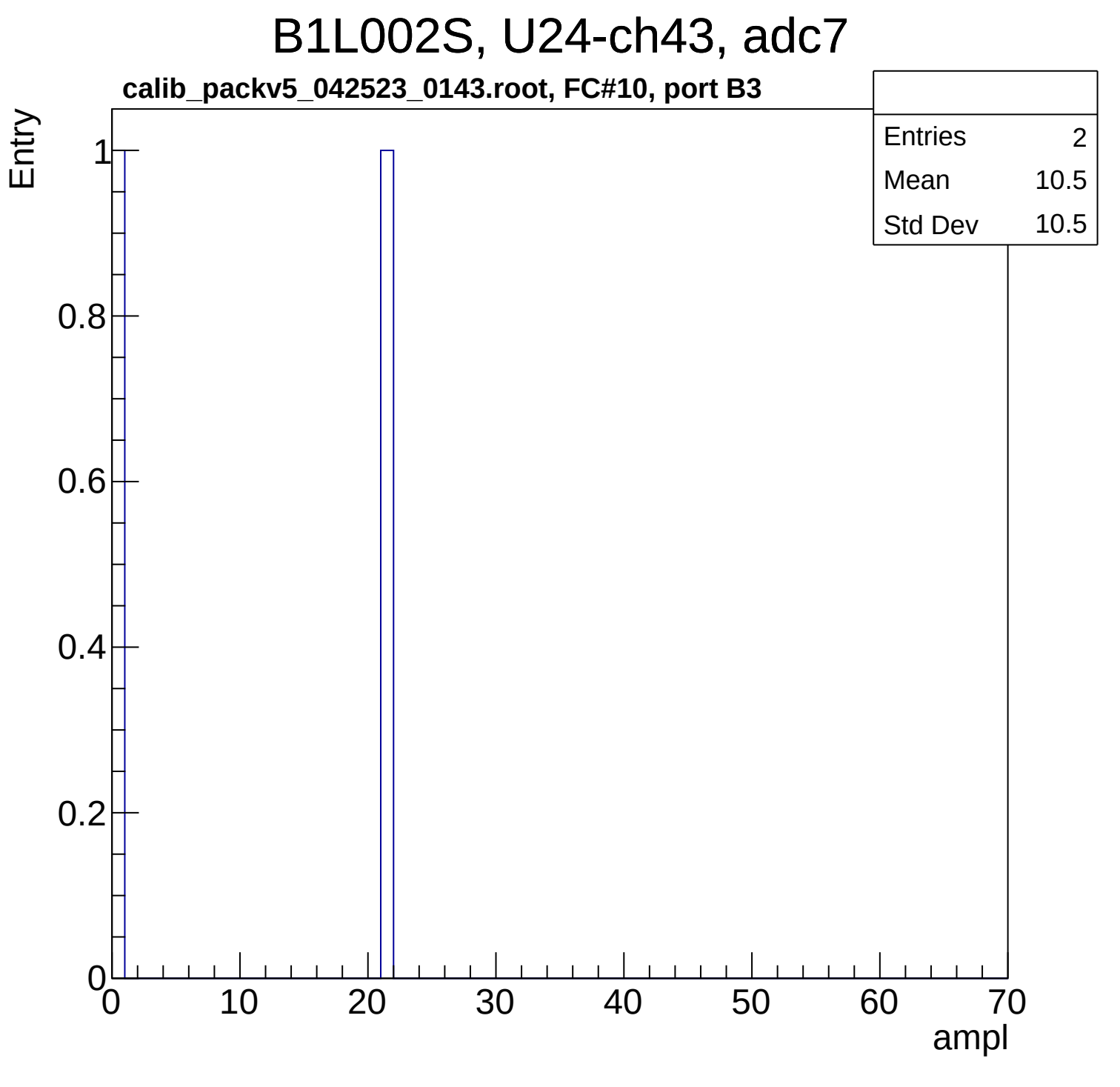
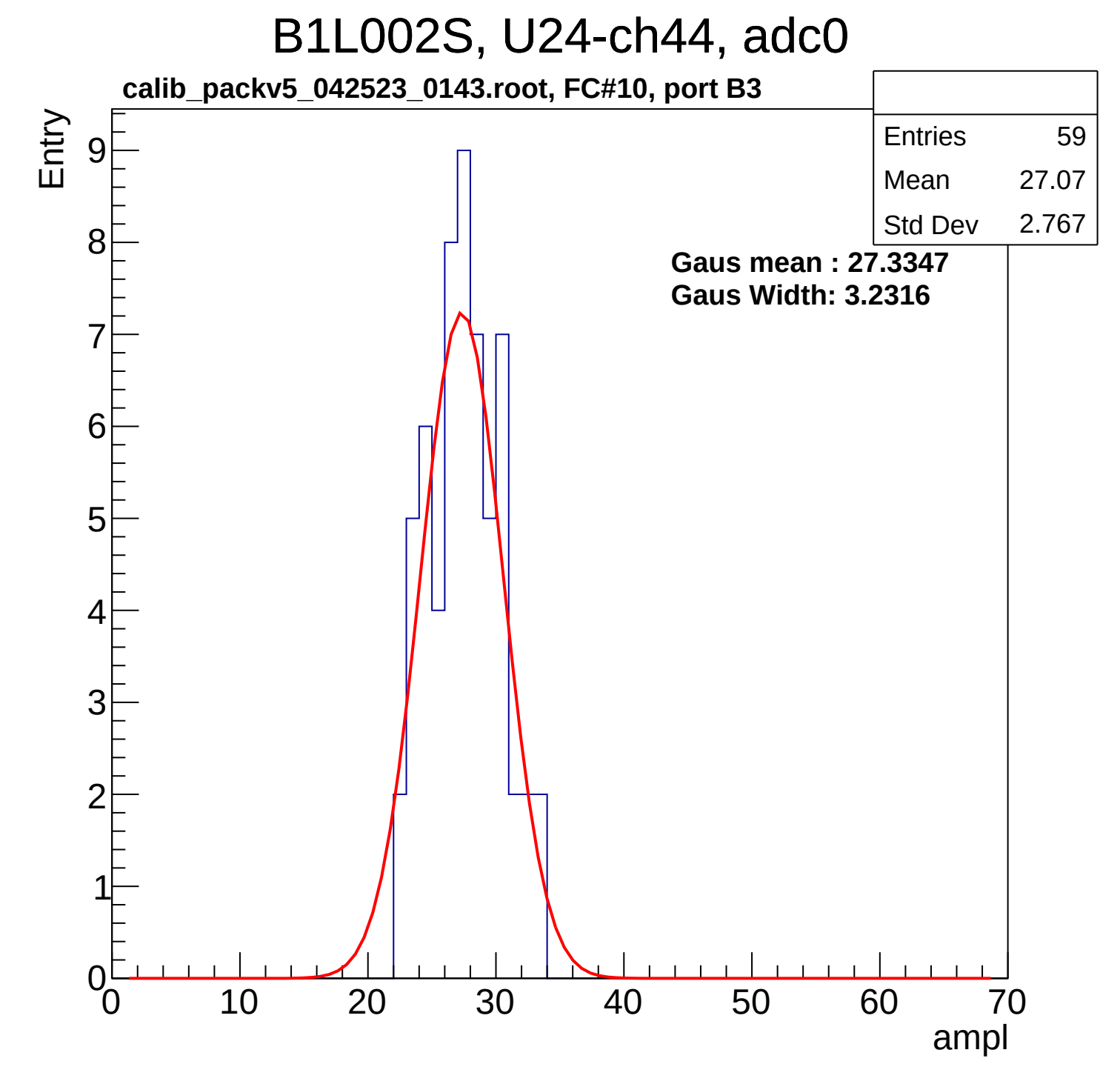
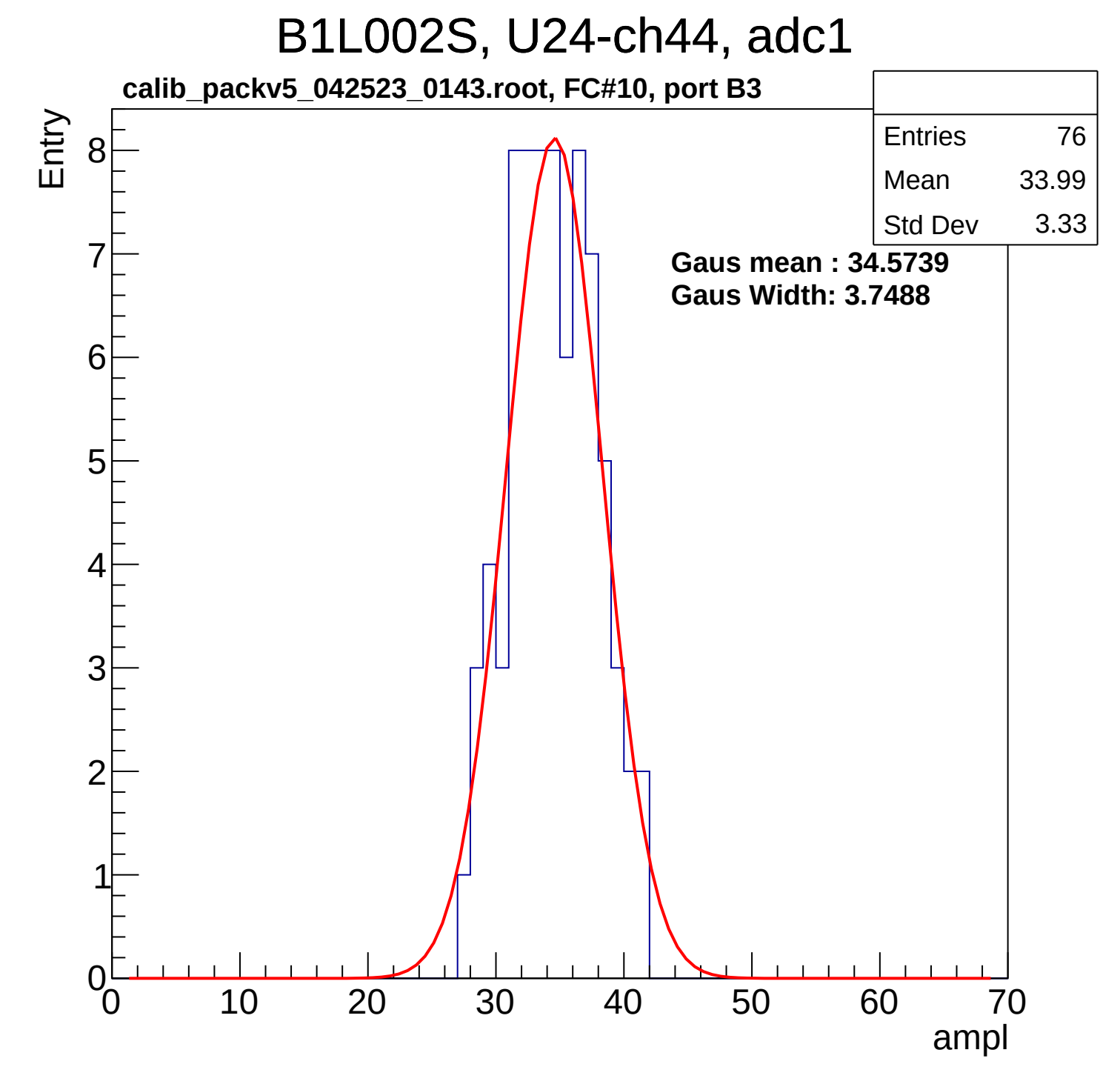
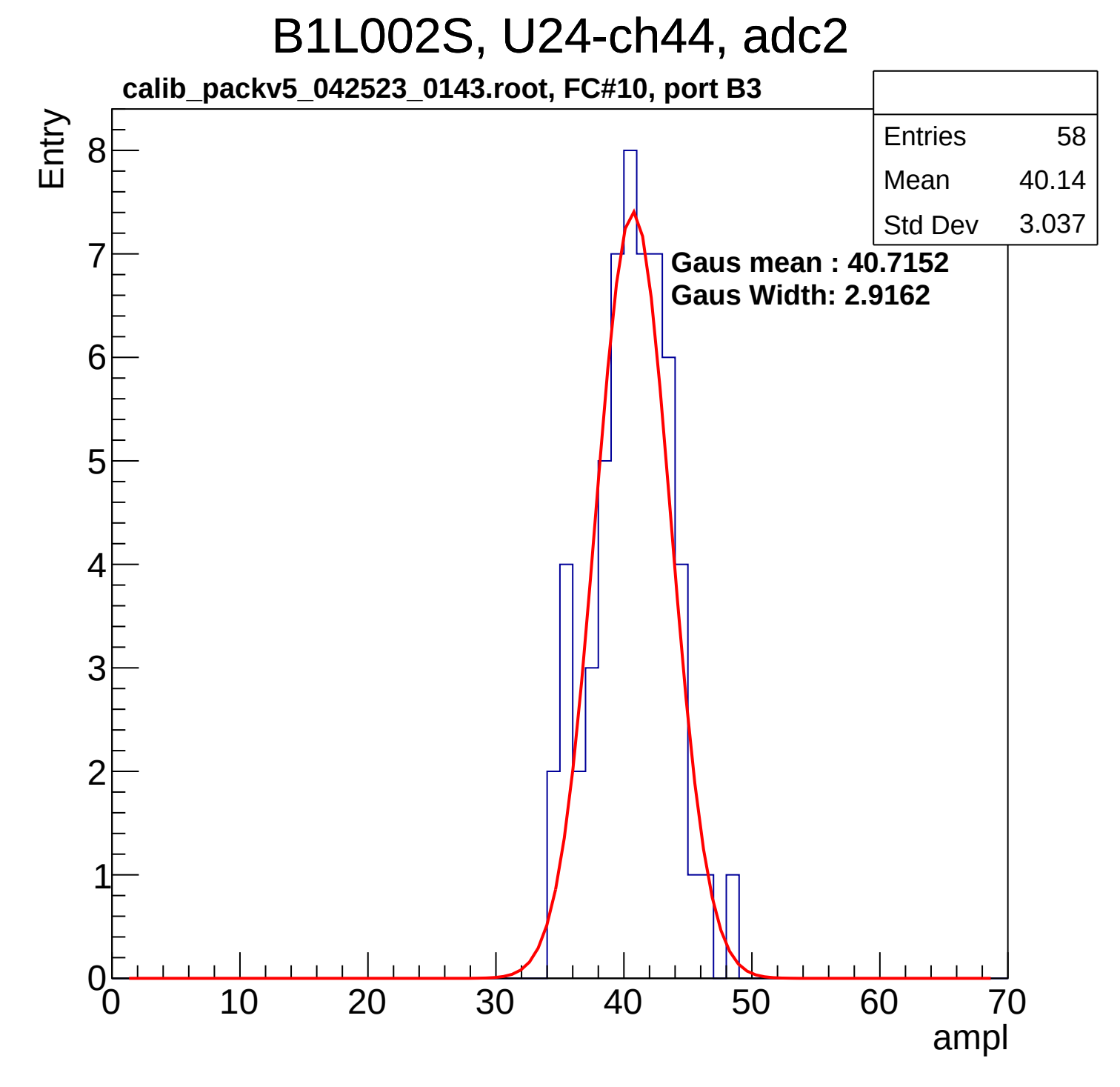

B1L002S, U24-ch43, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 57.78
Std Dev 8.784
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch43, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.6
Std Dev 1.405
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch43, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch44, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 27.07
Std Dev 2.767
Gaus mean : 27.3347
Gaus Width: 3.2316
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch44, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.99
Std Dev 3.33
Gaus mean : 34.5739
Gaus Width: 3.7488
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch44, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40.14
Std Dev 3.037
Gaus mean : 40.7152
Gaus Width: 2.9162
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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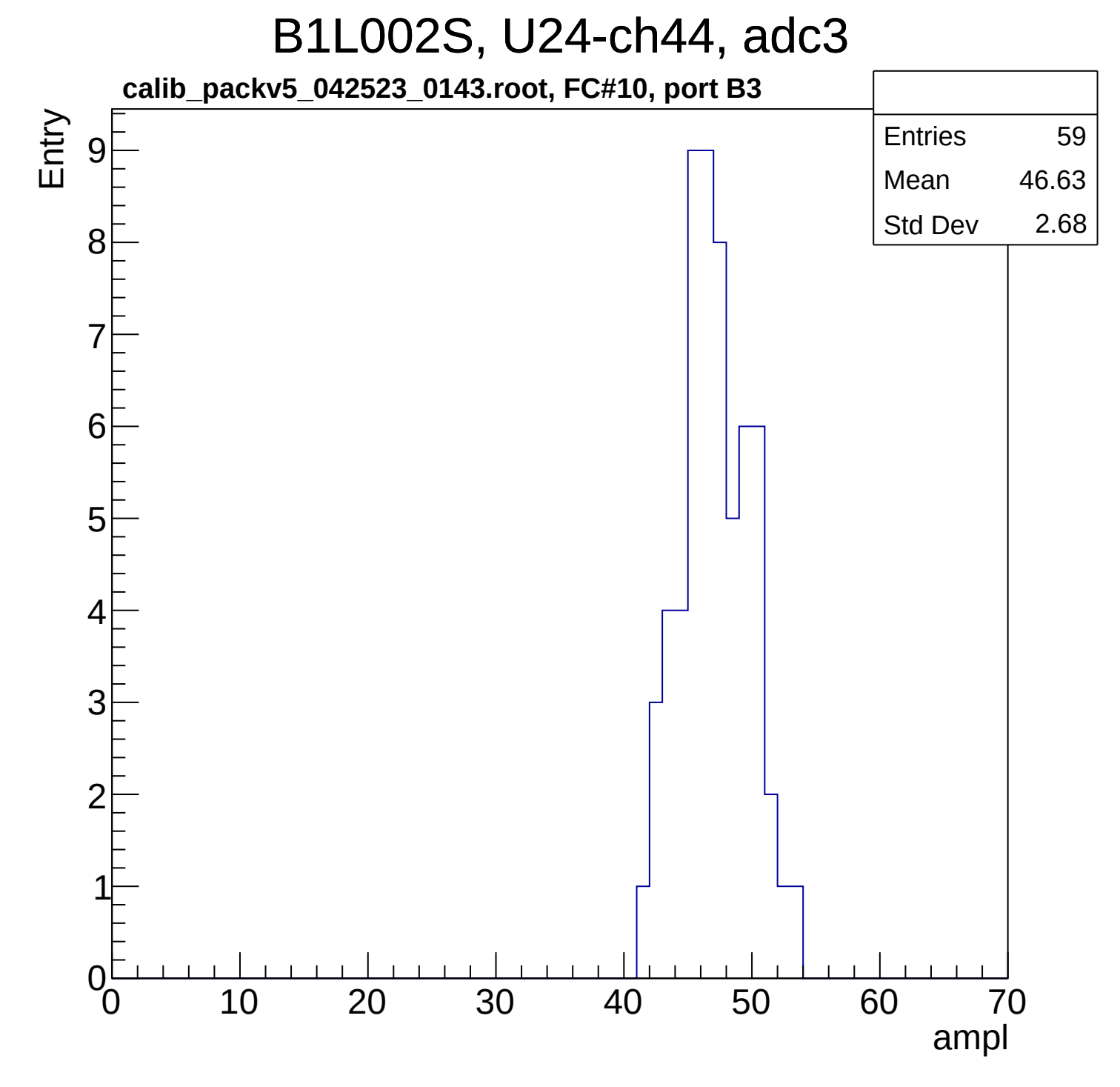
B1L002S, U24-ch44, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 46.63
Std Dev 2.68
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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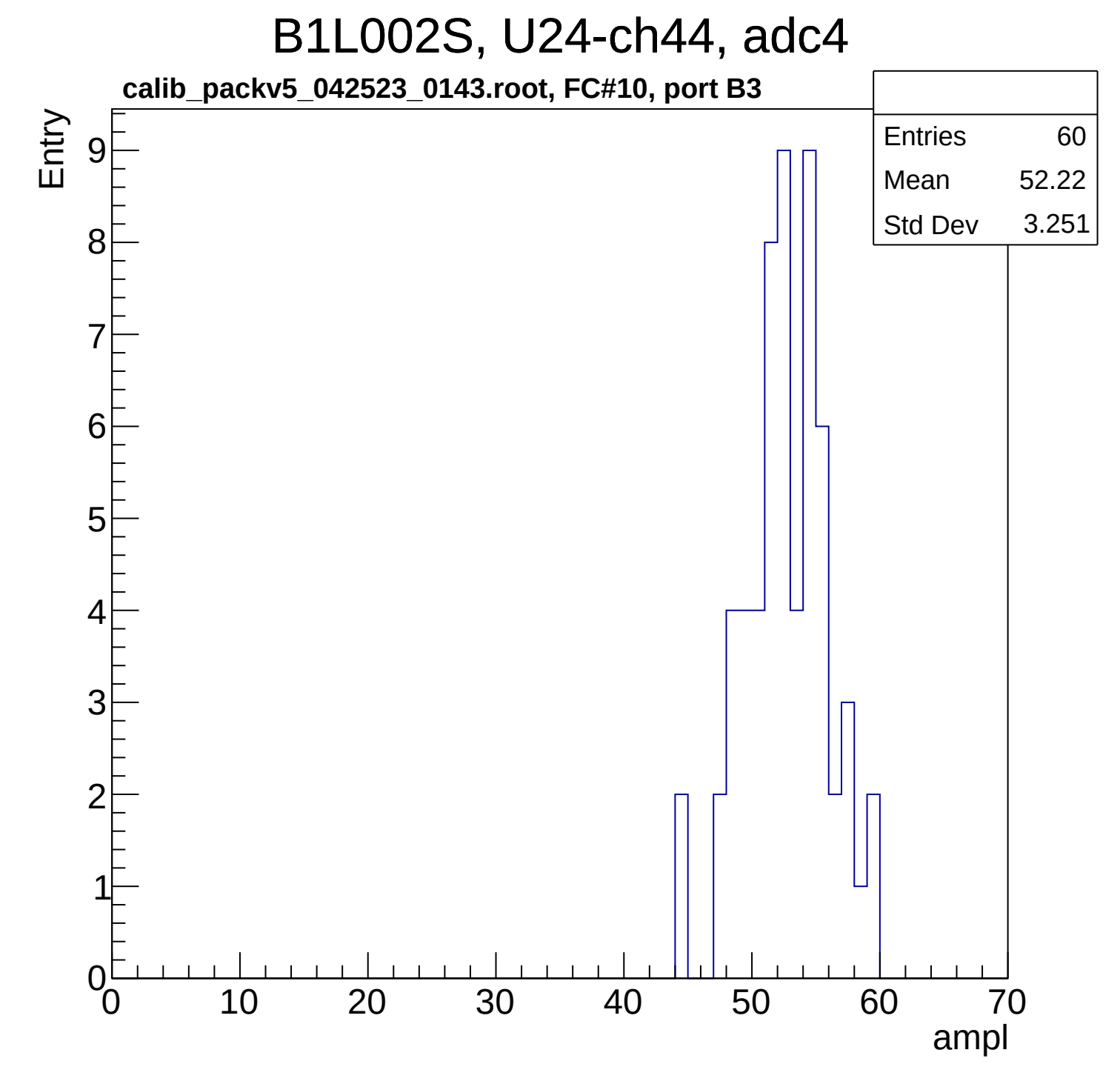
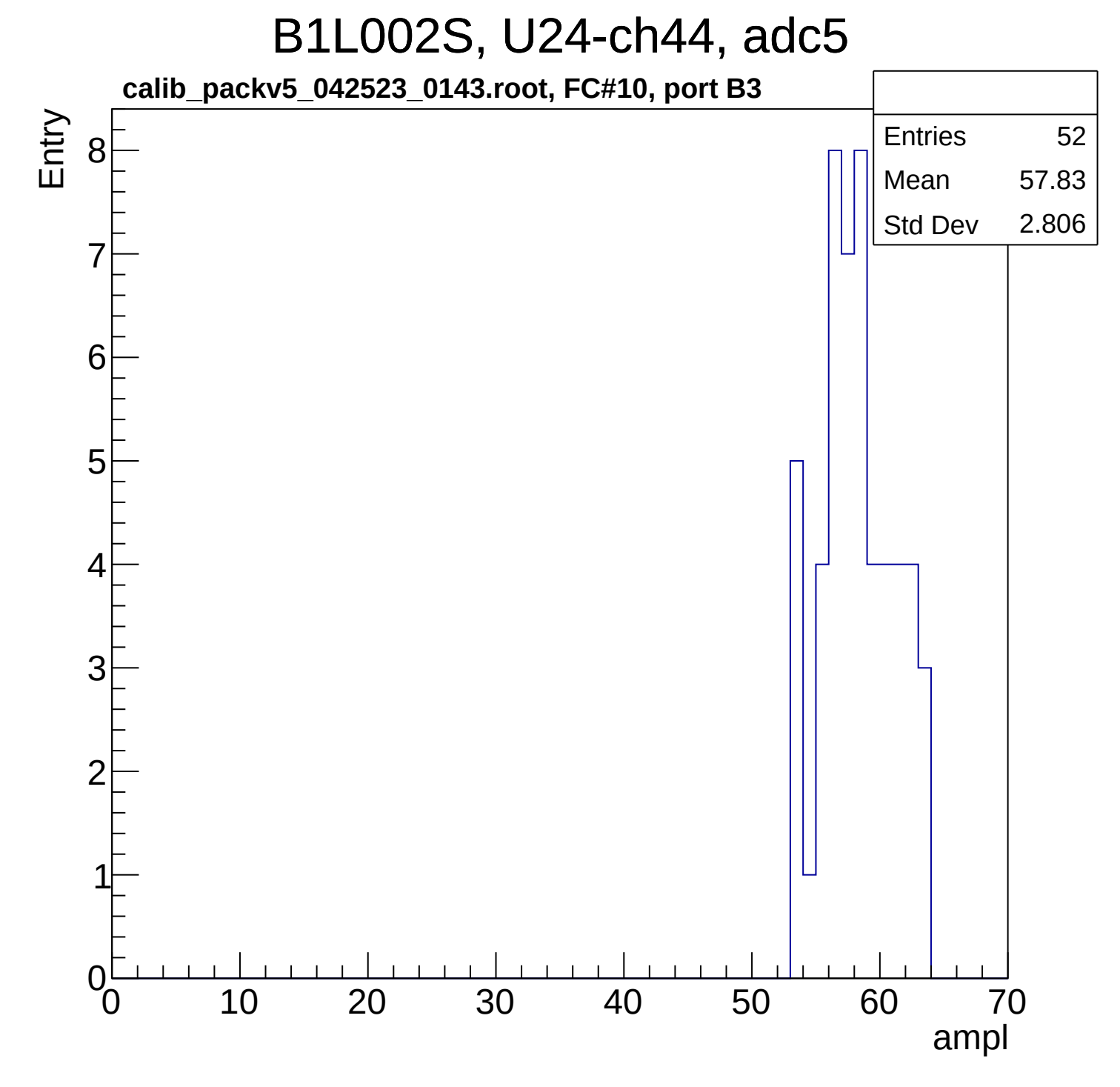

B1L002S, U24-ch44, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.22
Std Dev 3.251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch44, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.83
Std Dev 2.806
Entry
ampl
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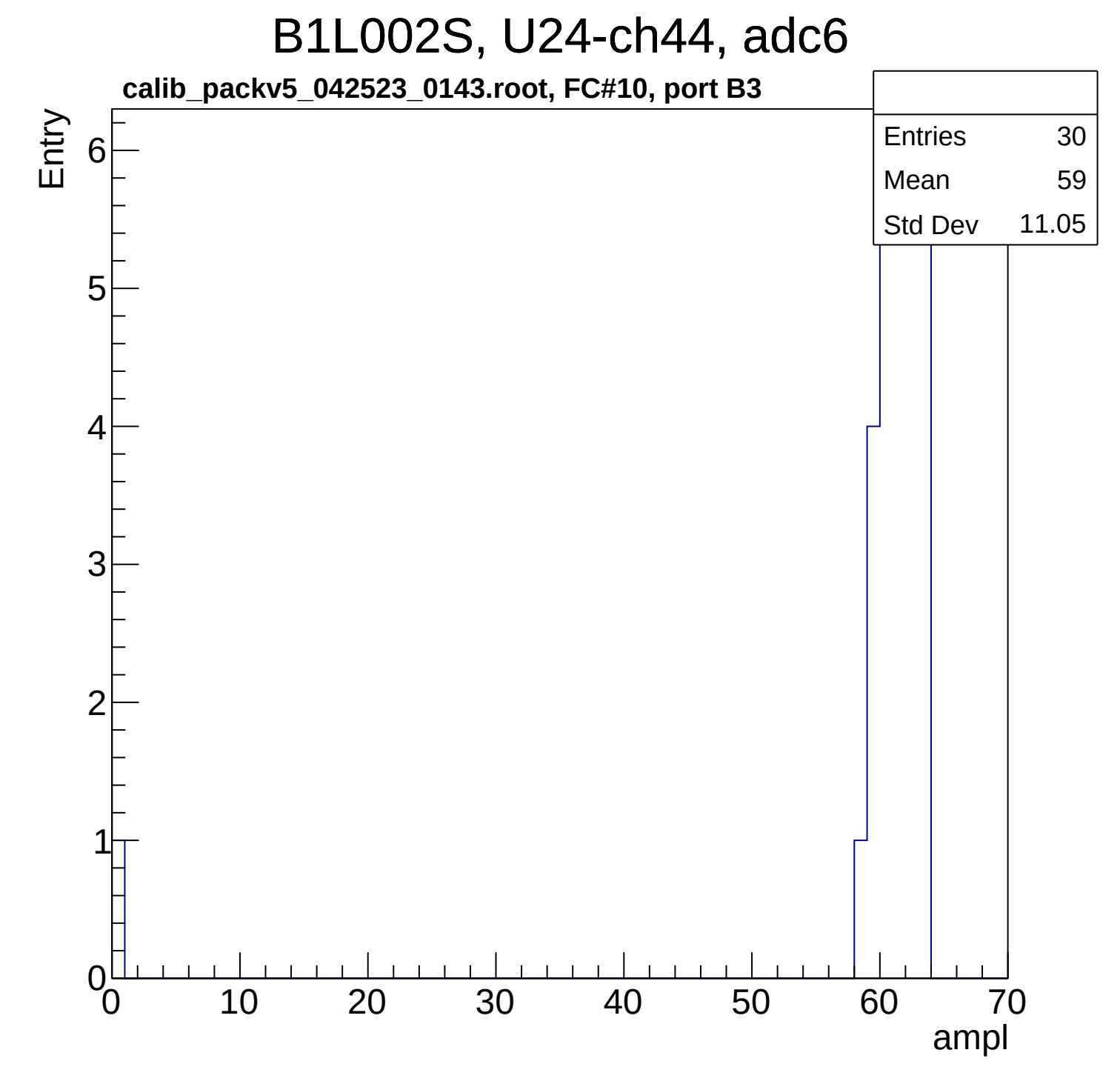
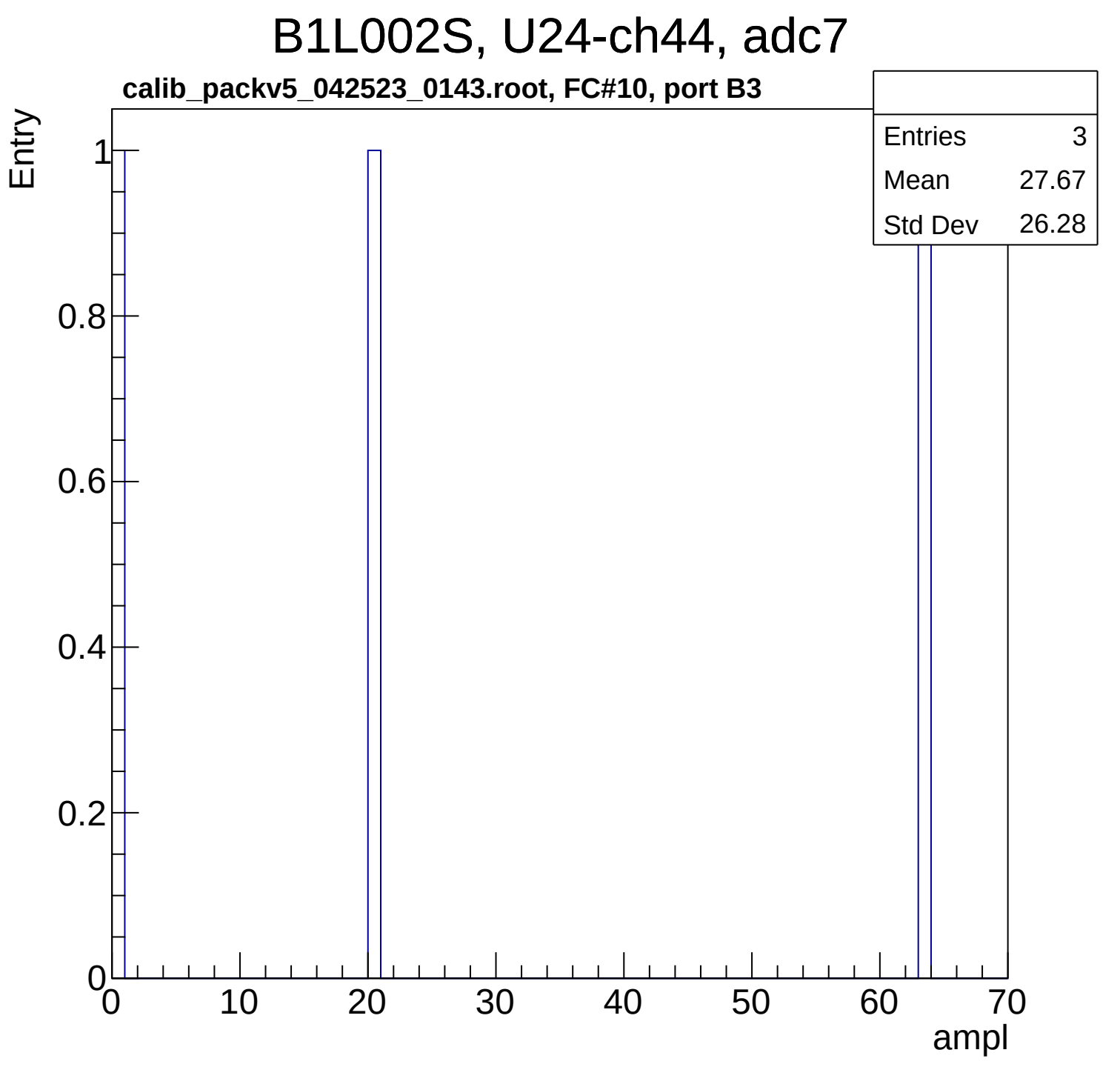
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch44, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 30
Mean 59
Std Dev 11.05
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch44, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.67
Std Dev 26.28
Entry
1
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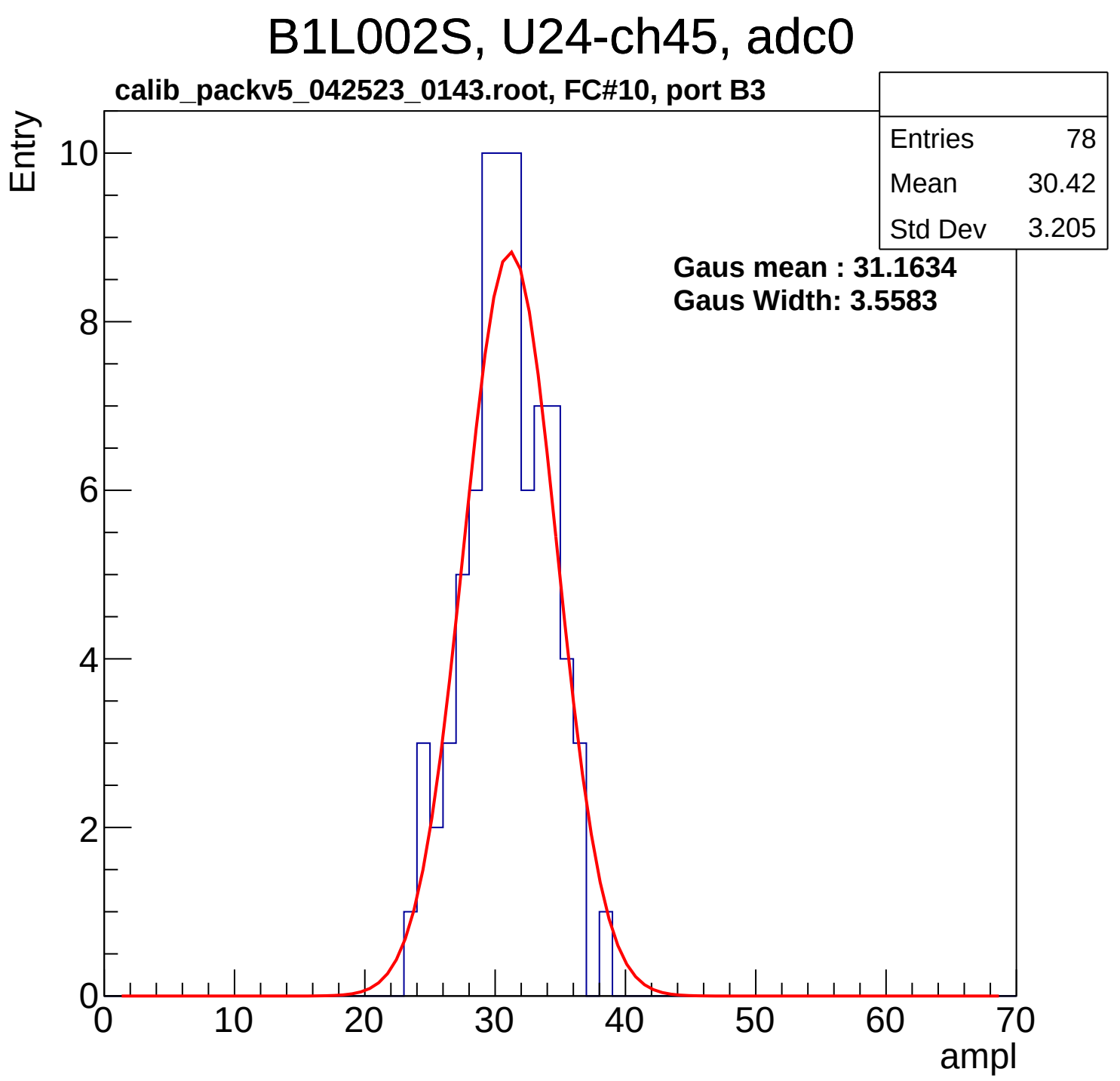
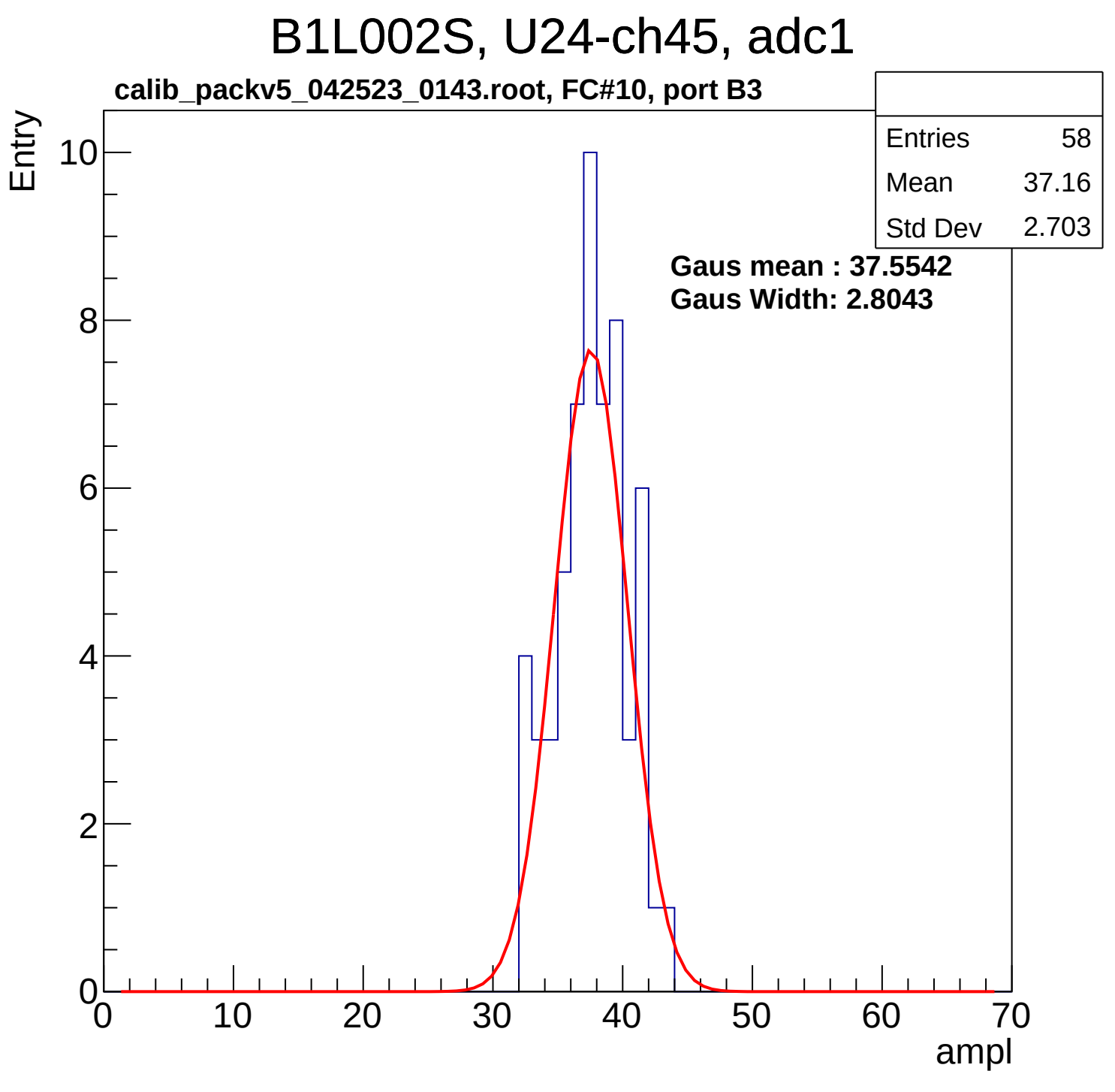
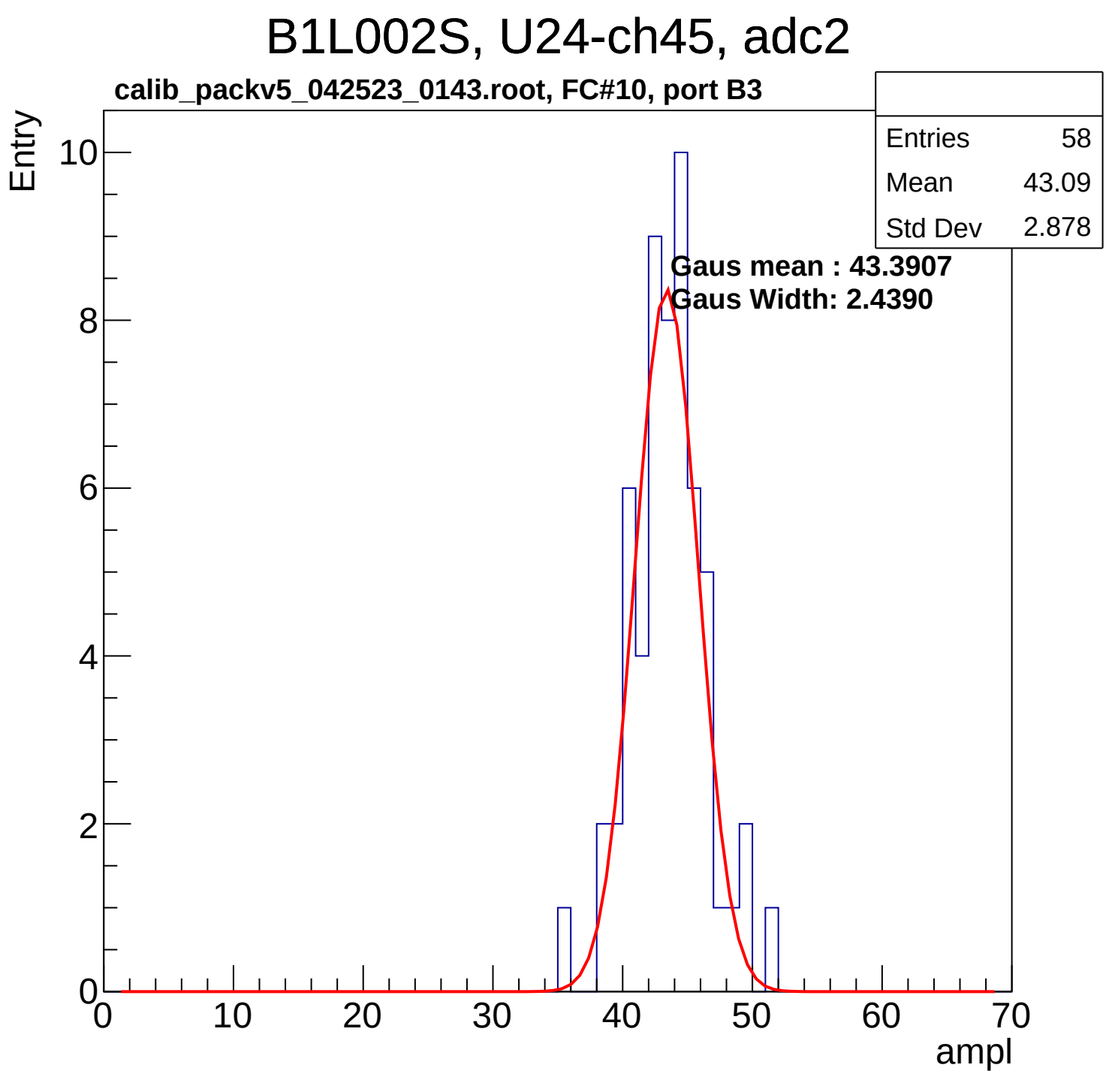
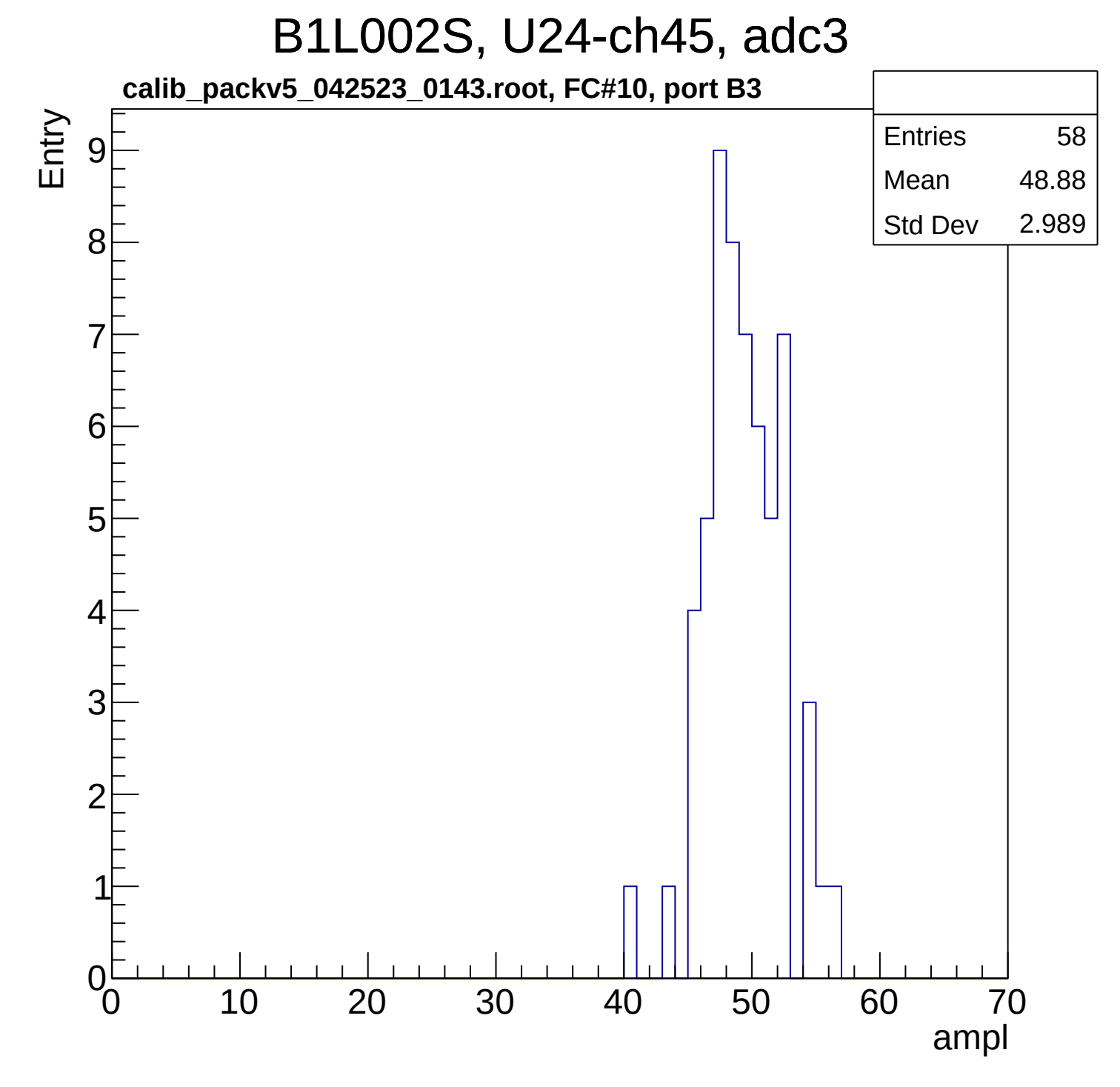
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch45, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 30.42
Std Dev 3.205
Gaus mean : 31.1634
Gaus Width: 3.5583
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch45, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 37.16
Std Dev 2.703
Gaus mean : 37.5542
Gaus Width: 2.8043
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch45, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 43.09
Std Dev 2.878
Gaus mean : 43.3907
Gaus Width: 2.4390
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch45, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 48.88
Std Dev 2.989
Entry
ampl
0
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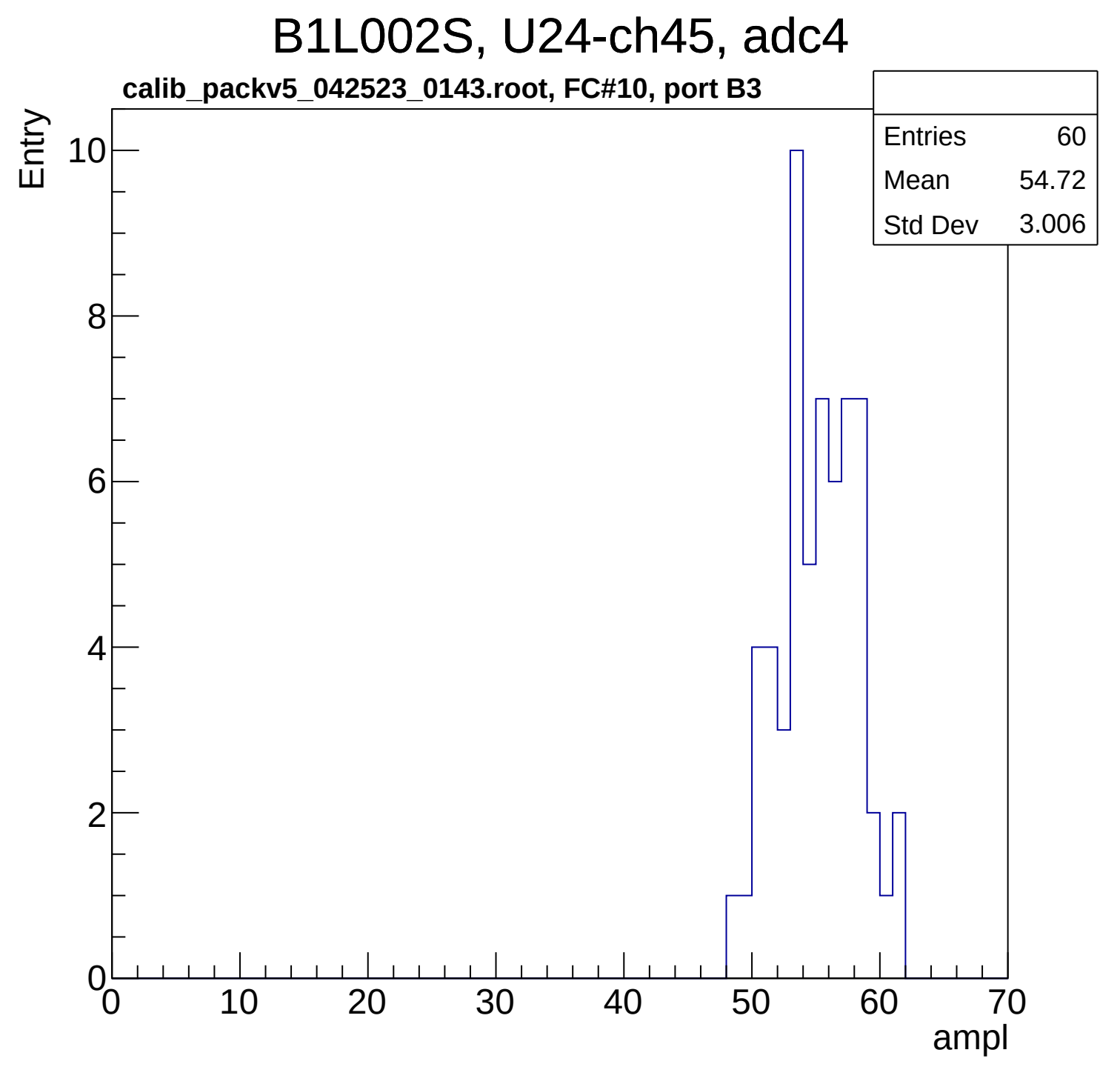
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch45, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 54.72
Std Dev 3.006
Entry
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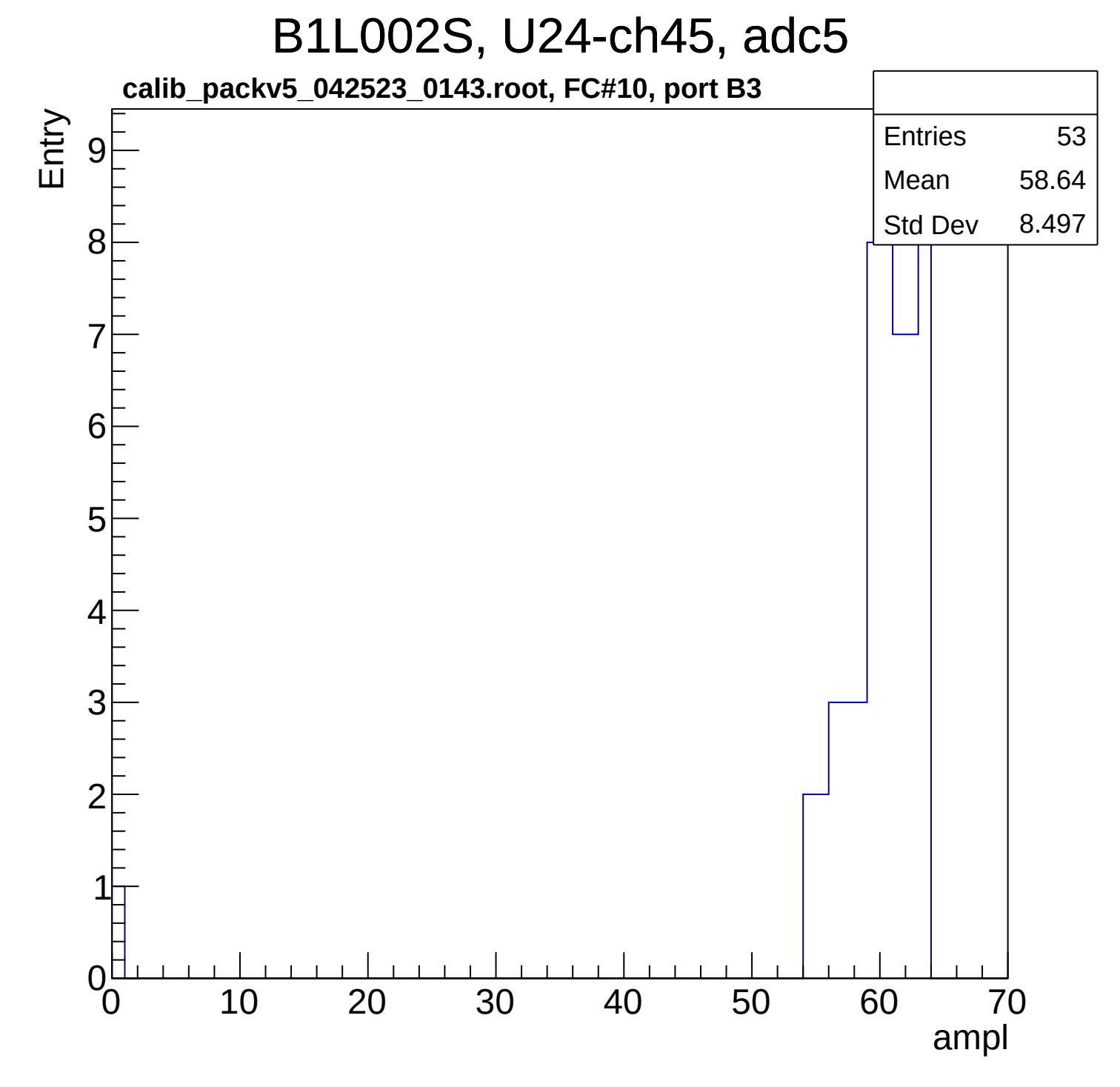
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch45, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.64
Std Dev 8.497
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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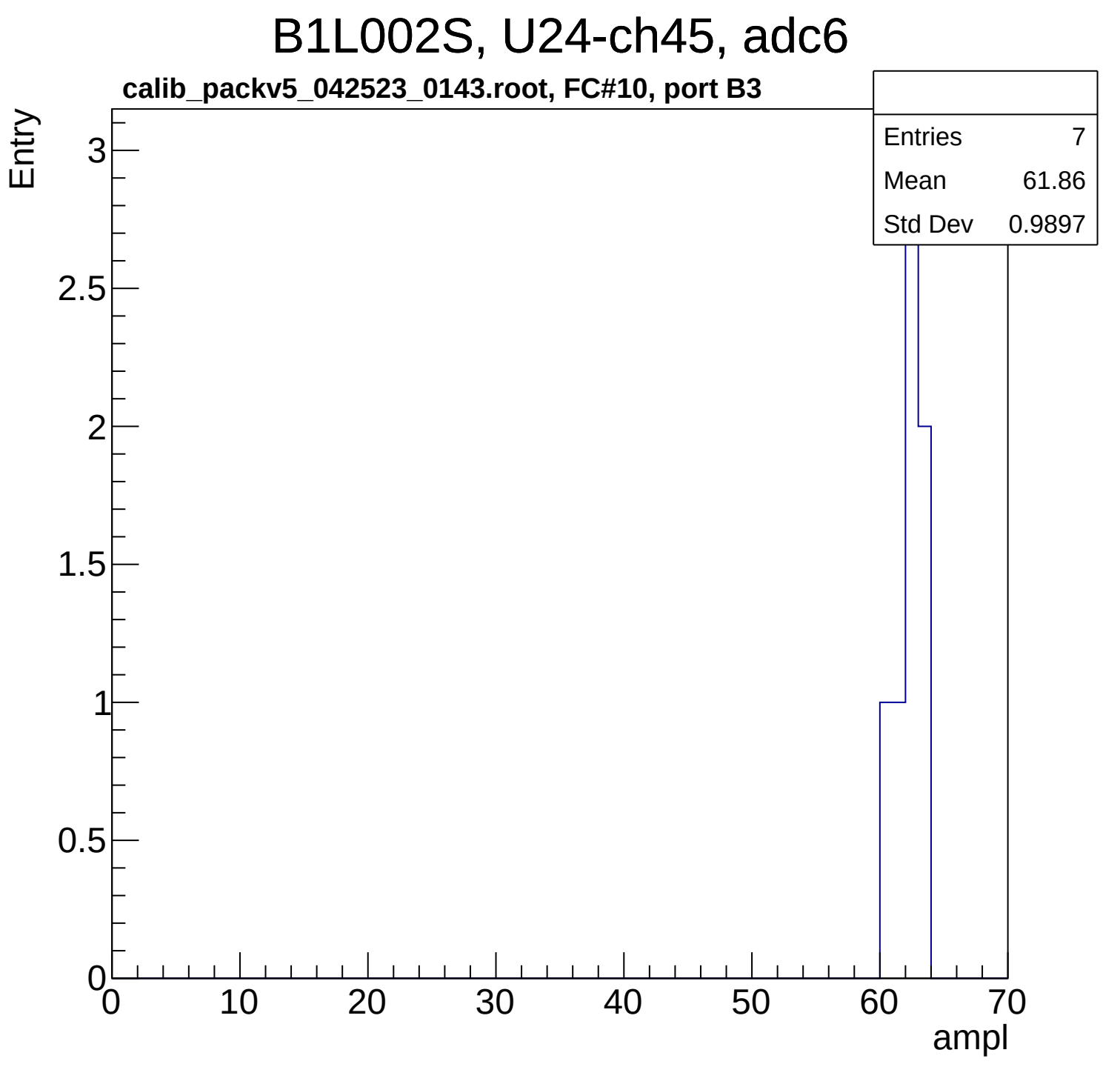
40
50
60
70


B1L002S, U24-ch45, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 61.86
Std Dev 0.9897
Entry
ampl
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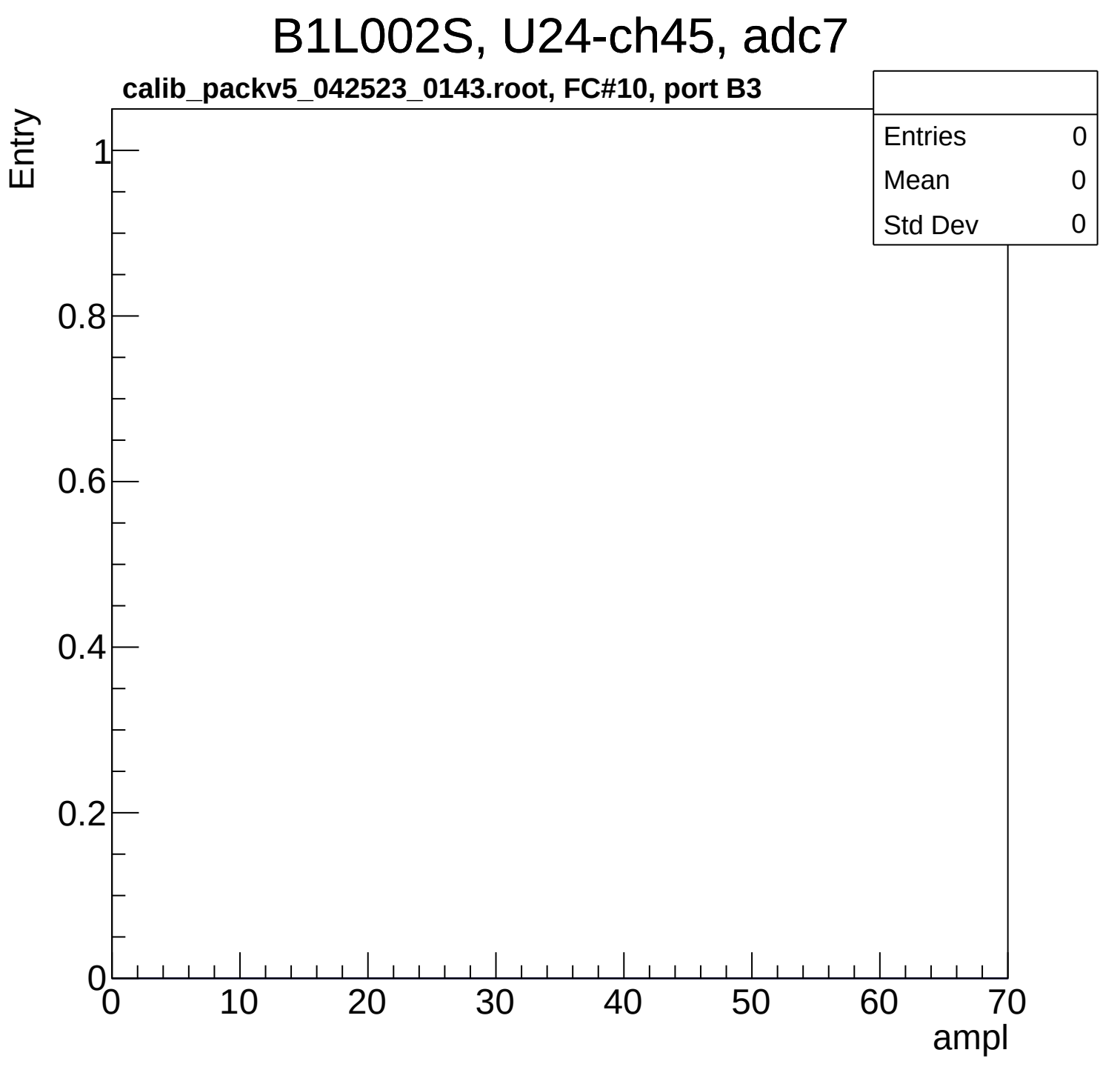
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch45, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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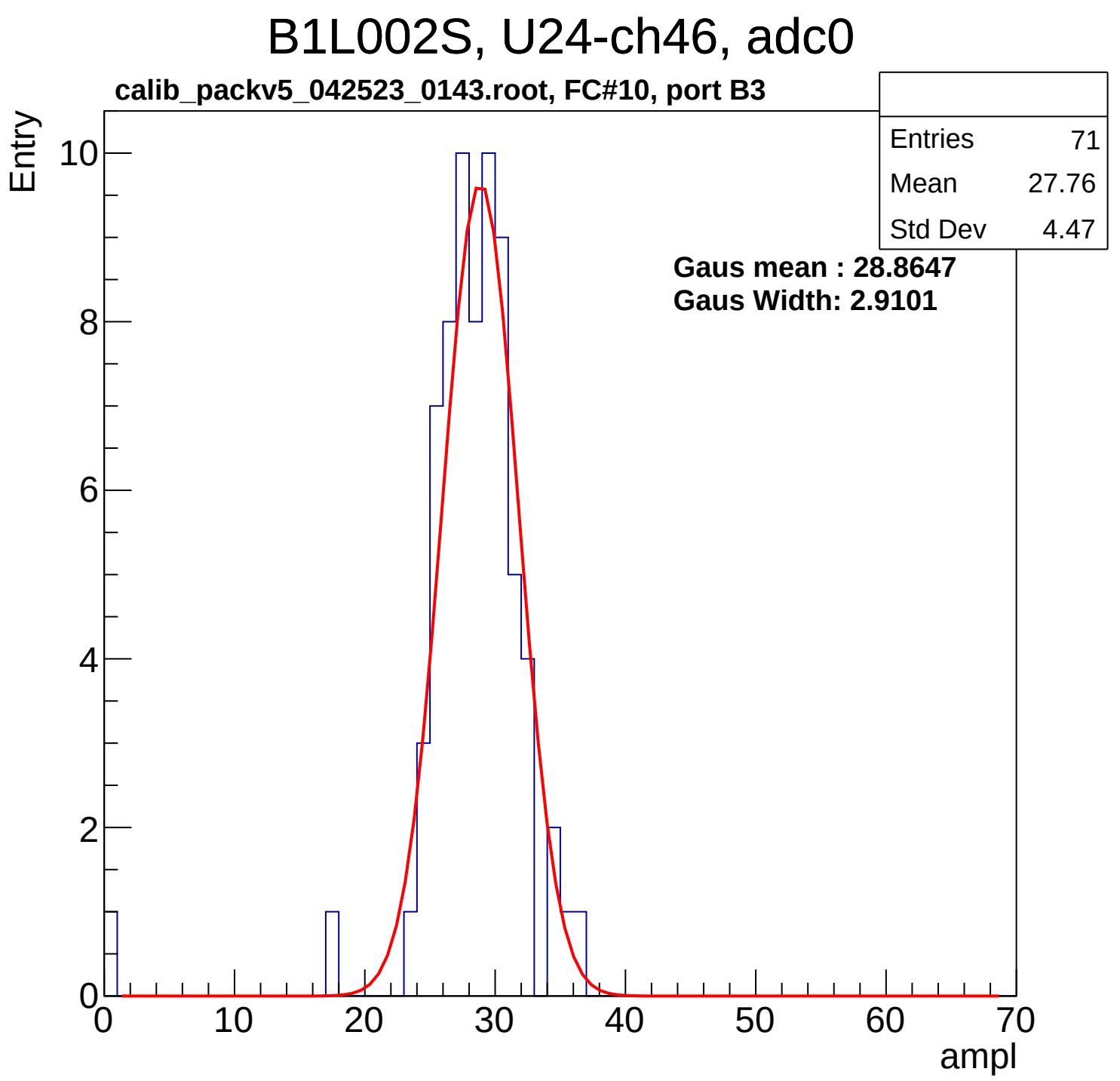
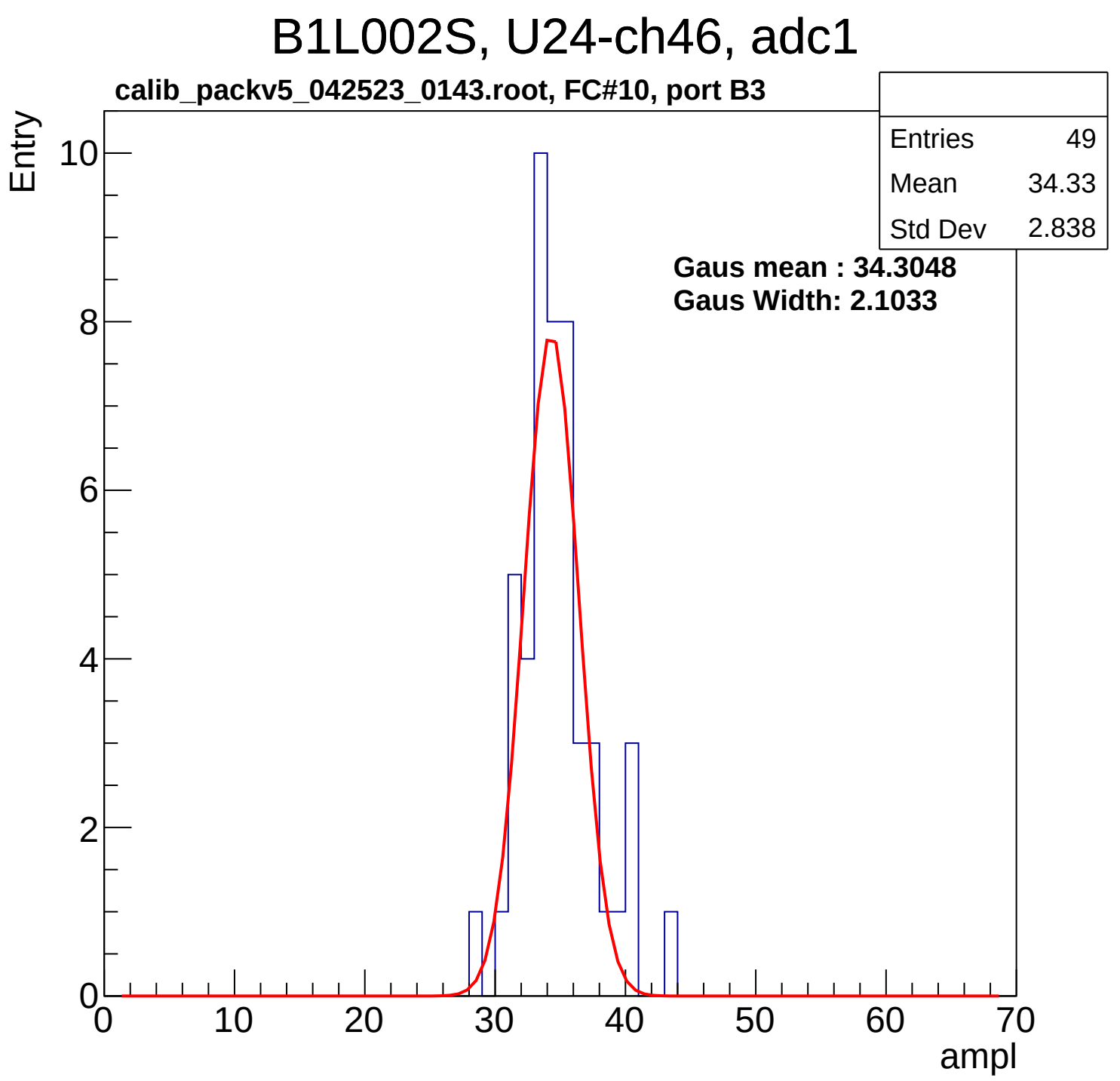
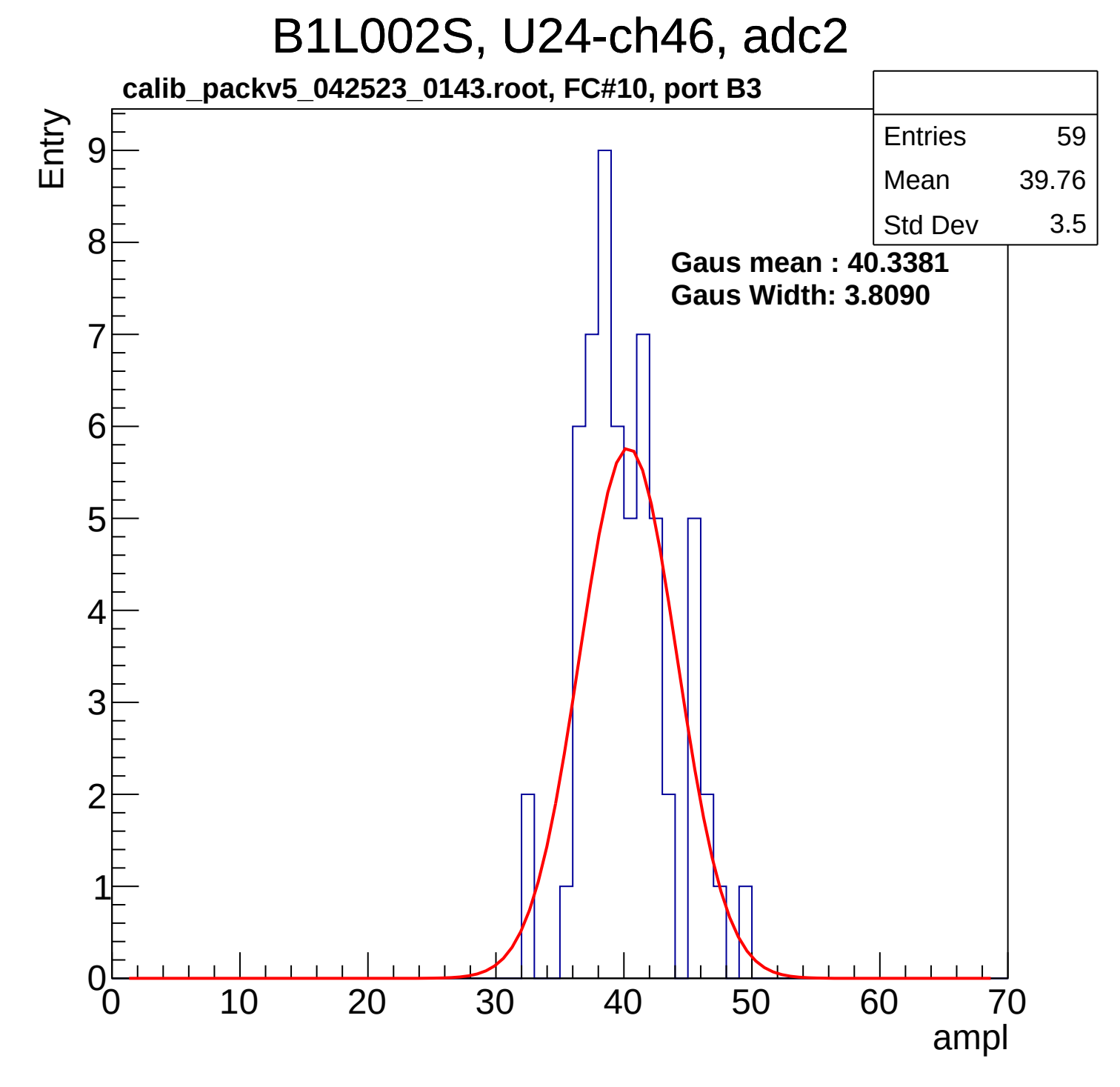
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch46, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.76
Std Dev 4.47
Gaus mean : 28.8647
Gaus Width: 2.9101
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch46, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 34.33
Std Dev 2.838
Gaus mean : 34.3048
Gaus Width: 2.1033
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch46, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 39.76
Std Dev 3.5
Gaus mean : 40.3381
Gaus Width: 3.8090
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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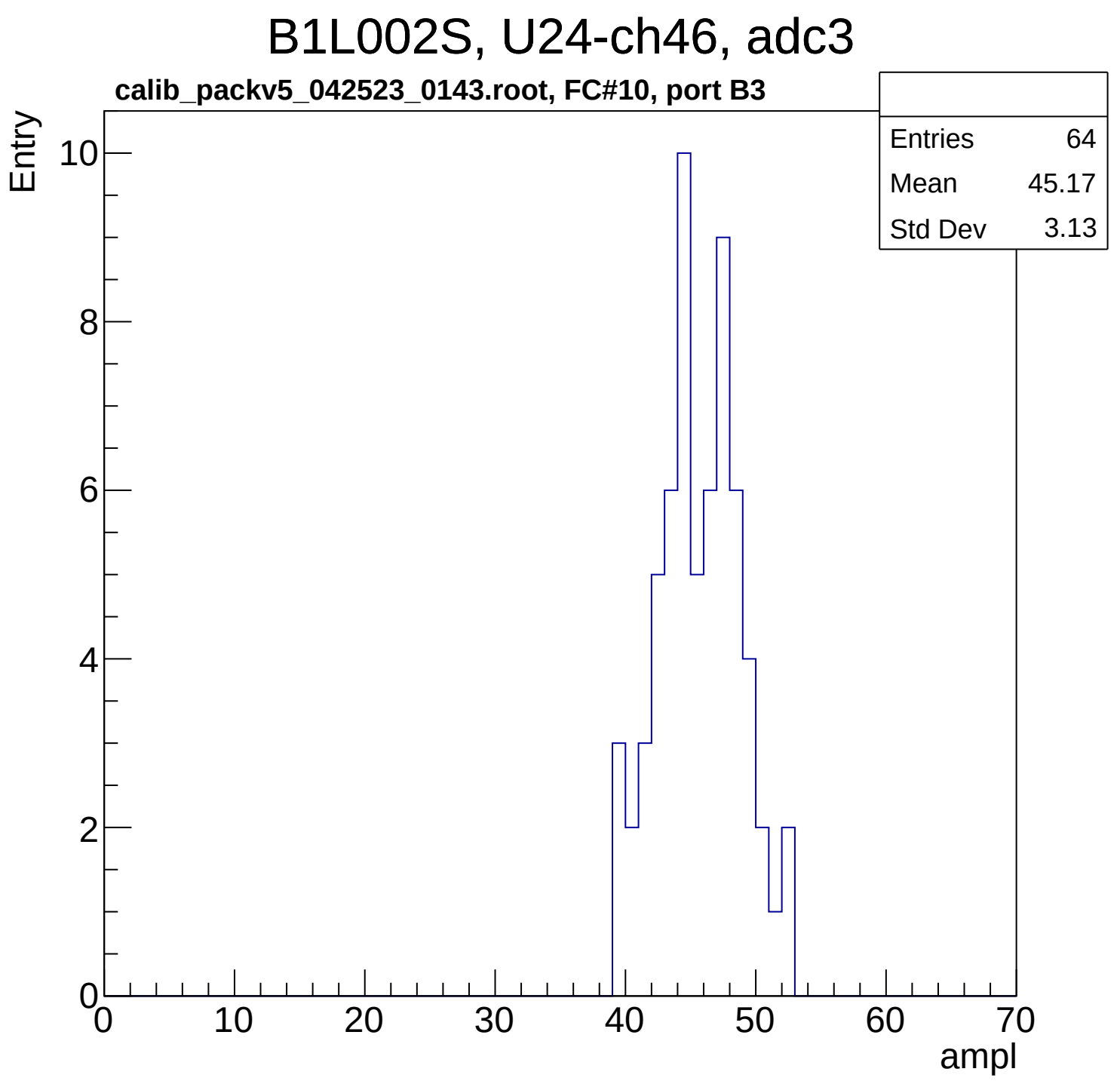
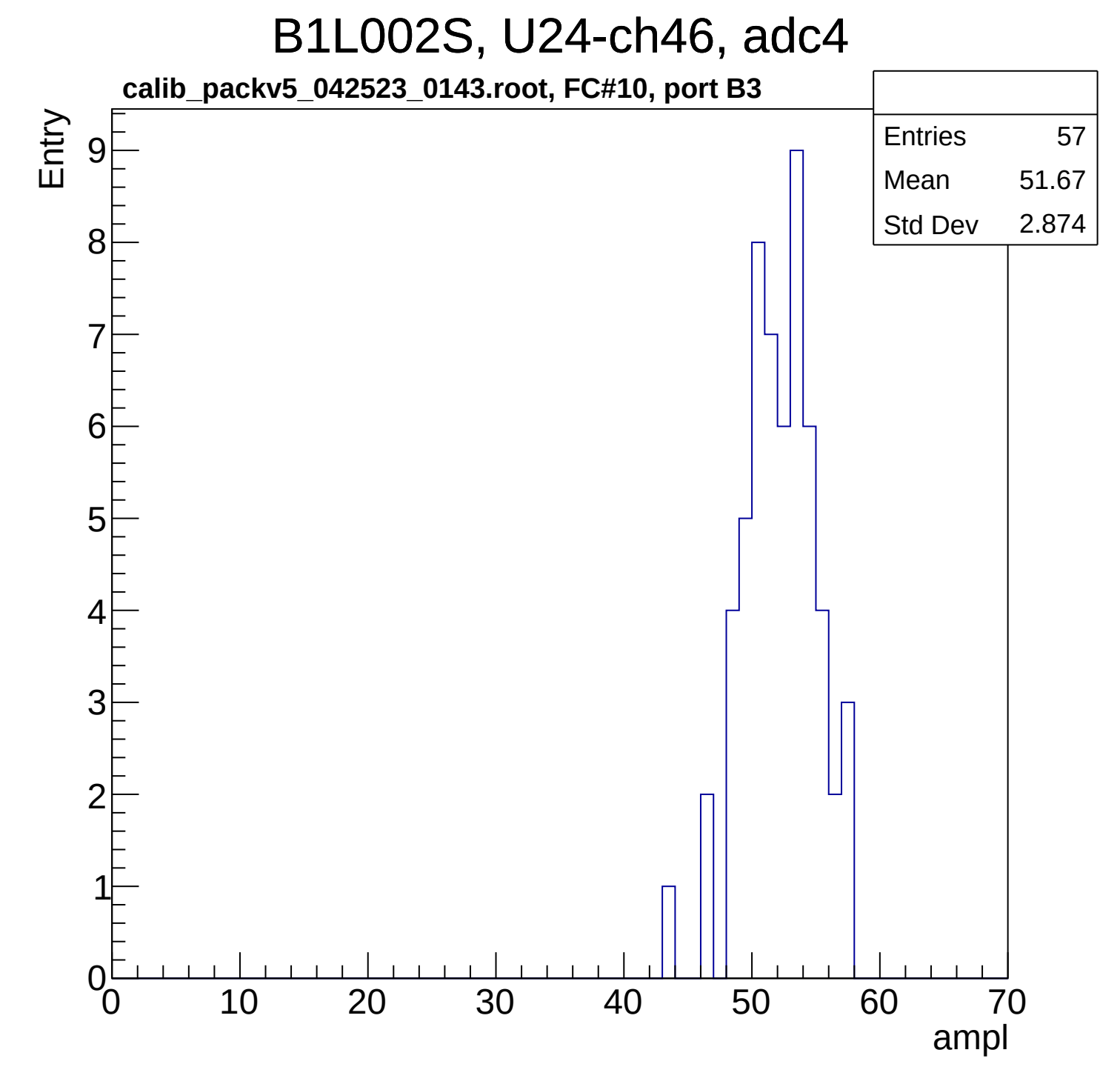

B1L002S, U24-ch46, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 45.17
Std Dev 3.13
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch46, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 51.67
Std Dev 2.874
Entry
ampl
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3
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6
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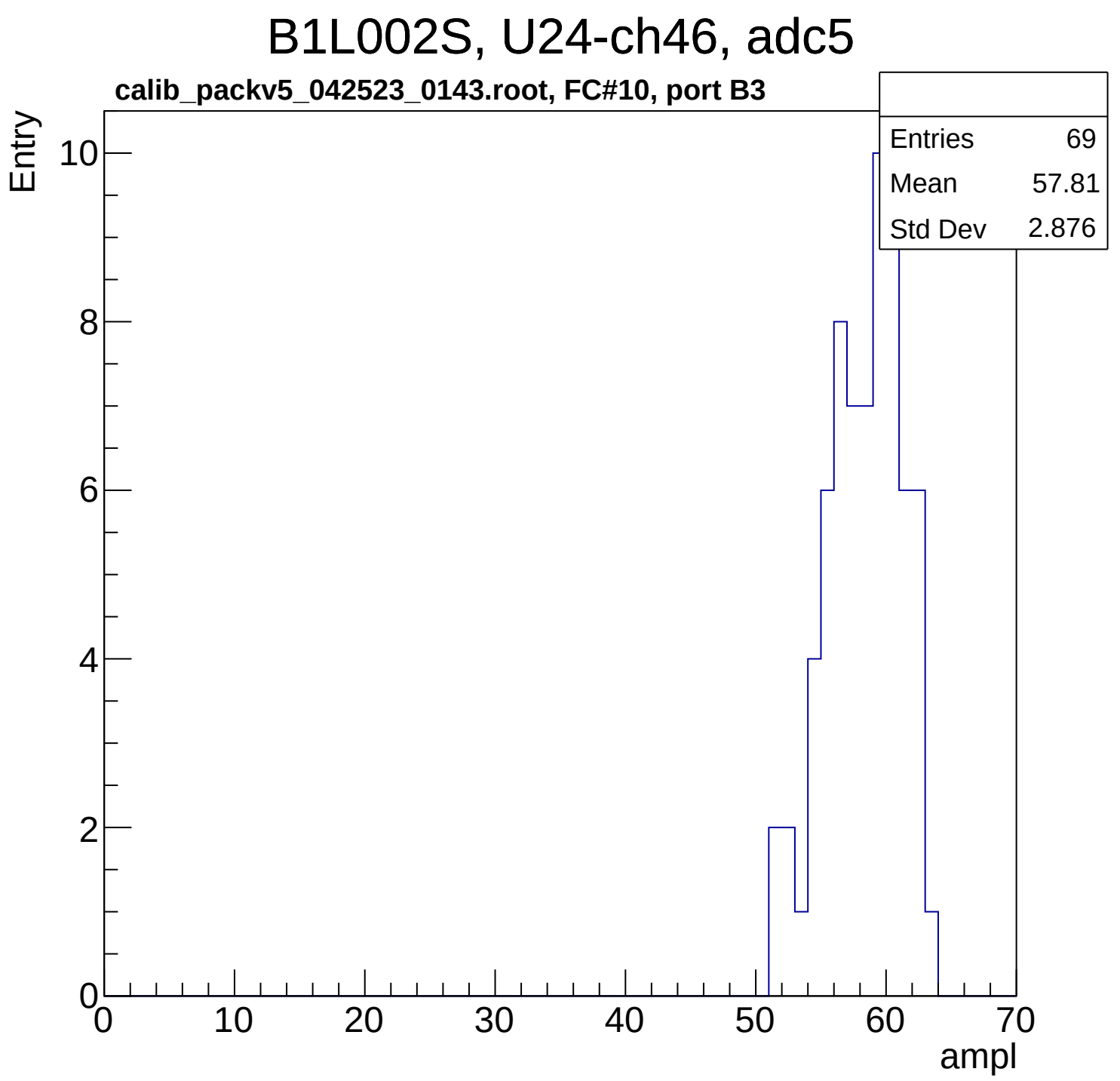
B1L002S, U24-ch46, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 57.81
Std Dev 2.876
Entry
ampl
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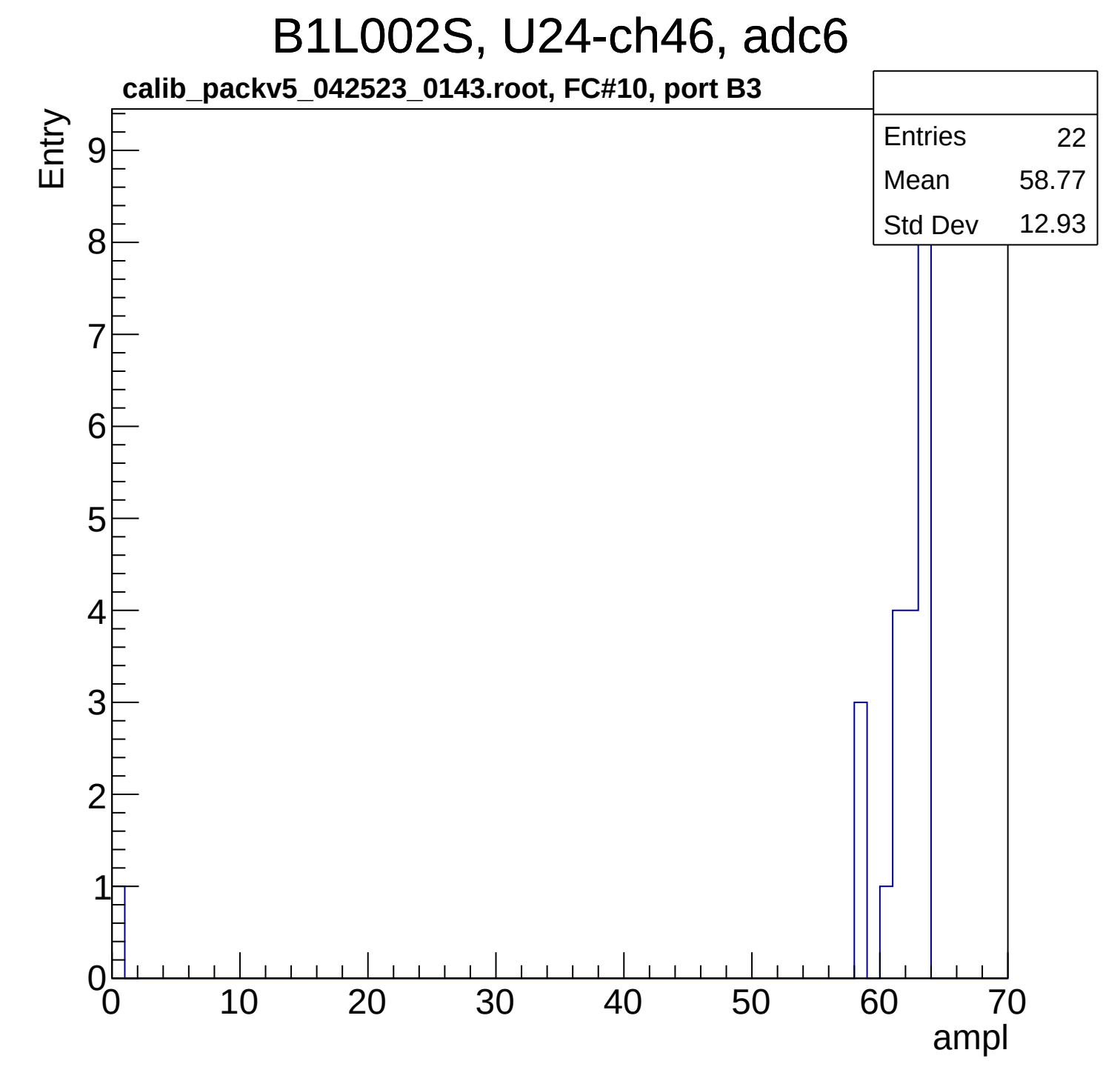
B1L002S, U24-ch46, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.77
Std Dev 12.93
Entry
ampl
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2
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6
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8
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0
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40
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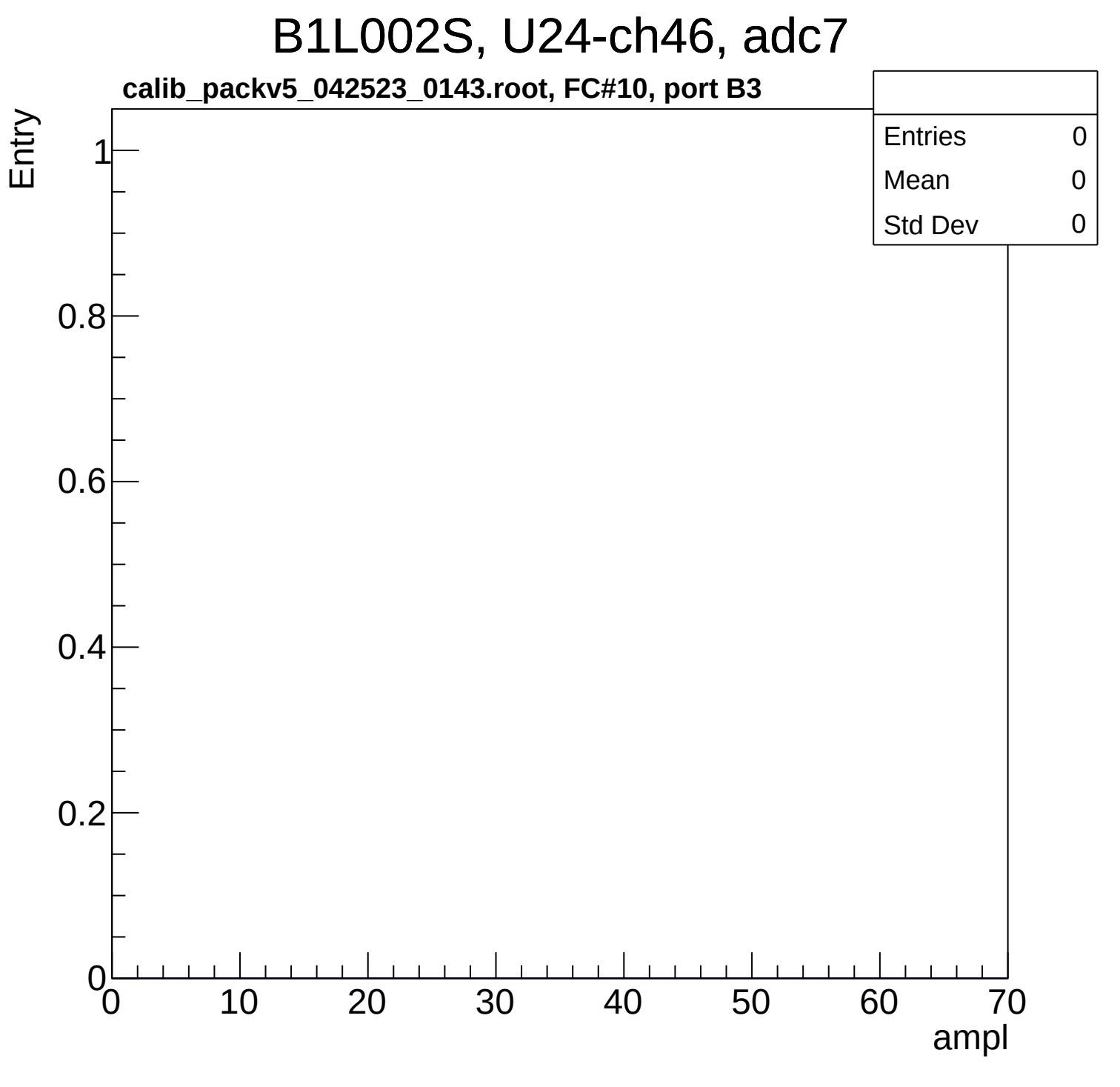
B1L002S, U24-ch46, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
0
10
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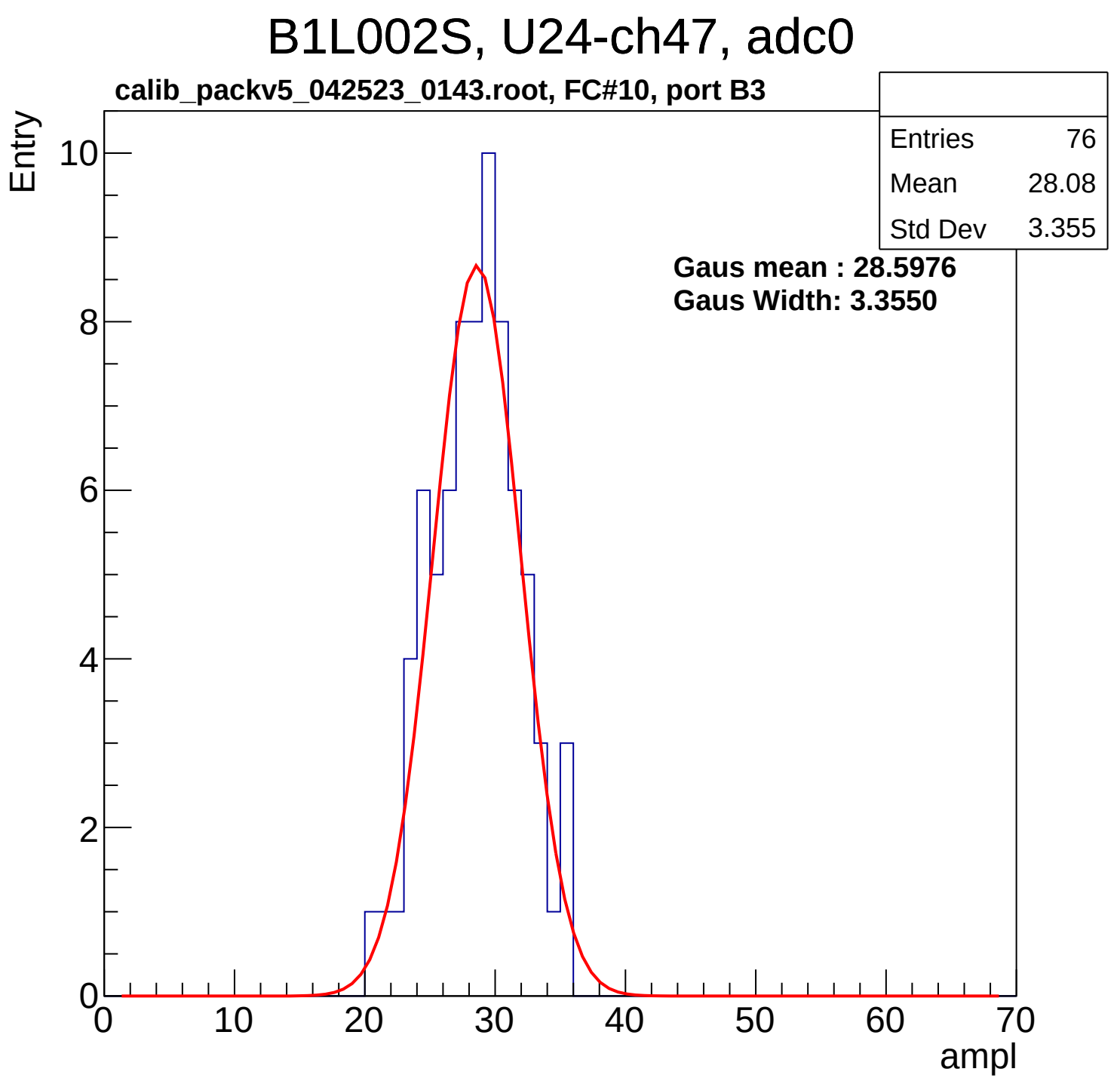
40
50
60
70
ampl


B1L002S, U24-ch47, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 28.08
Std Dev 3.355
Gaus mean : 28.5976
Gaus Width: 3.3550
Entry
ampl
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0
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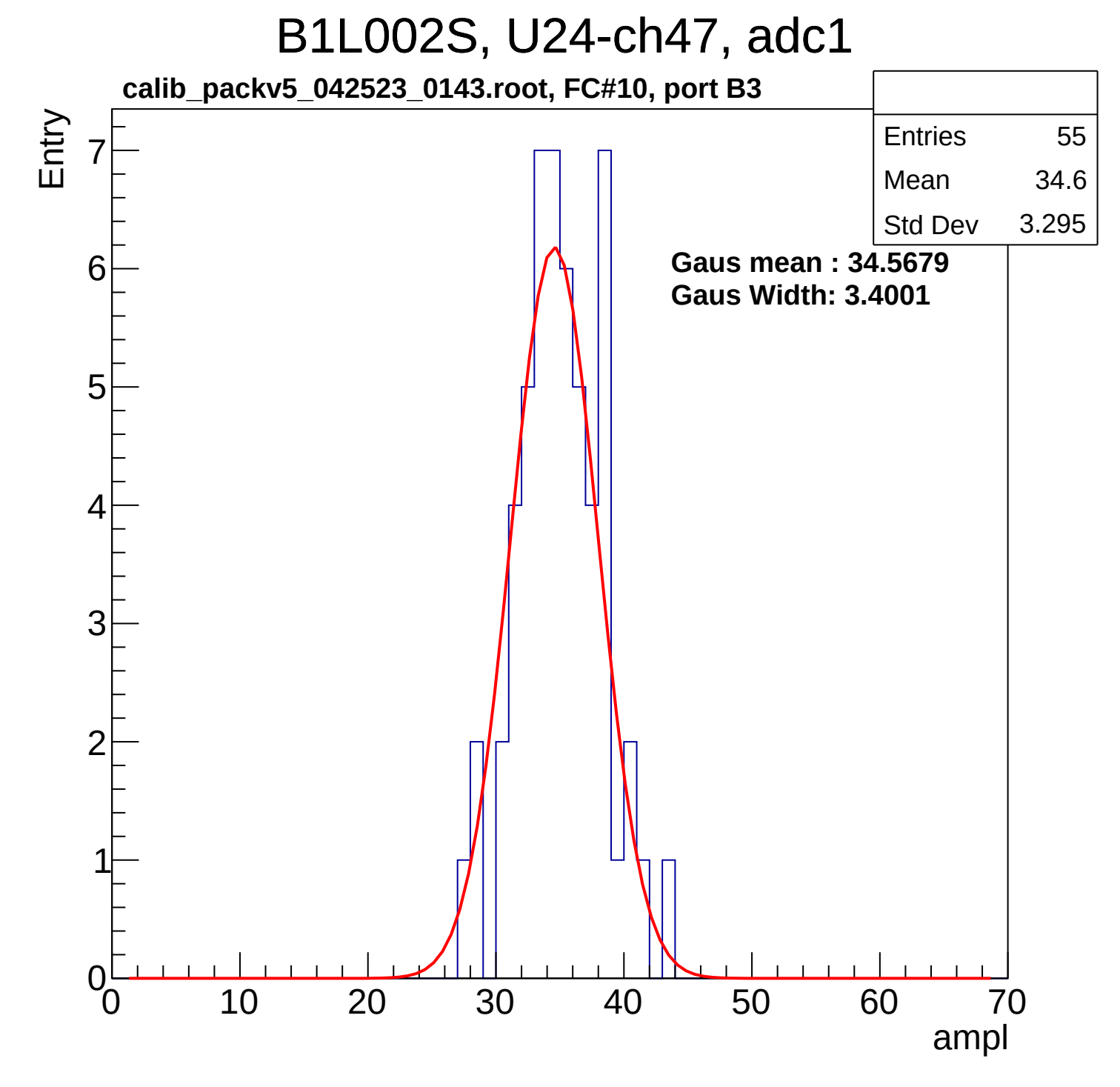
B1L002S, U24-ch47, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 34.6
Std Dev 3.295
Gaus mean : 34.5679
Gaus Width: 3.4001
Entry
ampl
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4
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0
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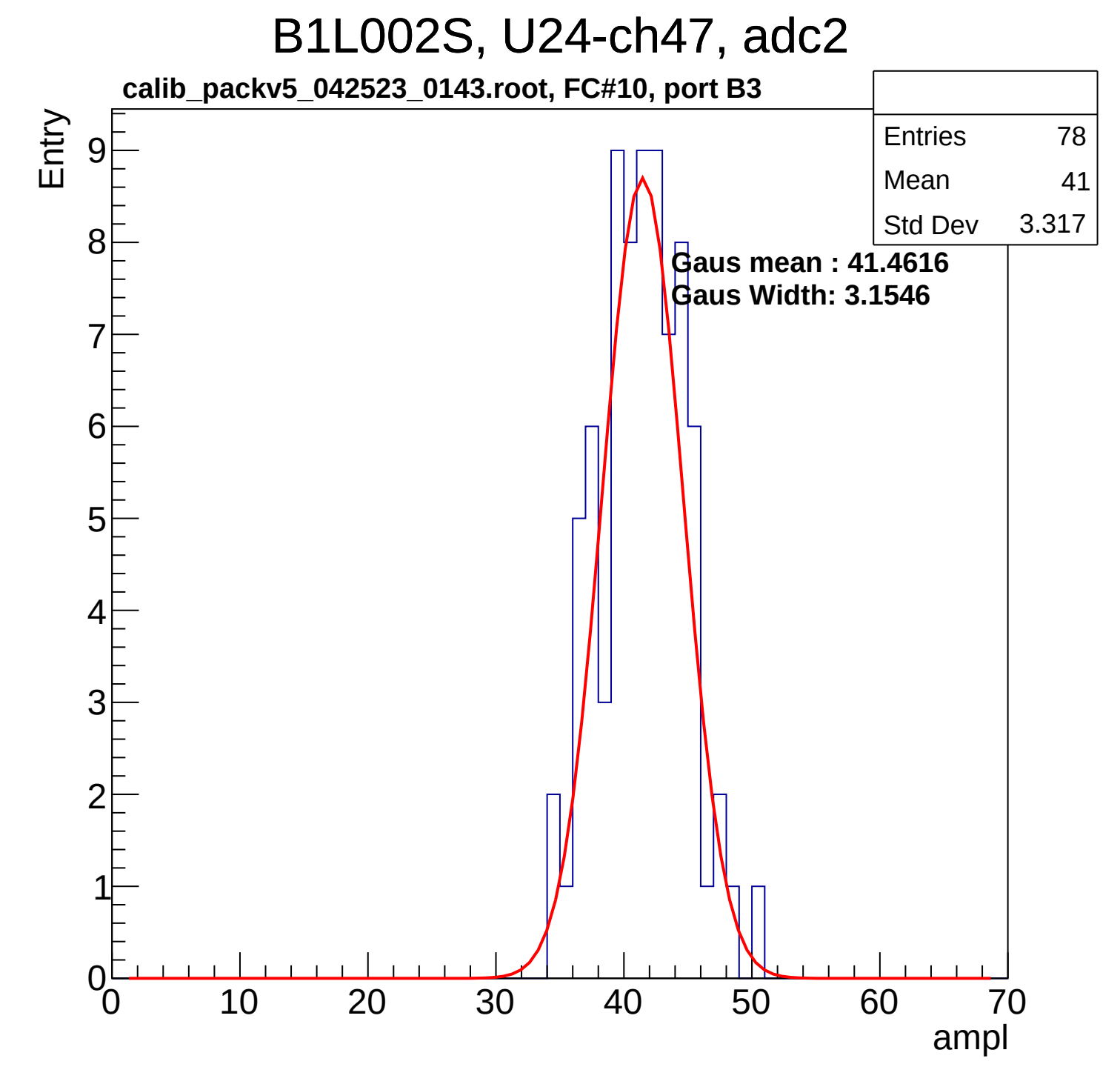
B1L002S, U24-ch47, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 41
Std Dev 3.317
Gaus mean : 41.4616
Gaus Width: 3.1546
Entry
ampl
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2
3
4
5
6
7
8
9
0
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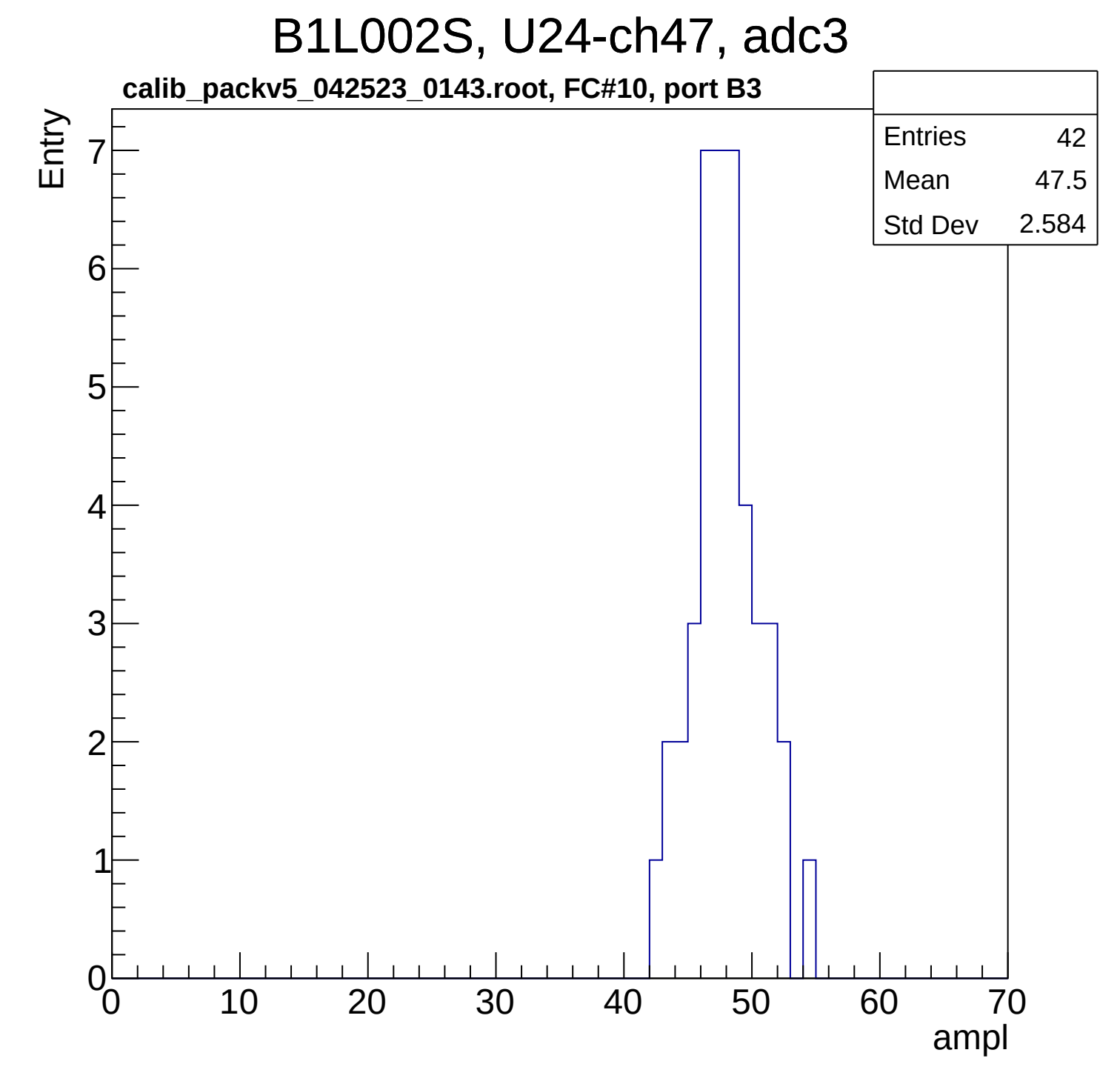
B1L002S, U24-ch47, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 47.5
Std Dev 2.584
Entry
ampl
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2
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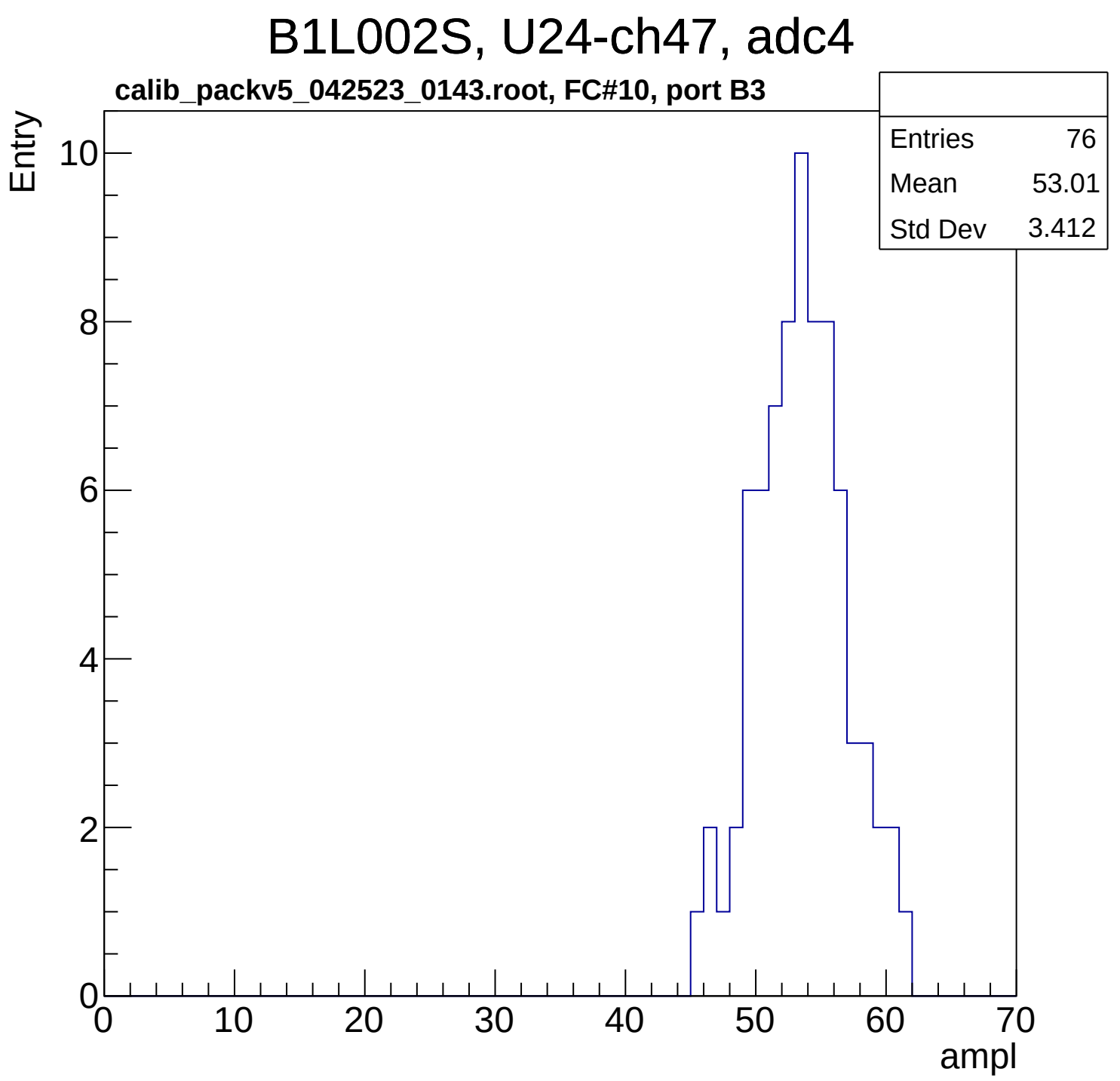
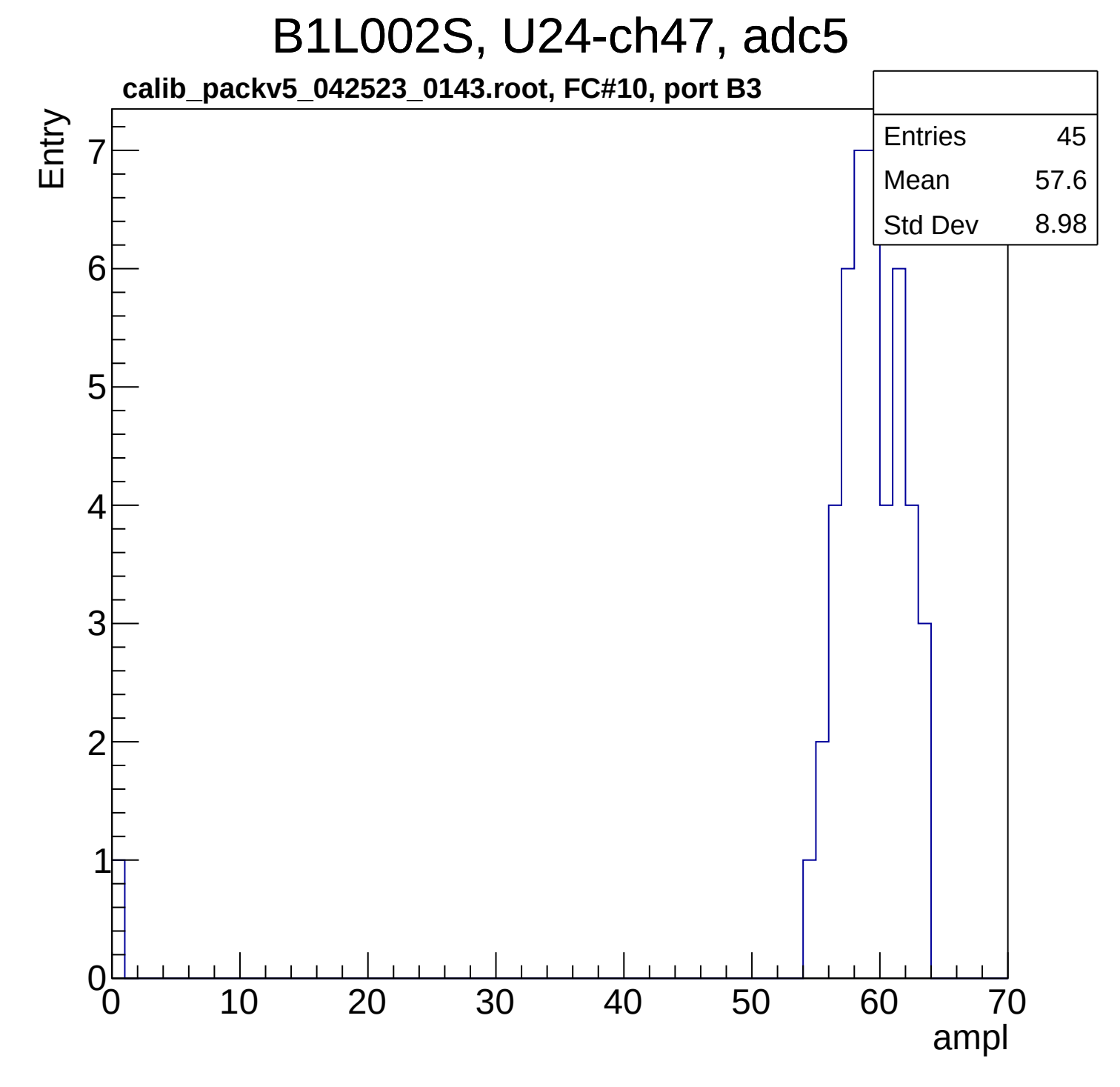

B1L002S, U24-ch47, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 53.01
Std Dev 3.412
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch47, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.6
Std Dev 8.98
Entry
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1
2
3
4
5
6
7
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10
20
30
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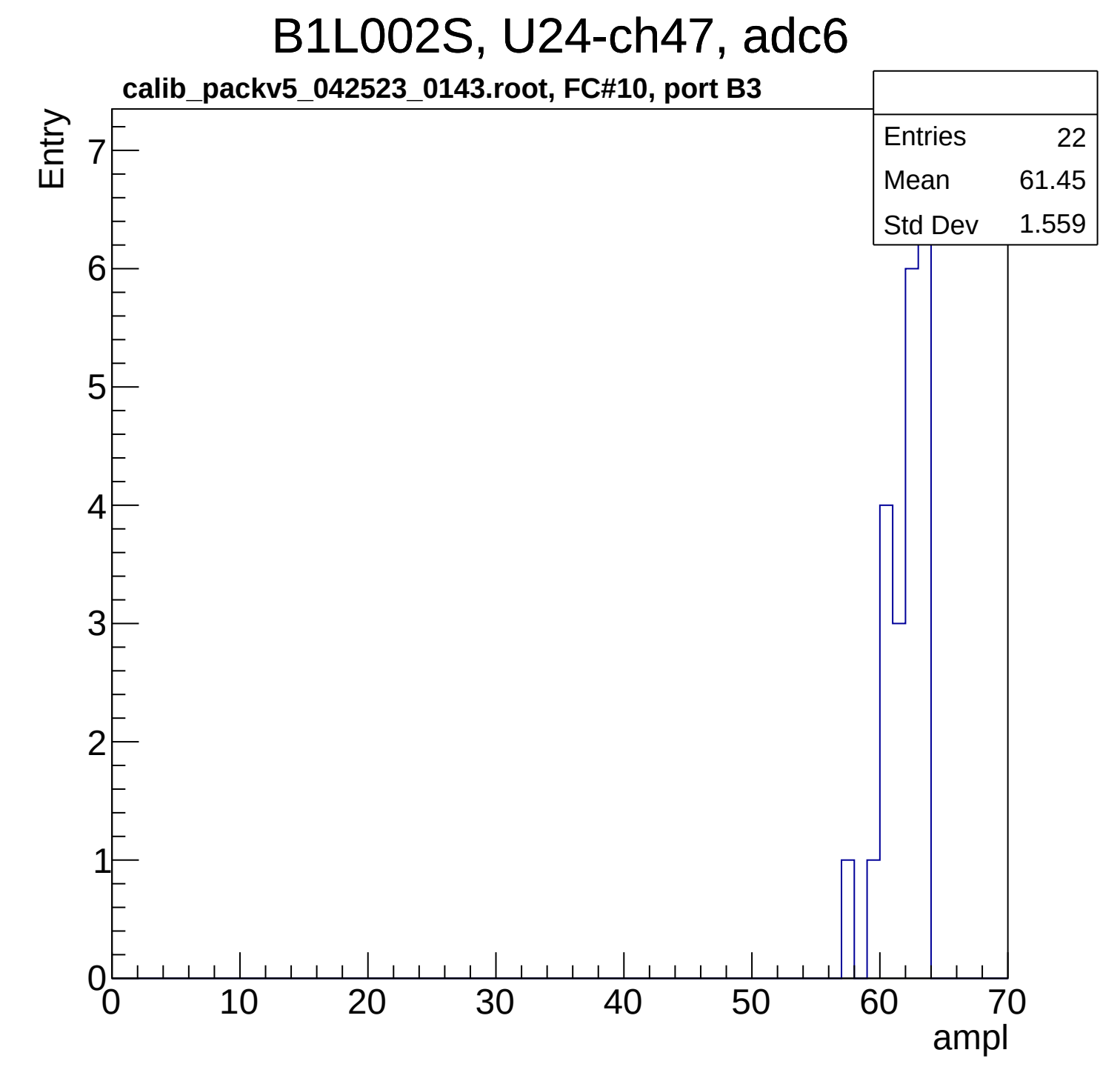
50
60
70
ampl


B1L002S, U24-ch47, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 61.45
Std Dev 1.559
Entry
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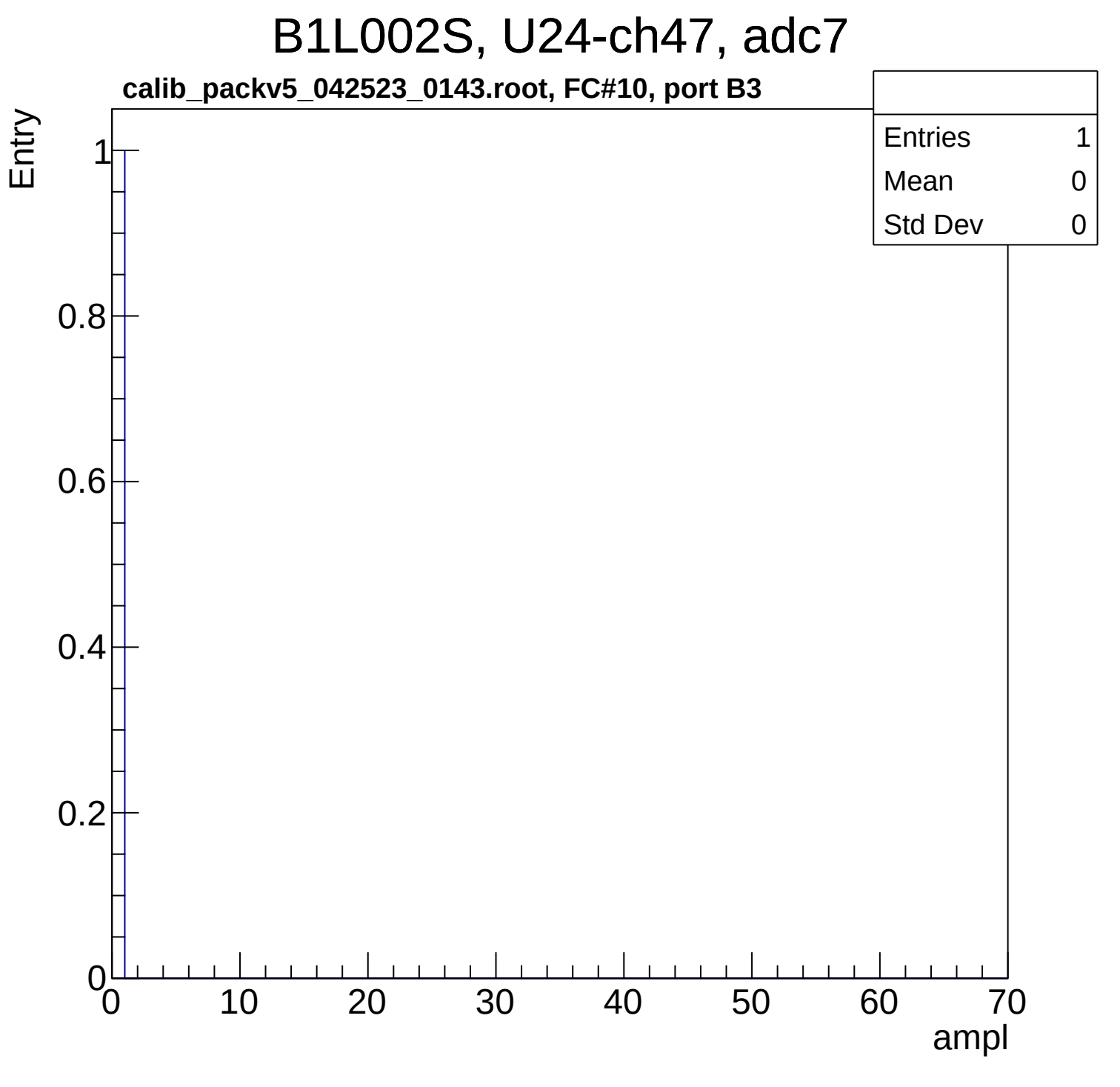
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch47, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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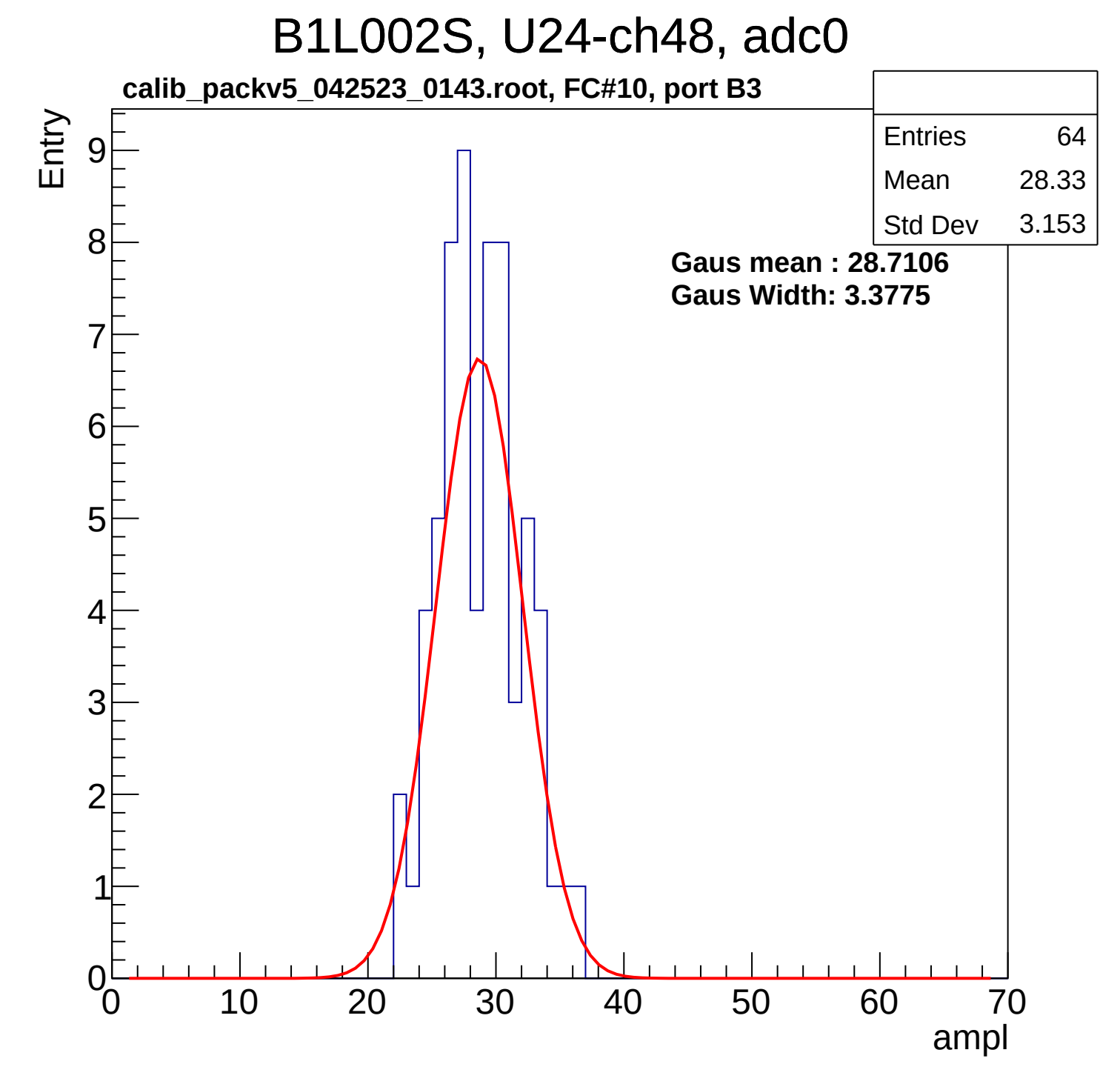
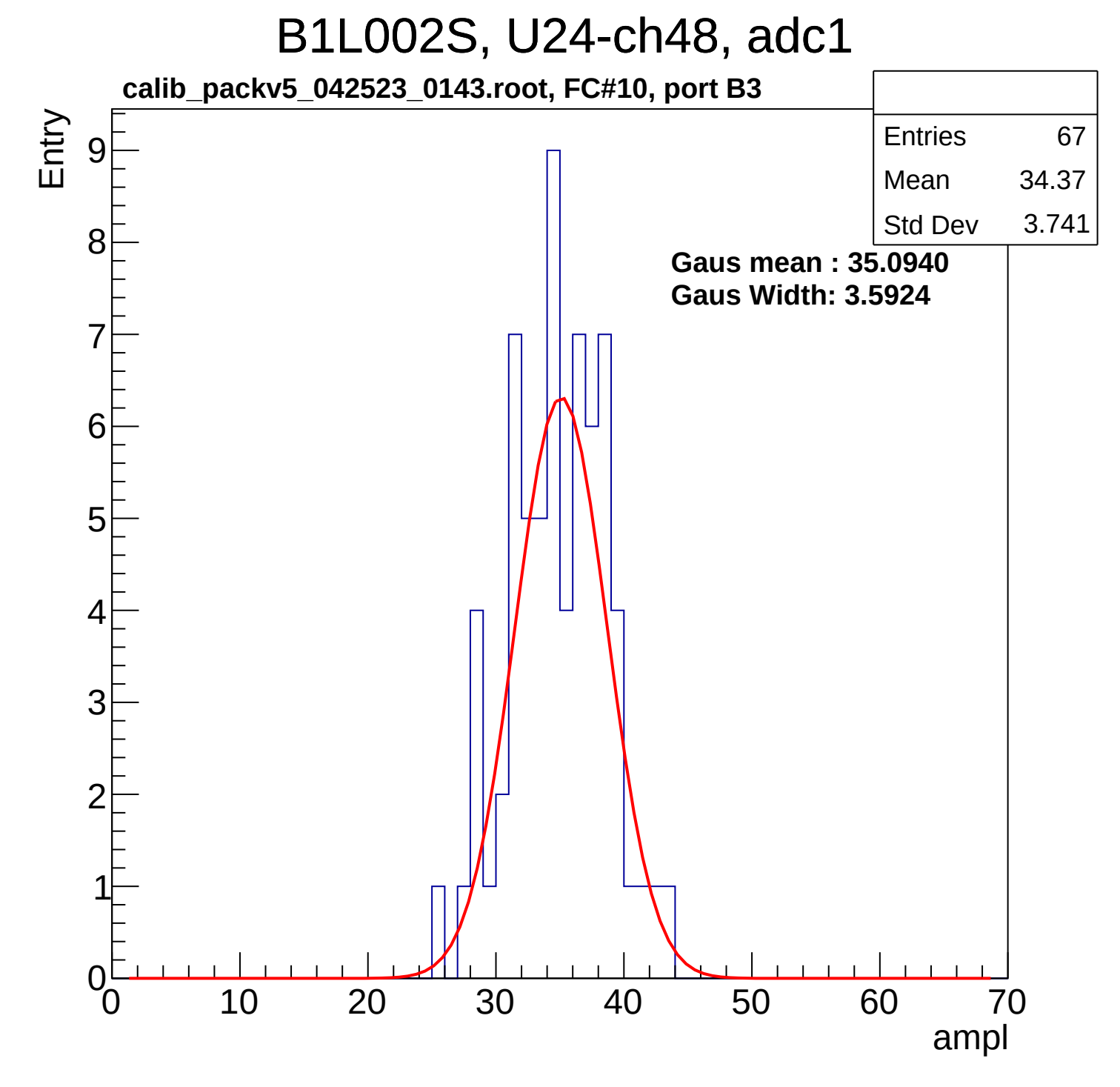
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch48, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.33
Std Dev 3.153
Gaus mean : 28.7106
Gaus Width: 3.3775
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch48, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.37
Std Dev 3.741
Gaus mean : 35.0940
Gaus Width: 3.5924
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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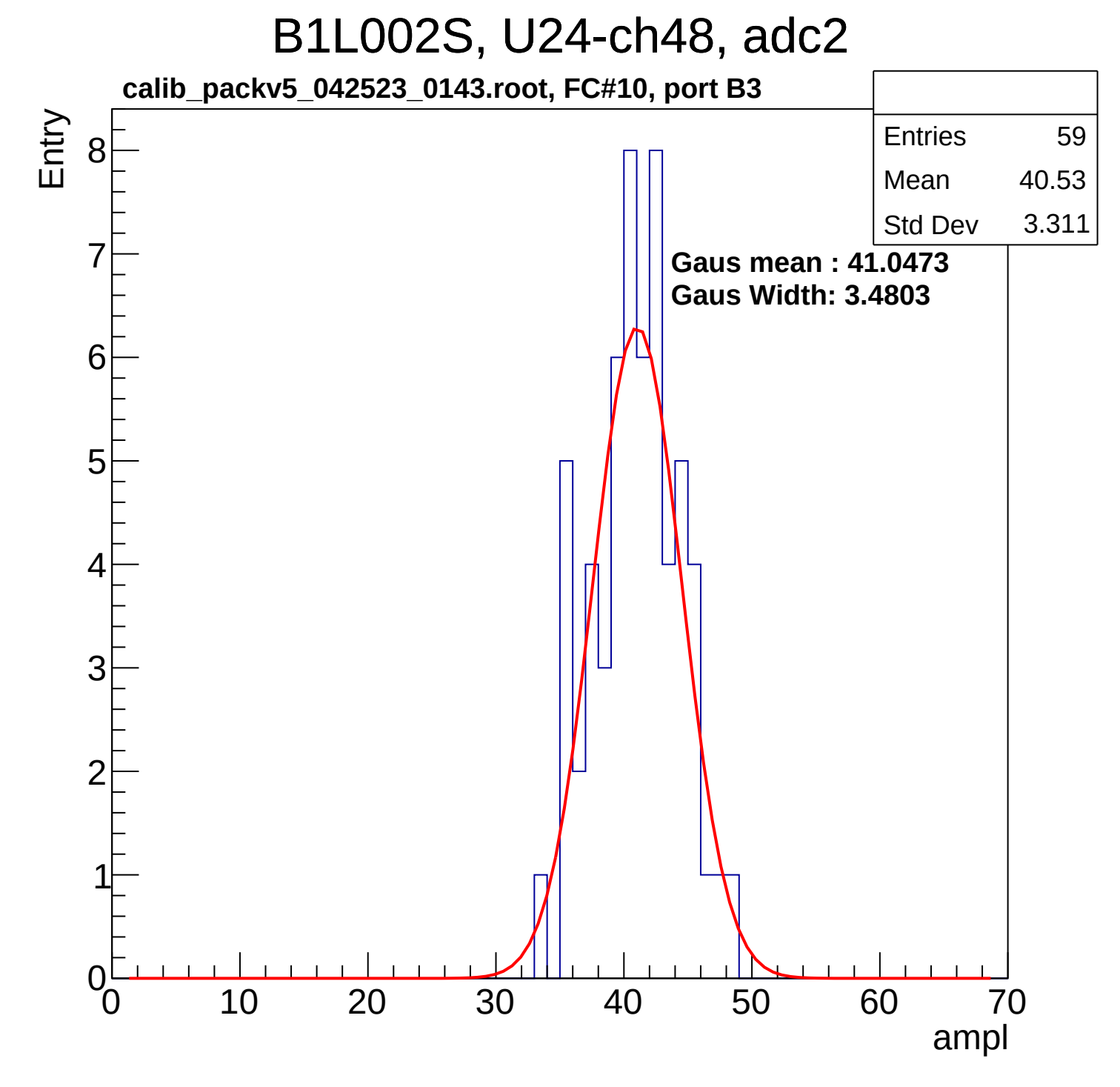
B1L002S, U24-ch48, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.53
Std Dev 3.311
Gaus mean : 41.0473
Gaus Width: 3.4803
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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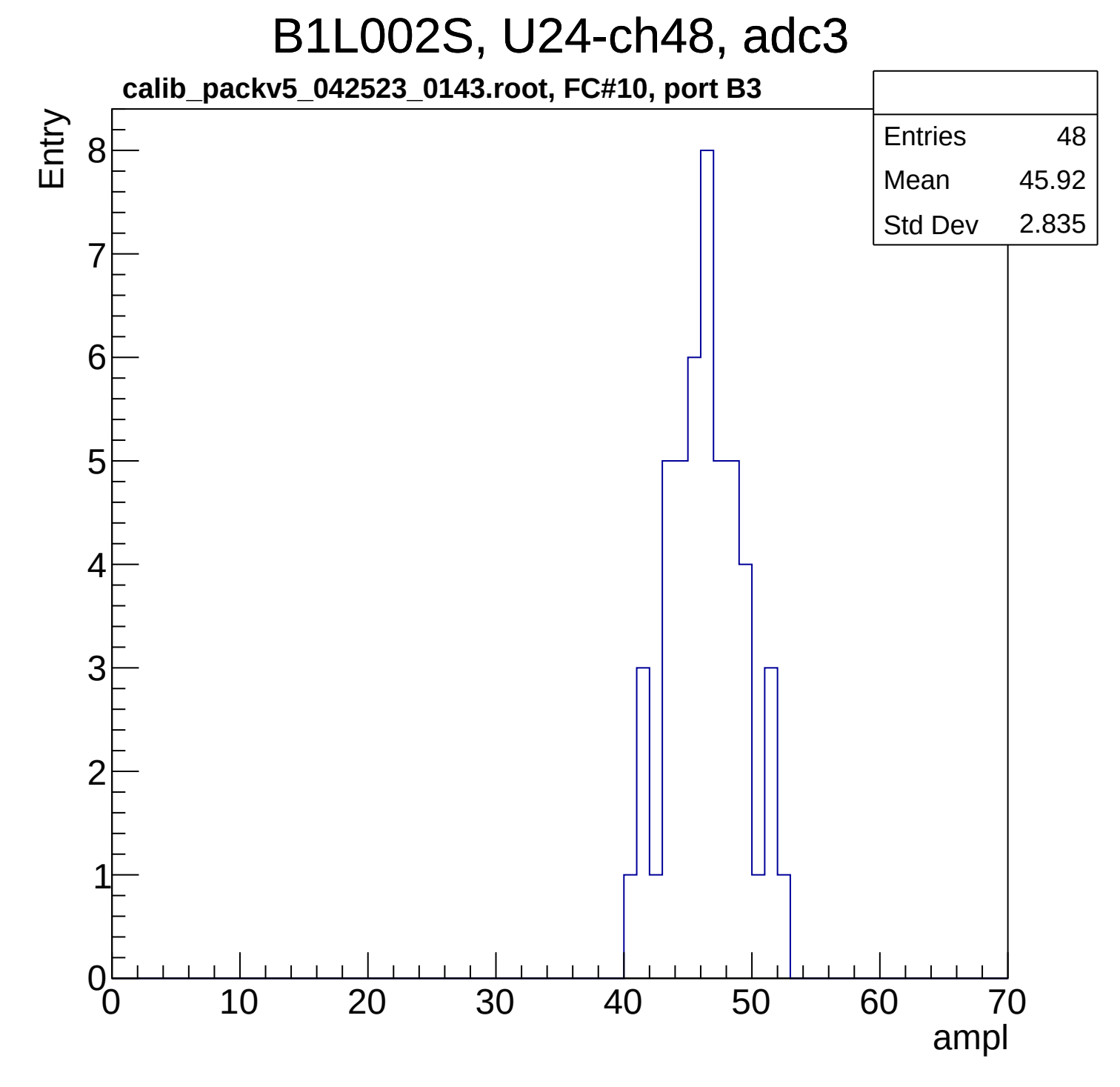
B1L002S, U24-ch48, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 45.92
Std Dev 2.835
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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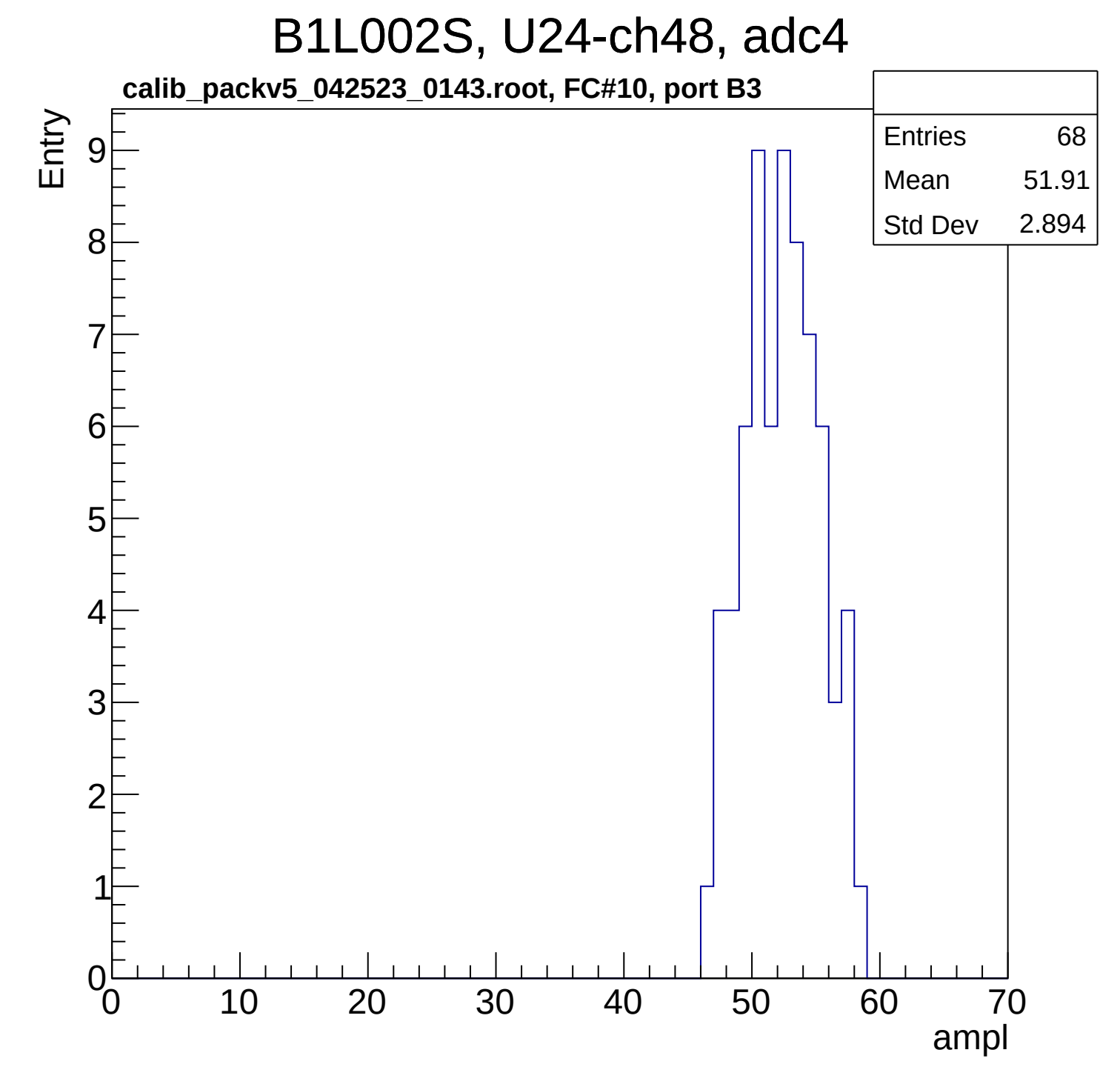
B1L002S, U24-ch48, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 51.91
Std Dev 2.894
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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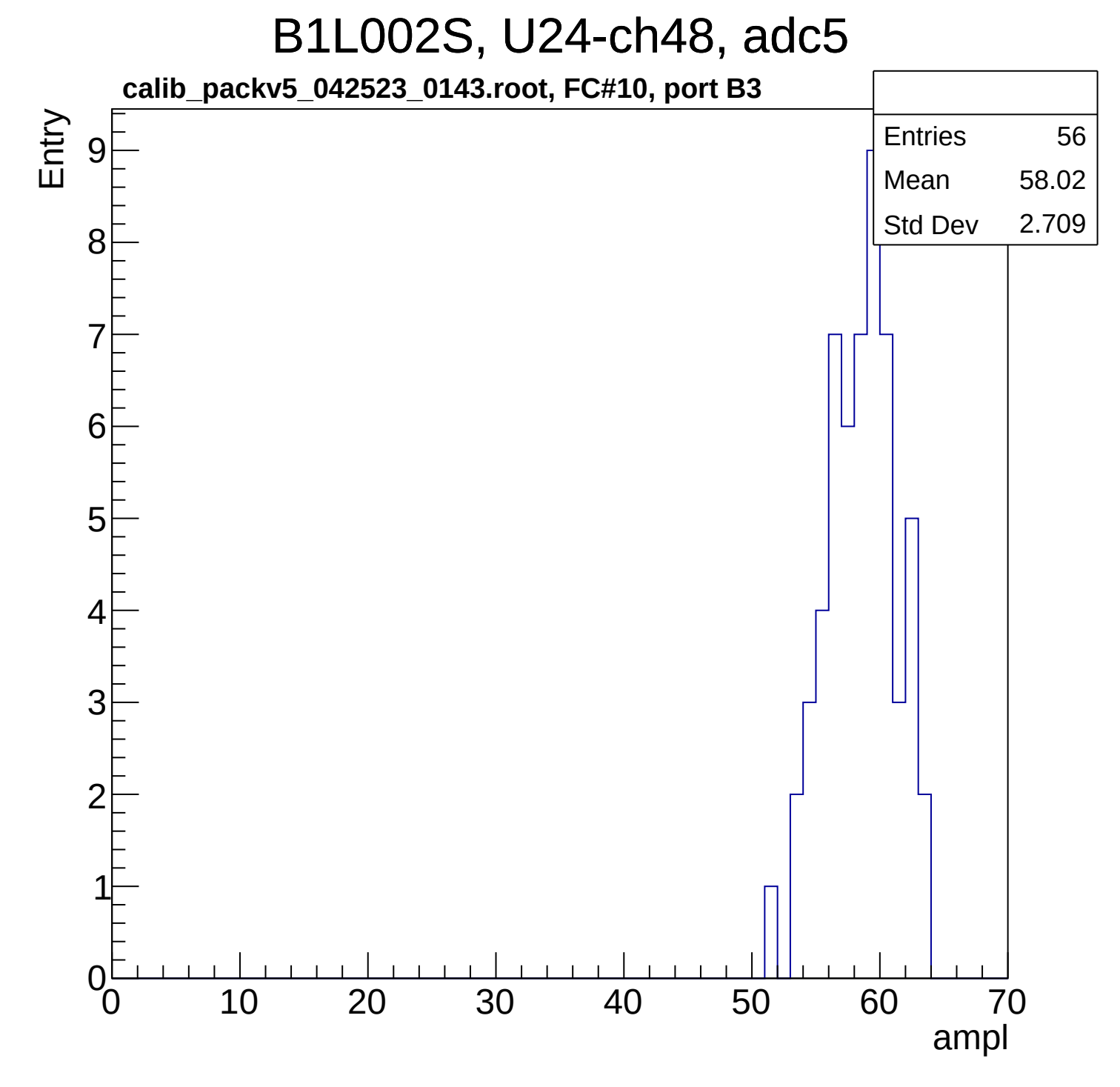
B1L002S, U24-ch48, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.02
Std Dev 2.709
Entry
ampl
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3
4
5
6
7
8
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0
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60
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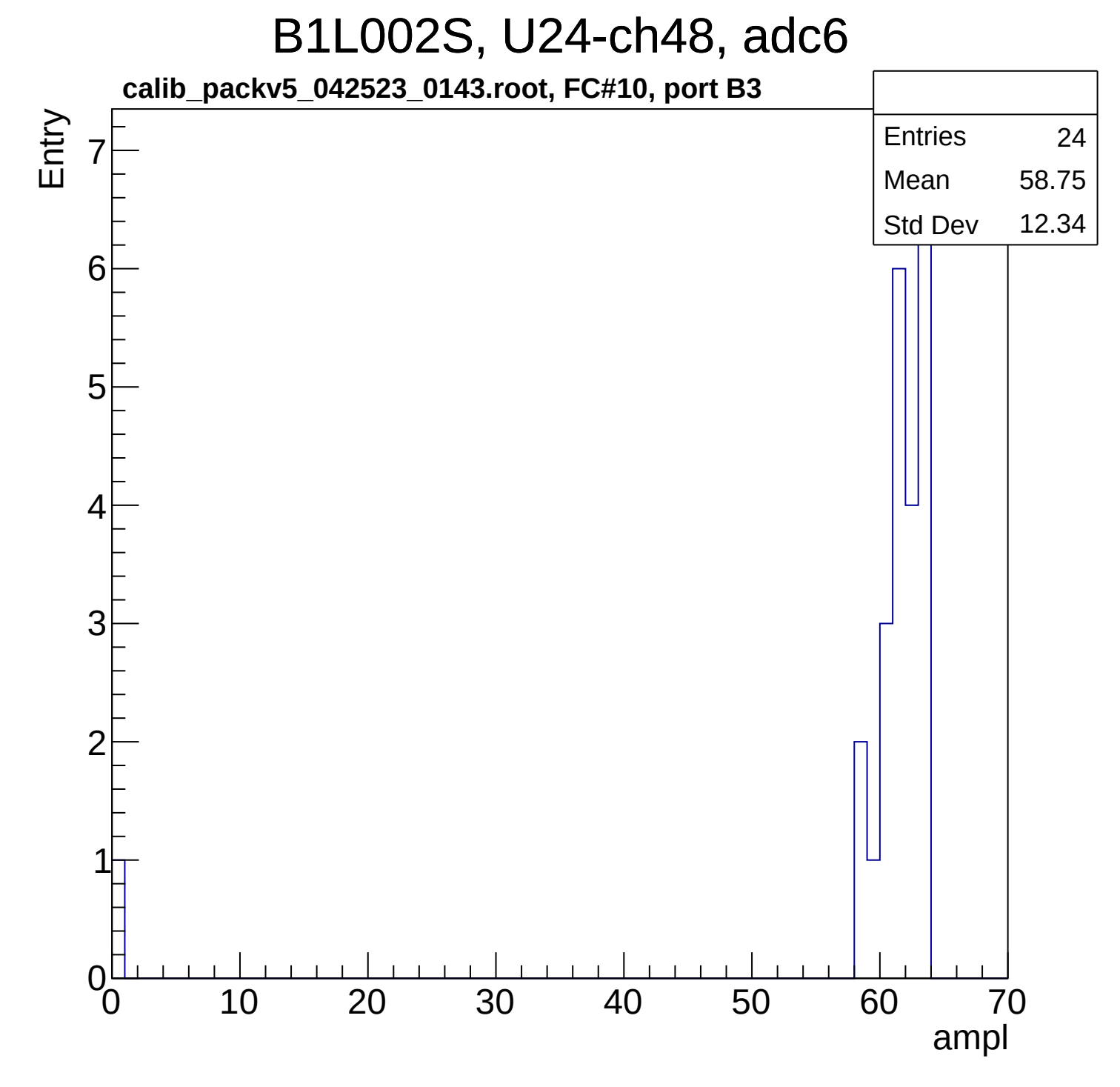
B1L002S, U24-ch48, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.75
Std Dev 12.34
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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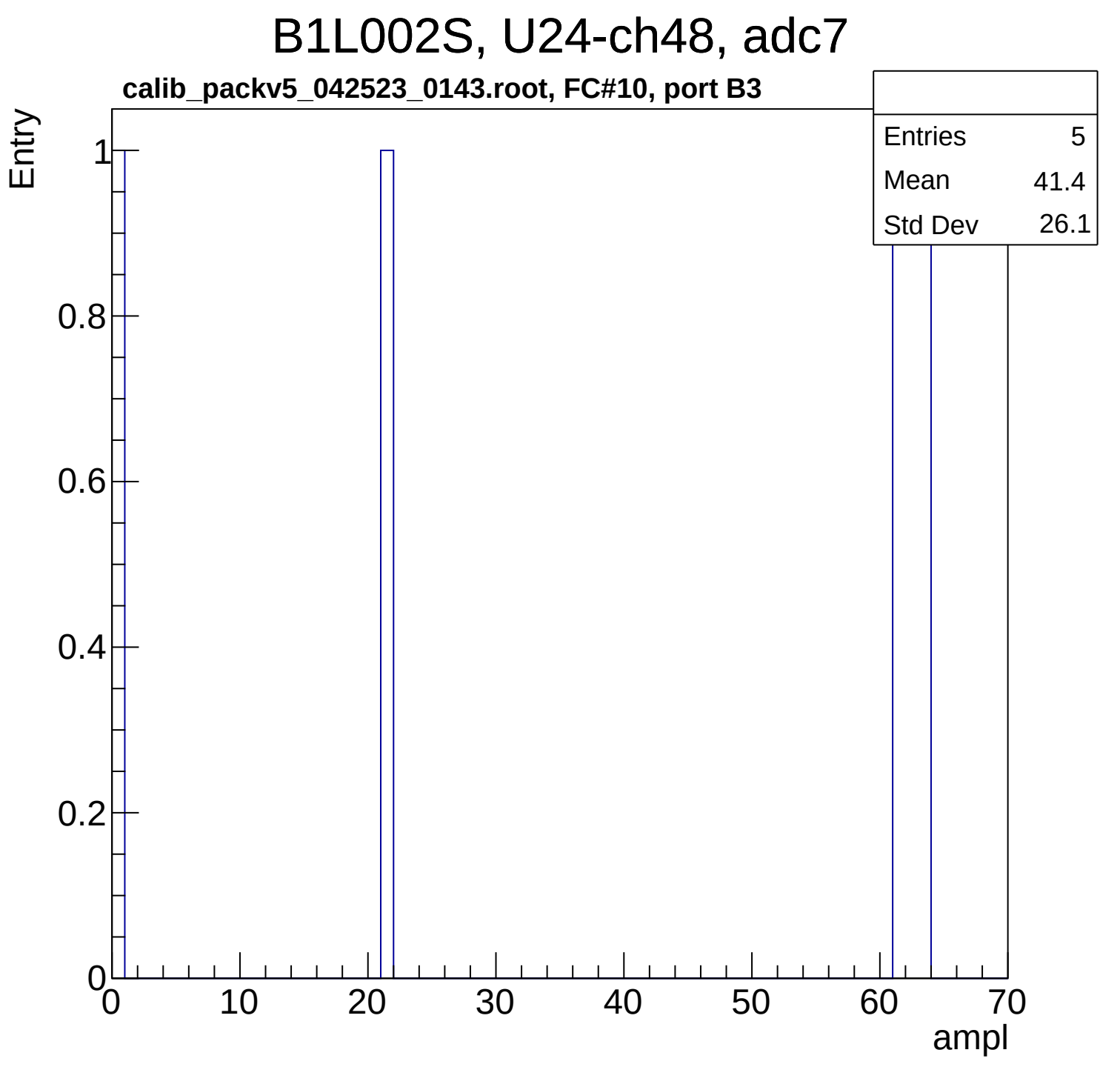
B1L002S, U24-ch48, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 41.4
Std Dev 26.1
Entry
ampl
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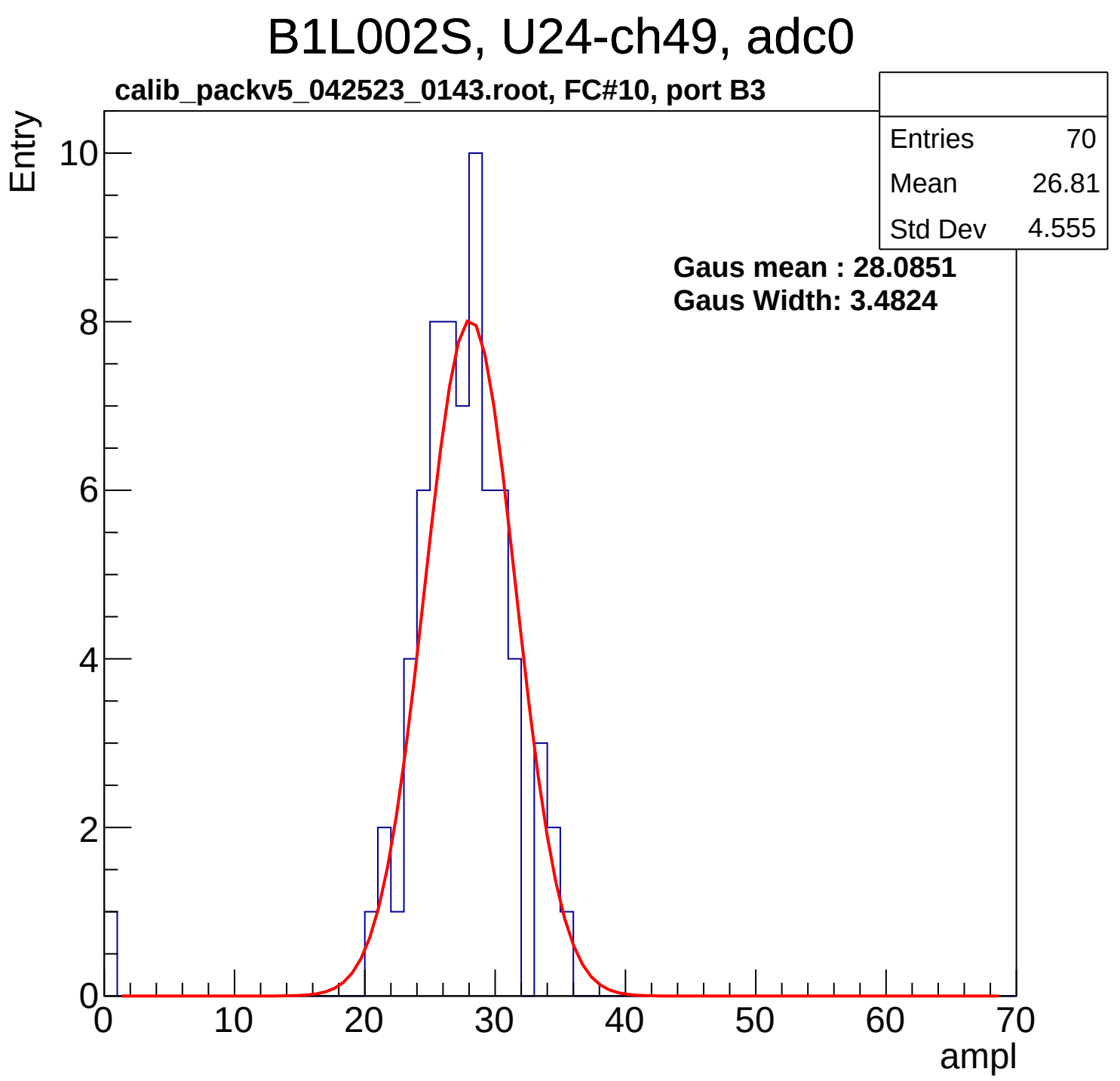
B1L002S, U24-ch49, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 26.81
Std Dev 4.555
Gaus mean : 28.0851
Gaus Width: 3.4824
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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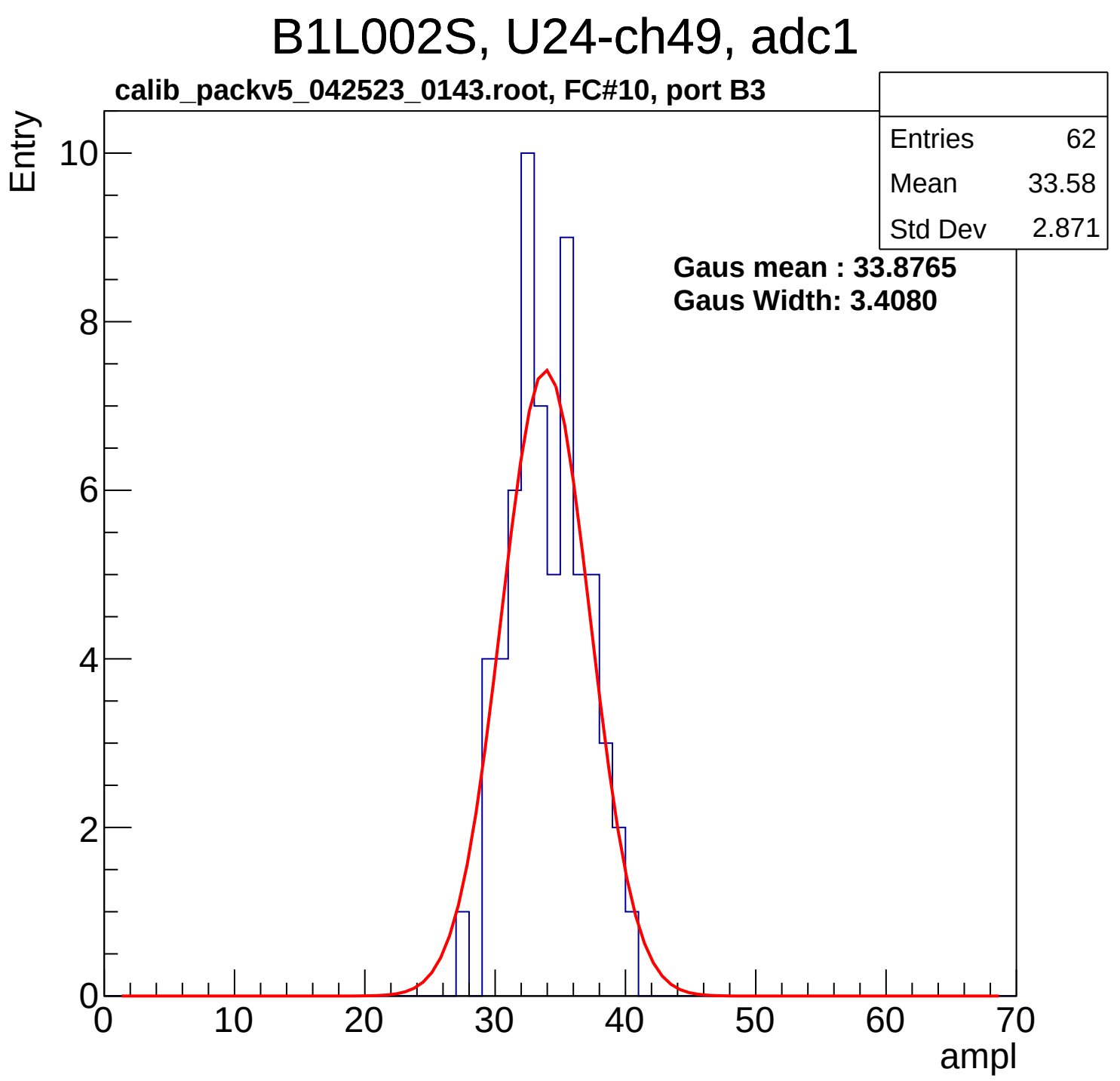
B1L002S, U24-ch49, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.58
Std Dev 2.871
Gaus mean : 33.8765
Gaus Width: 3.4080
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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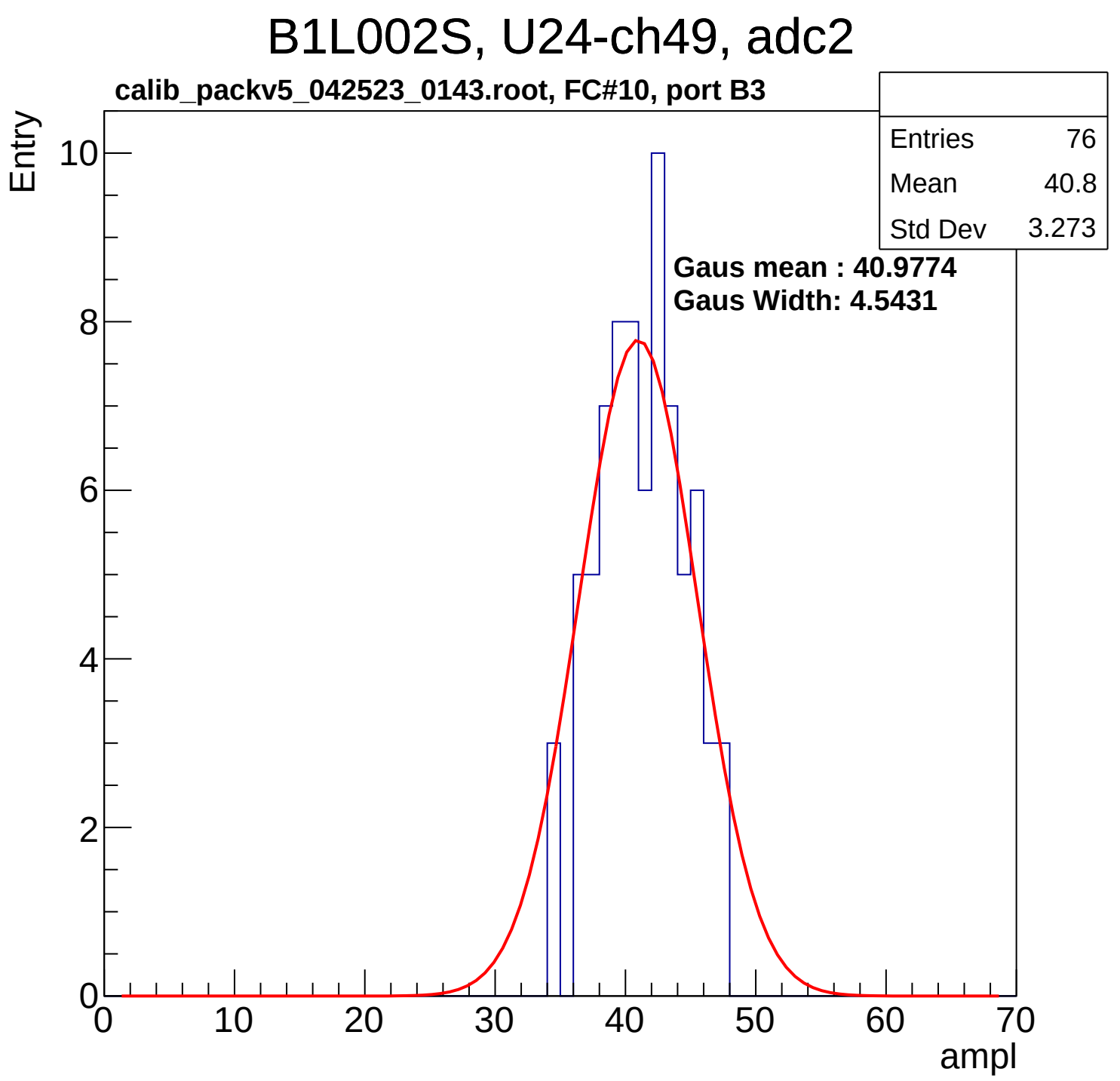
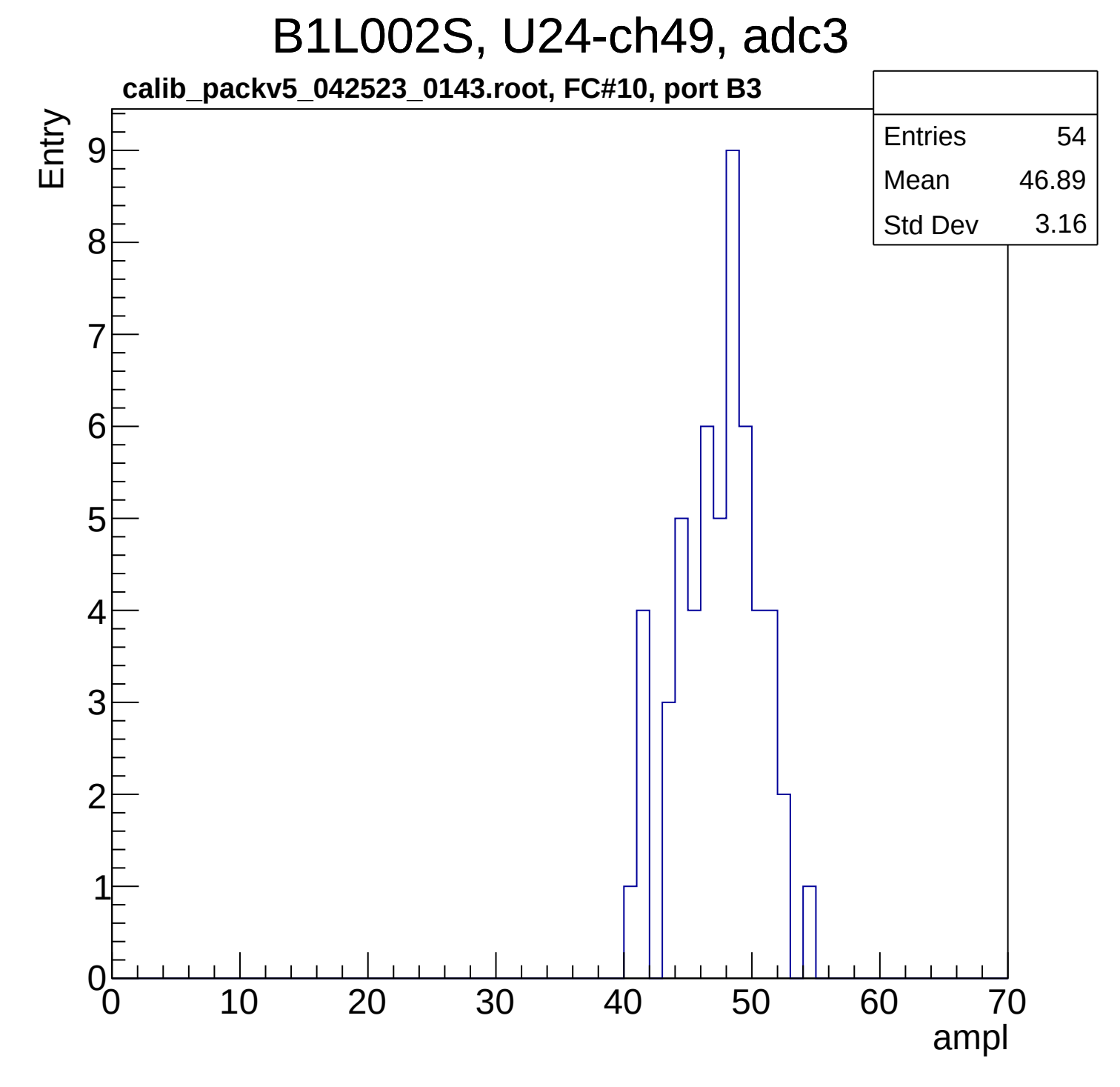

B1L002S, U24-ch49, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 40.8
Std Dev 3.273
Gaus mean : 40.9774
Gaus Width: 4.5431
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch49, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.89
Std Dev 3.16
Entry
ampl
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4
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6
7
8
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0
10
20
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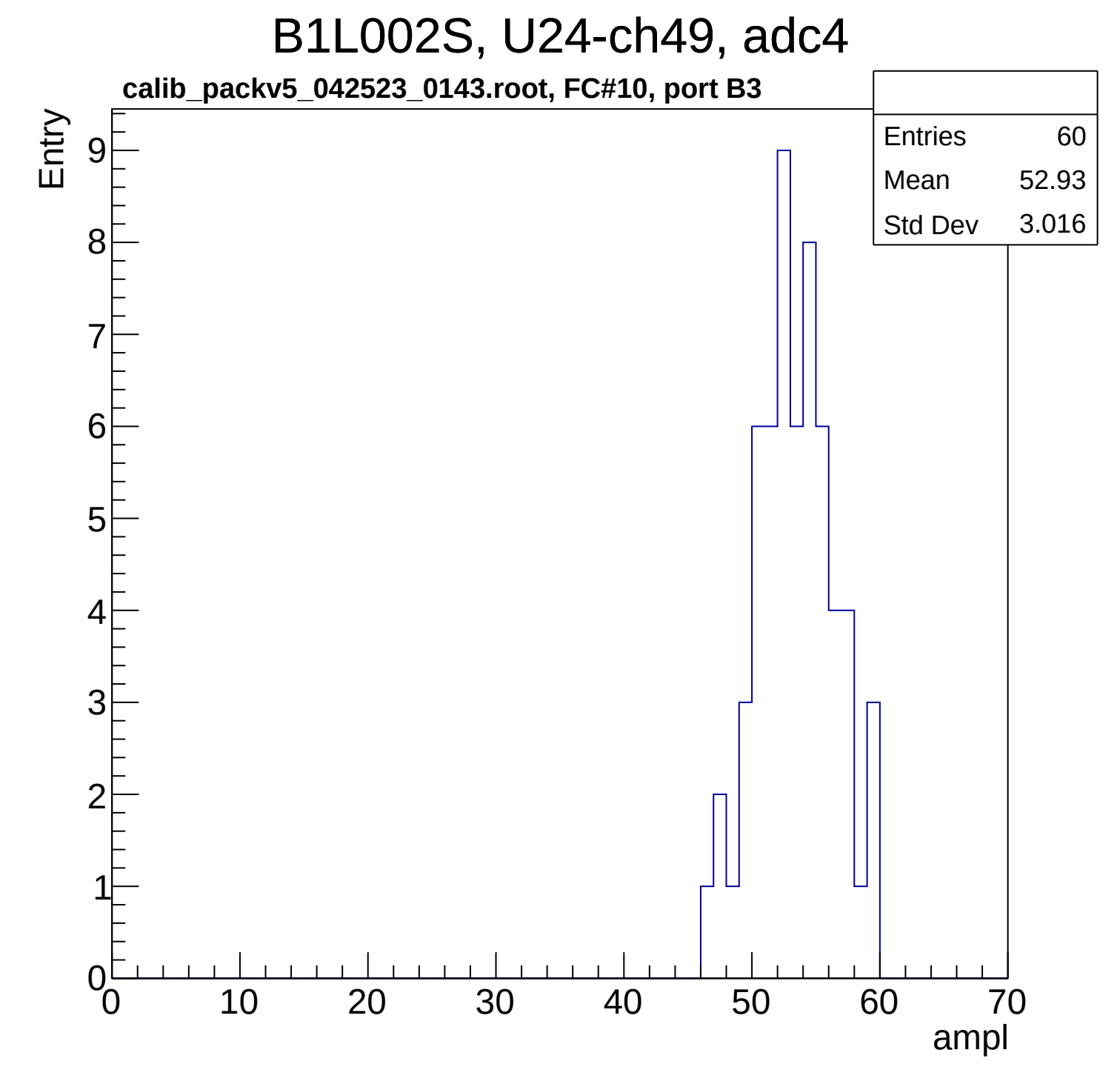
40
50
60
70


B1L002S, U24-ch49, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 52.93
Std Dev 3.016
Entry
ampl
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6
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0
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60
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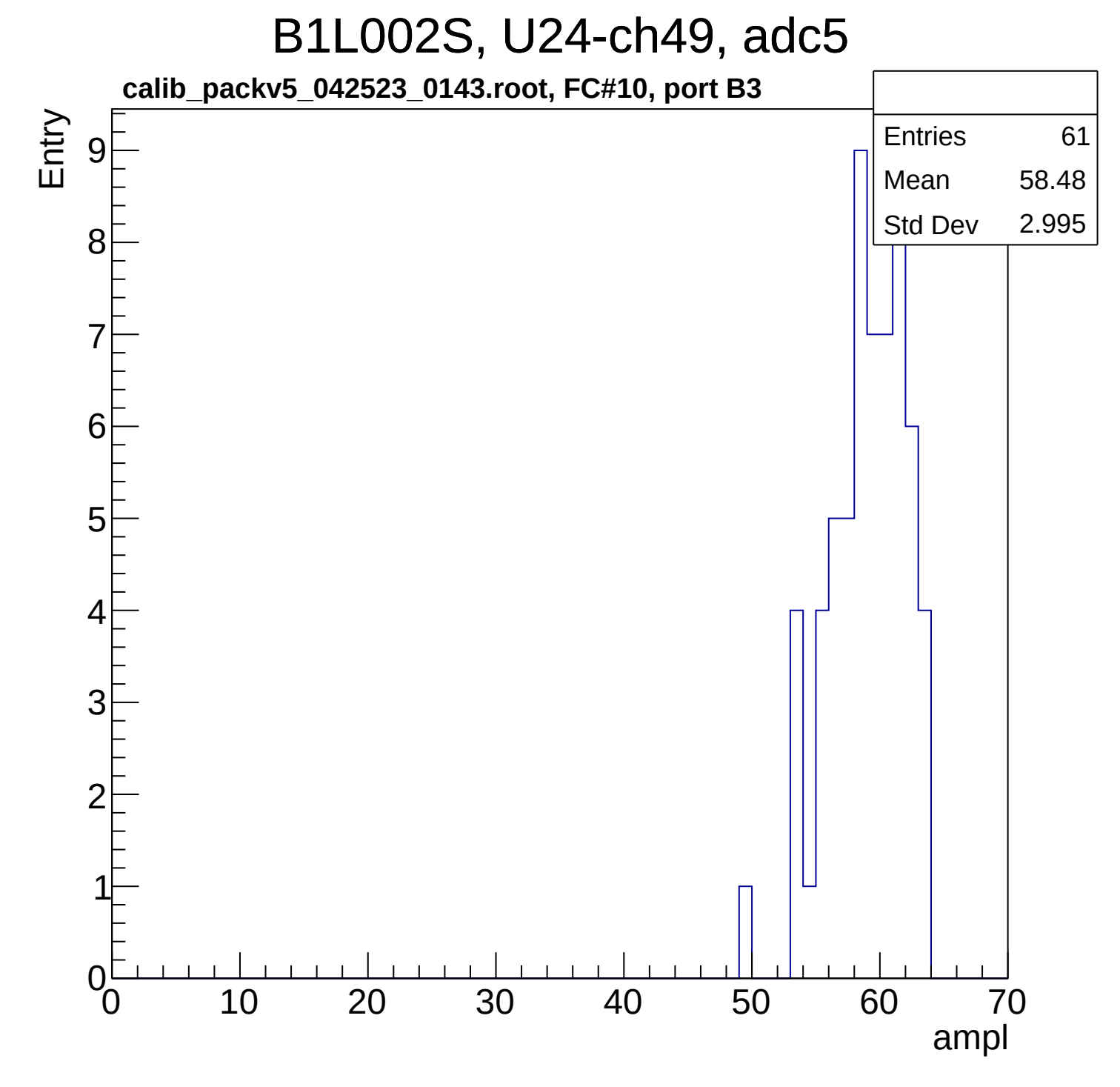
B1L002S, U24-ch49, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 58.48
Std Dev 2.995
Entry
ampl
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2
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4
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6
7
8
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0
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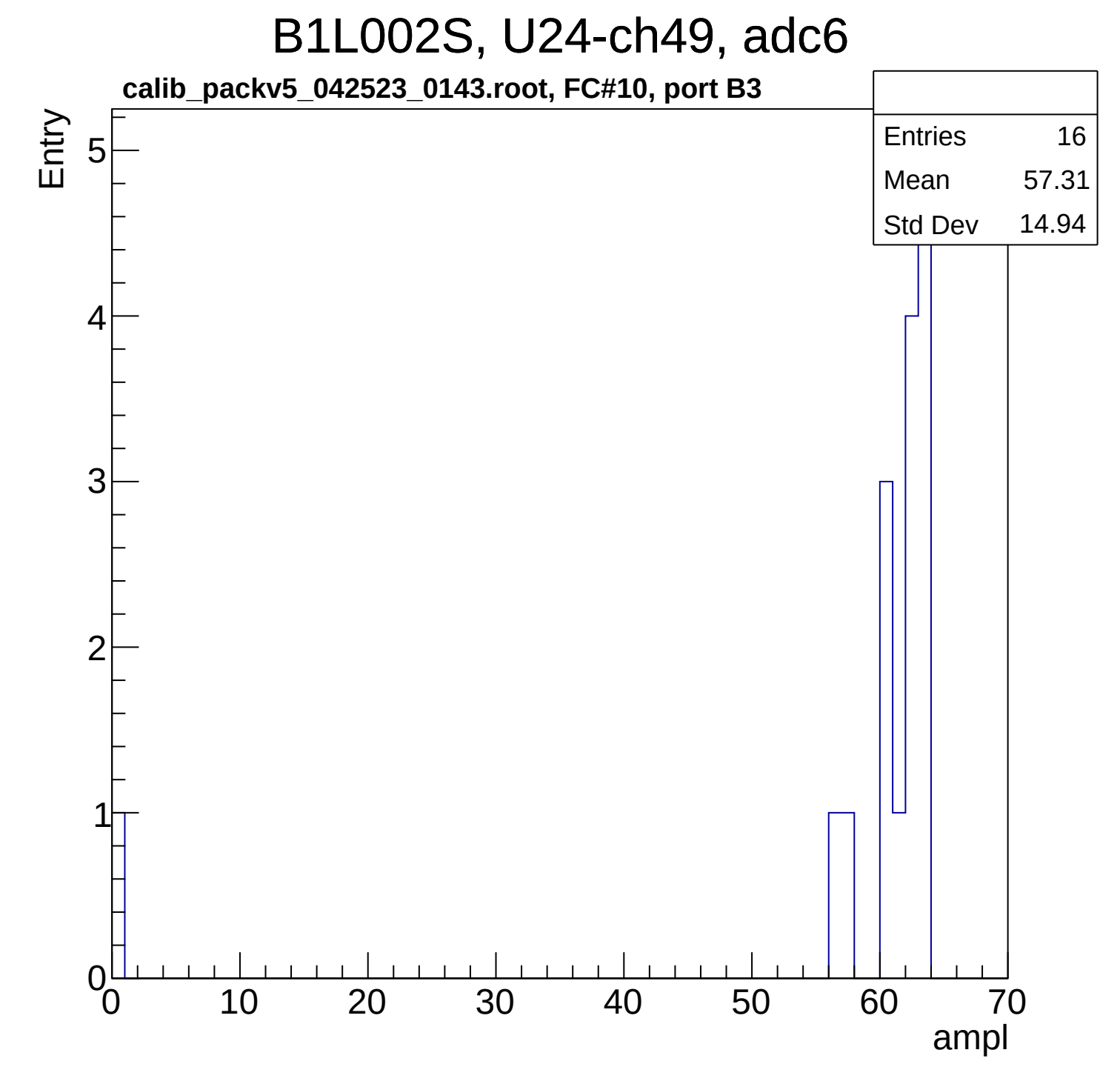
B1L002S, U24-ch49, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.31
Std Dev 14.94
Entry
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3
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5
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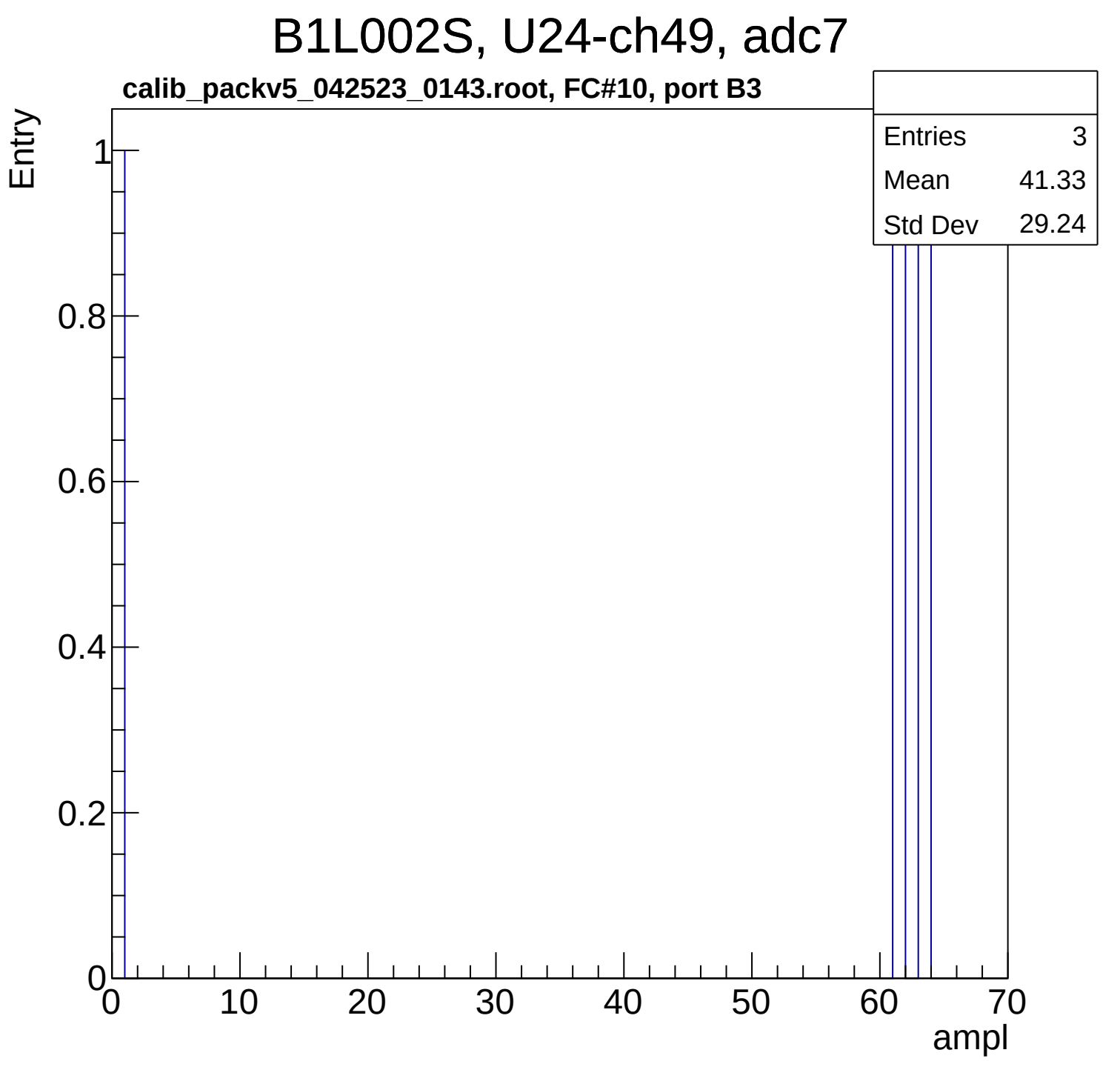
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch49, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.33
Std Dev 29.24
Entry
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0.4
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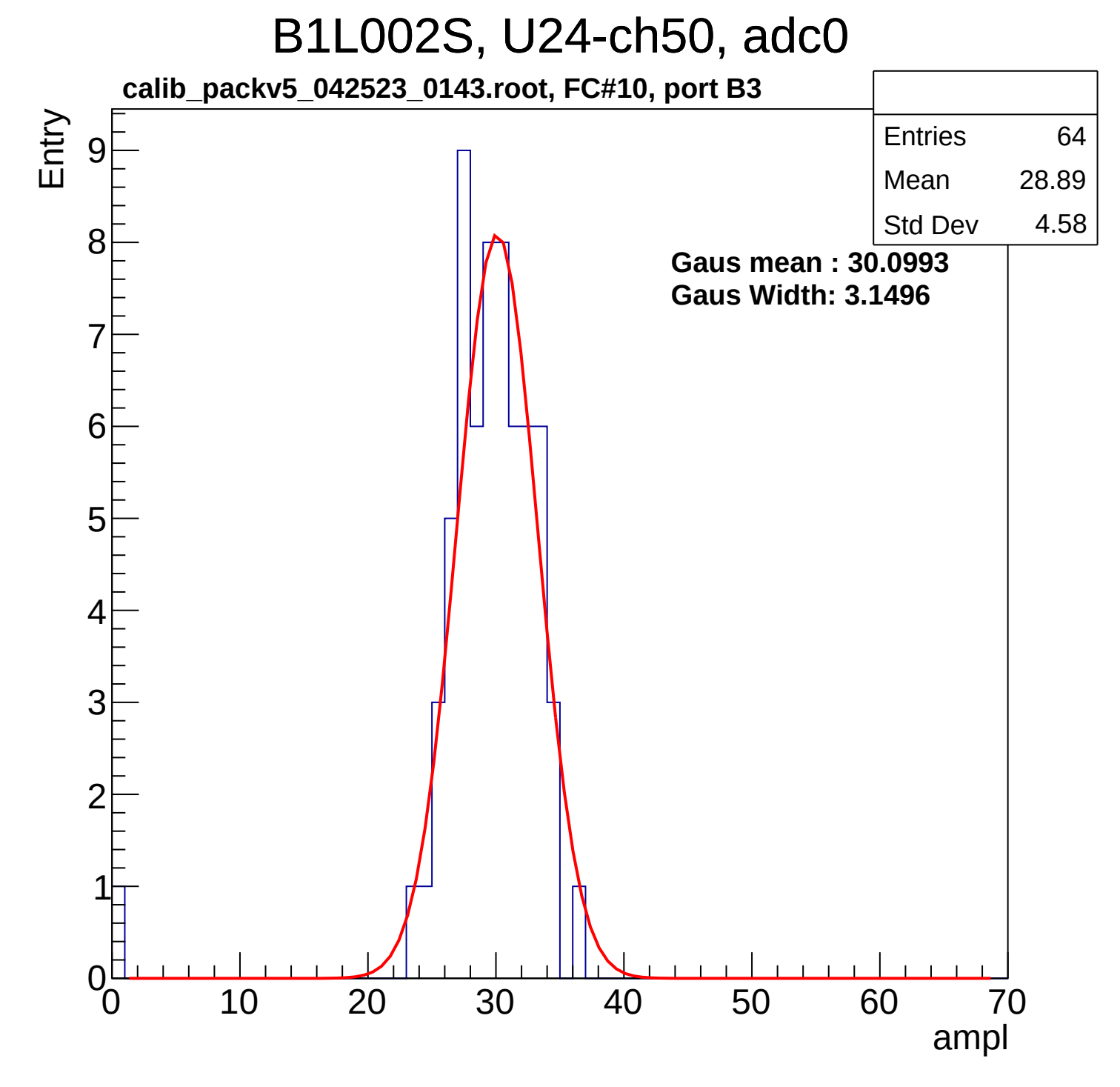
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch50, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.89
Std Dev 4.58
Gaus mean : 30.0993
Gaus Width: 3.1496
Entry
ampl
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2
3
4
5
6
7
8
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0
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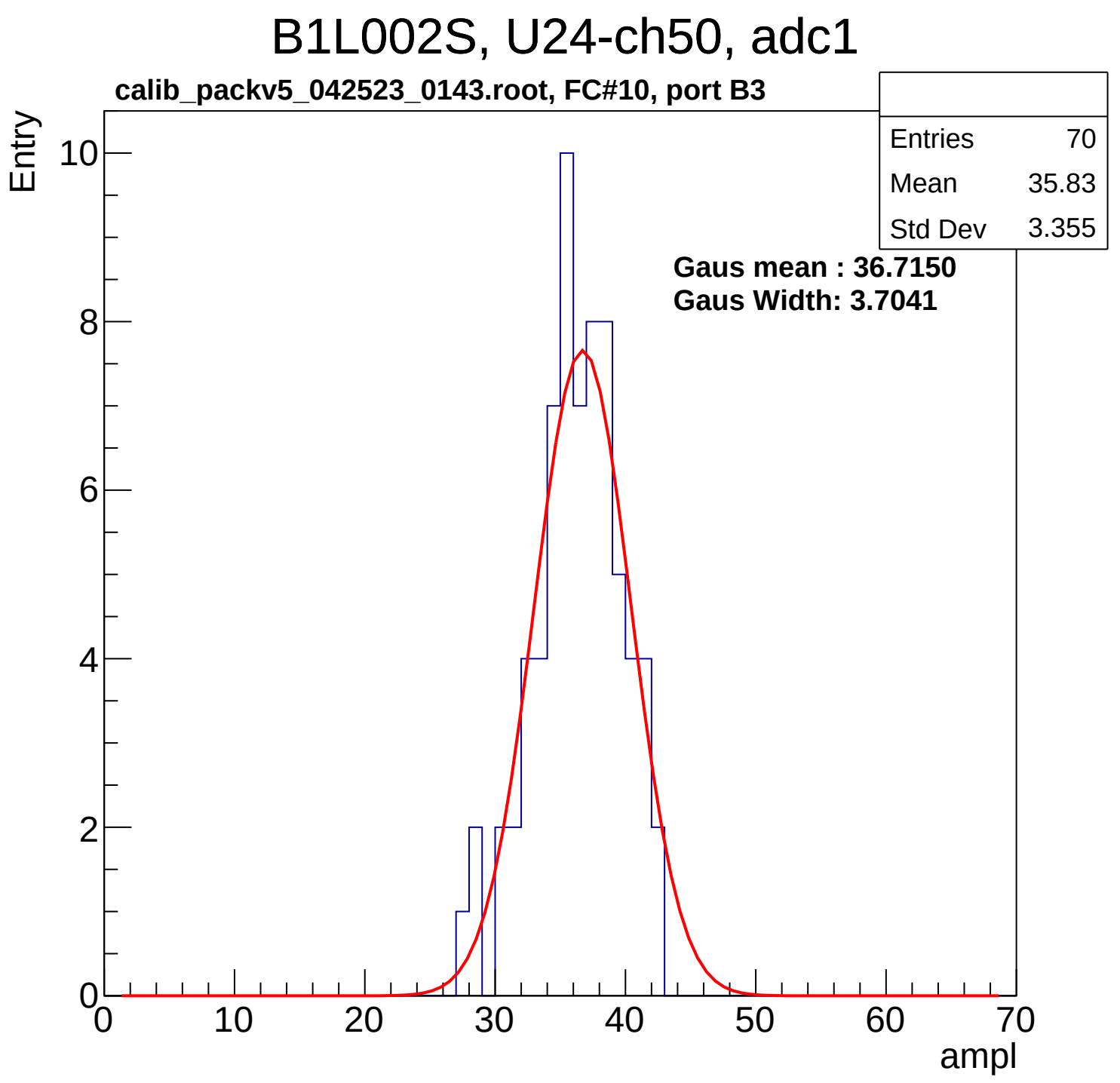
B1L002S, U24-ch50, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.83
Std Dev 3.355
Gaus mean : 36.7150
Gaus Width: 3.7041
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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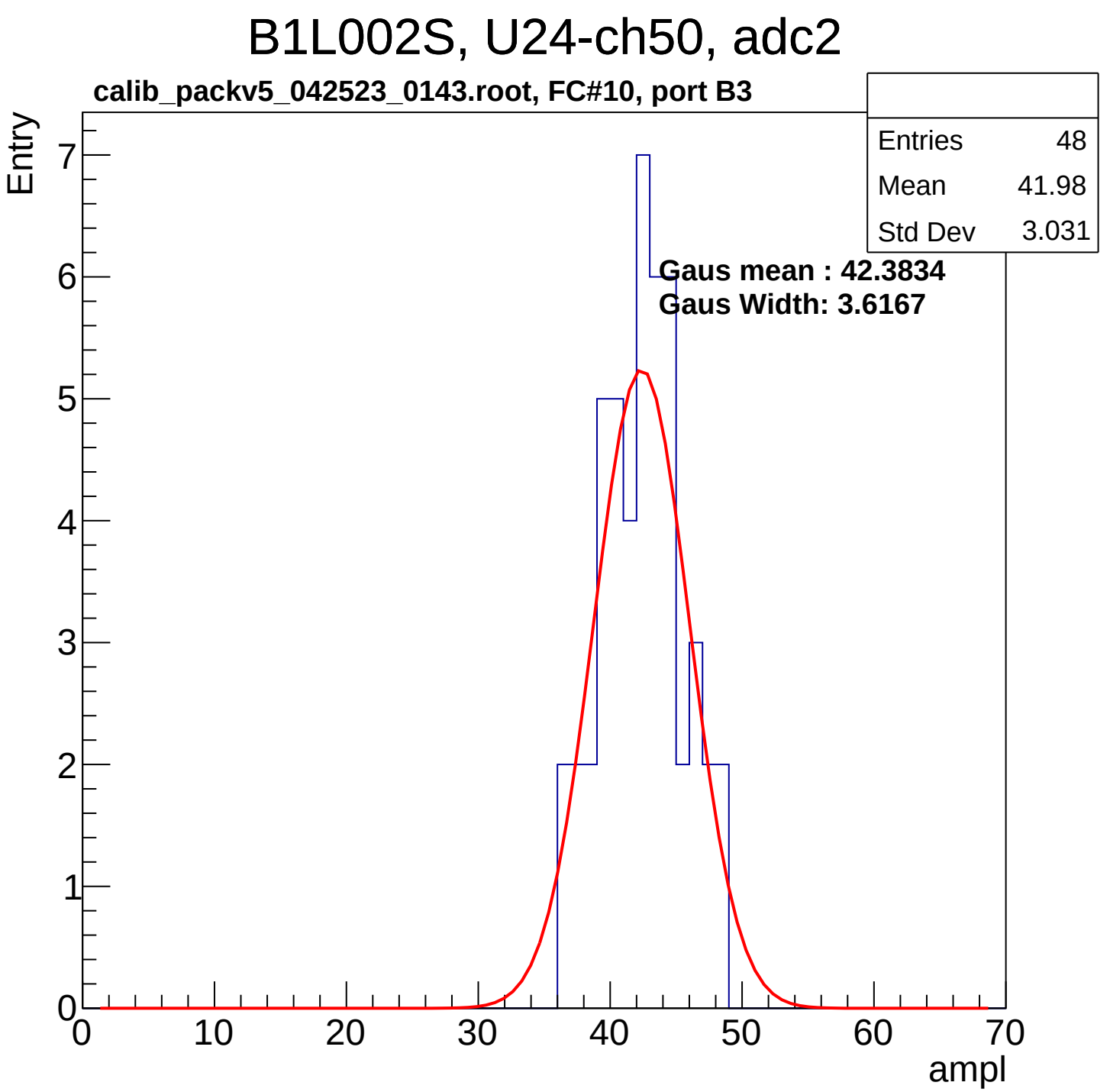
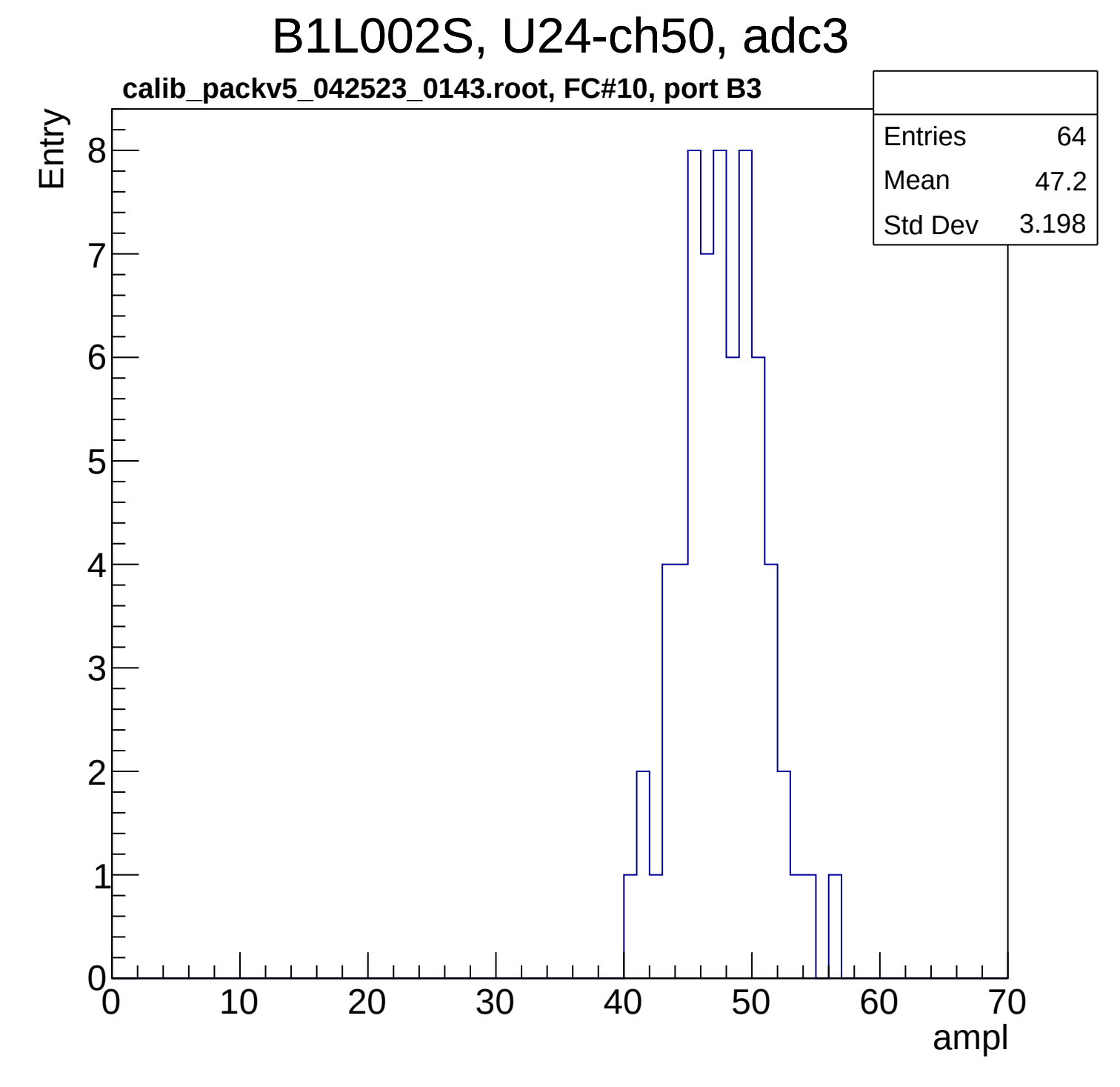

B1L002S, U24-ch50, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.98
Std Dev 3.031
Gaus mean : 42.3834
Gaus Width: 3.6167
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch50, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.2
Std Dev 3.198
Entry
ampl
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2
3
4
5
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0
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40
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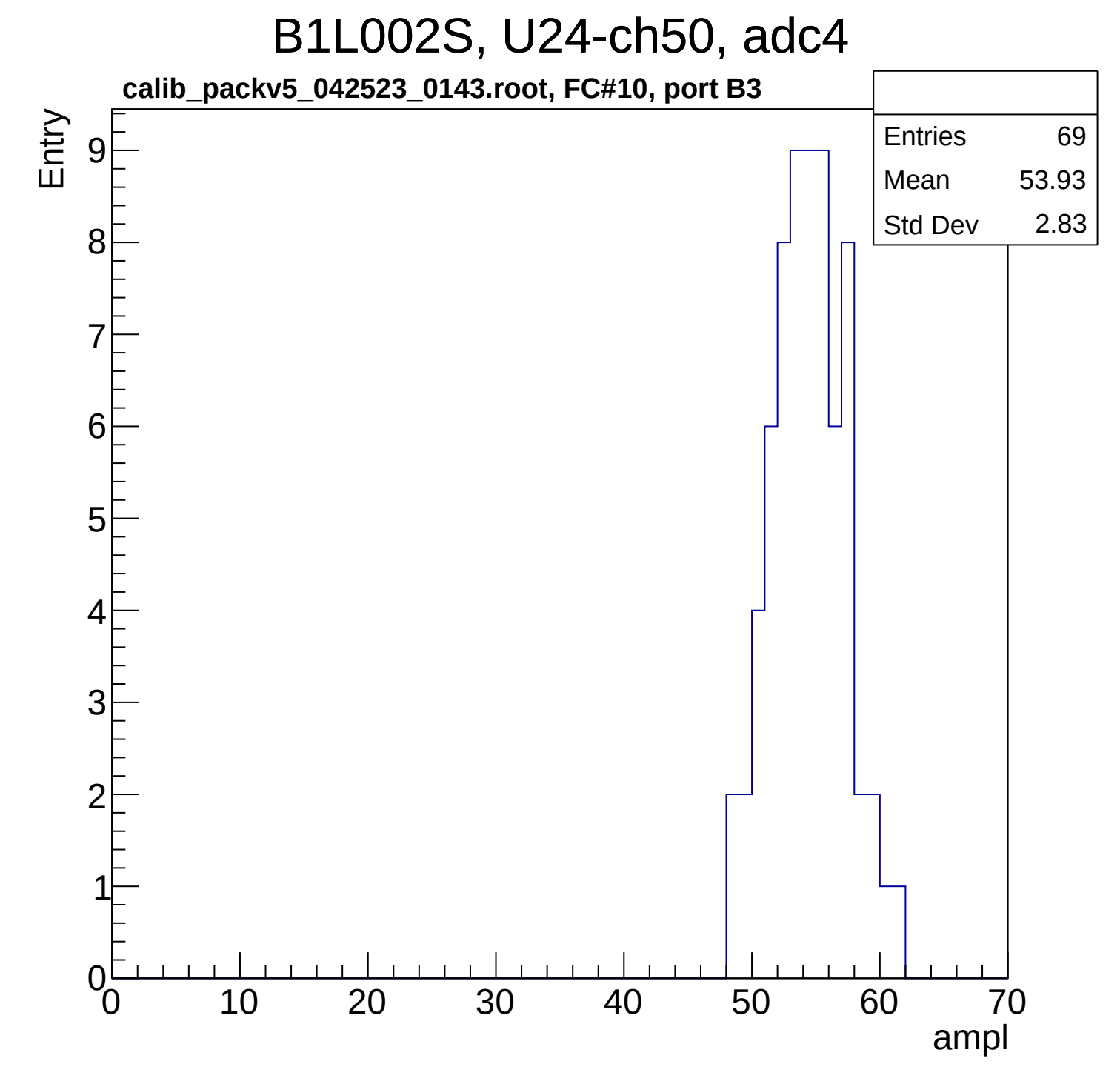
B1L002S, U24-ch50, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.93
Std Dev 2.83
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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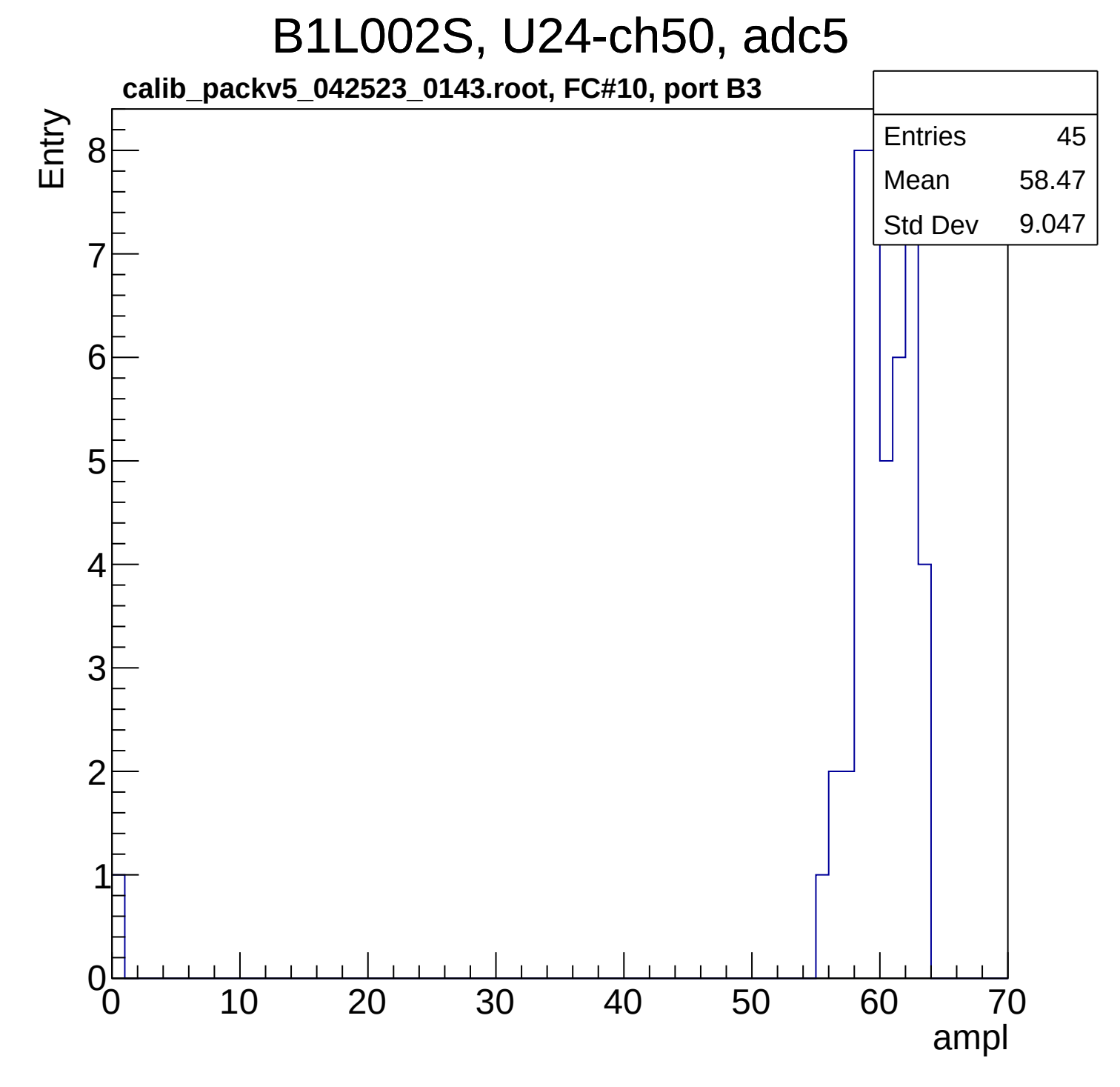
B1L002S, U24-ch50, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 58.47
Std Dev 9.047
Entry
ampl
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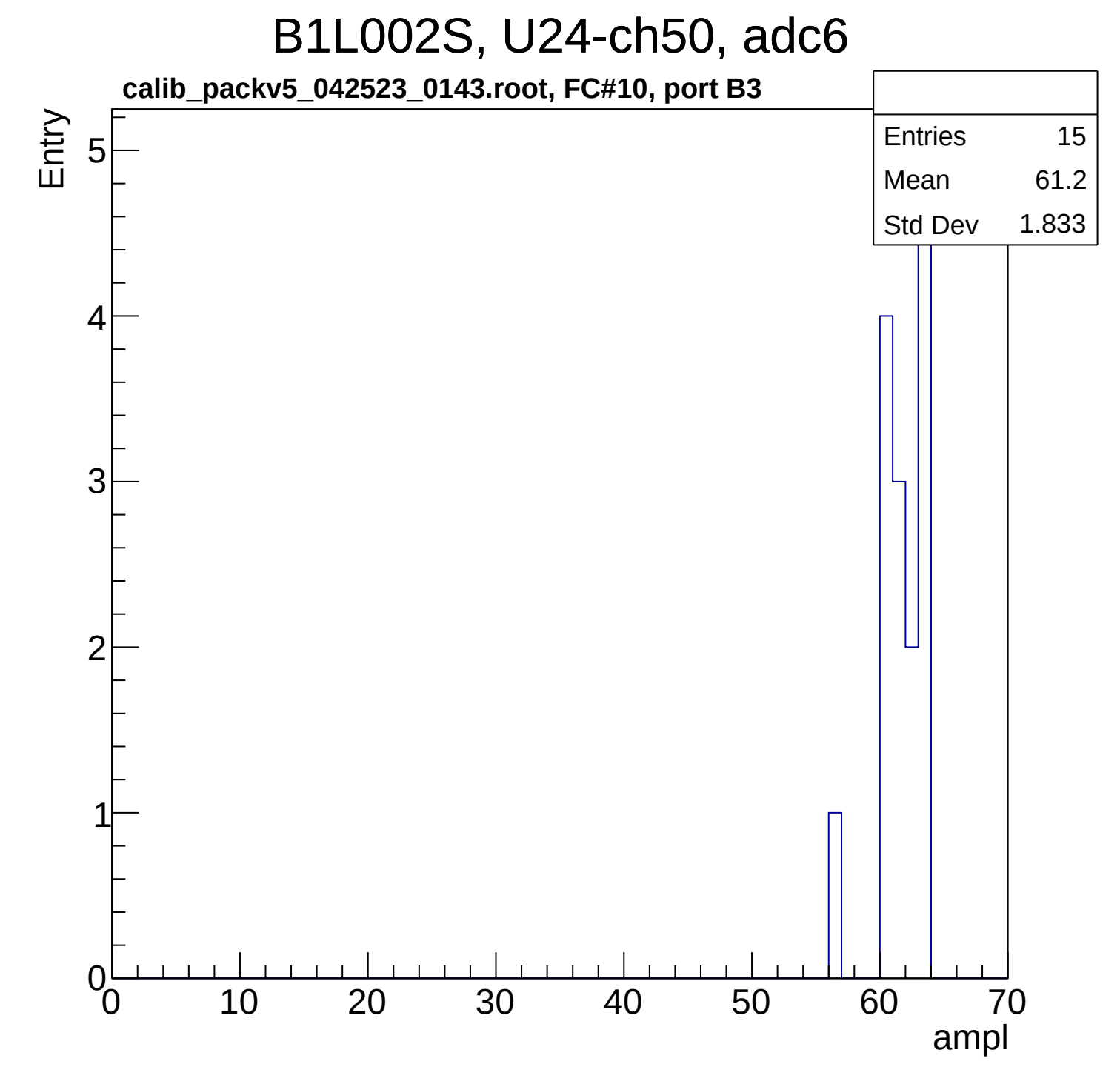
B1L002S, U24-ch50, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.2
Std Dev 1.833
Entry
0
1
2
3
4
5
0
10
20
30
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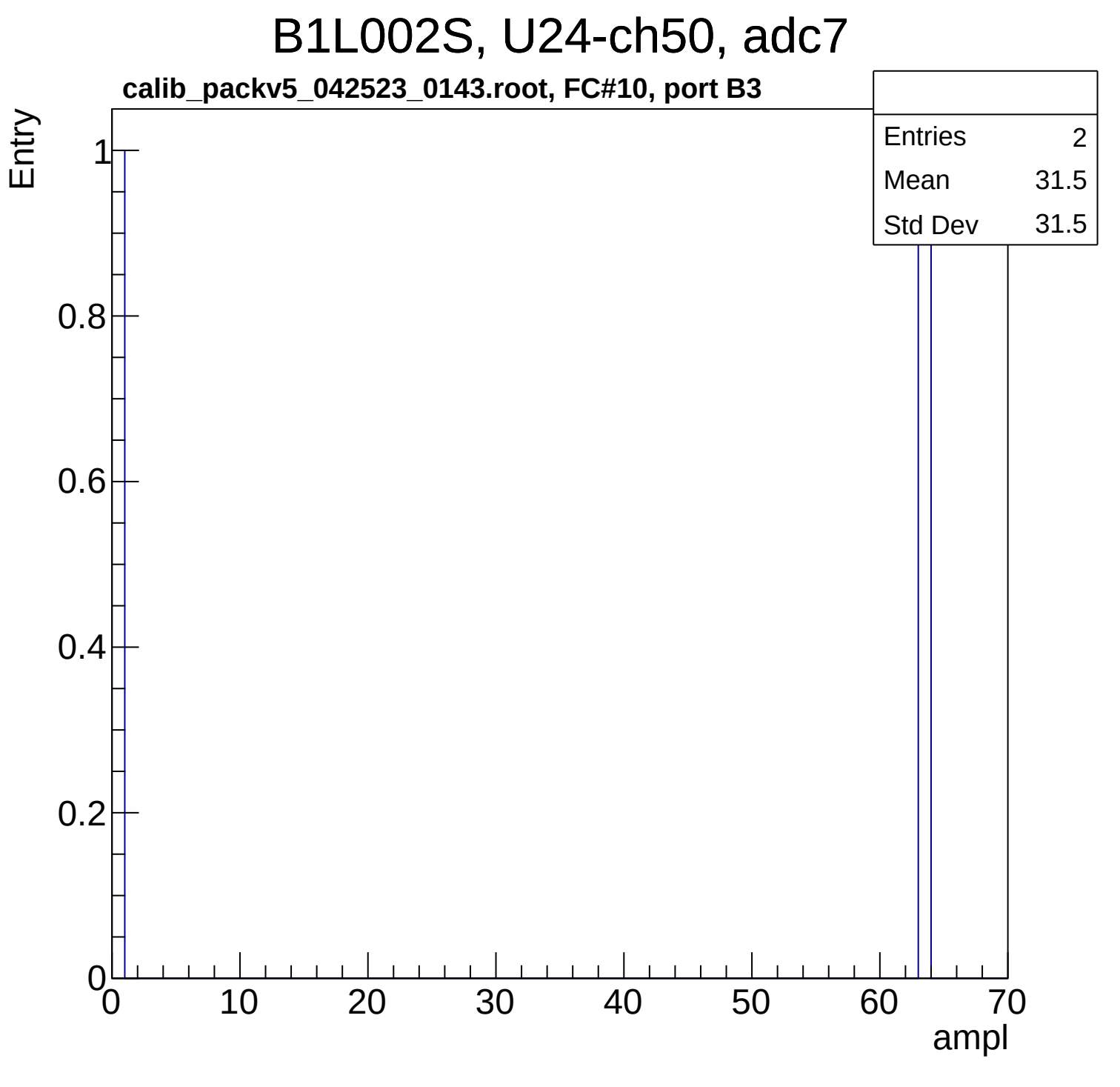
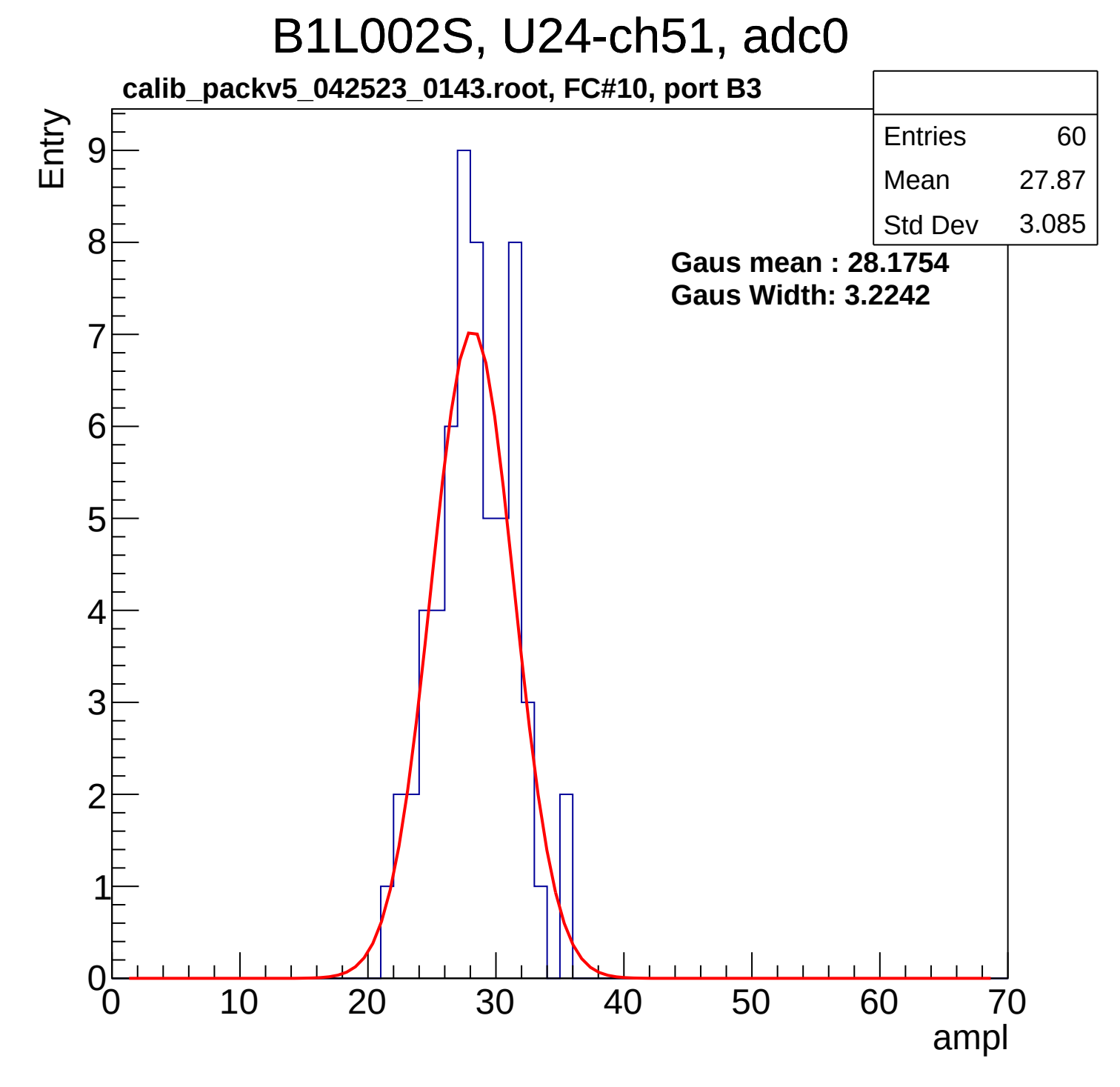
60
70
ampl


B1L002S, U24-ch50, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch51, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.87
Std Dev 3.085
Gaus mean : 28.1754
Gaus Width: 3.2242
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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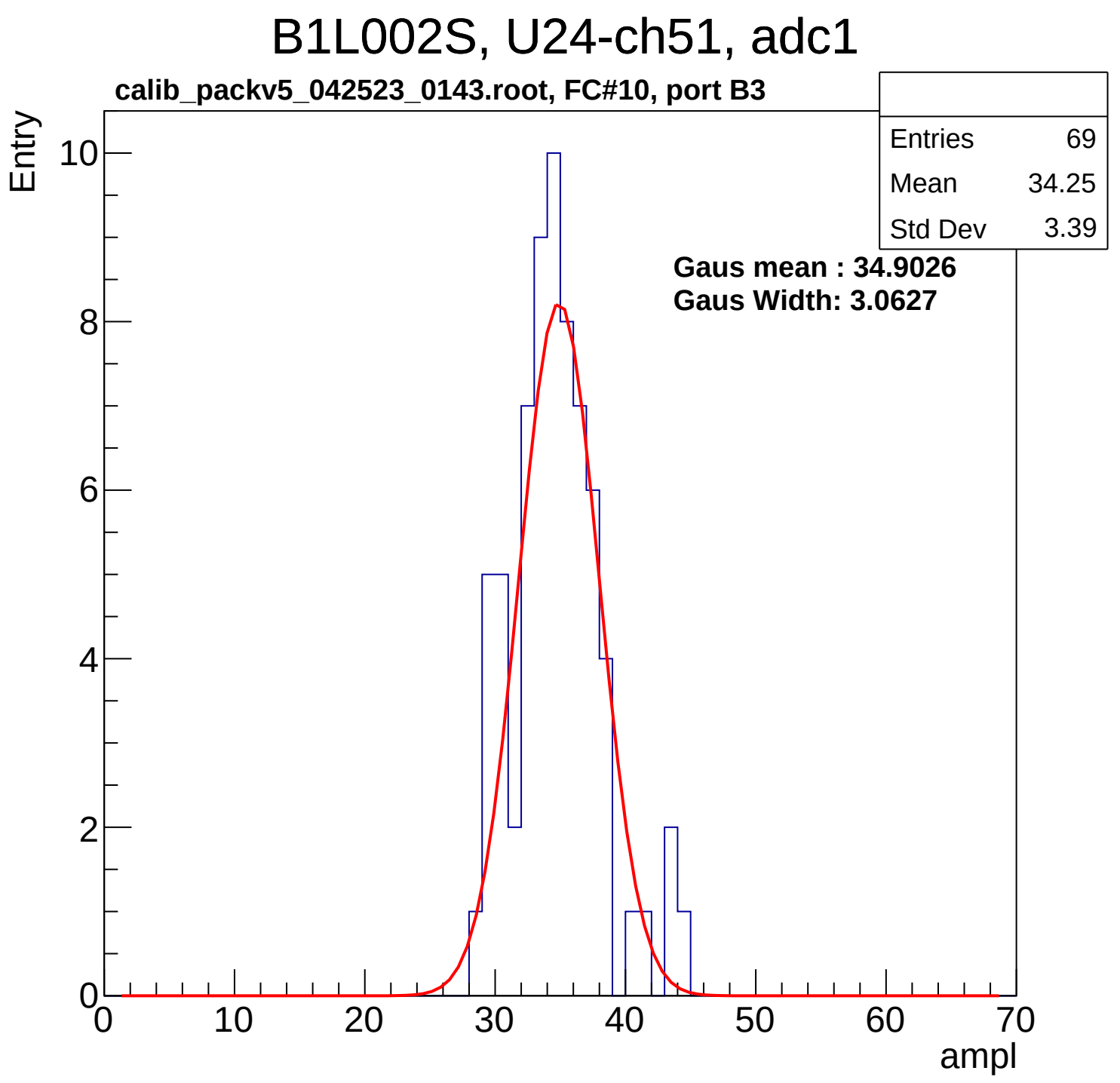
B1L002S, U24-ch51, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 34.25
Std Dev 3.39
Gaus mean : 34.9026
Gaus Width: 3.0627
Entry
ampl
0
2
4
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8
10
0
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40
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60
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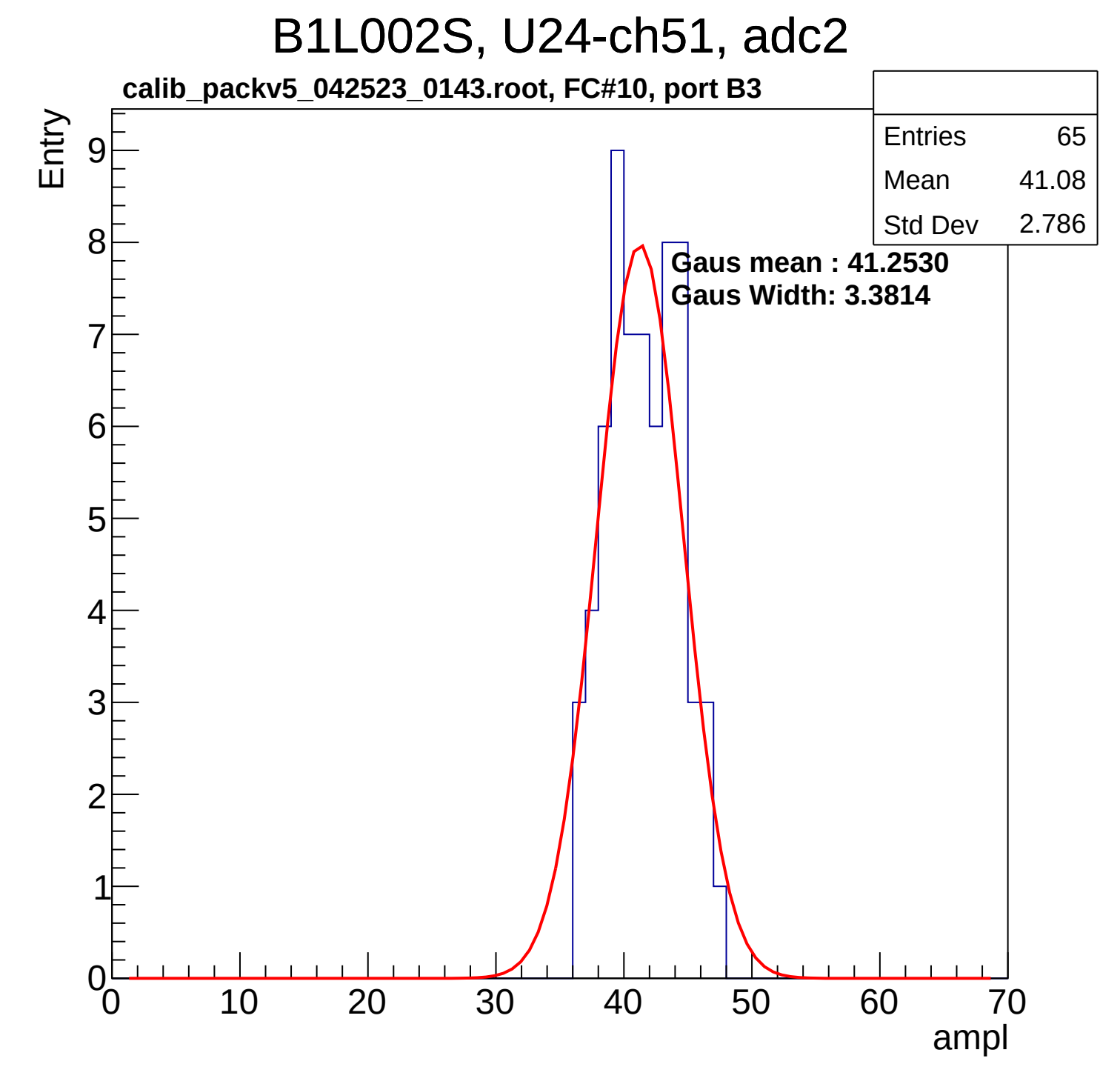
B1L002S, U24-ch51, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.08
Std Dev 2.786
Gaus mean : 41.2530
Gaus Width: 3.3814
Entry
ampl
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2
3
4
5
6
7
8
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0
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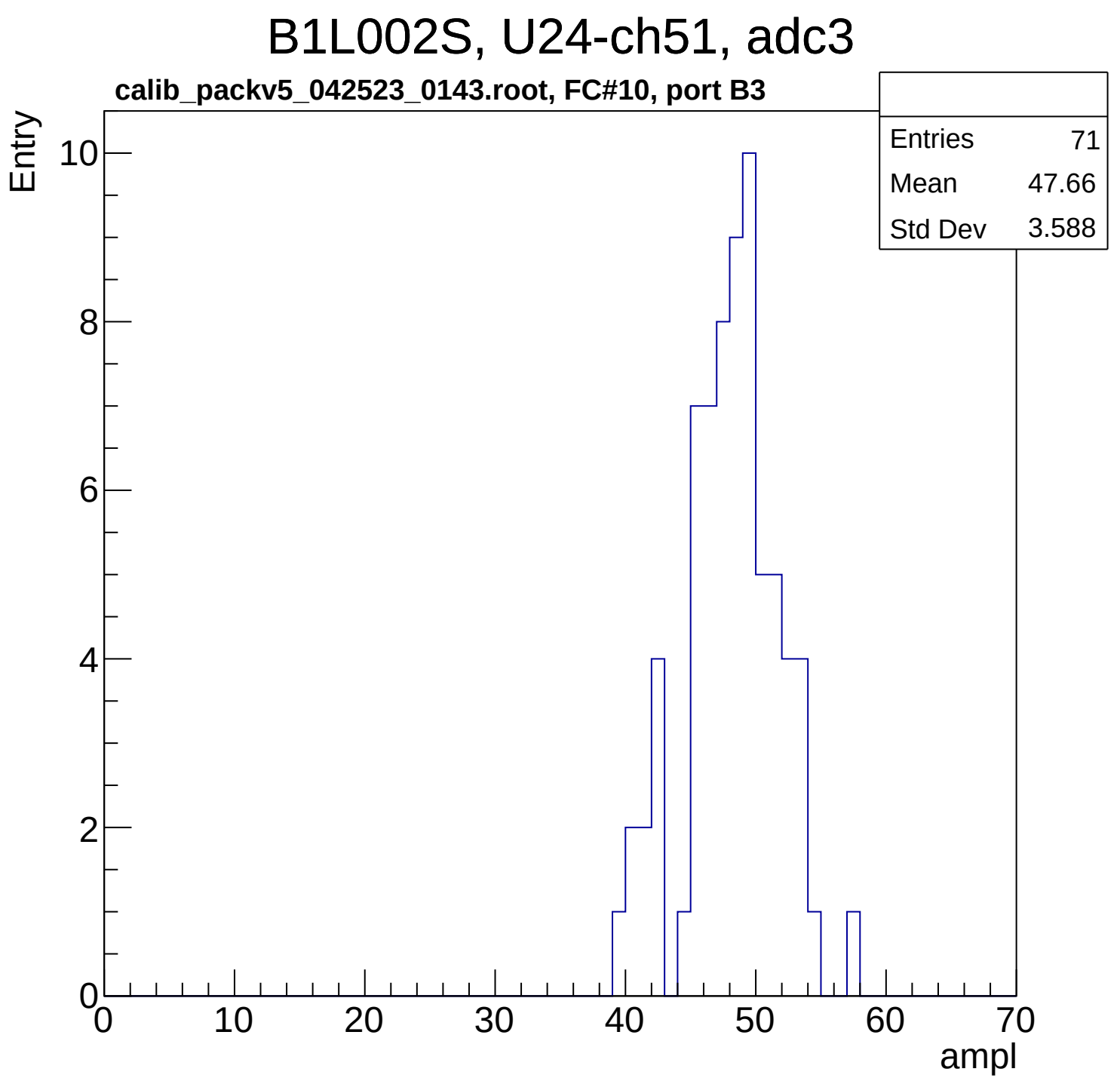
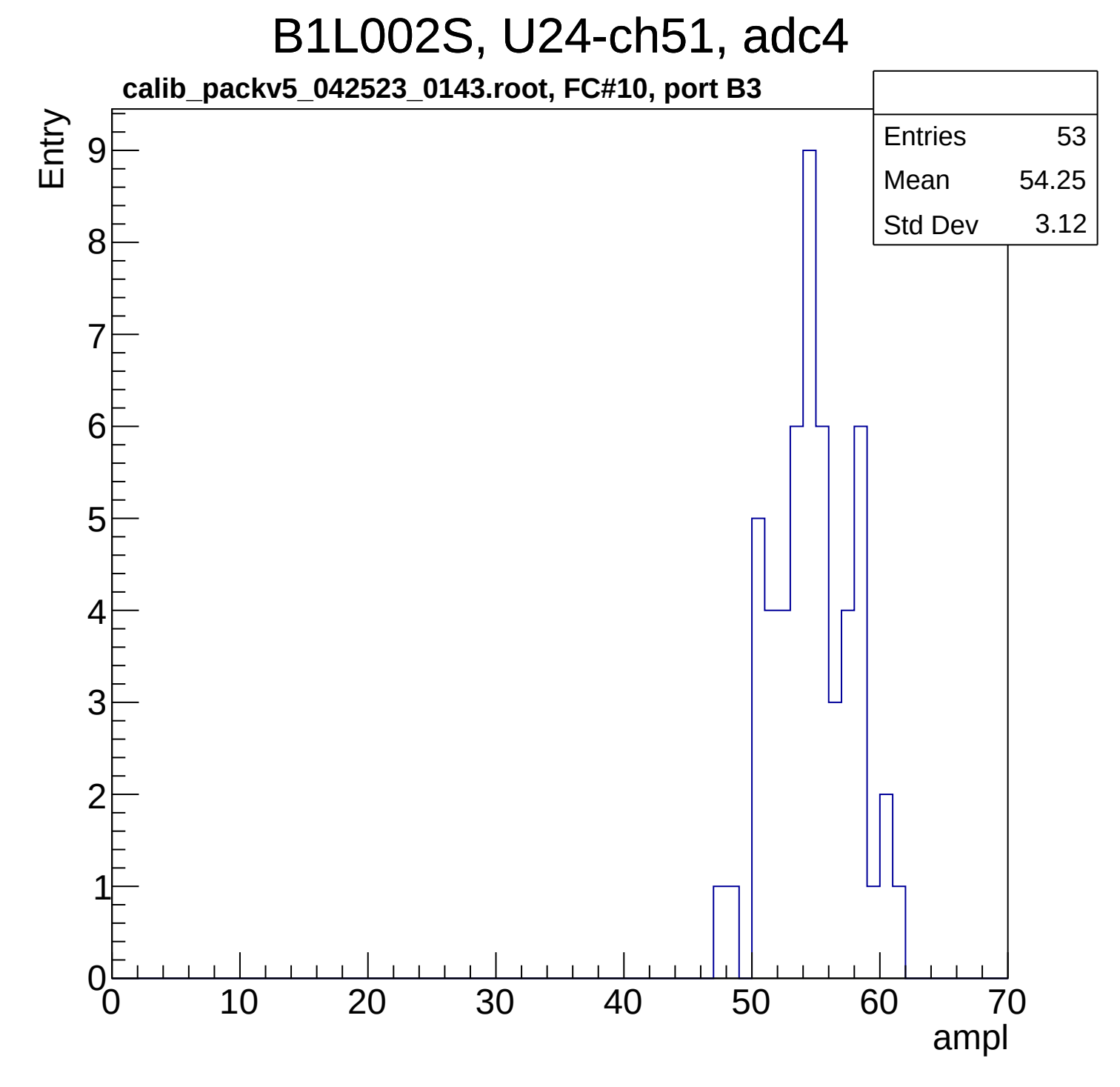
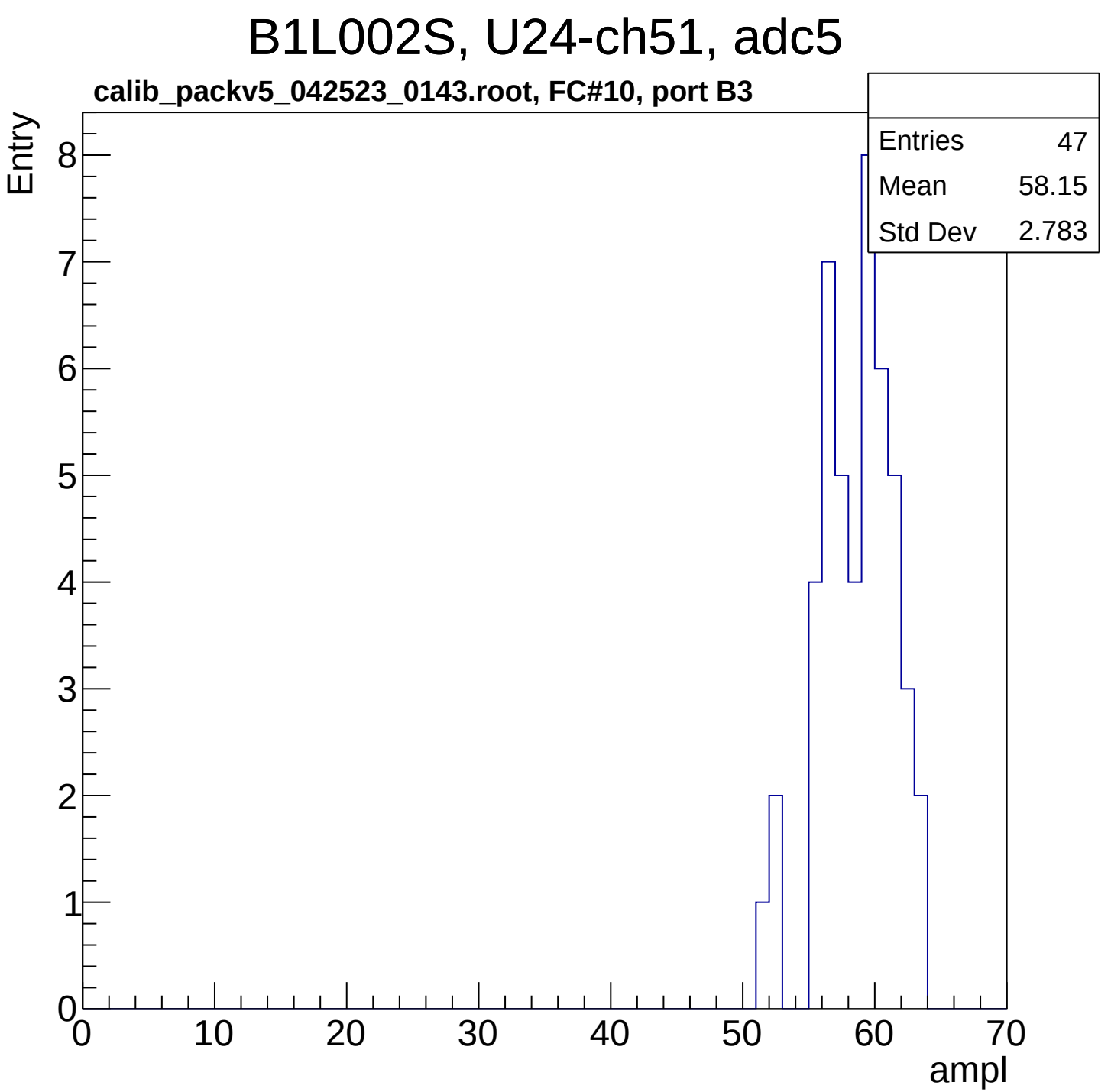

B1L002S, U24-ch51, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 47.66
Std Dev 3.588
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch51, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 54.25
Std Dev 3.12
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch51, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.15
Std Dev 2.783
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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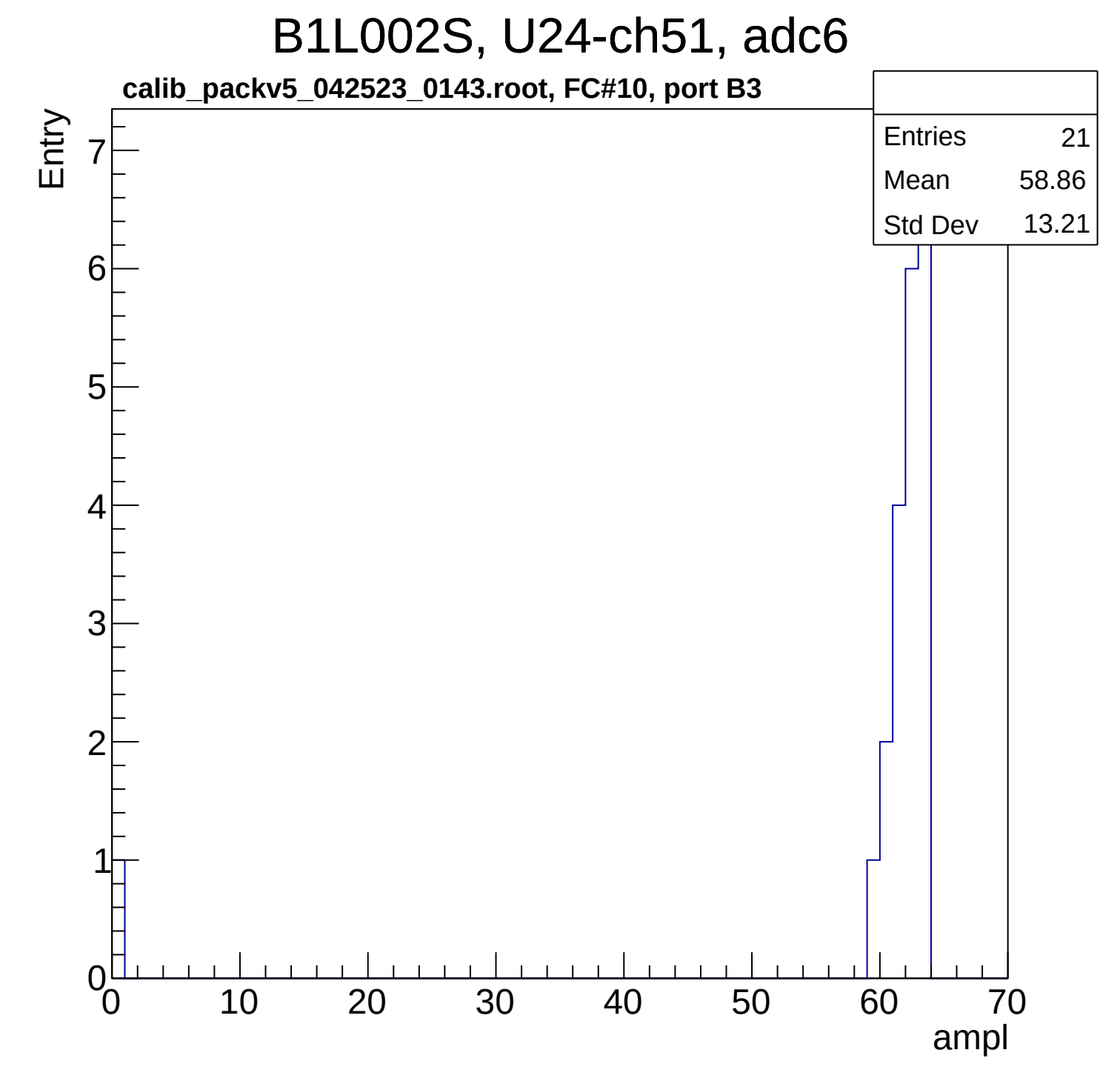
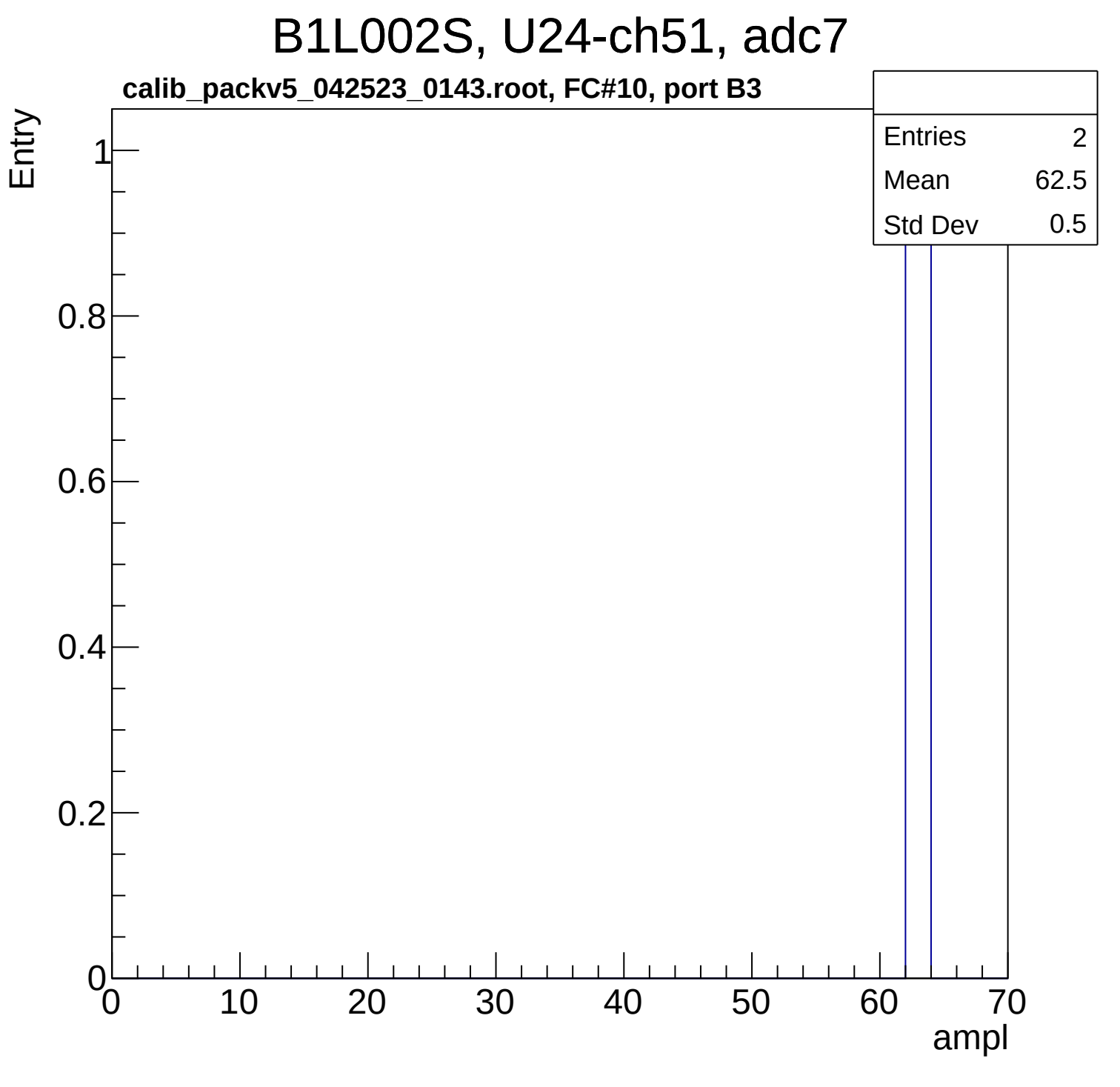
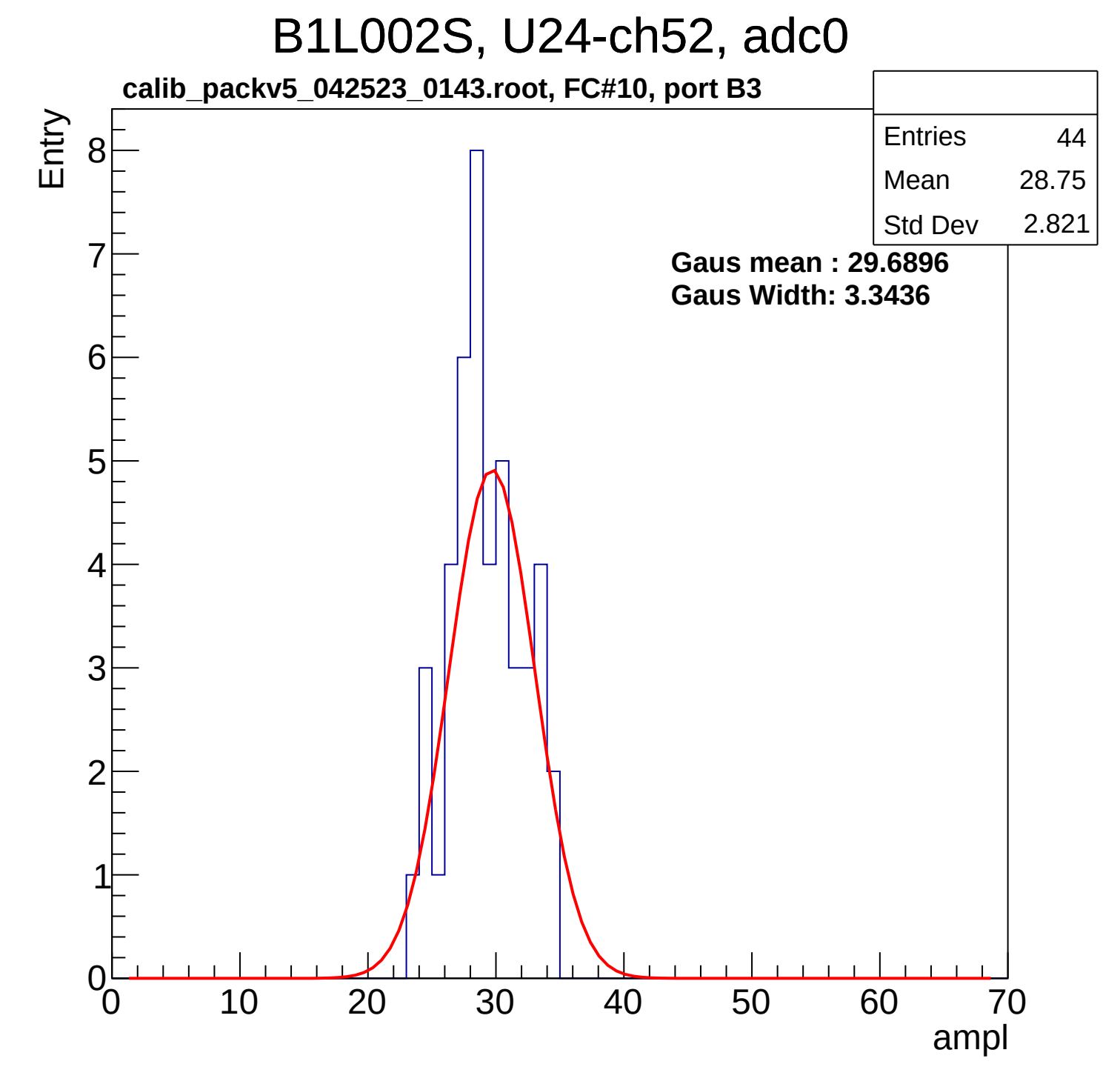

B1L002S, U24-ch51, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.86
Std Dev 13.21
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch51, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch52, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 28.75
Std Dev 2.821
Gaus mean : 29.6896
Gaus Width: 3.3436
Entry
ampl
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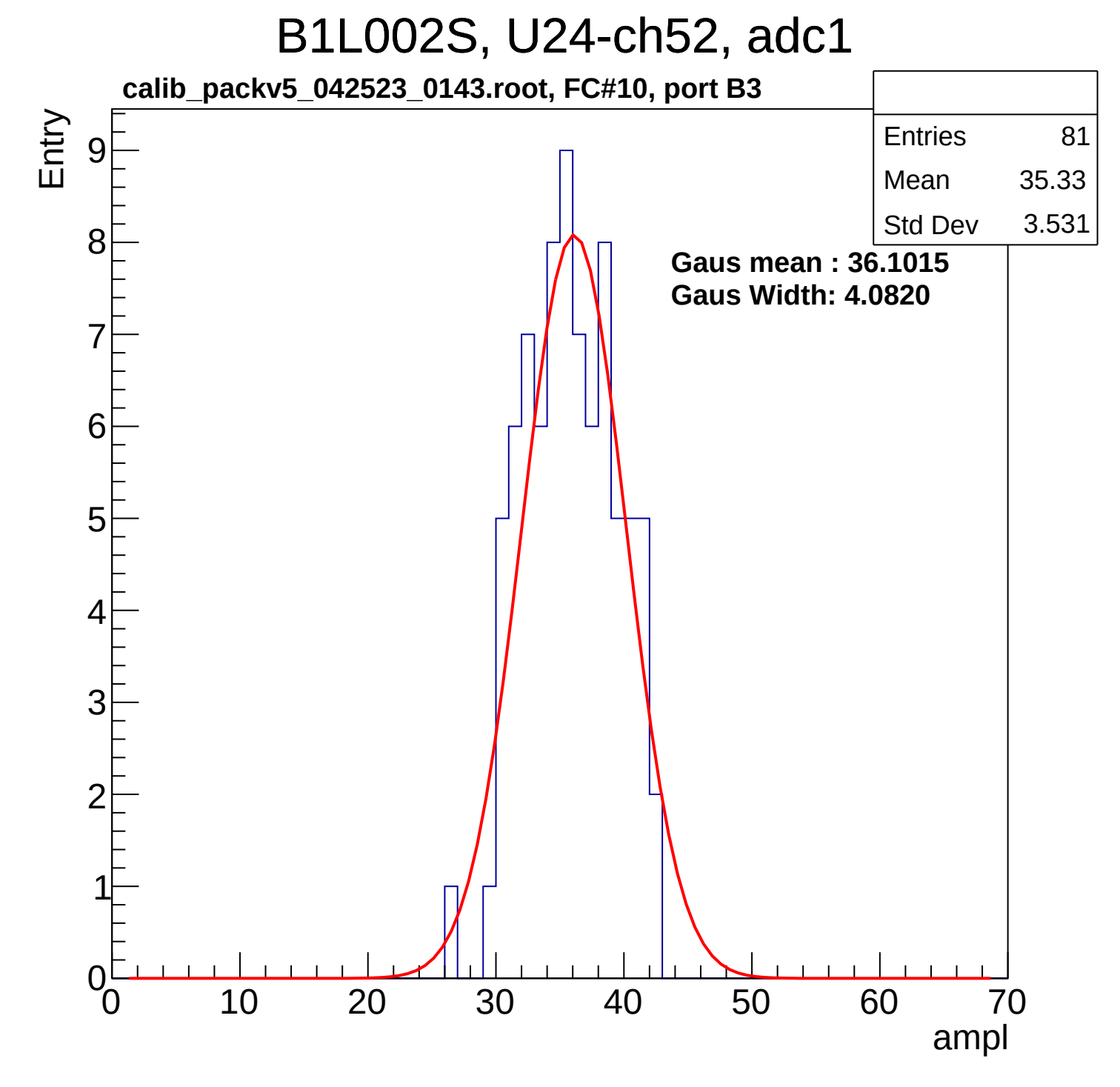
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch52, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.33
Std Dev 3.531
Gaus mean : 36.1015
Gaus Width: 4.0820
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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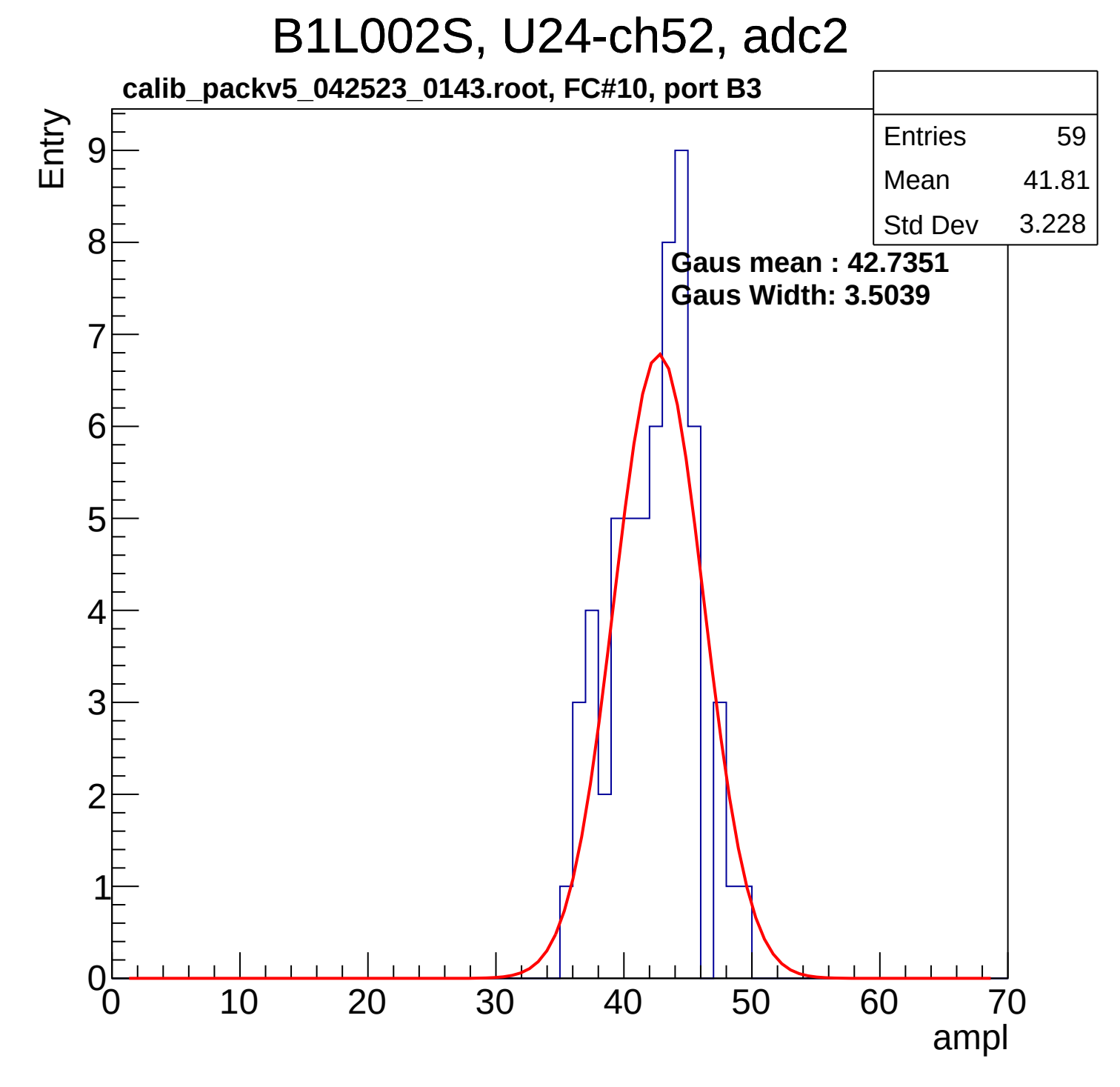
B1L002S, U24-ch52, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.81
Std Dev 3.228
Gaus mean : 42.7351
Gaus Width: 3.5039
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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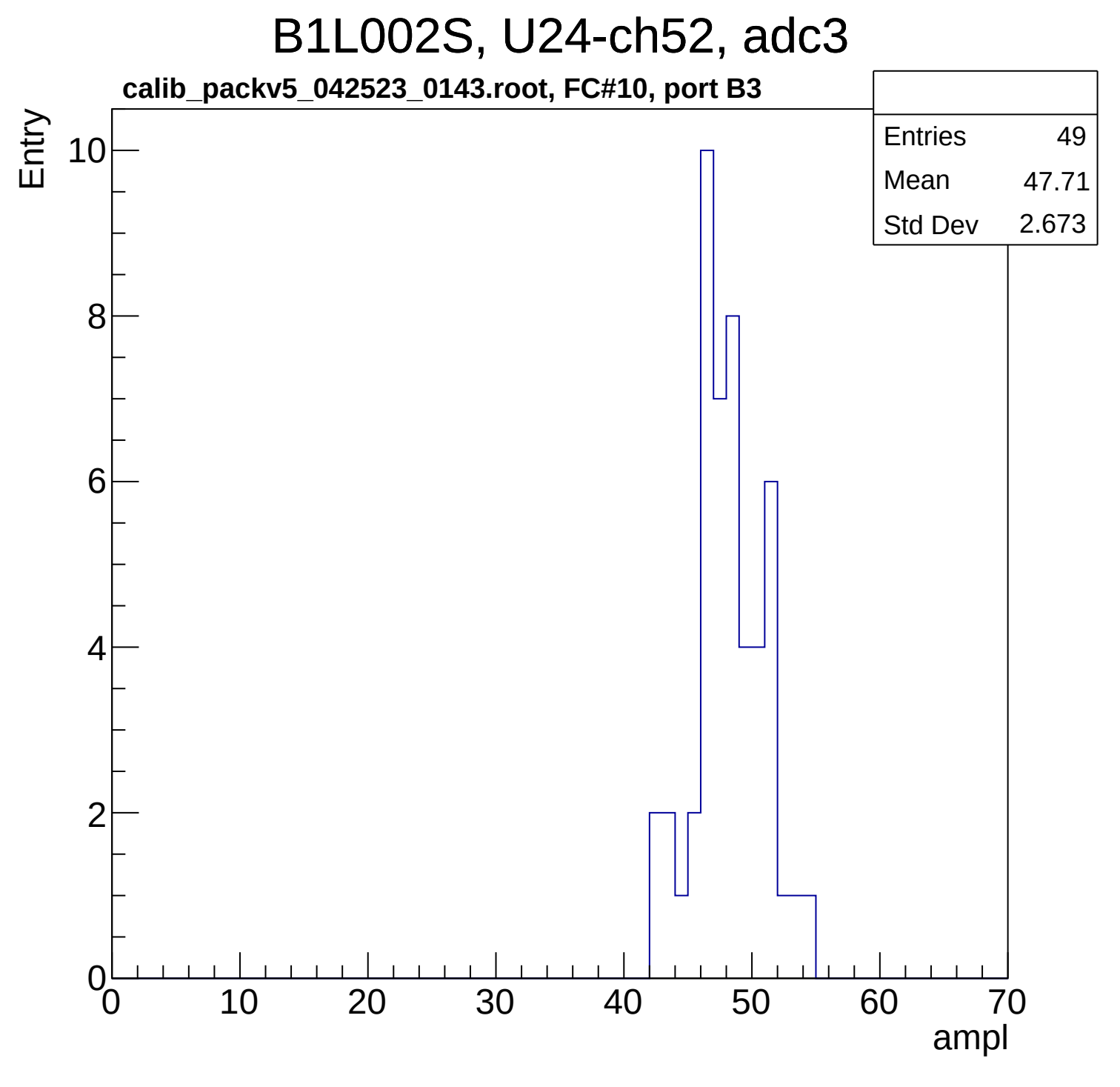
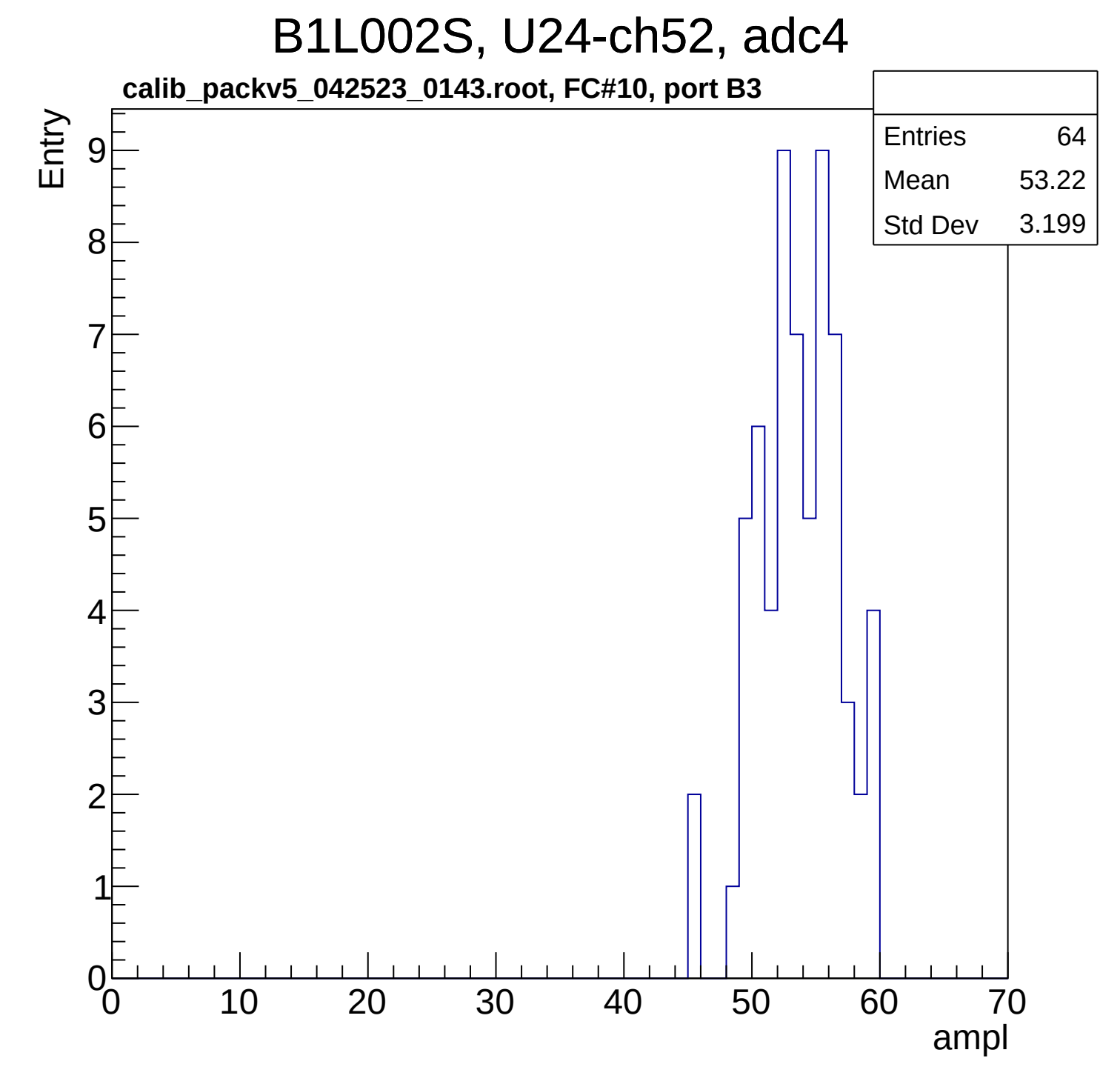

B1L002S, U24-ch52, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 47.71
Std Dev 2.673
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch52, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.22
Std Dev 3.199
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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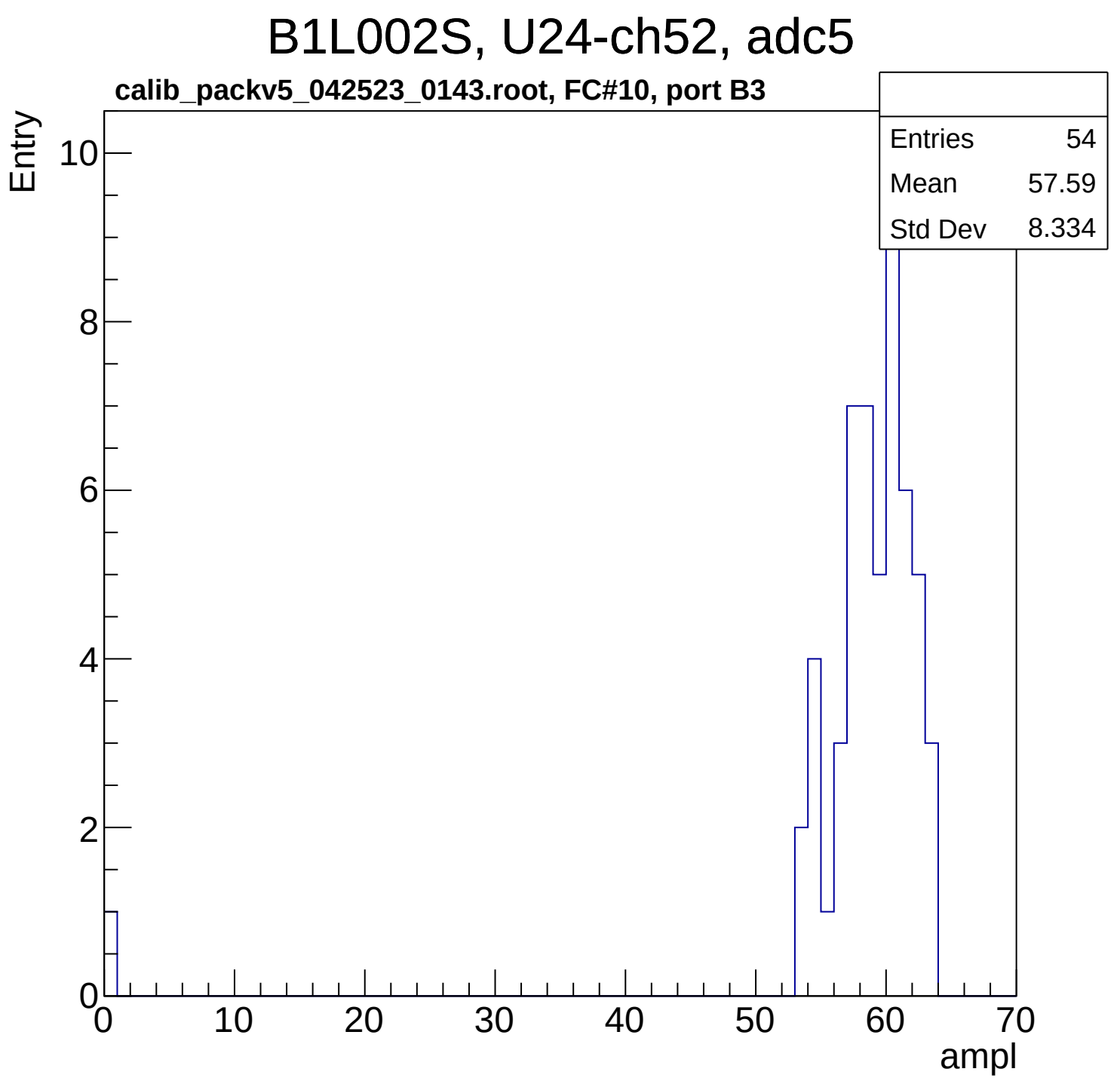
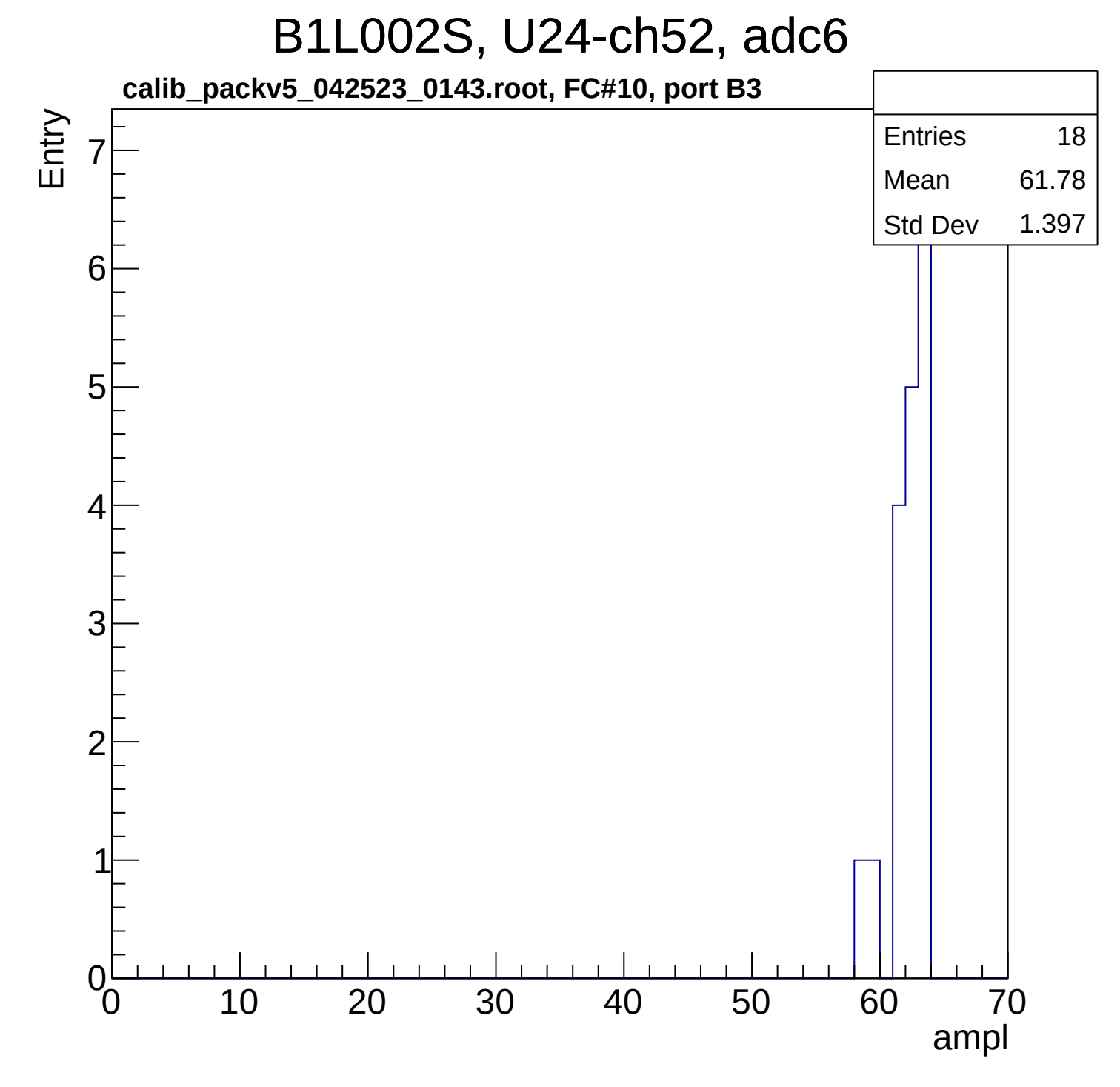

B1L002S, U24-ch52, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.59
Std Dev 8.334
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch52, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.78
Std Dev 1.397
Entry
ampl
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1
2
3
4
5
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7
0
10
20
30
40
50
60
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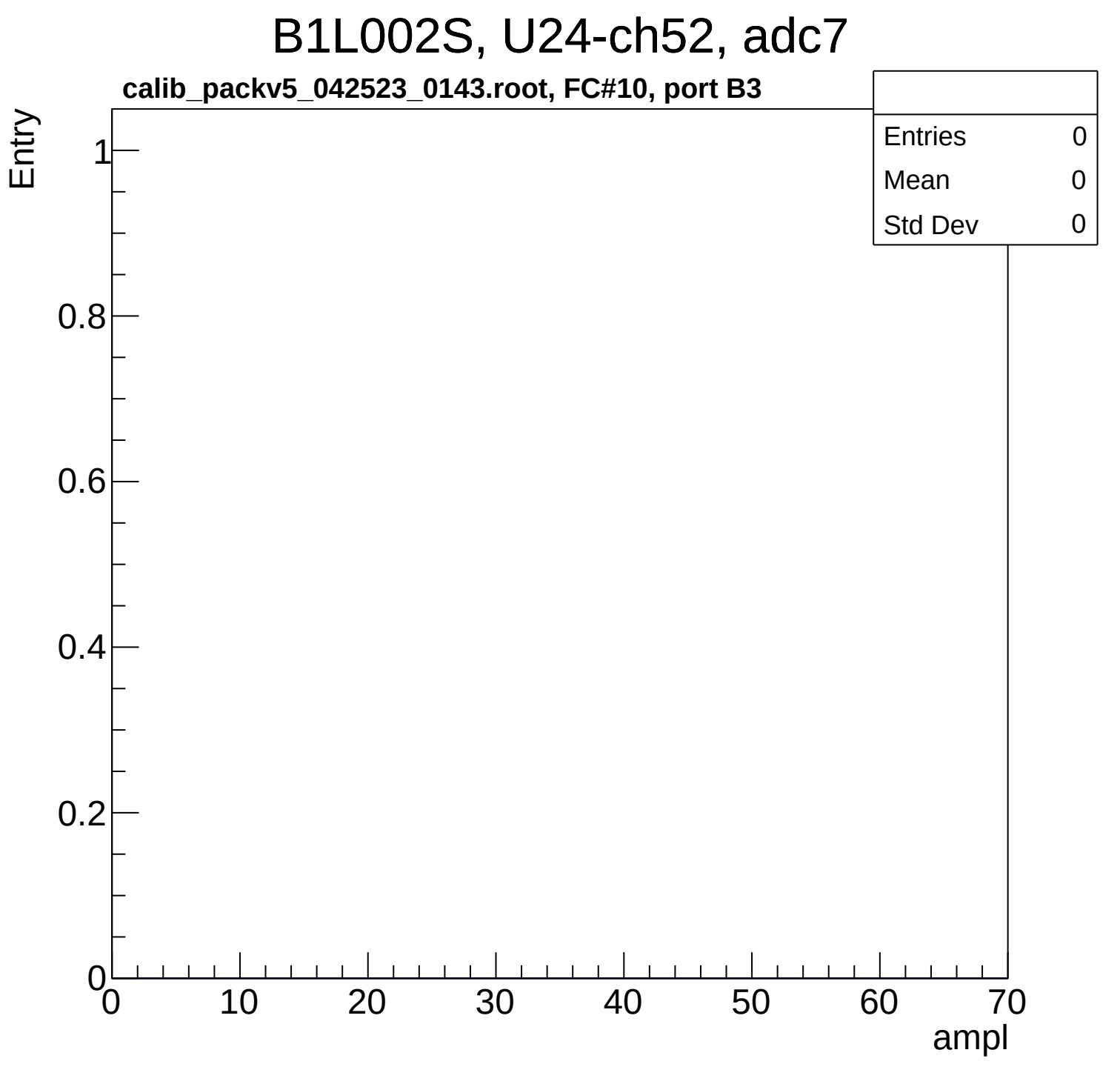
B1L002S, U24-ch52, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
0
10
20
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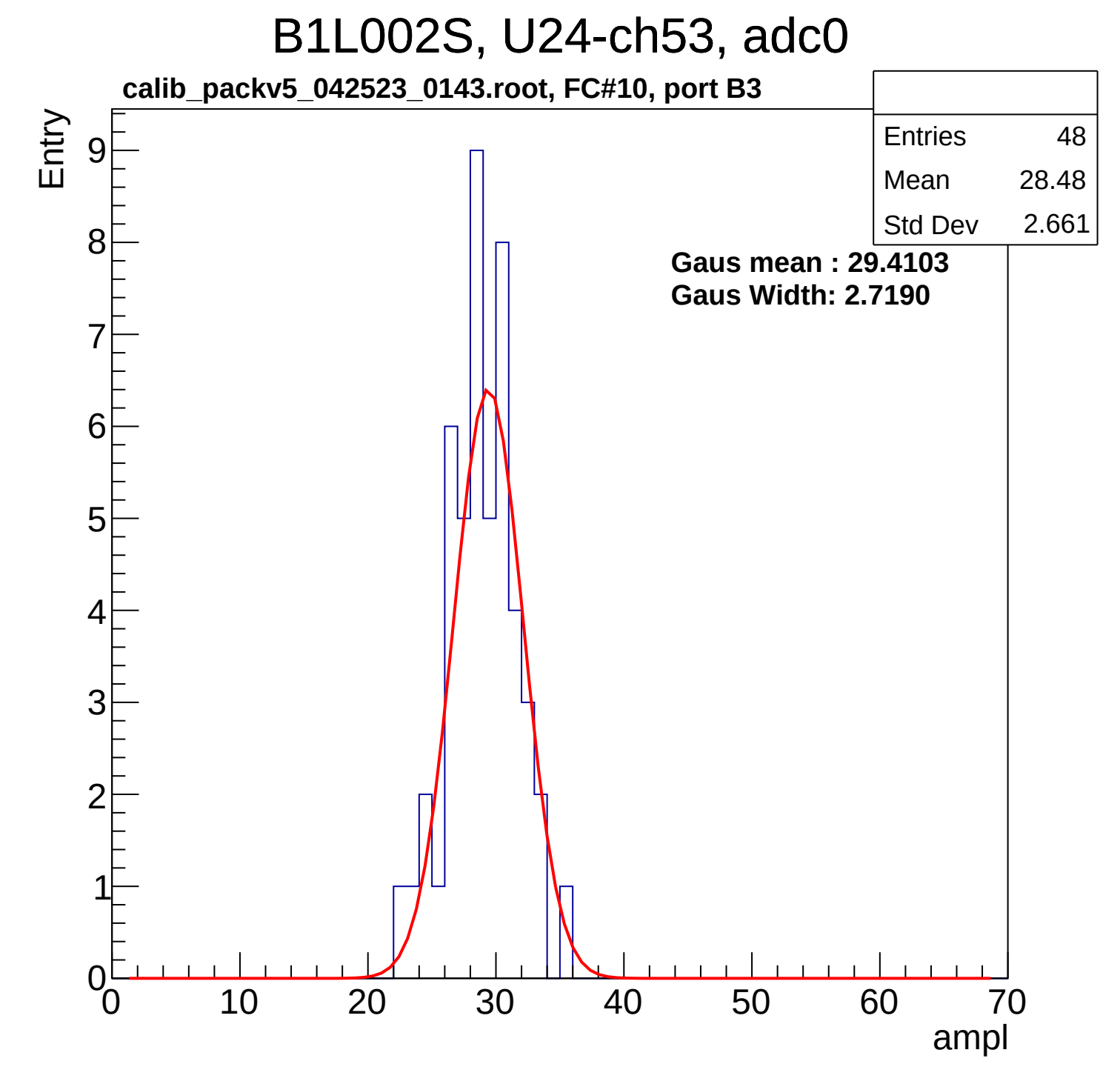
40
50
60
70
ampl


B1L002S, U24-ch53, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 28.48
Std Dev 2.661
Gaus mean : 29.4103
Gaus Width: 2.7190
Entry
ampl
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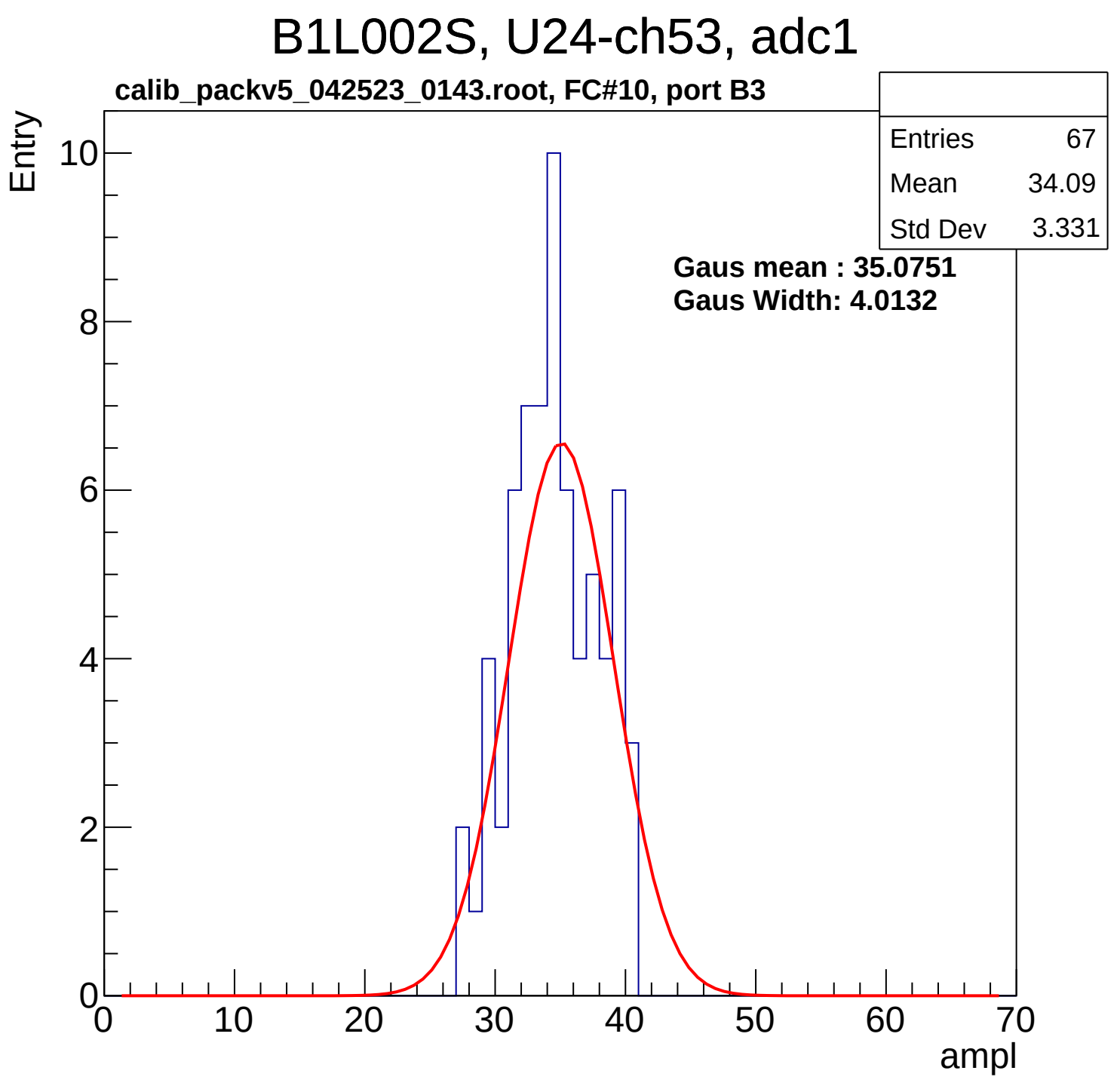
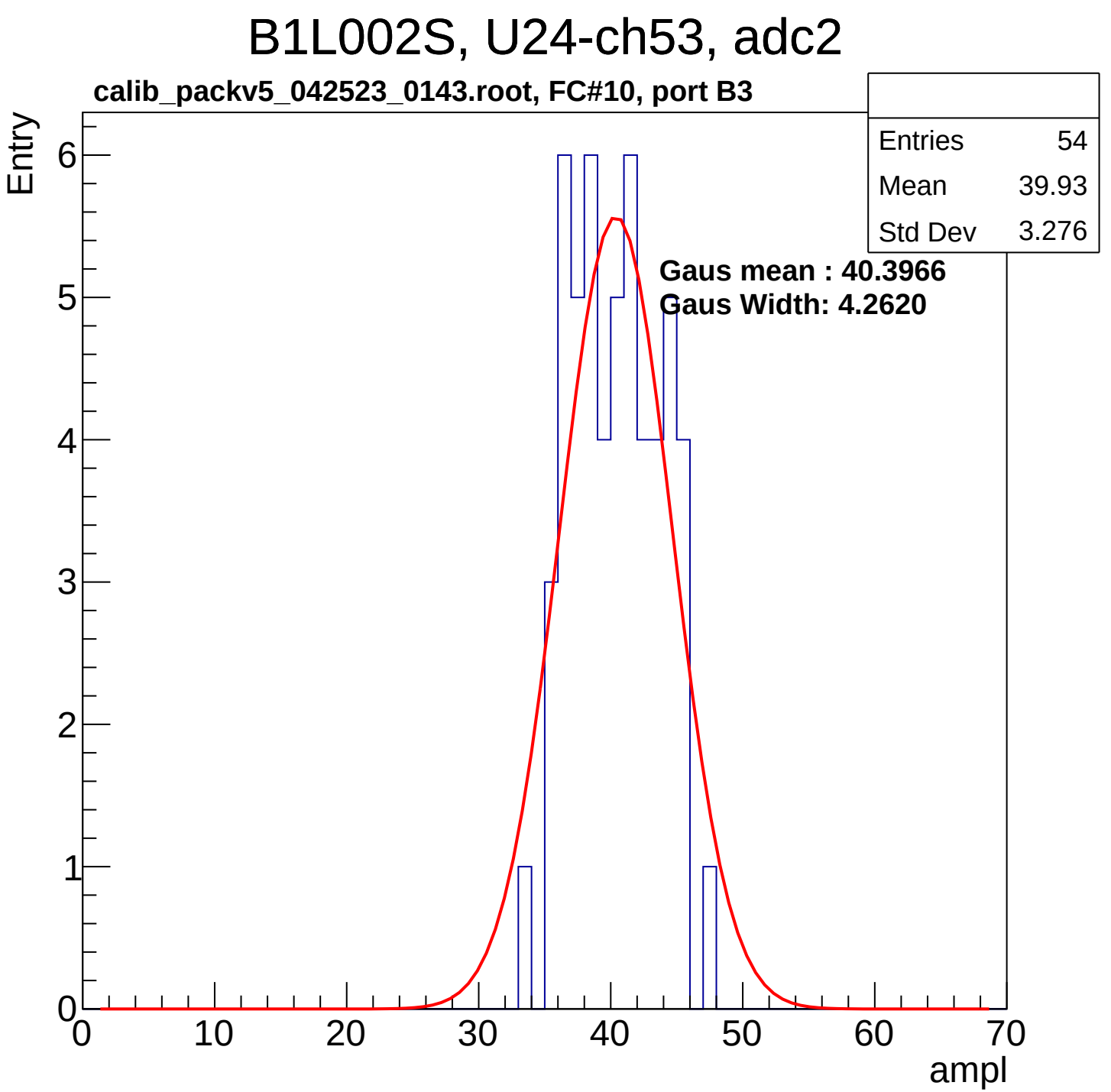
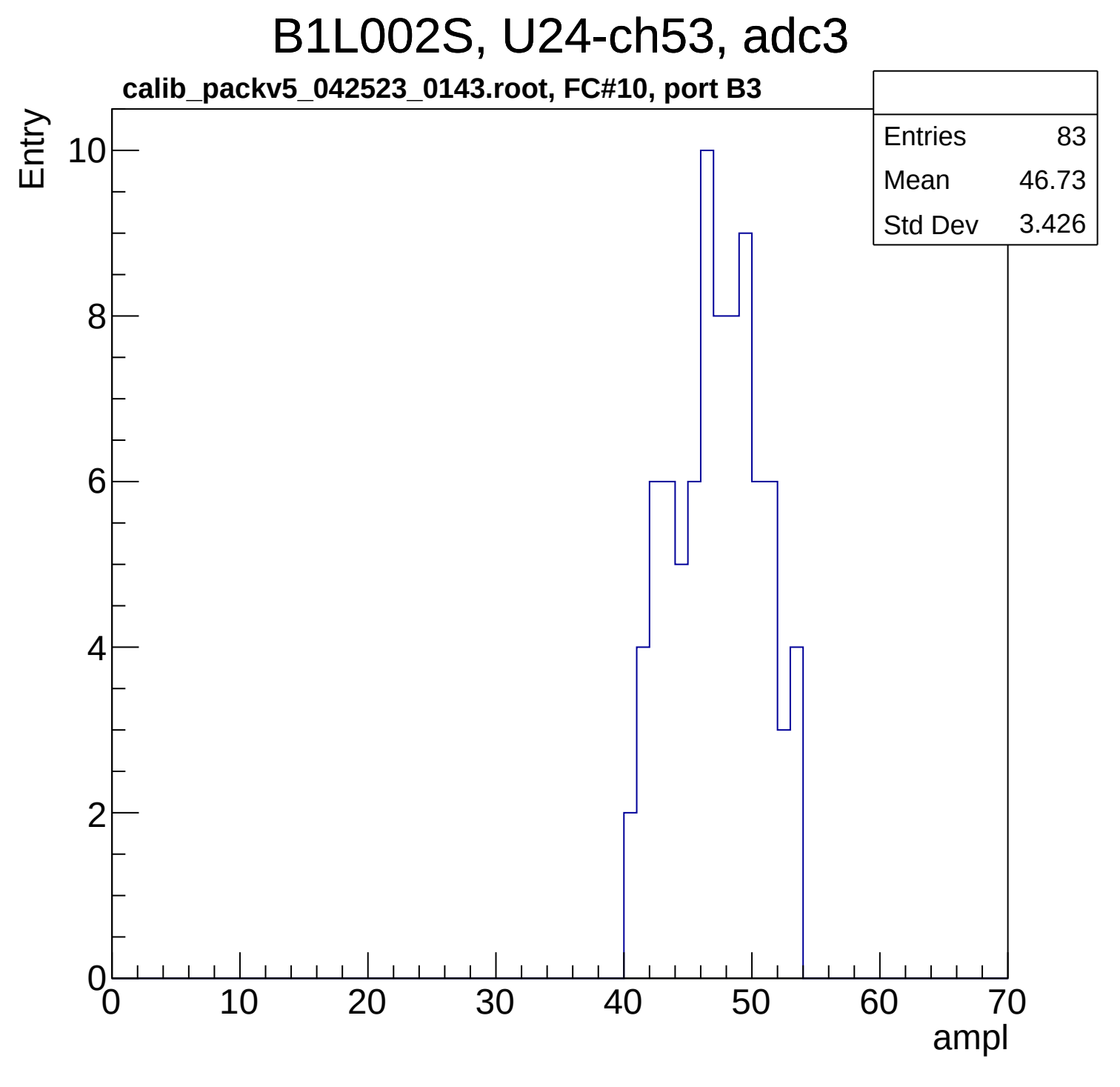
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch53, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34.09
Std Dev 3.331
Gaus mean : 35.0751
Gaus Width: 4.0132
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch53, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 39.93
Std Dev 3.276
Gaus mean : 40.3966
Gaus Width: 4.2620
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch53, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 46.73
Std Dev 3.426
Entry
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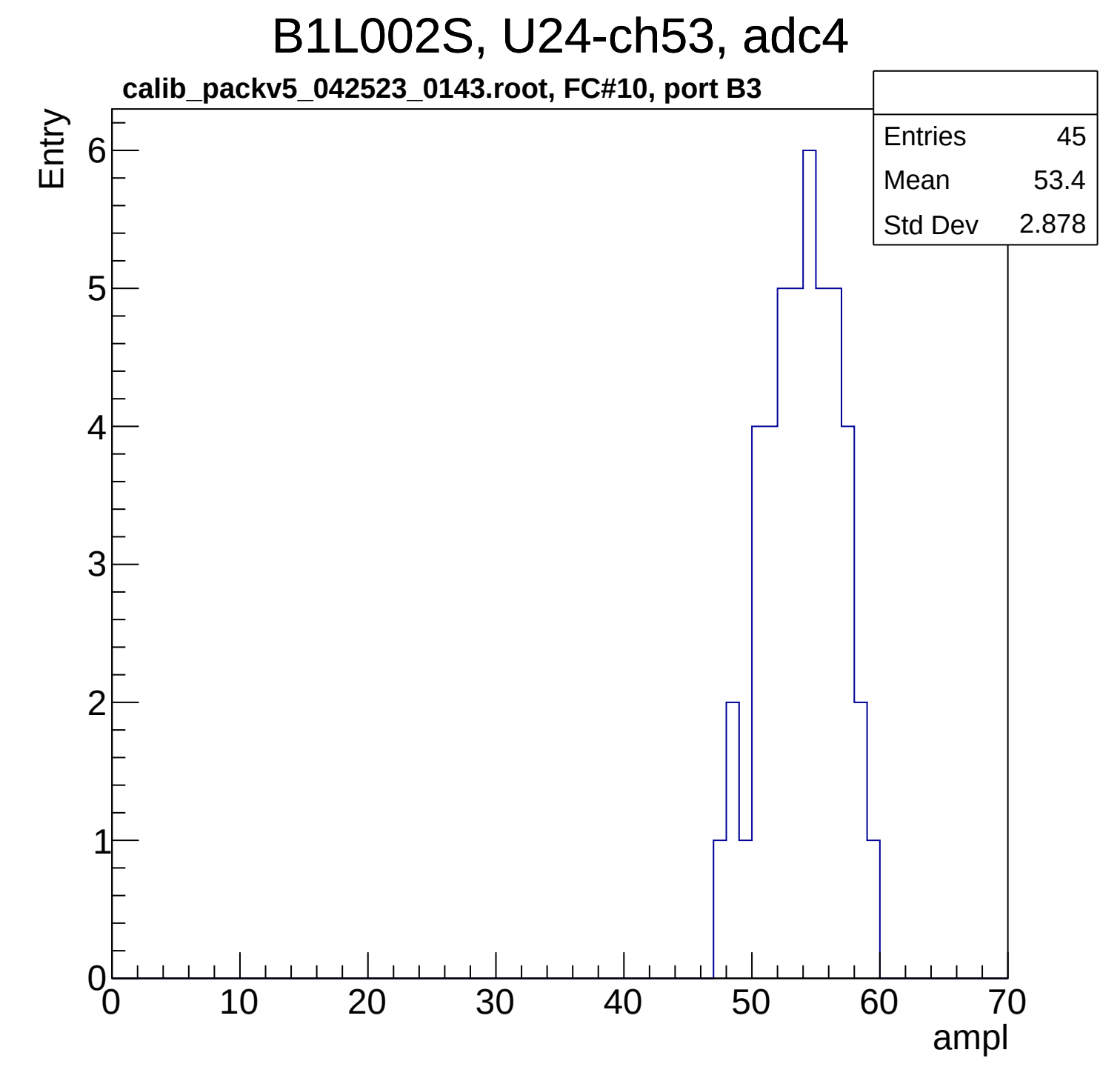
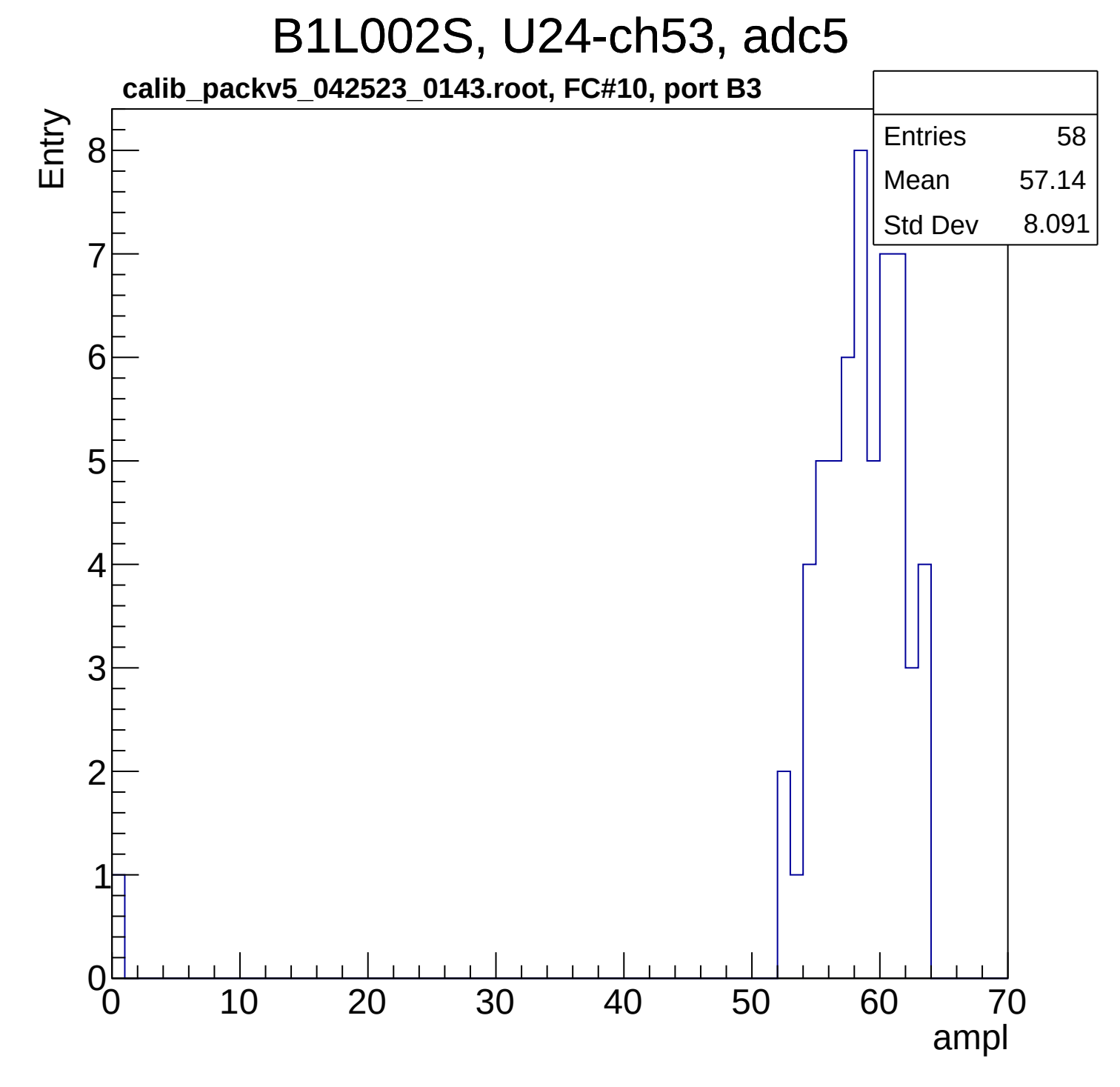
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch53, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53.4
Std Dev 2.878
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch53, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 57.14
Std Dev 8.091
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
30
40
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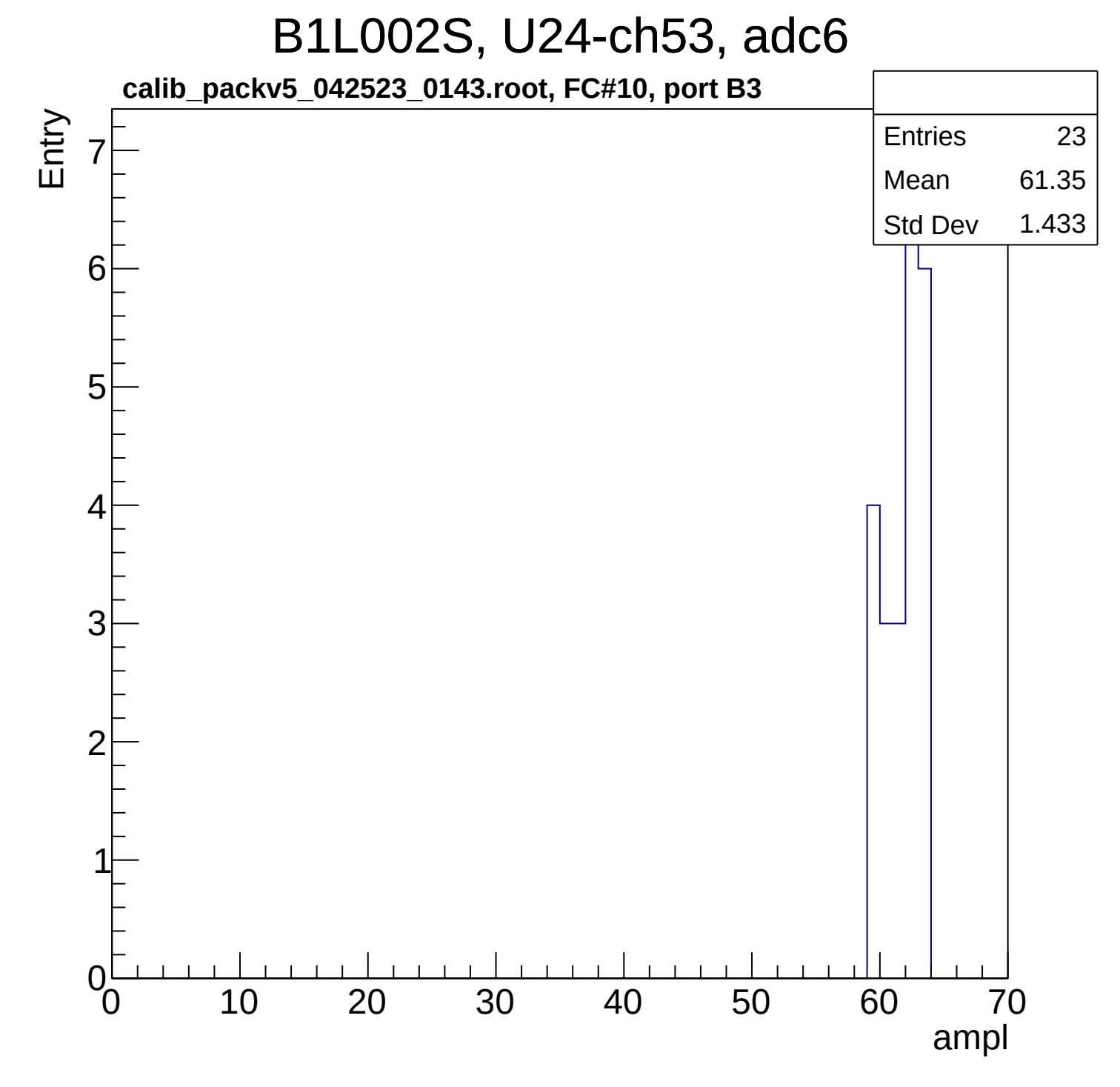
60
70


B1L002S, U24-ch53, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.35
Std Dev 1.433
Entry
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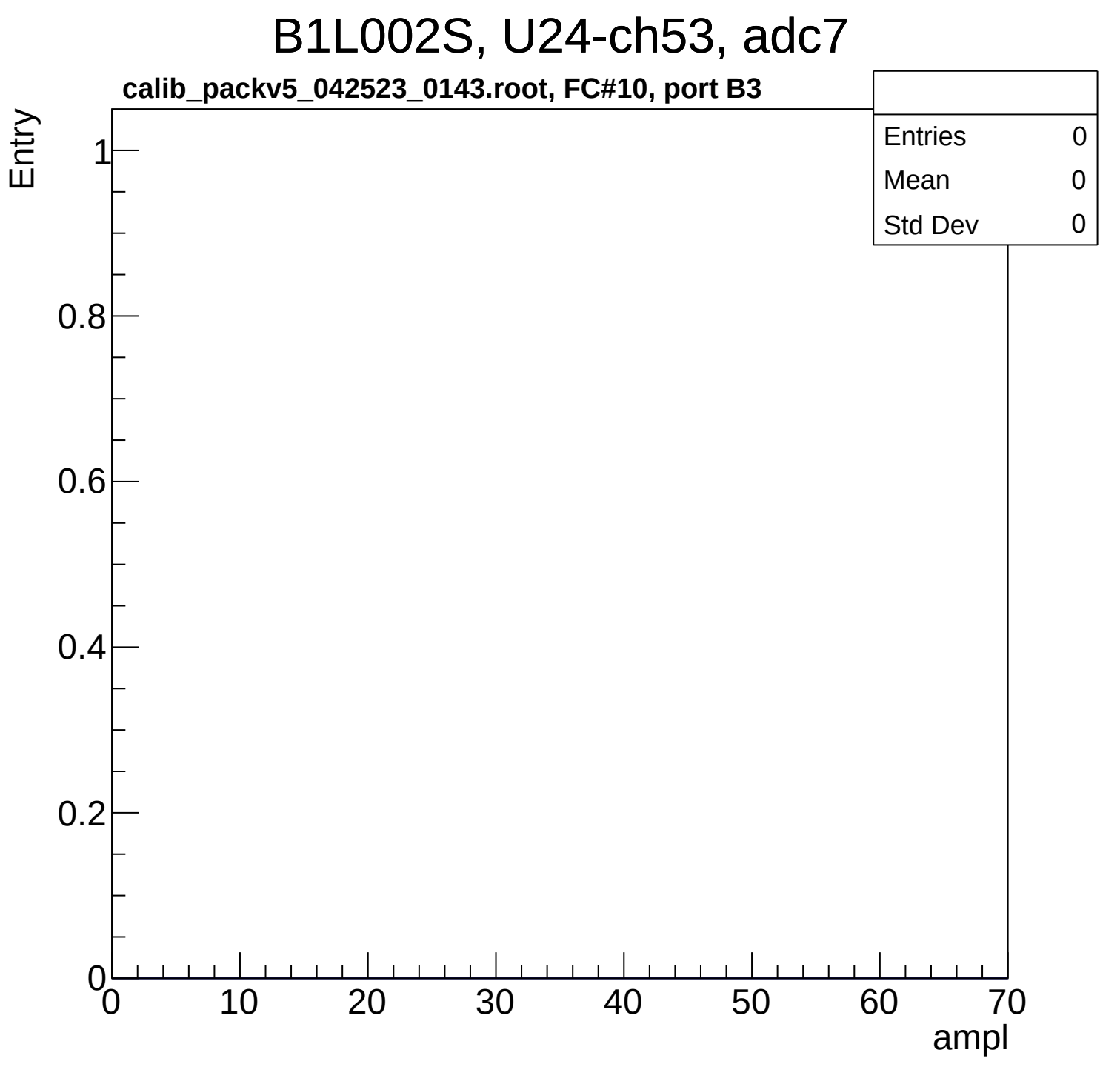
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch53, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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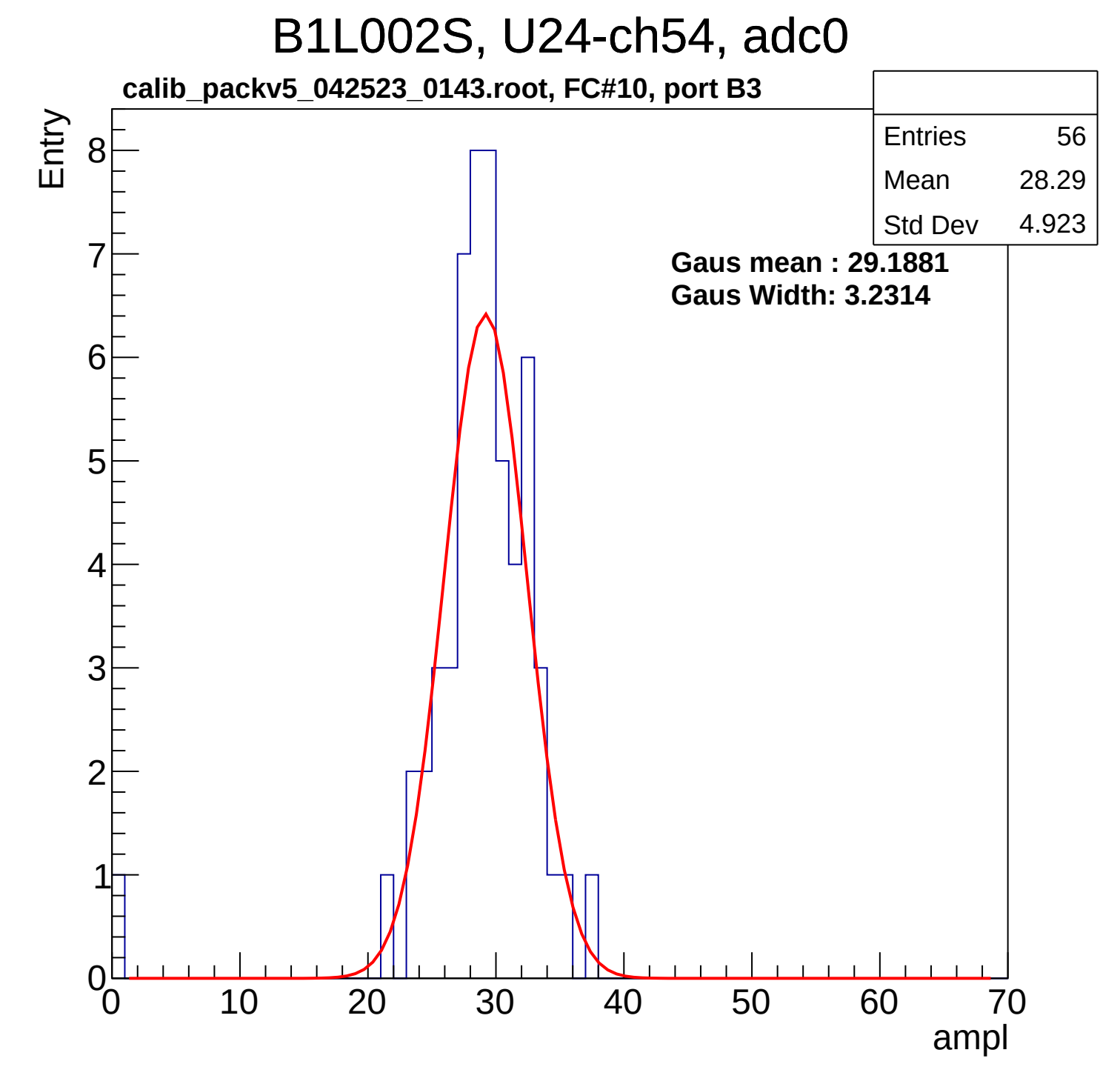
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch54, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 28.29
Std Dev 4.923
Gaus mean : 29.1881
Gaus Width: 3.2314
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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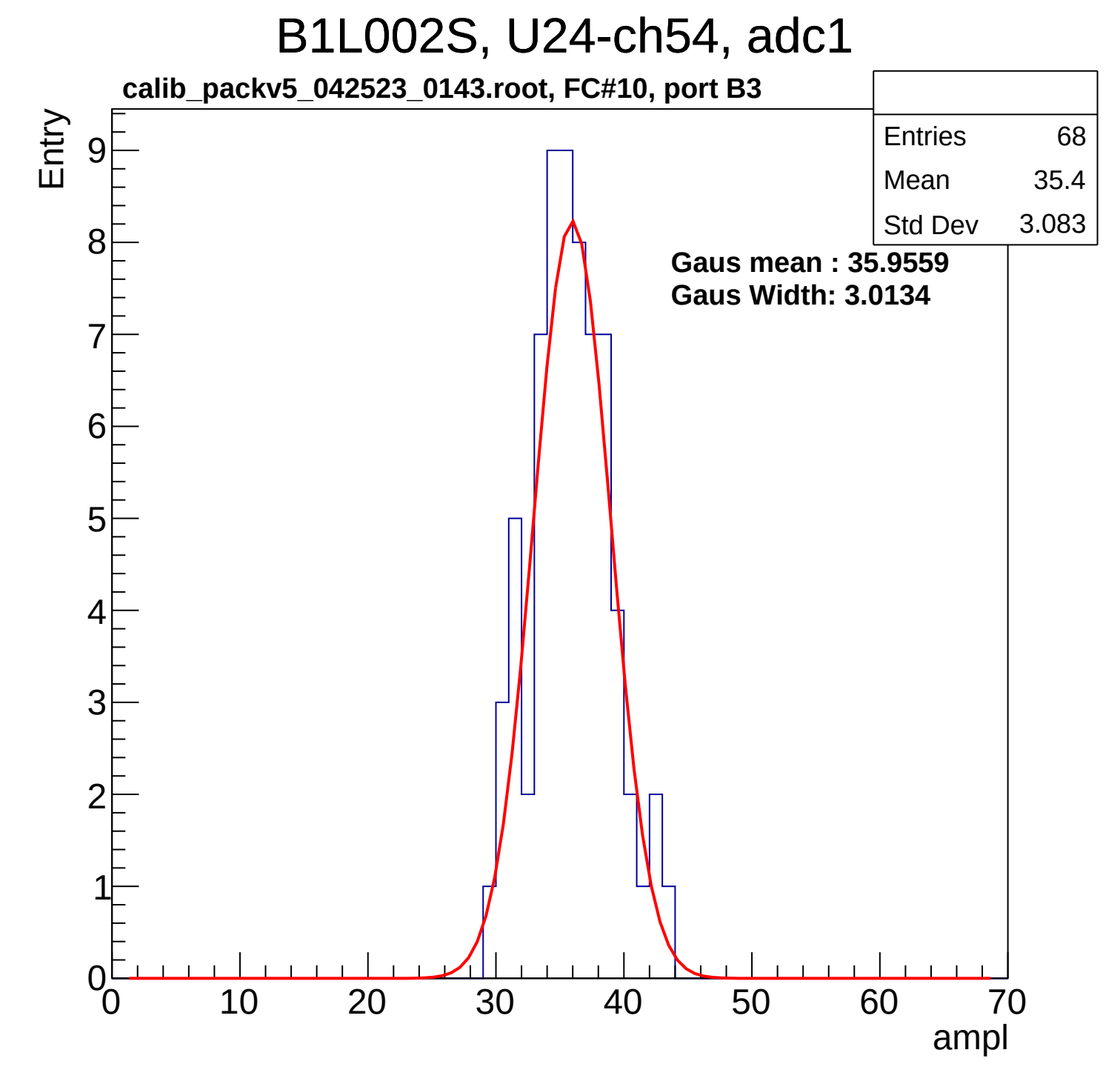
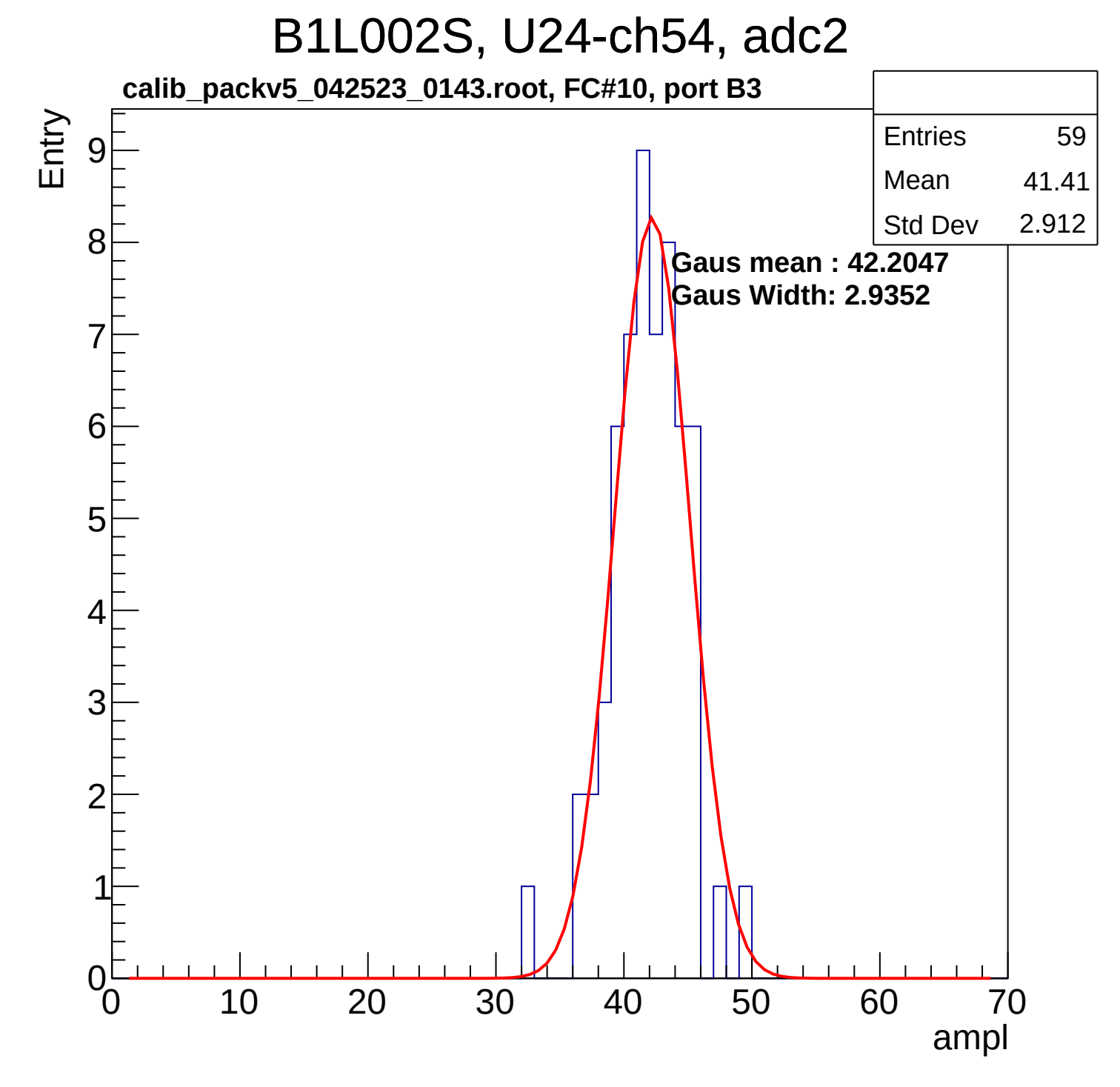
40
50
60
70


B1L002S, U24-ch54, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.4
Std Dev 3.083
Gaus mean : 35.9559
Gaus Width: 3.0134
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch54, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 41.41
Std Dev 2.912
Gaus mean : 42.2047
Gaus Width: 2.9352
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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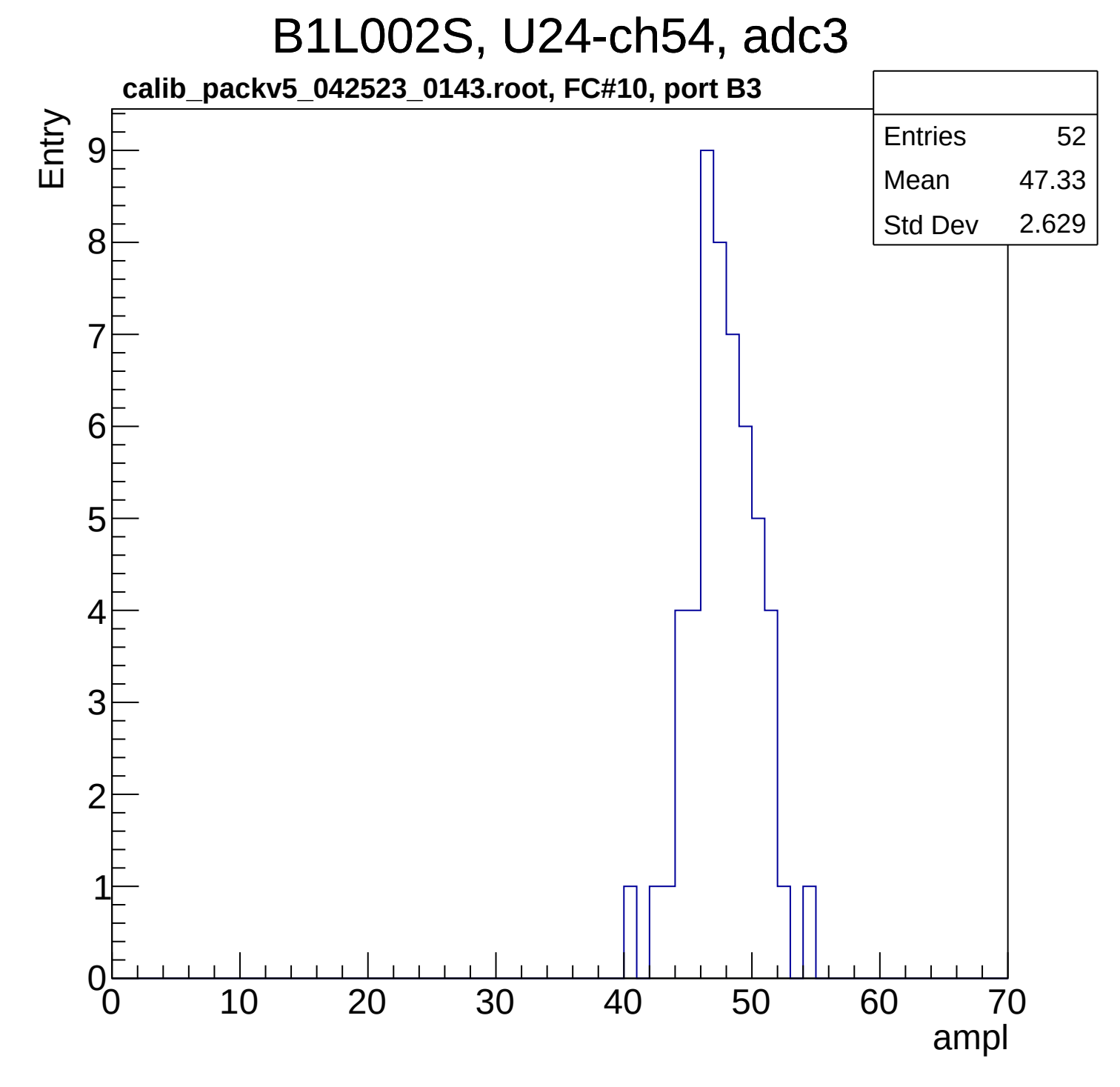
B1L002S, U24-ch54, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 47.33
Std Dev 2.629
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
50
60
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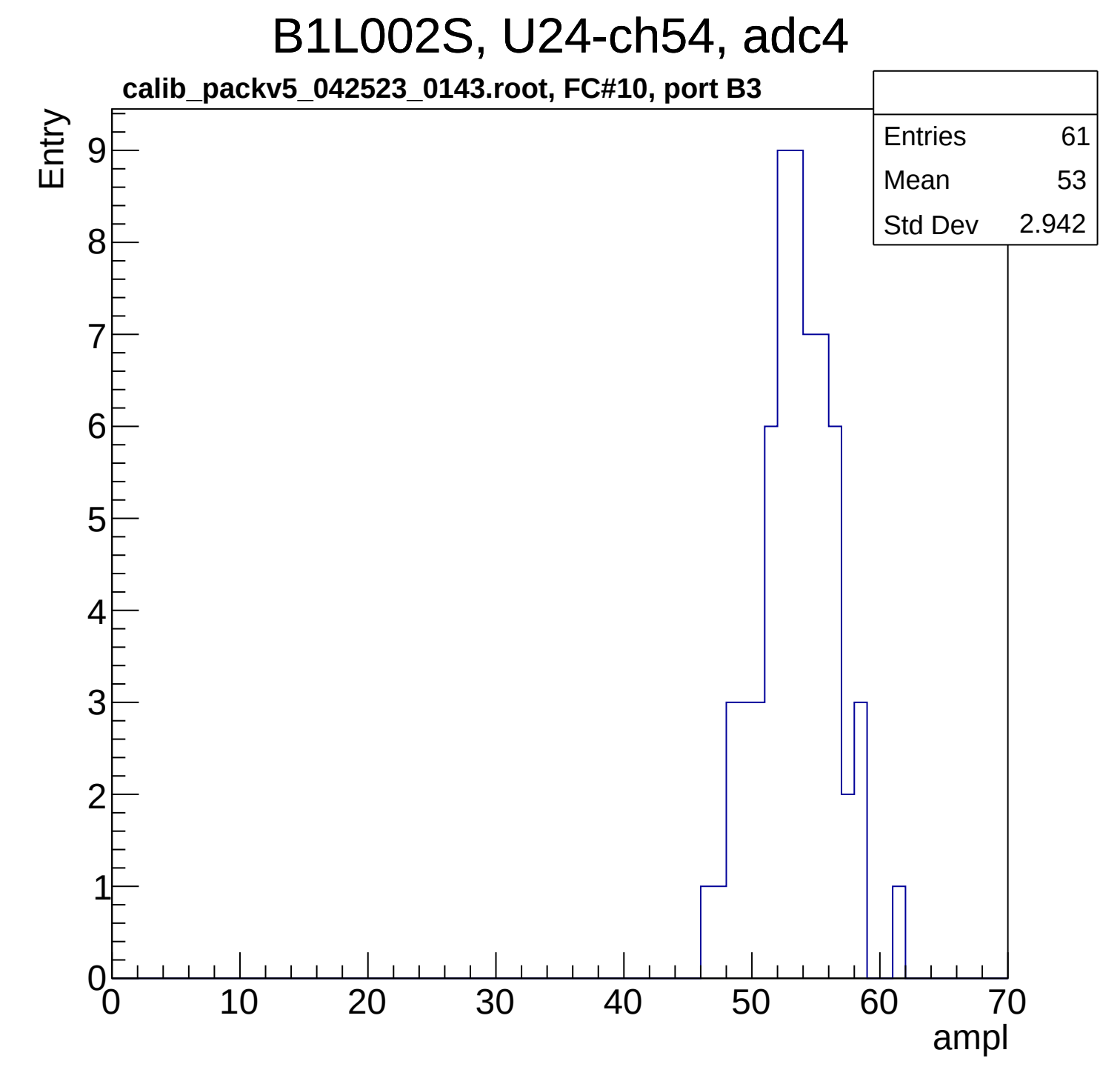
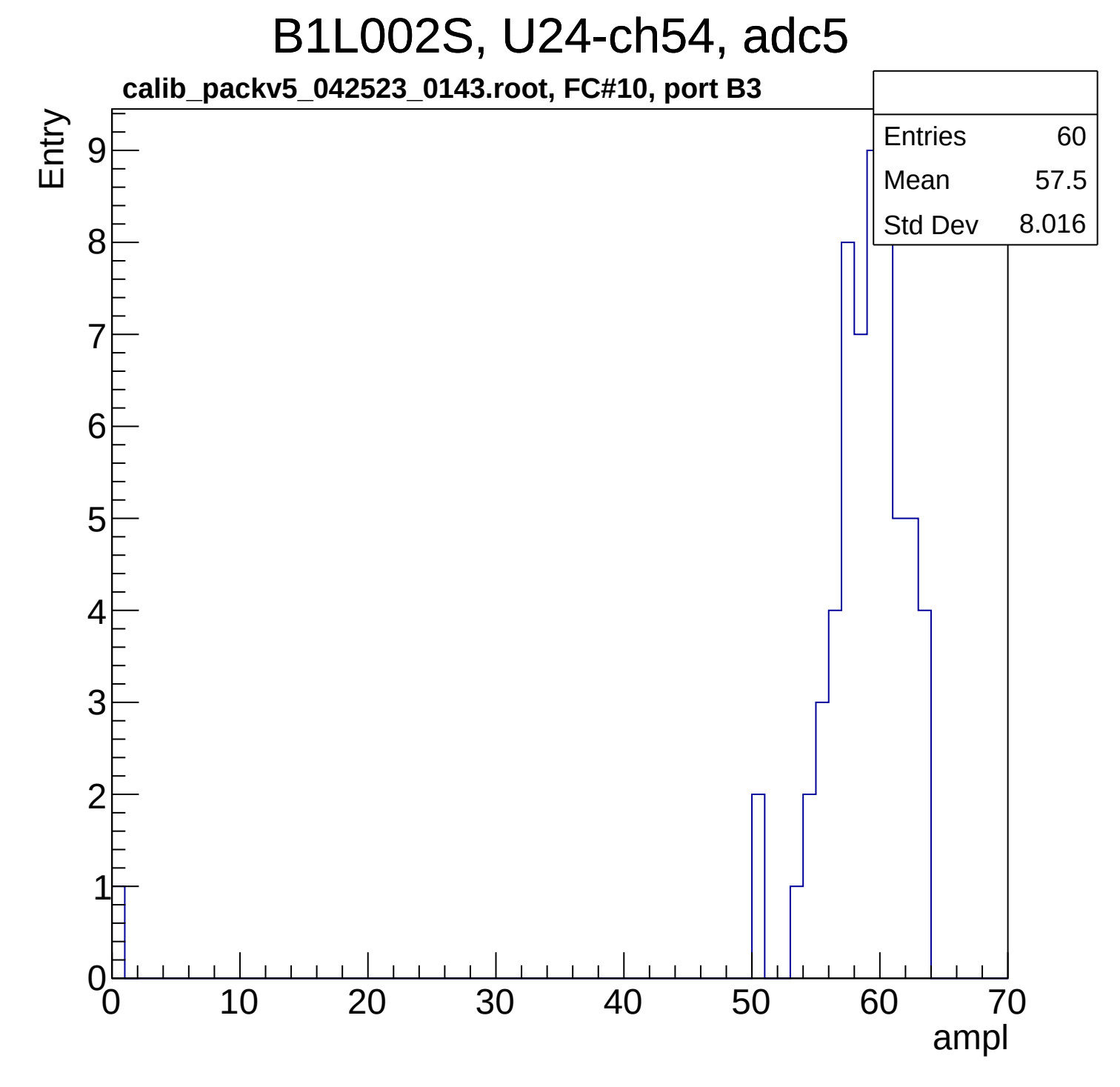
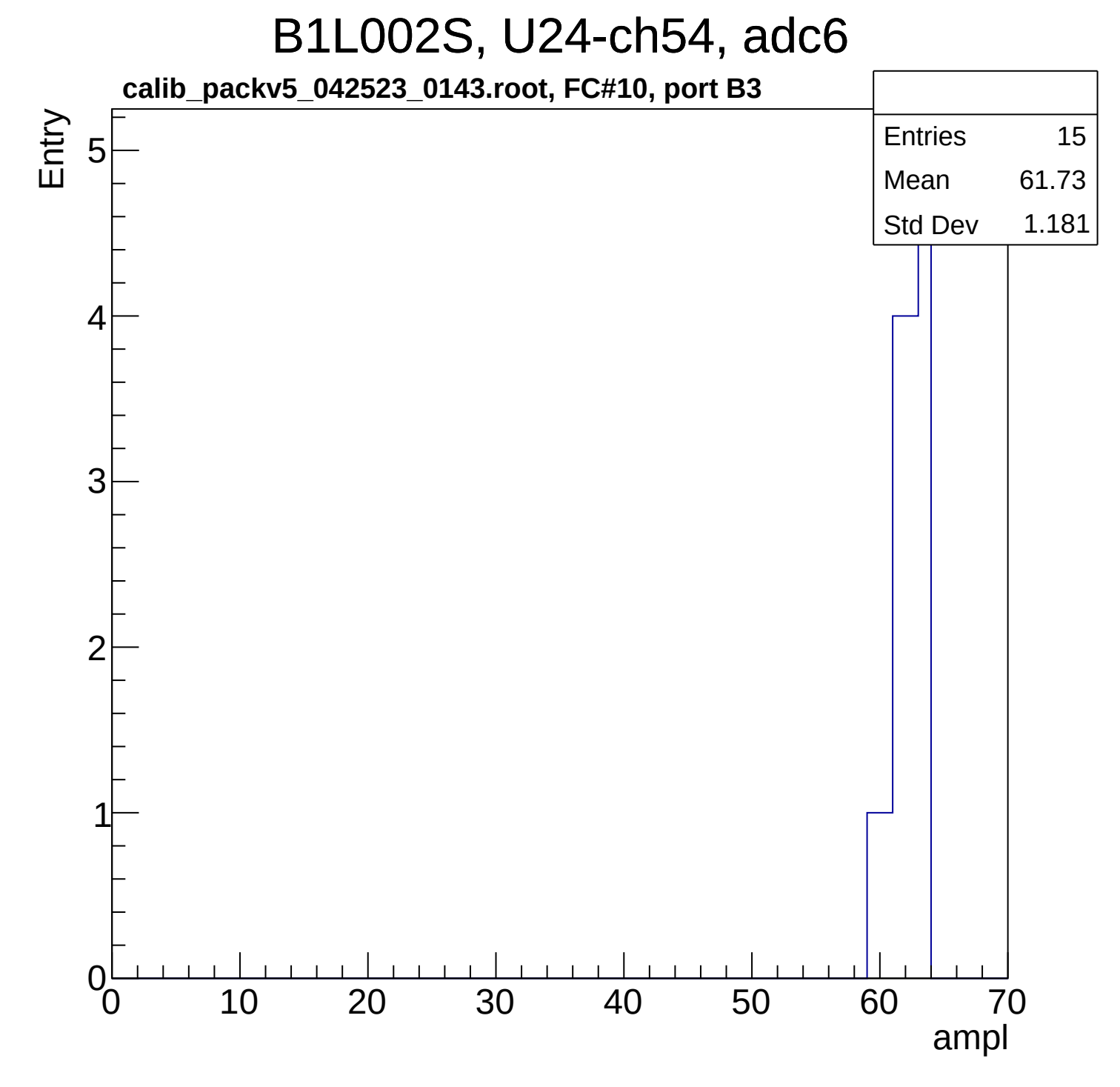
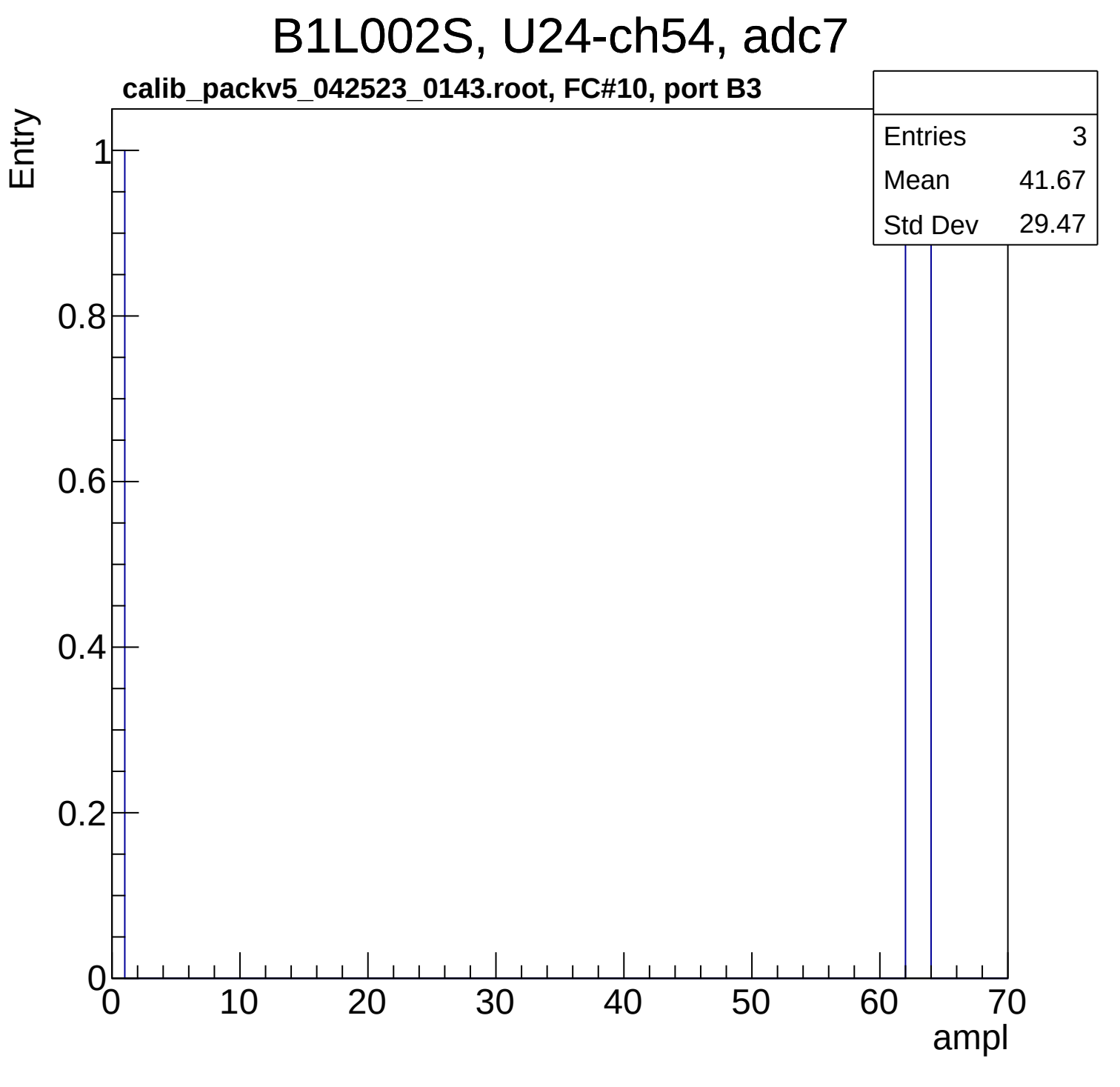
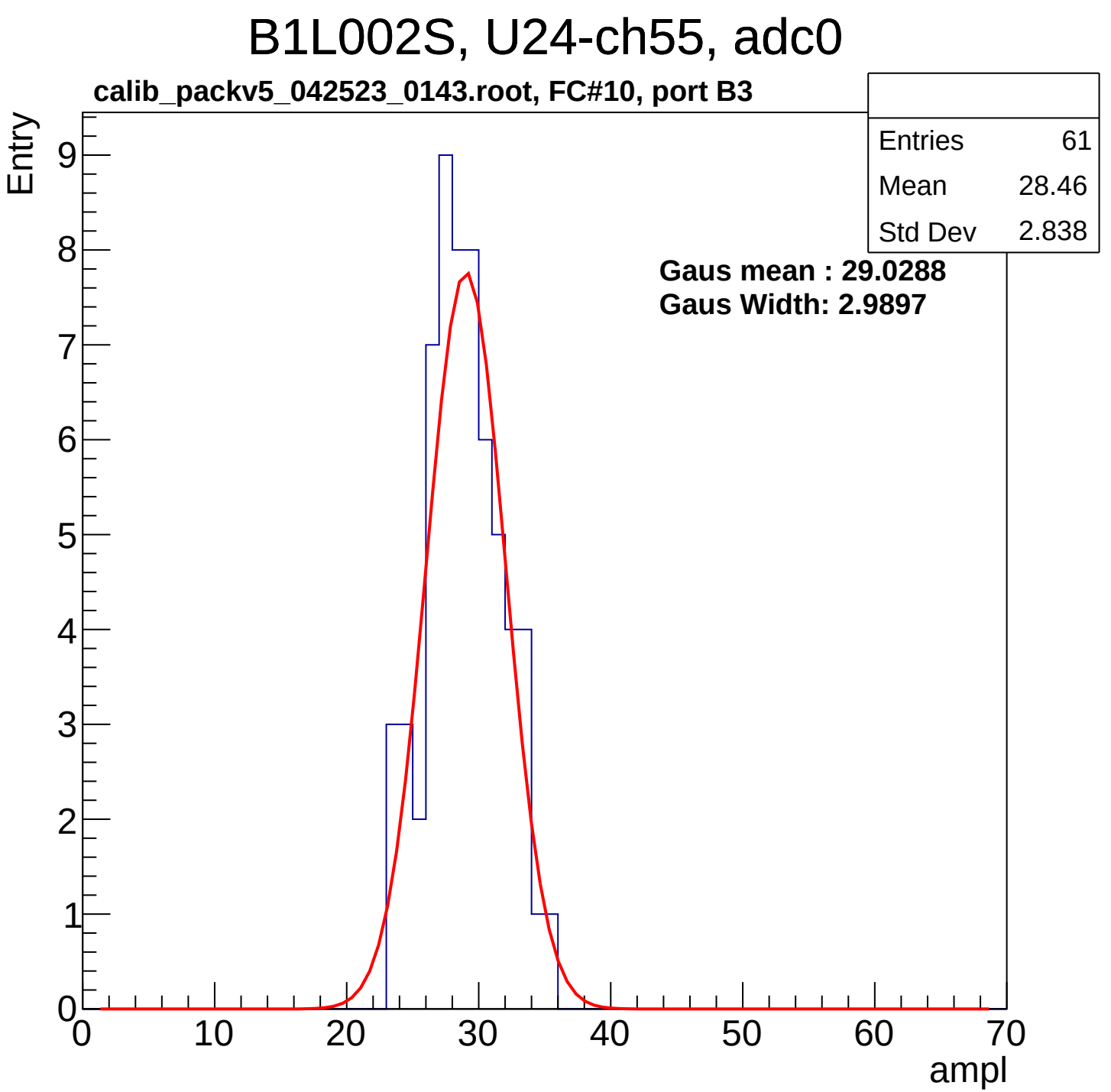
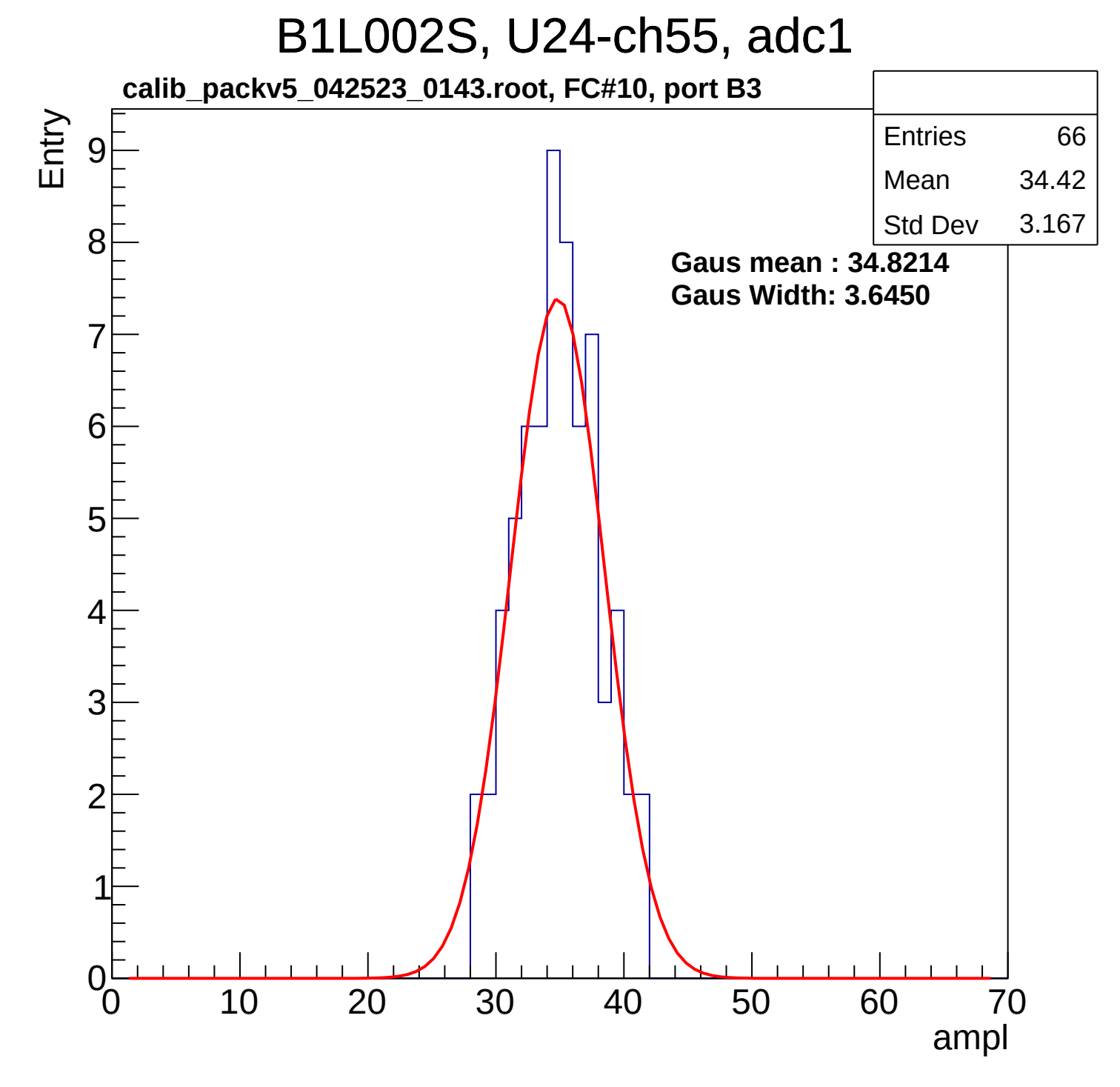

B1L002S, U24-ch54, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53
Std Dev 2.942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

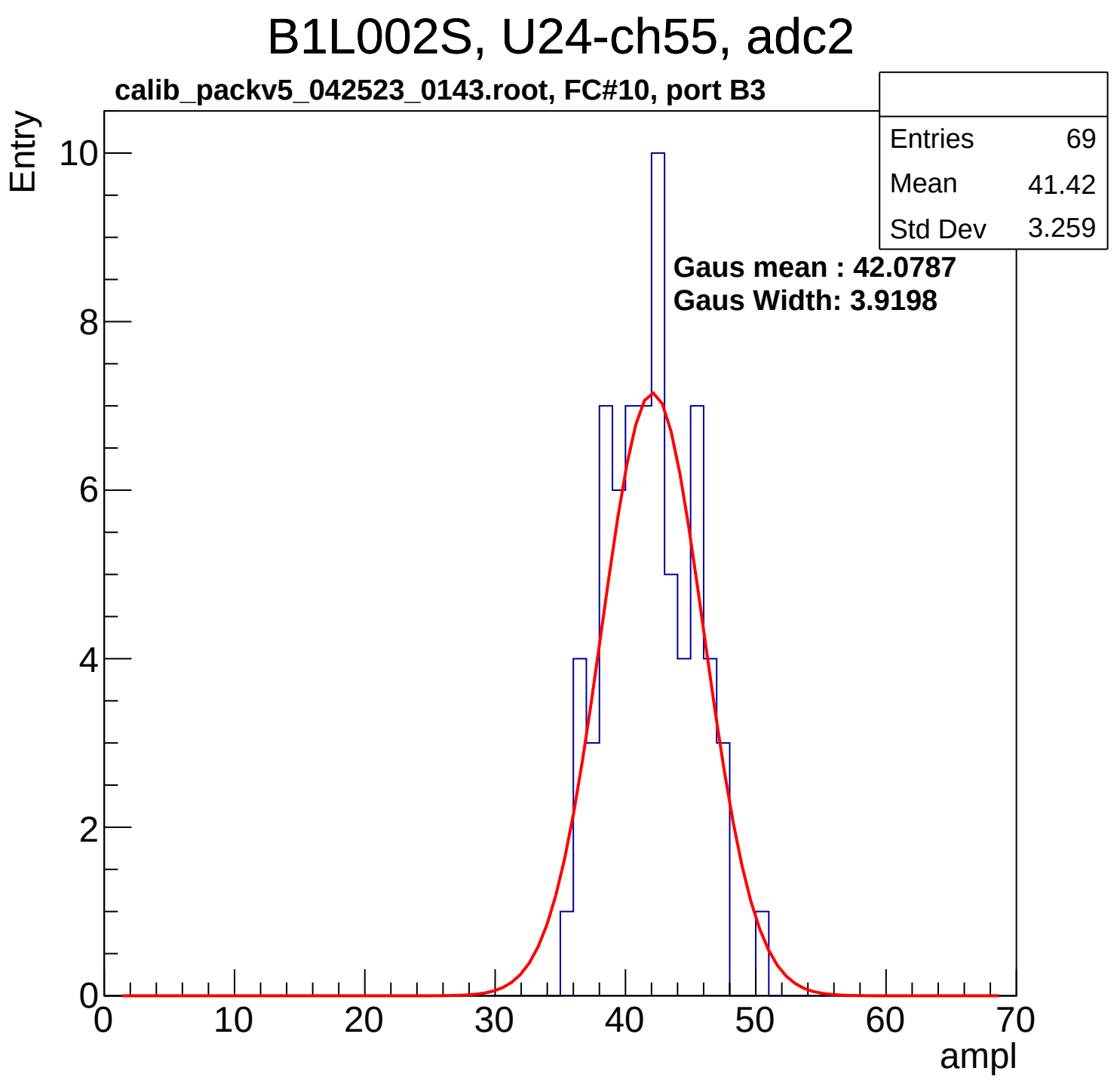

B1L002S, U24-ch54, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.5
Std Dev 8.016
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

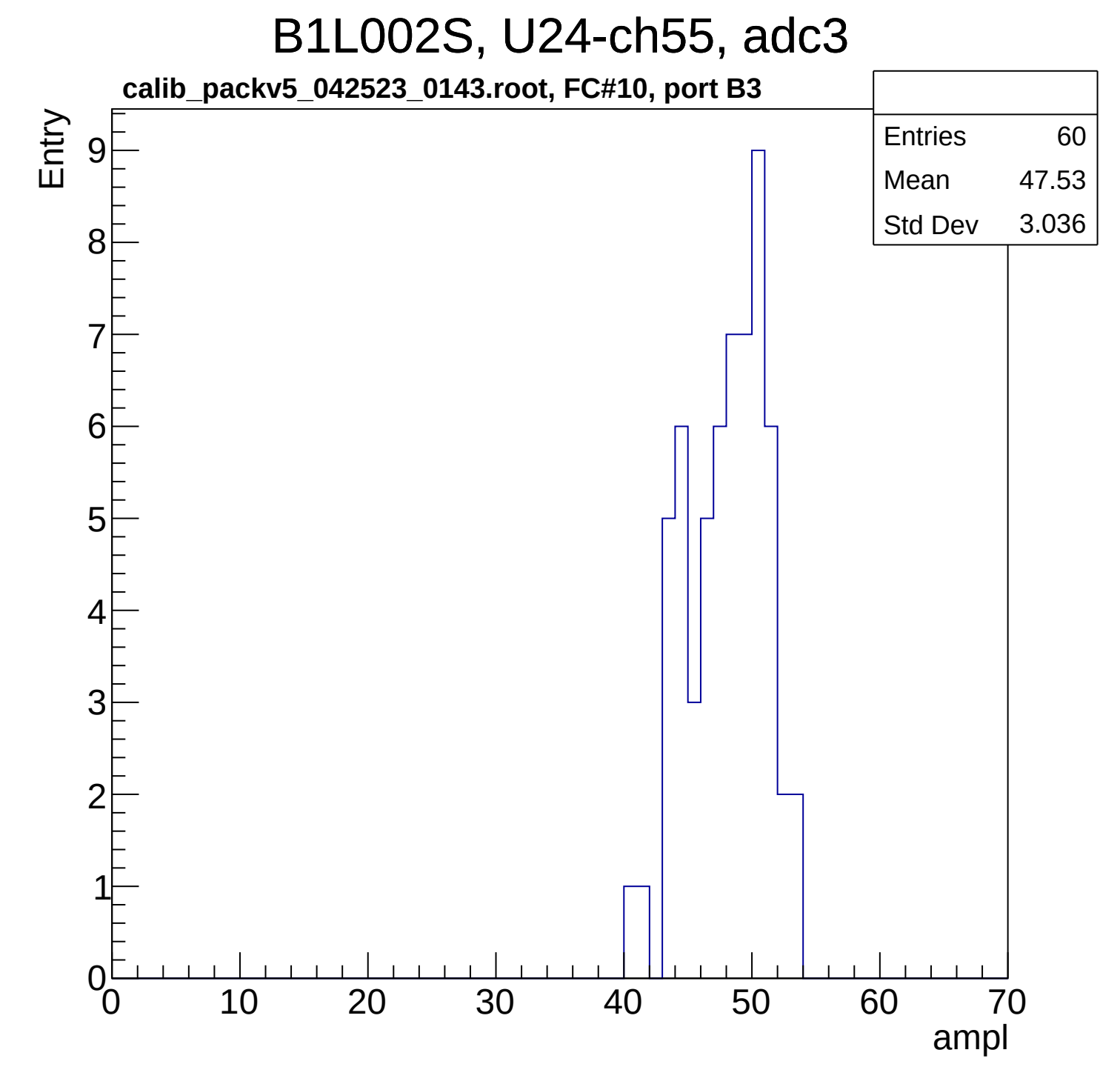

B1L002S, U24-ch54, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.73
Std Dev 1.181
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

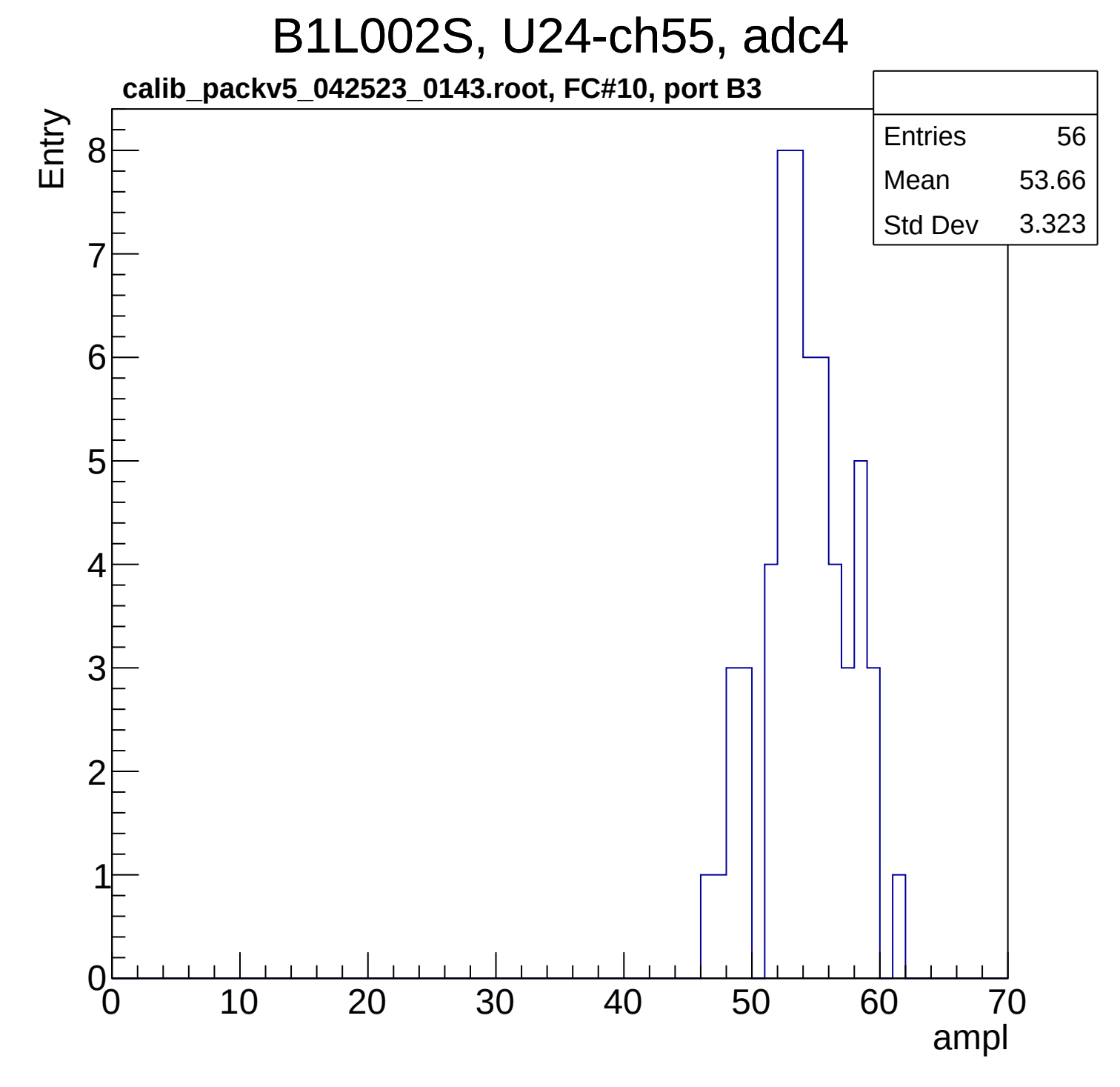

B1L002S, U24-ch54, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

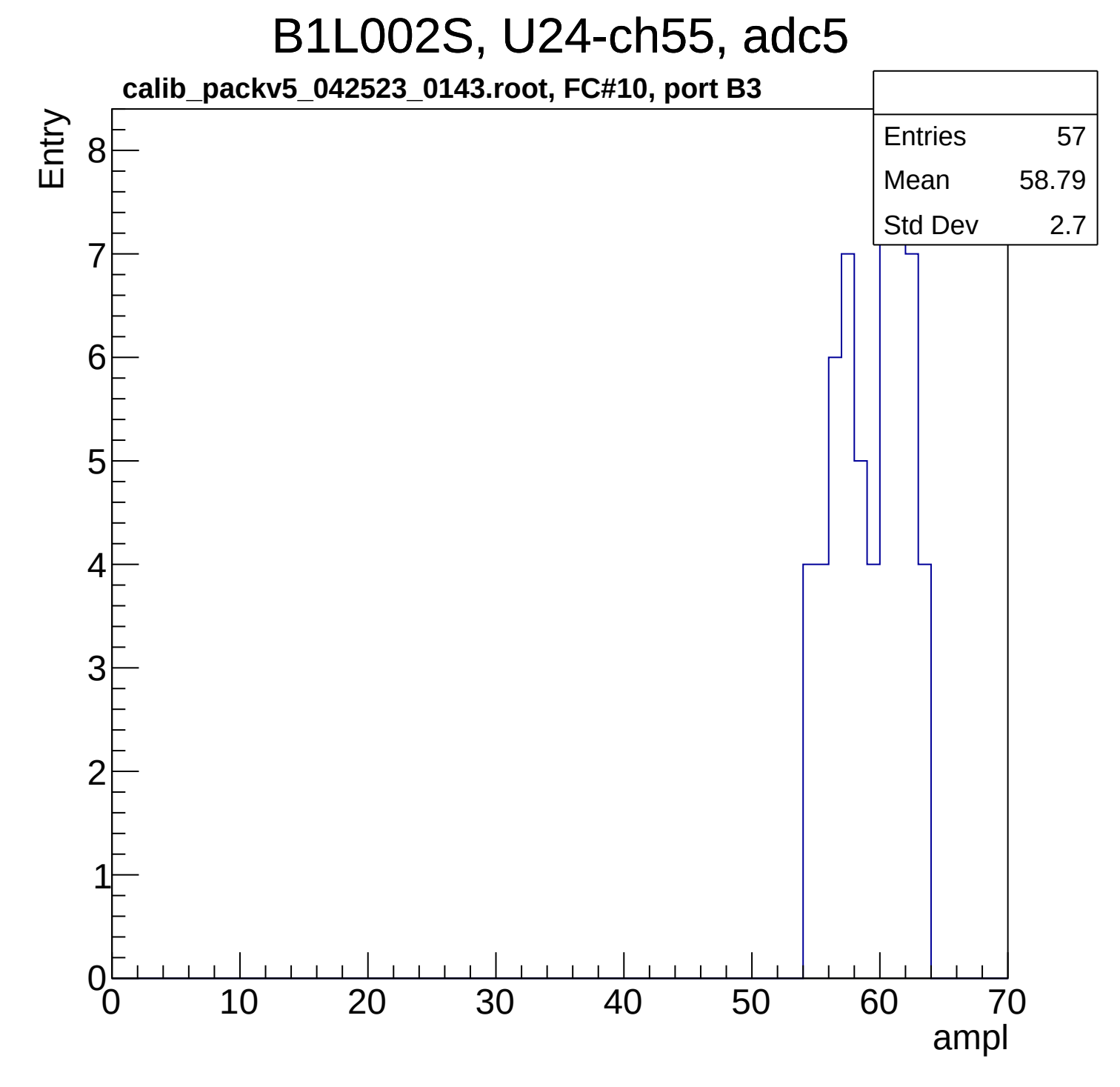

B1L002S, U24-ch55, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.46
Std Dev 2.838
Gaus mean : 29.0288
Gaus Width: 2.9897
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

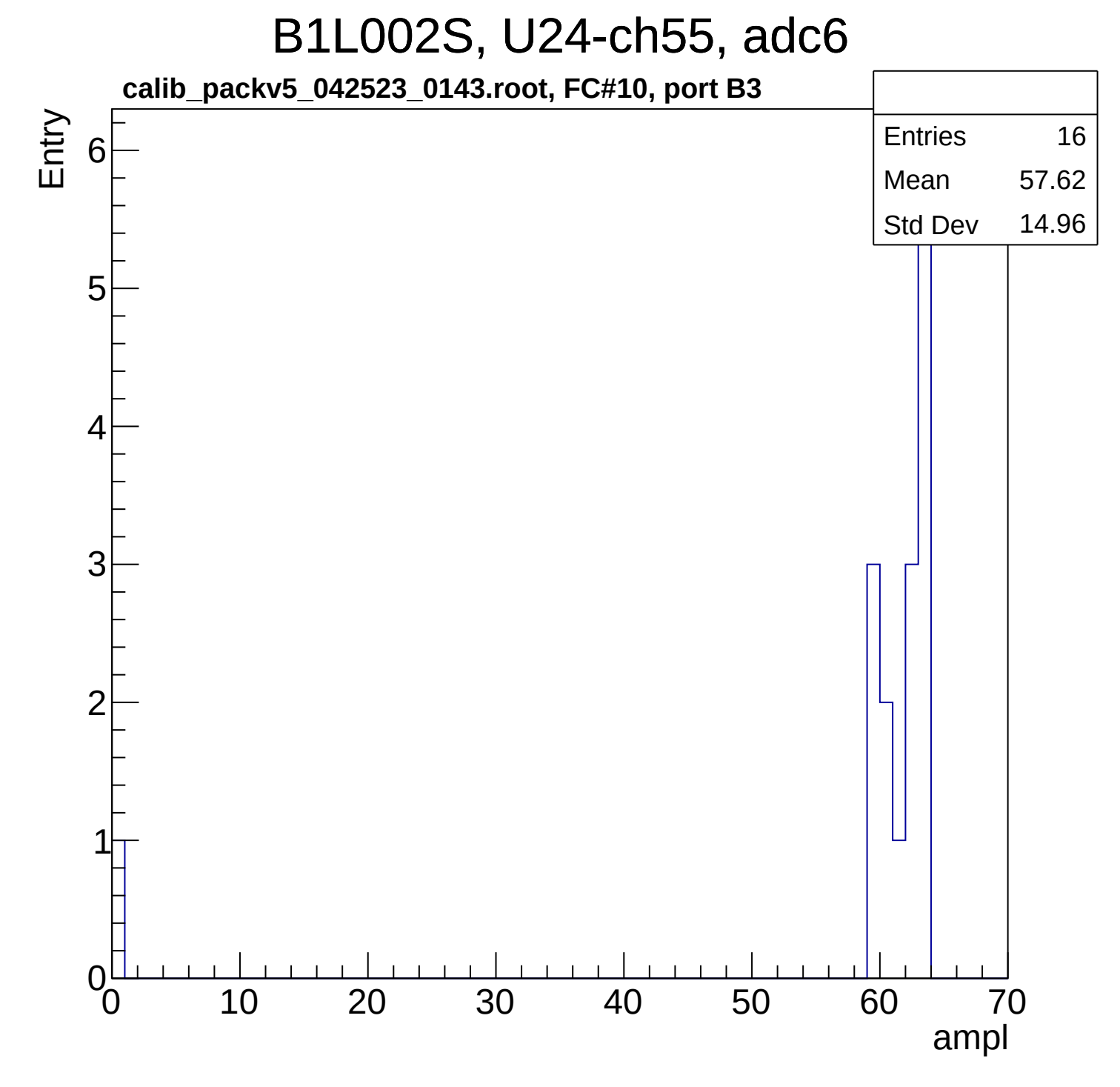

B1L002S, U24-ch55, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.42
Std Dev 3.167
Gaus mean : 34.8214
Gaus Width: 3.6450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

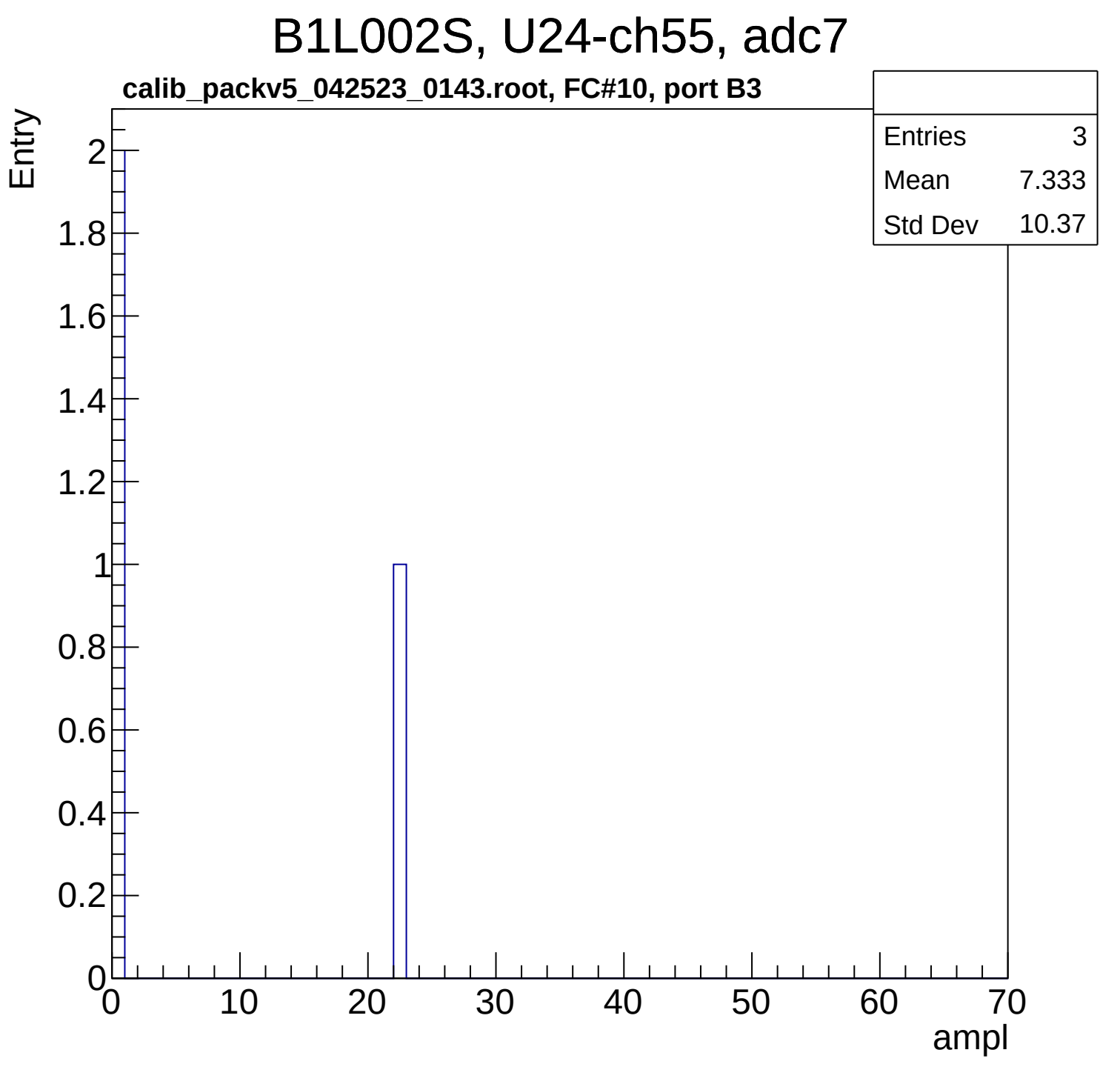

B1L002S, U24-ch55, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.42
Std Dev 3.259
Gaus mean : 42.0787
Gaus Width: 3.9198
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

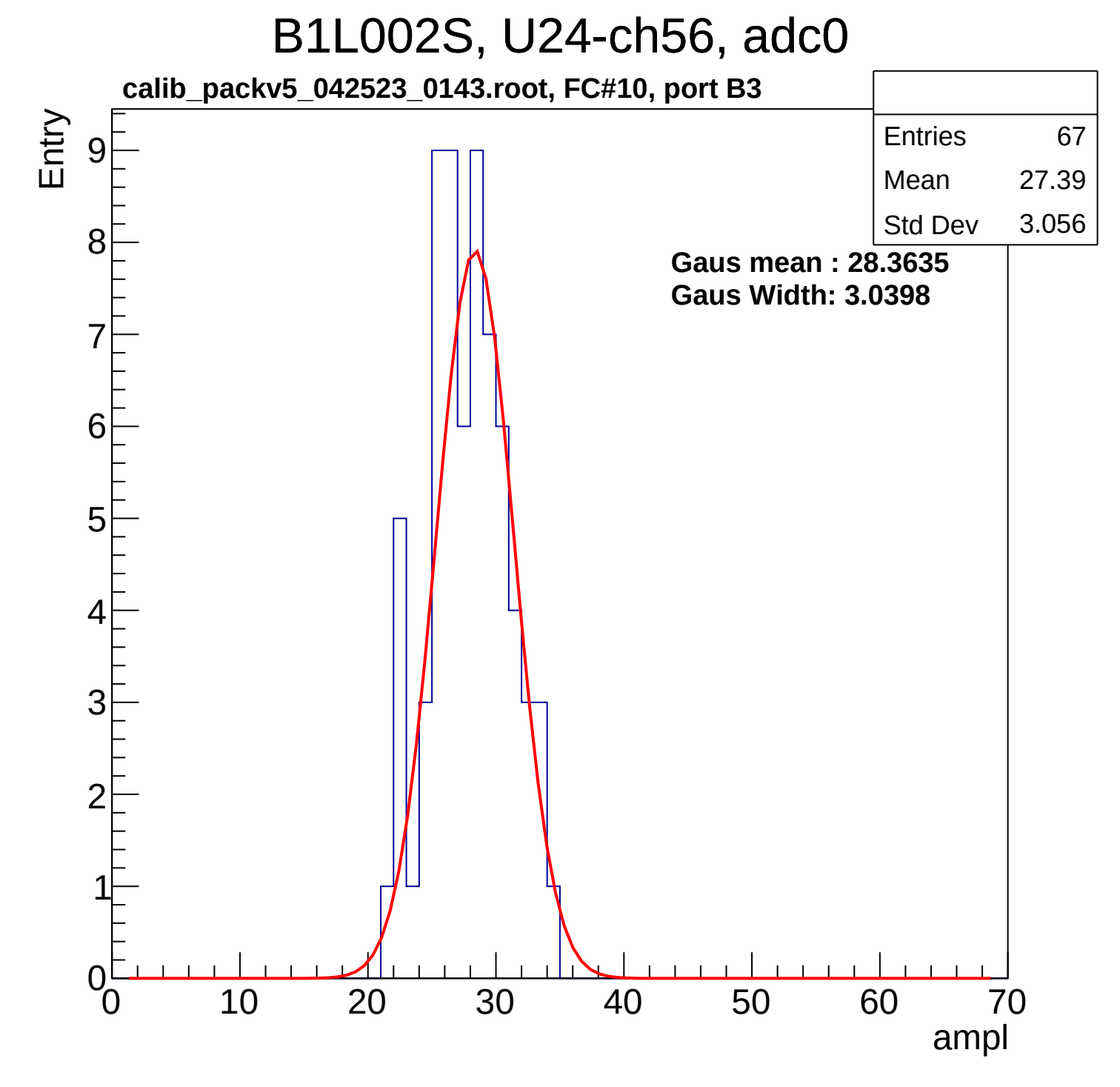

B1L002S, U24-ch55, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.53
Std Dev 3.036
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

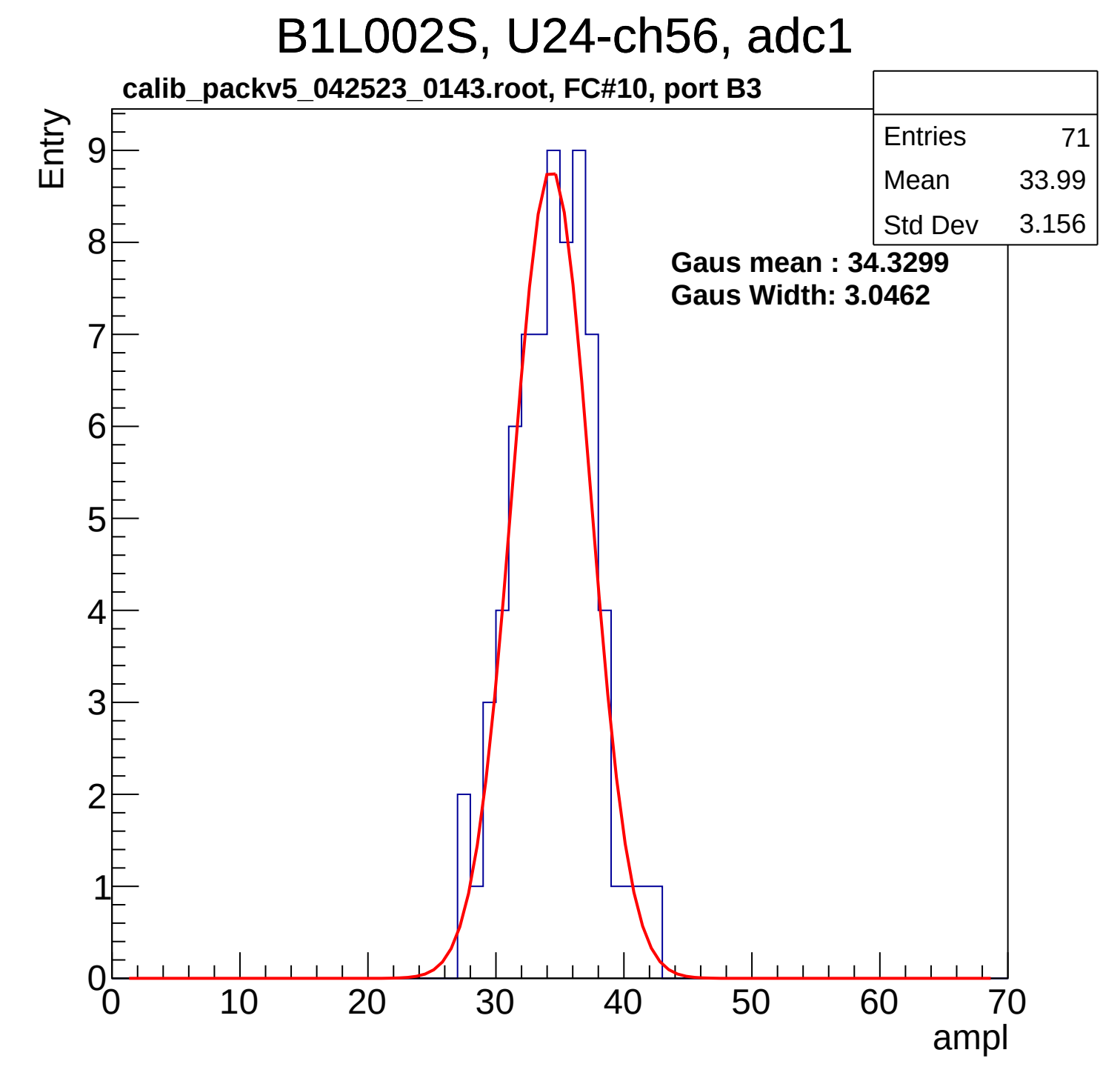

B1L002S, U24-ch55, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.66
Std Dev 3.323
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

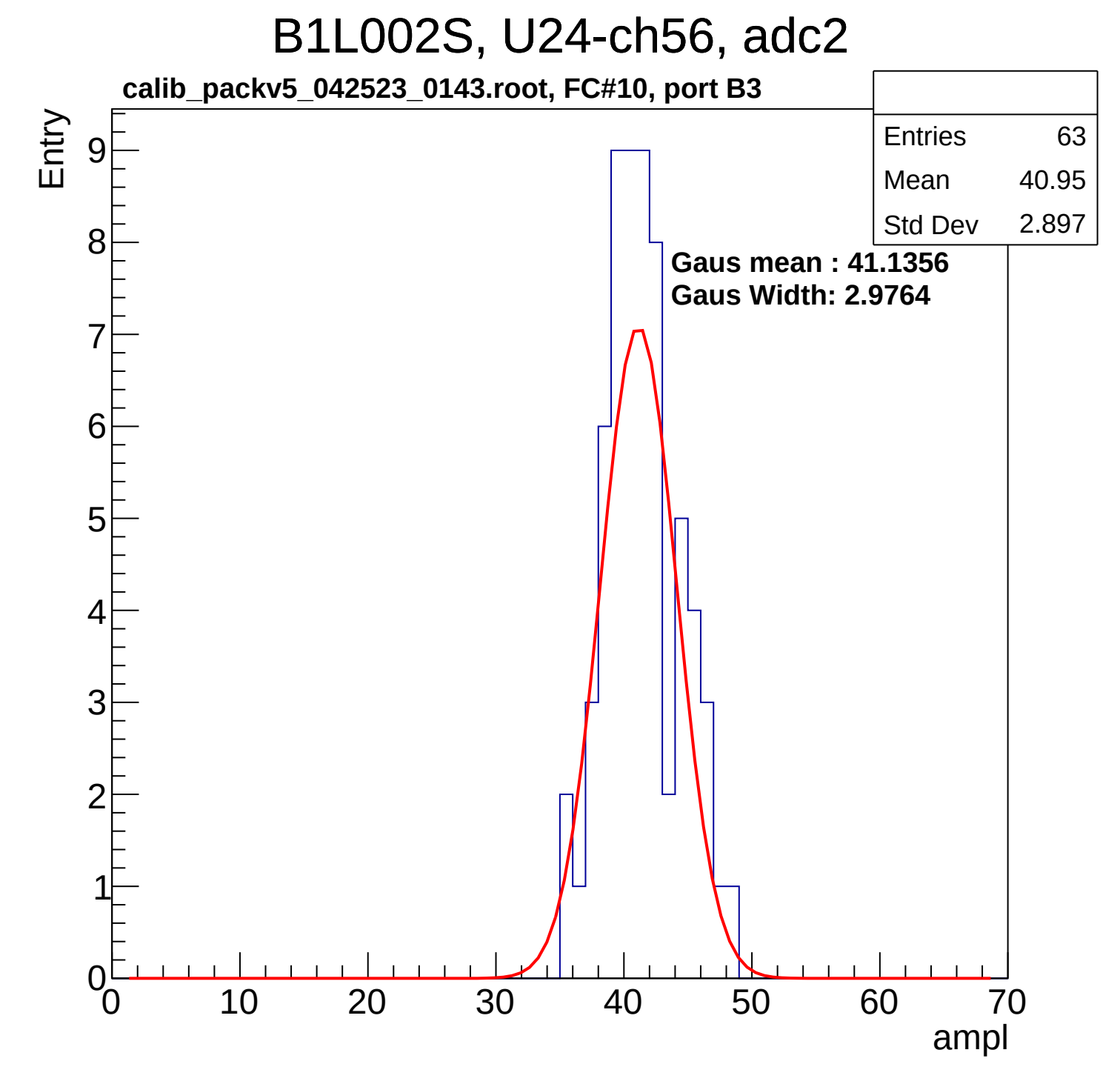

B1L002S, U24-ch55, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.79
Std Dev 2.7
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

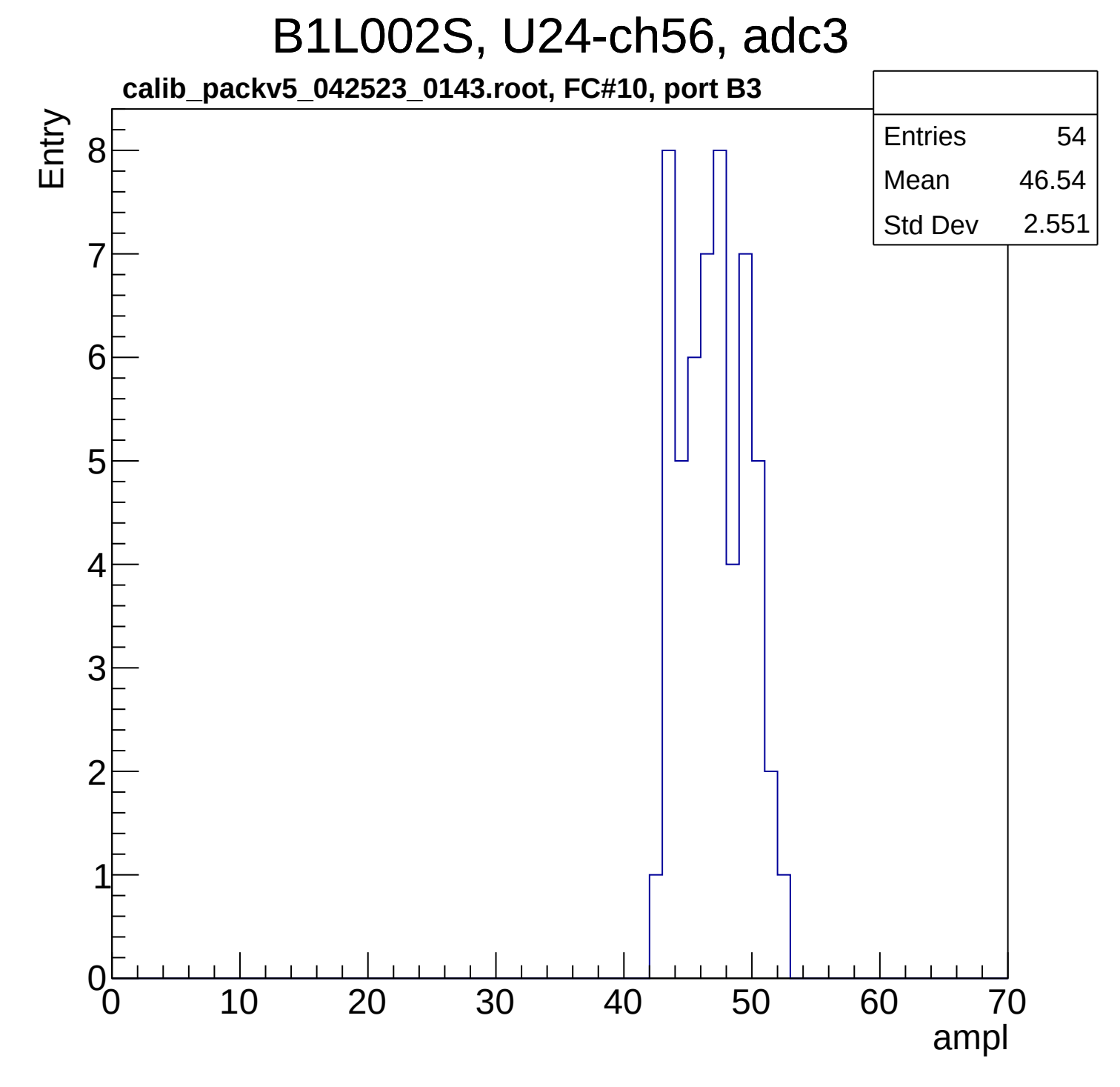

B1L002S, U24-ch55, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.62
Std Dev 14.96
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch55, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.39
Std Dev 3.056
Gaus mean : 28.3635
Gaus Width: 3.0398
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 33.99
Std Dev 3.156
Gaus mean : 34.3299
Gaus Width: 3.0462
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 40.95
Std Dev 2.897
Gaus mean : 41.1356
Gaus Width: 2.9764
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 46.54
Std Dev 2.551
Entry
ampl
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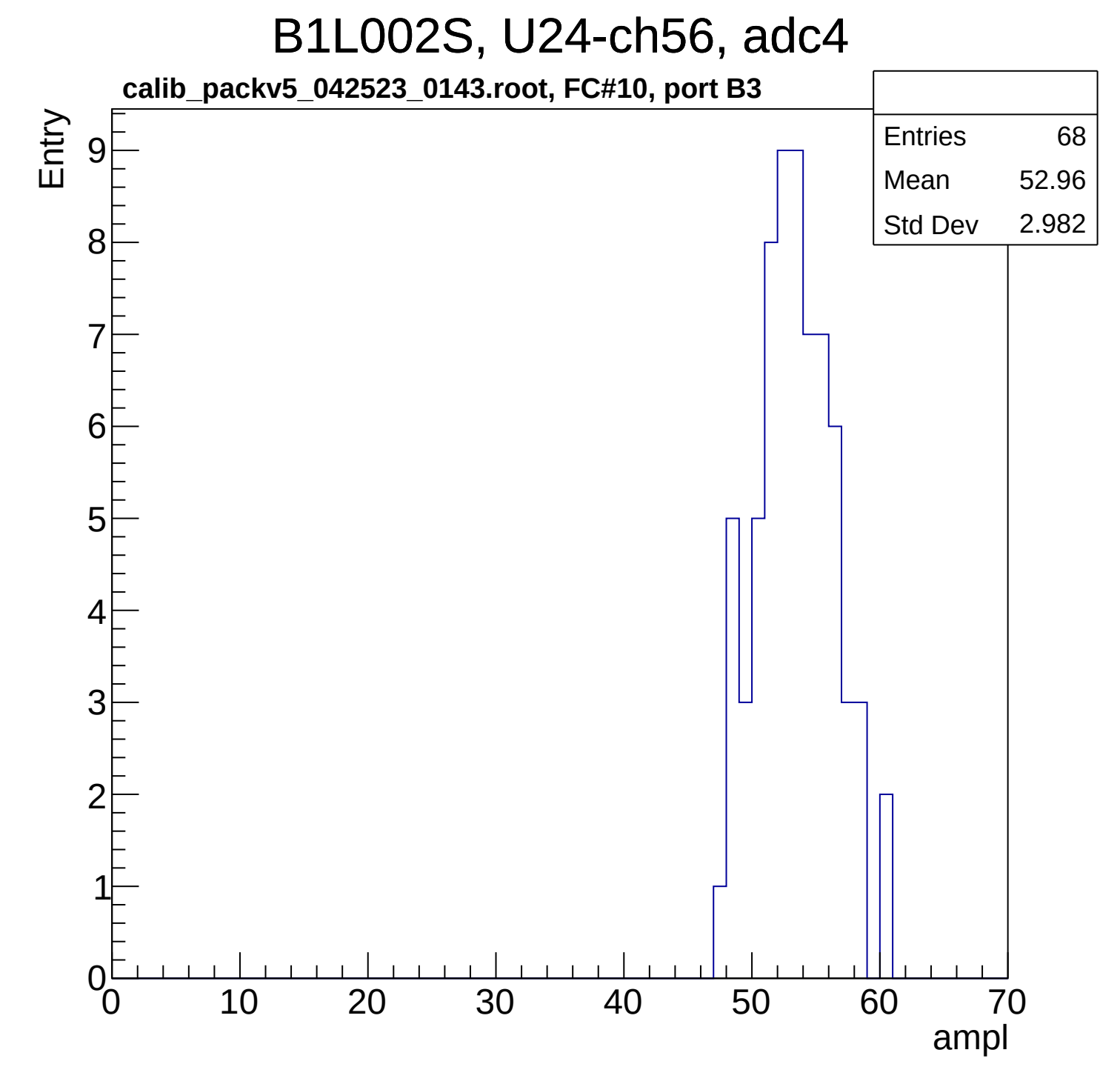
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 52.96
Std Dev 2.982
Entry
ampl
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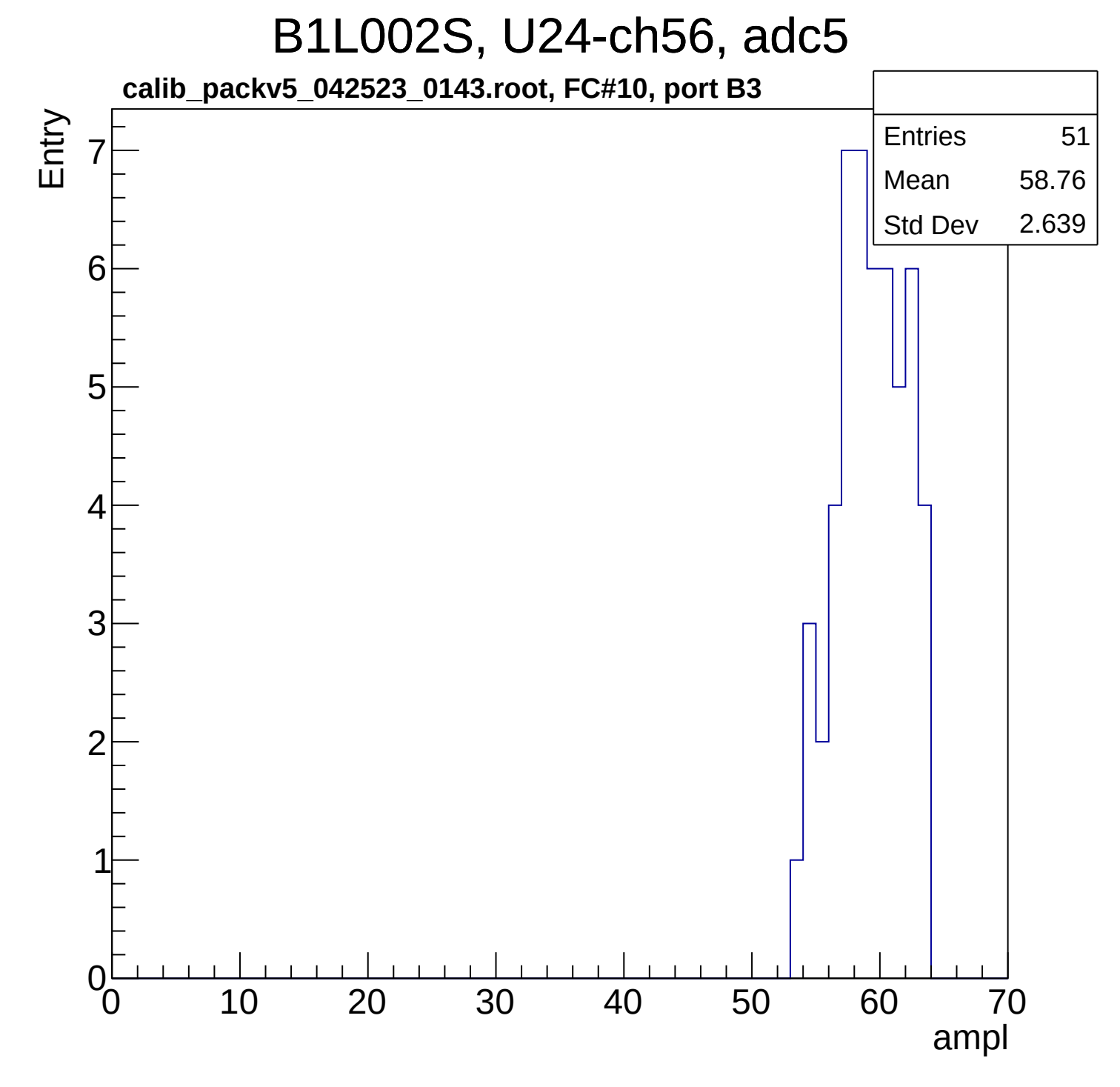
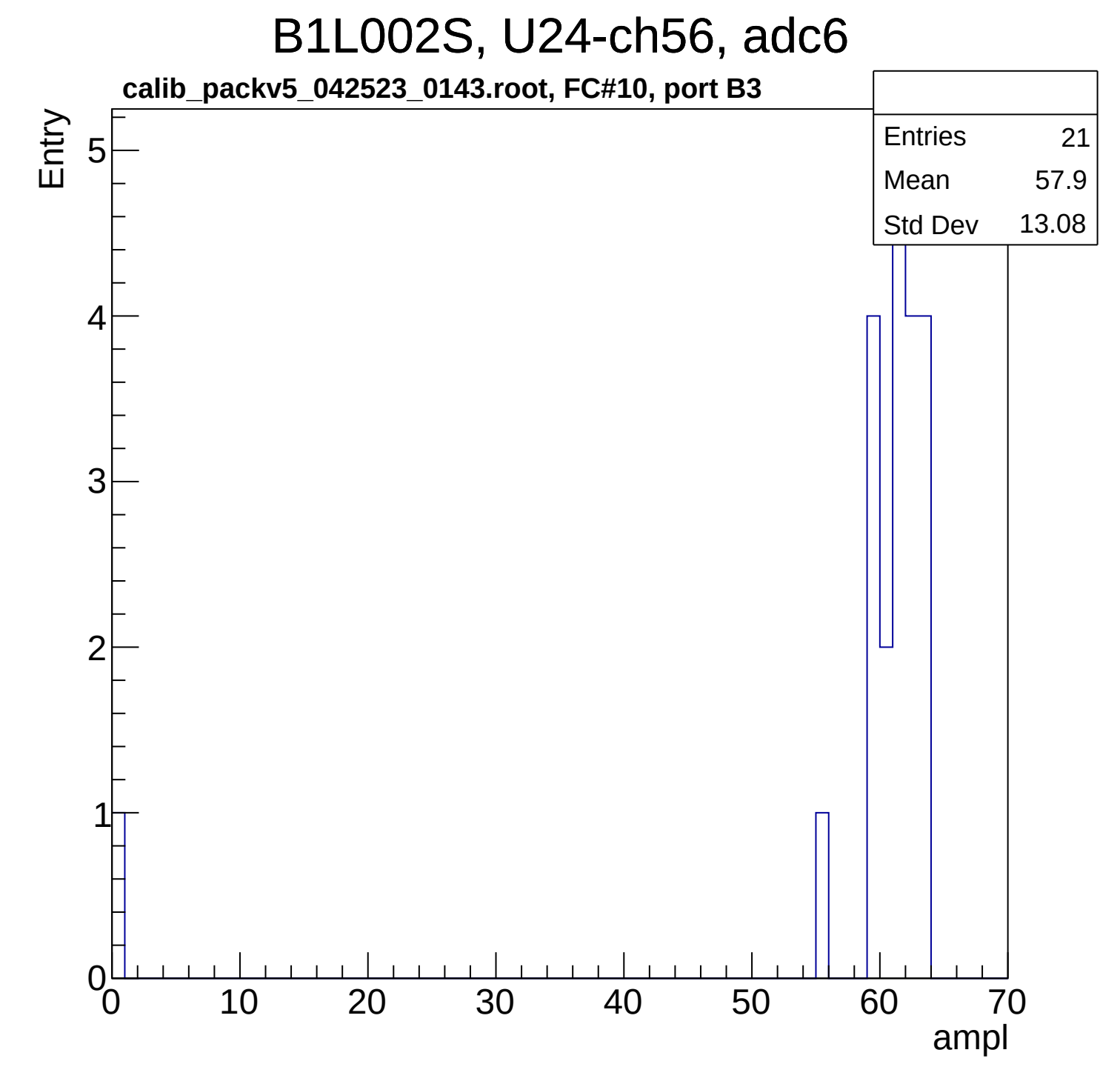
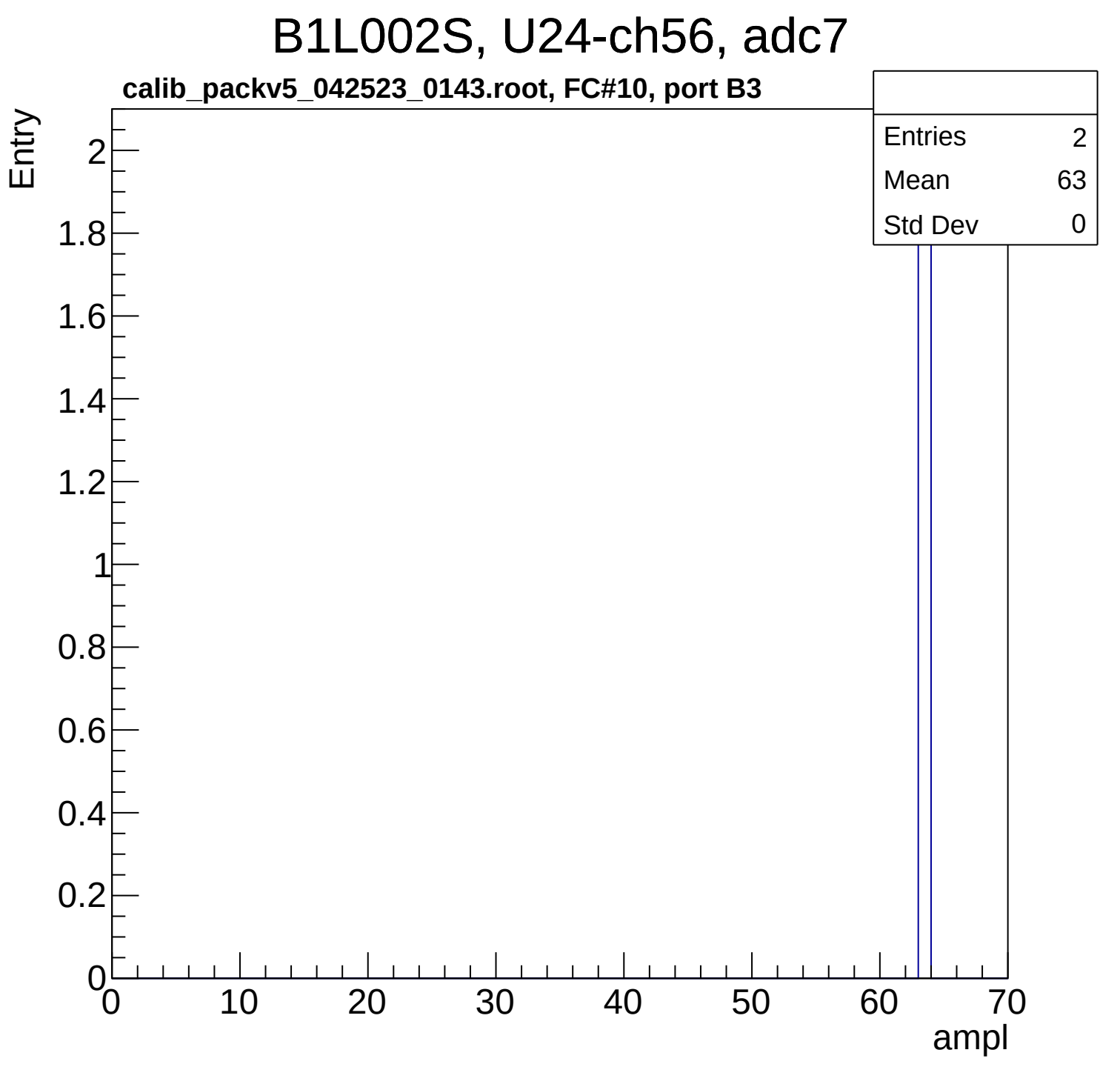
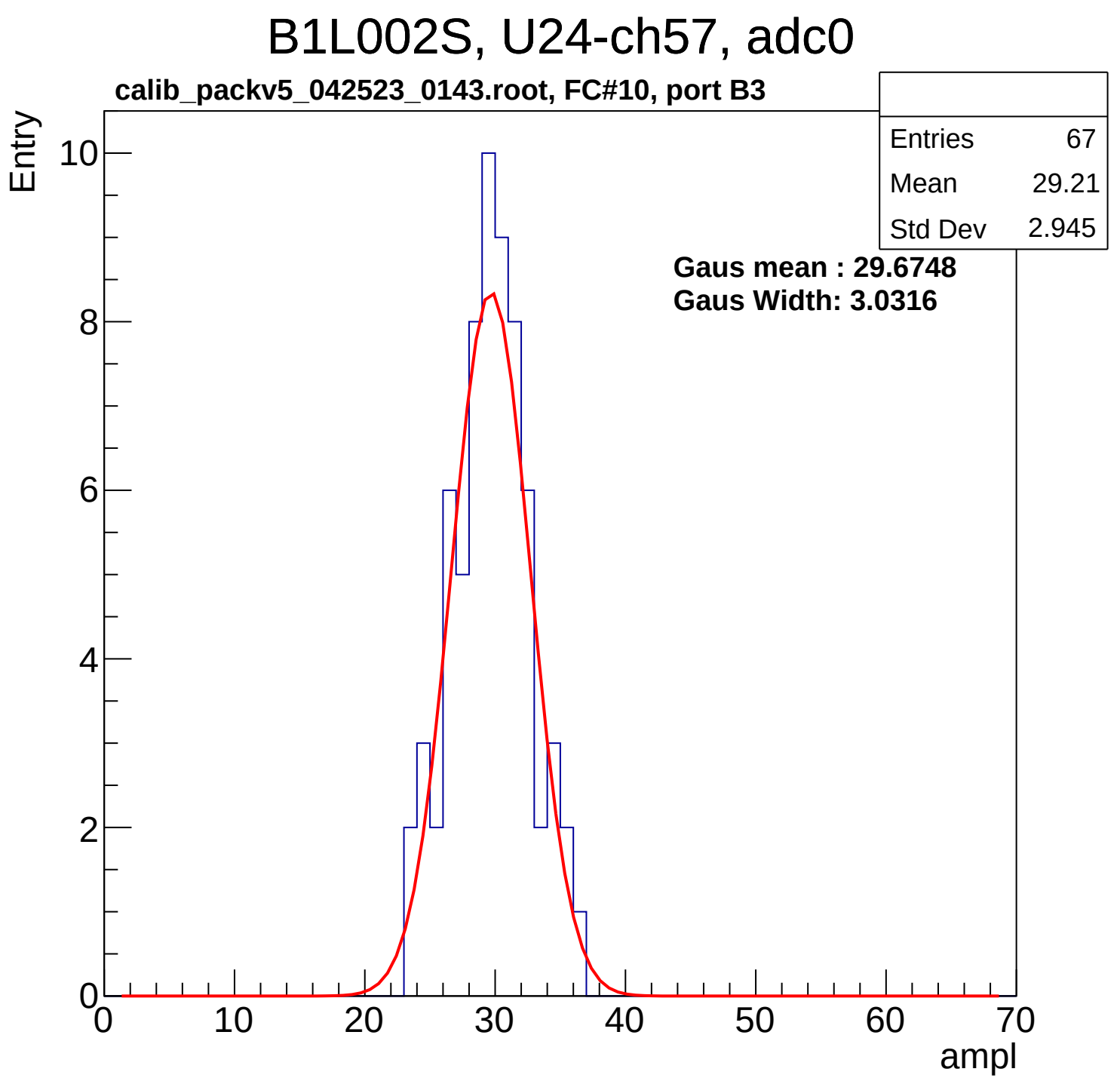
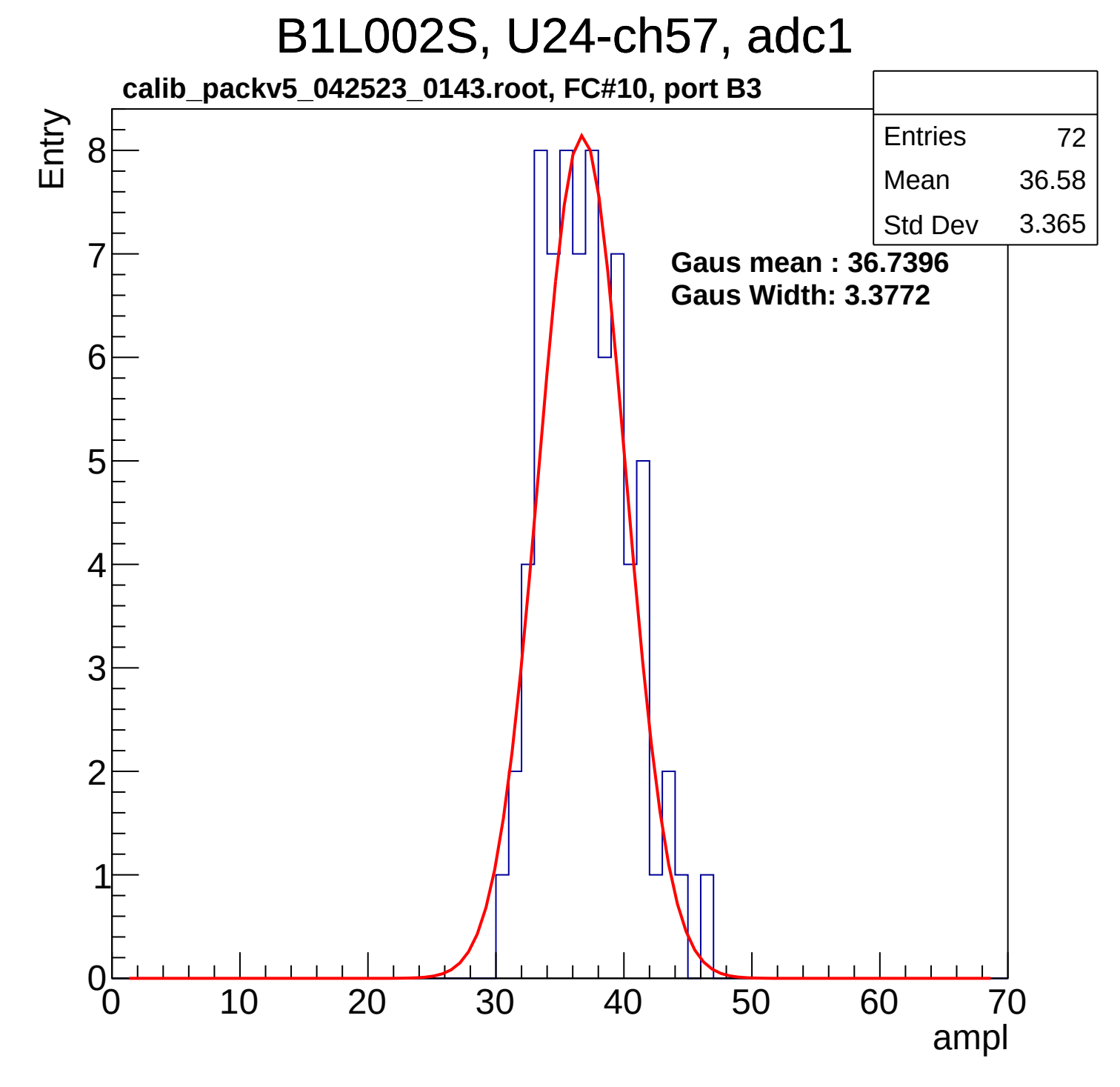
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 58.76
Std Dev 2.639
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch56, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 57.9
Std Dev 13.08
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch56, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 29.21
Std Dev 2.945
Gaus mean : 29.6748
Gaus Width: 3.0316
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 36.58
Std Dev 3.365
Gaus mean : 36.7396
Gaus Width: 3.3772
Entry
ampl
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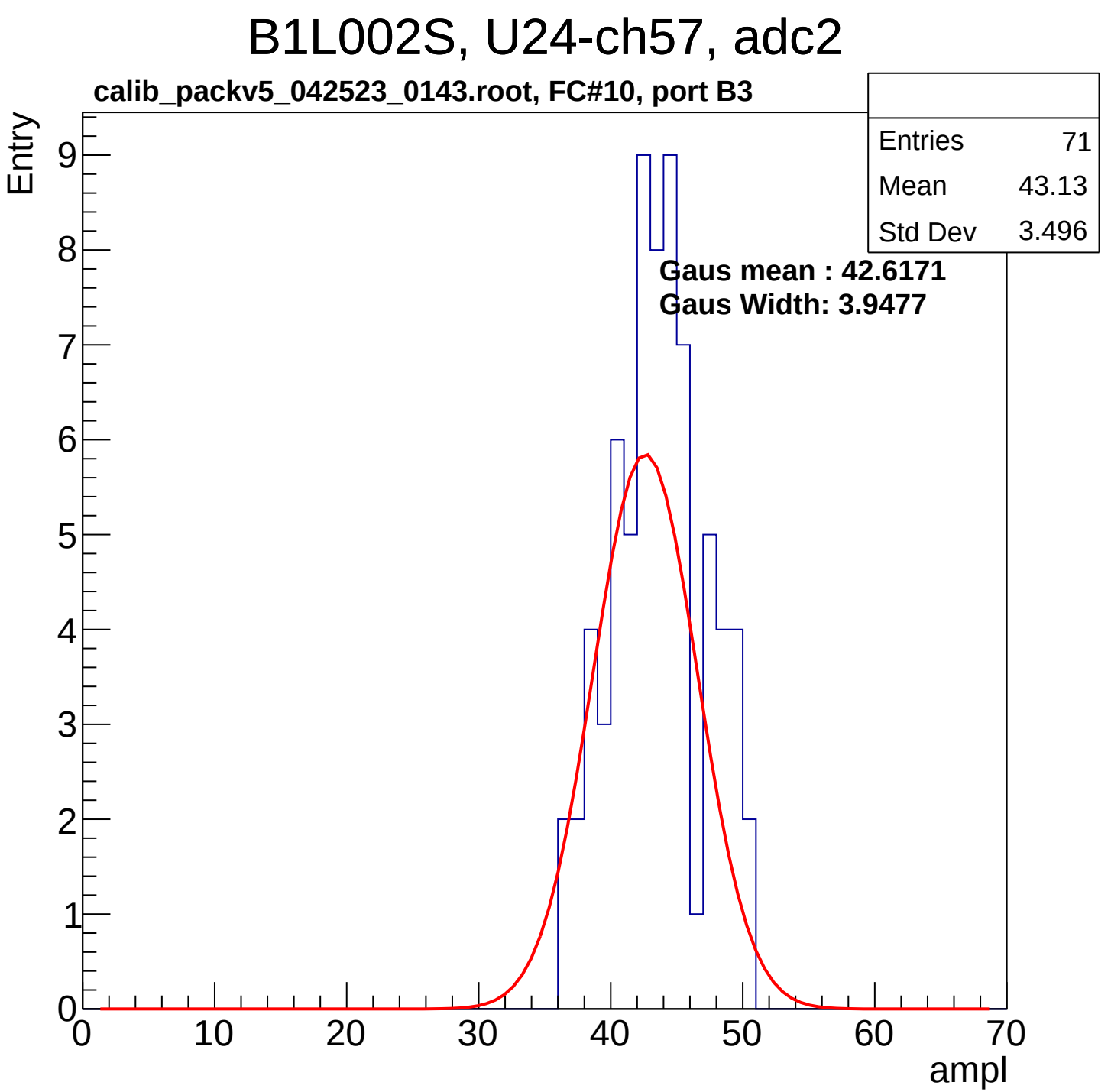
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 43.13
Std Dev 3.496
Gaus mean : 42.6171
Gaus Width: 3.9477
Entry
ampl
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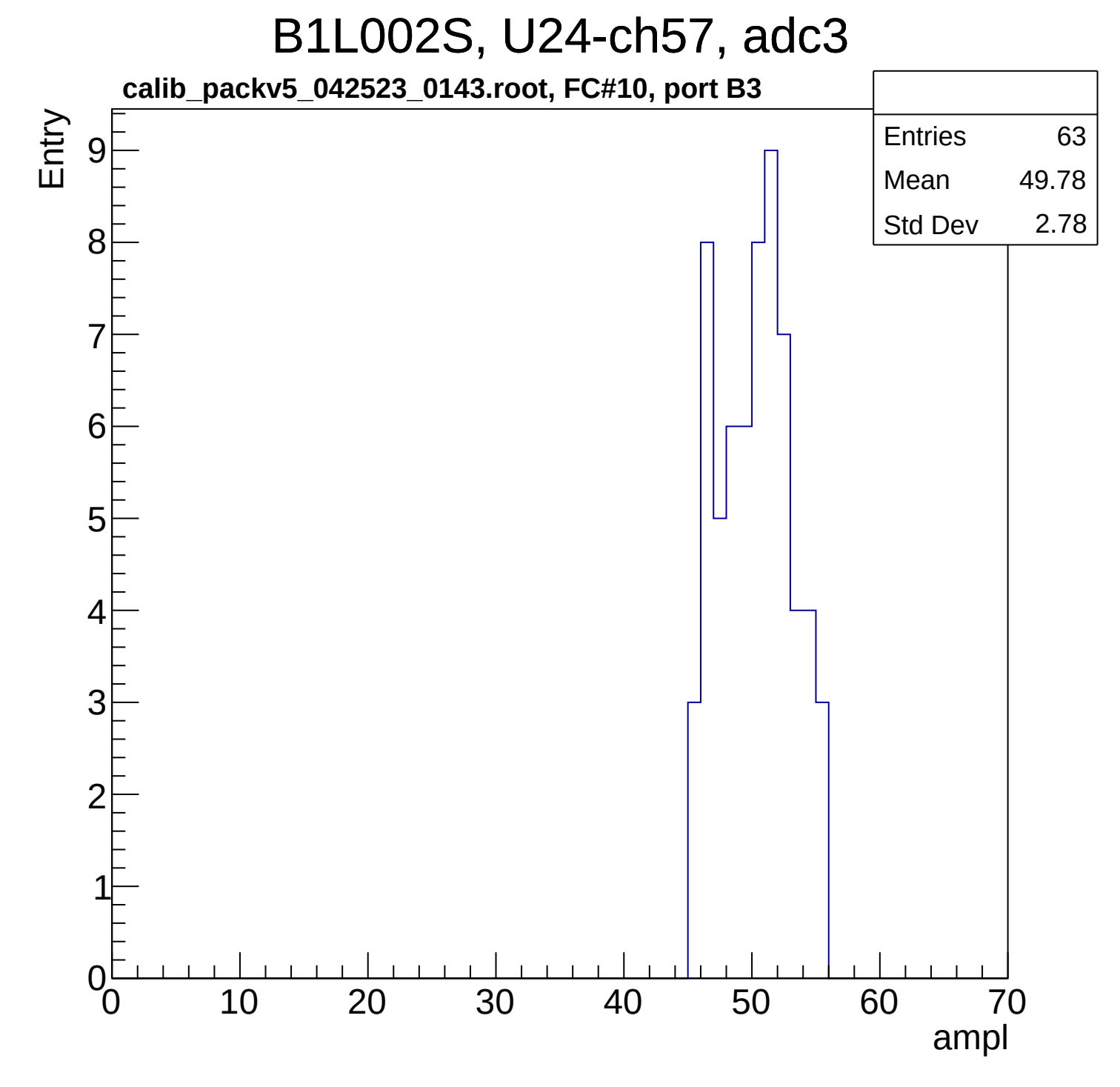
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U24-ch57, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 49.78
Std Dev 2.78
Entry
ampl
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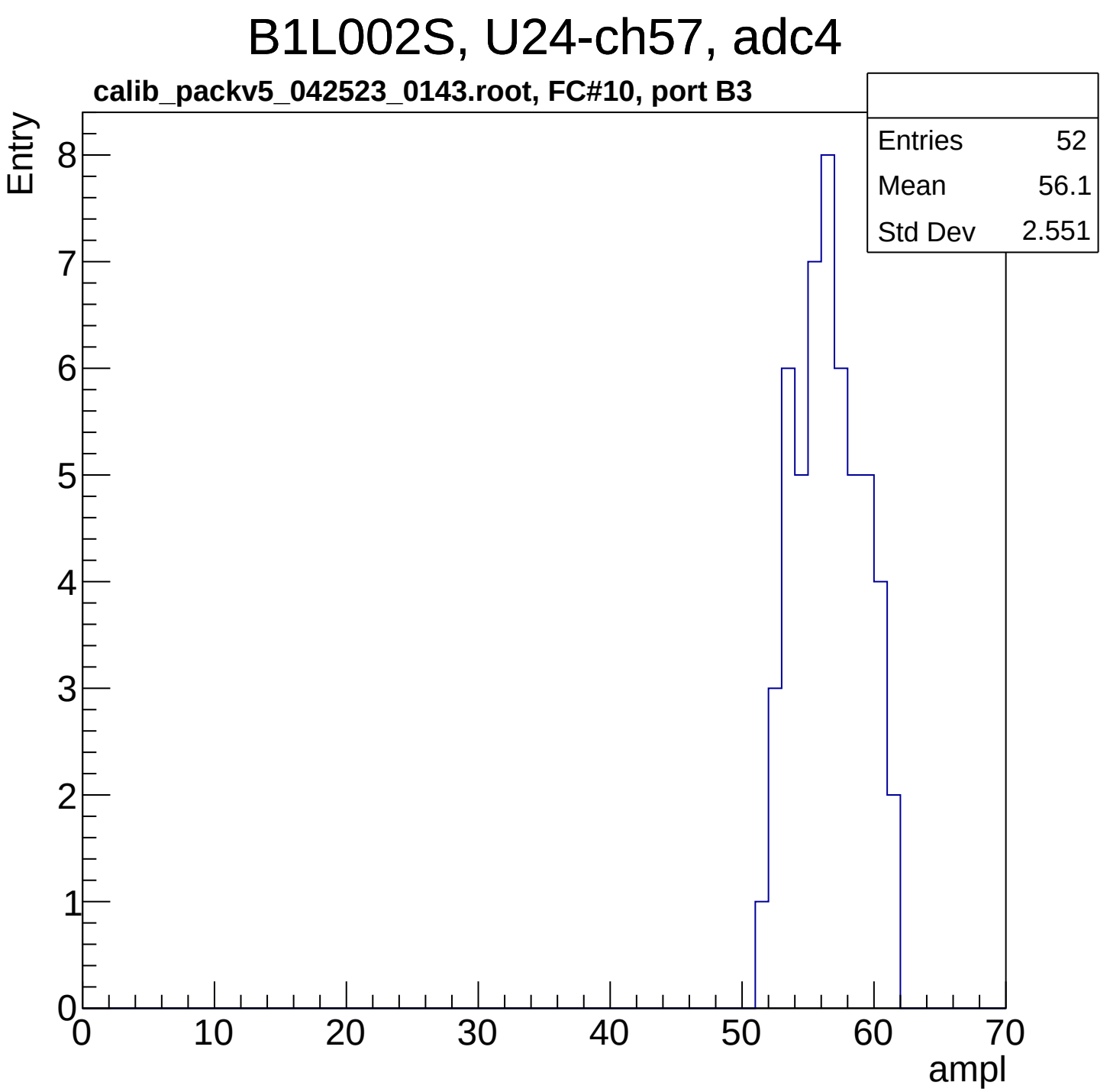
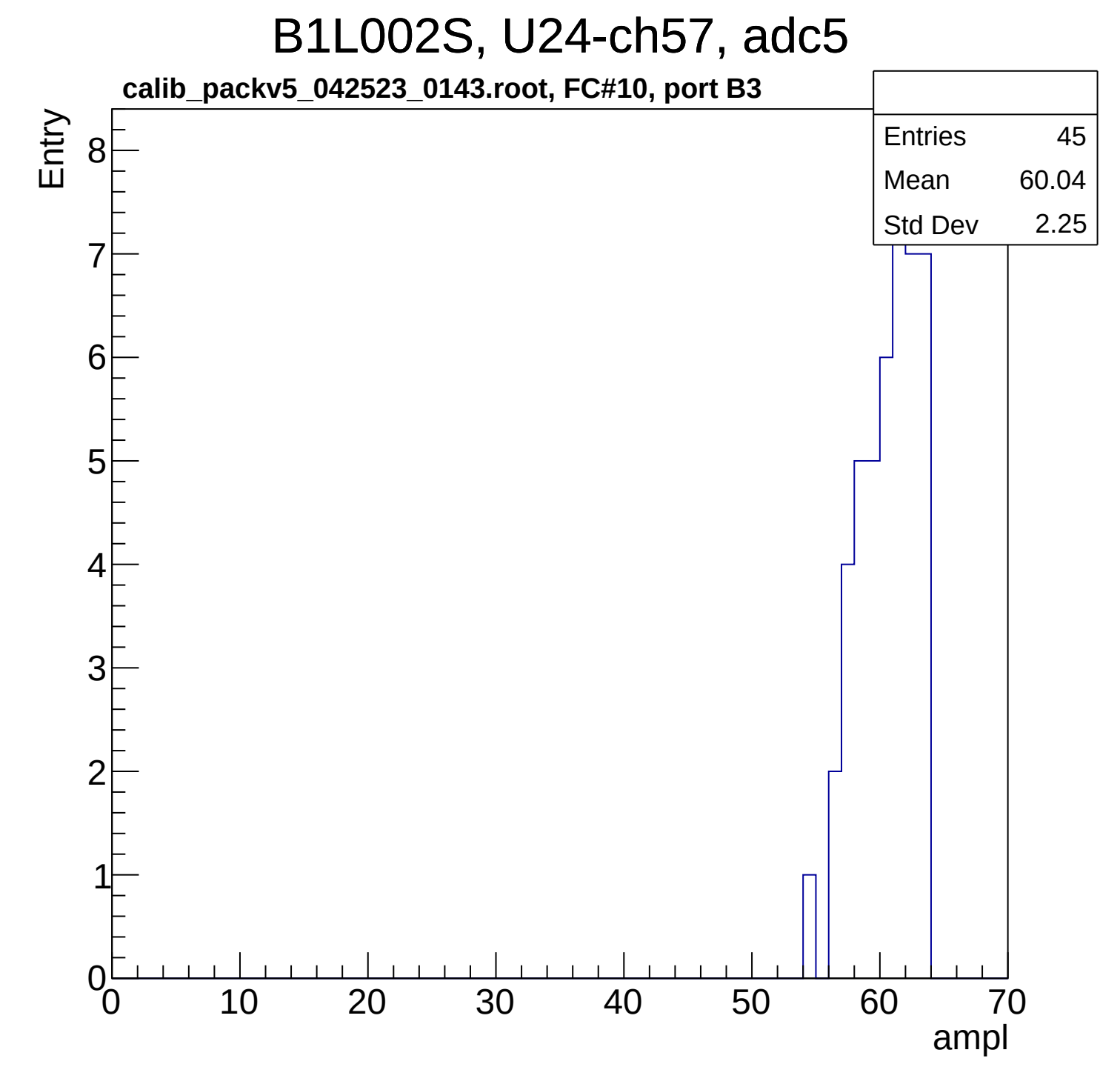
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 56.1
Std Dev 2.551
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 60.04
Std Dev 2.25
Entry
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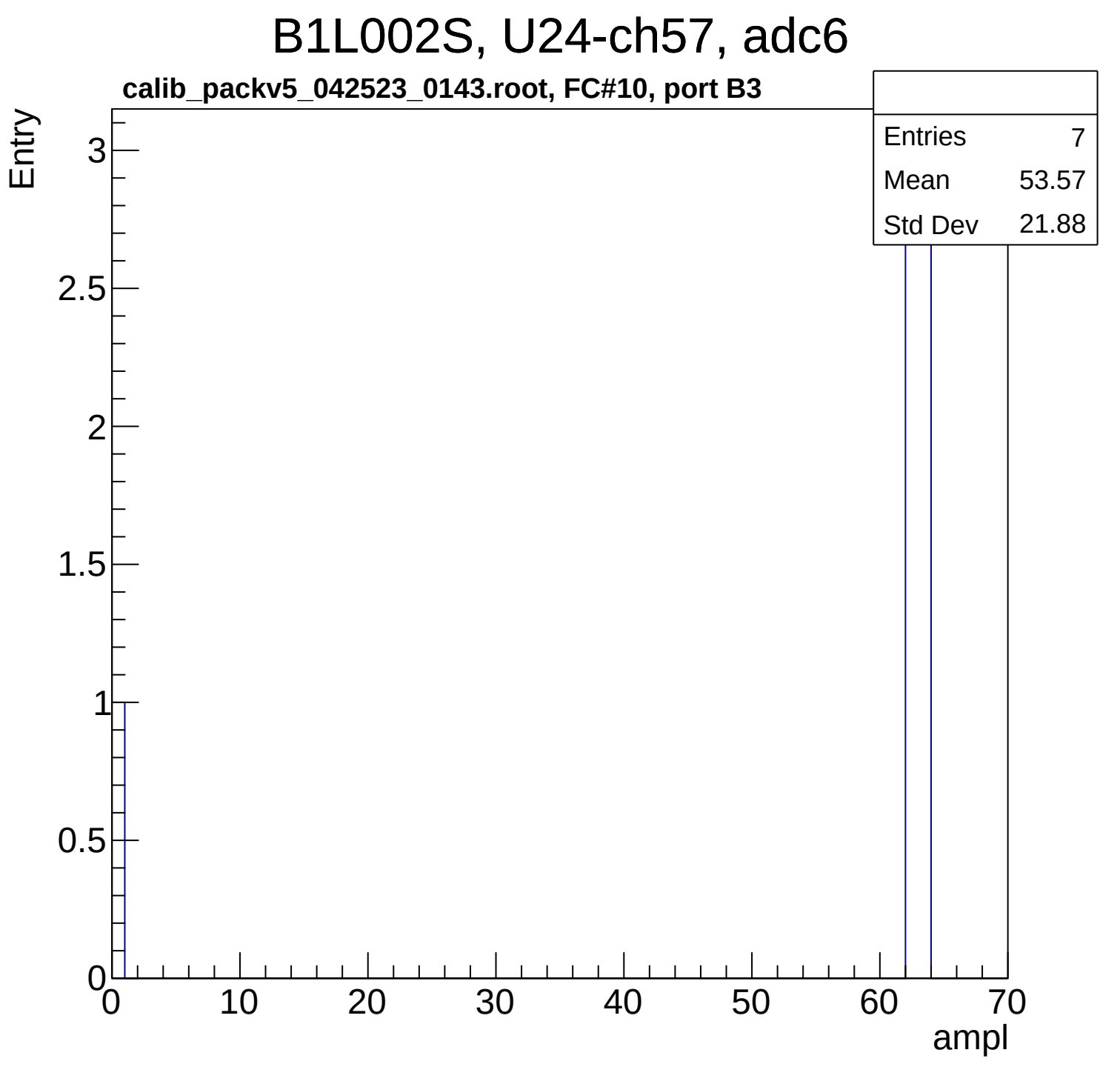
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch57, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53.57
Std Dev 21.88
Entry
ampl
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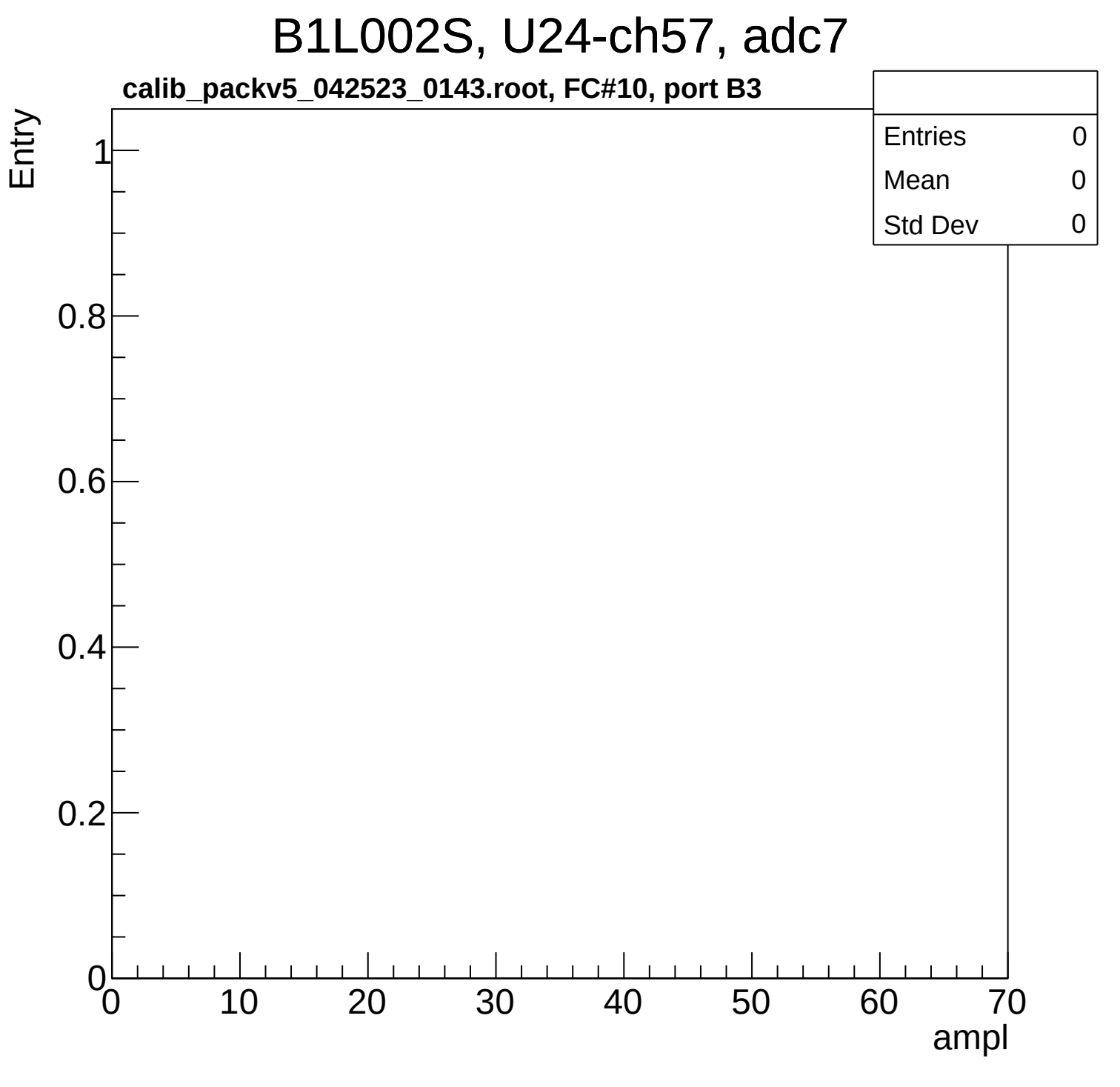
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch57, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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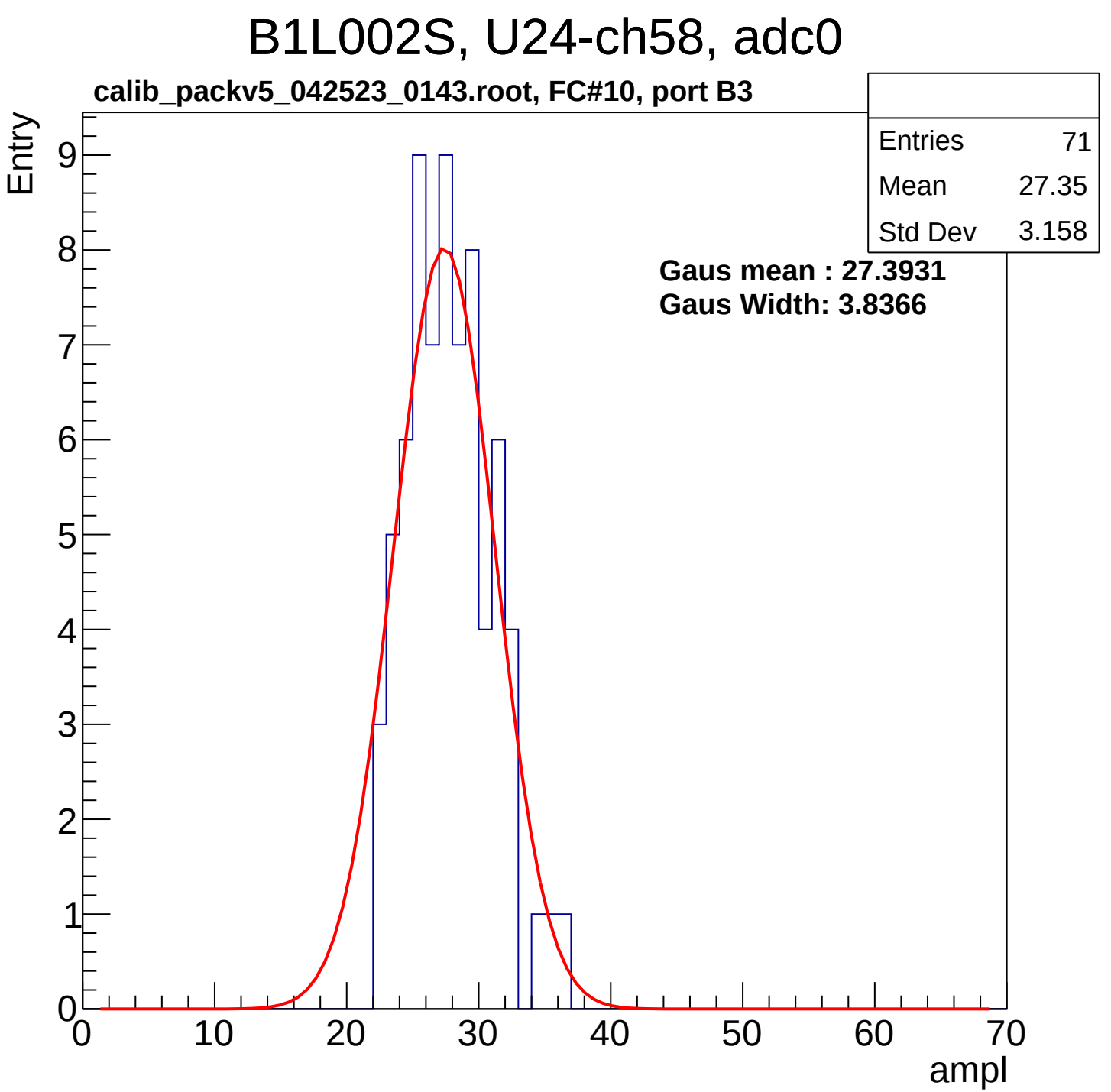
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch58, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 27.35
Std Dev 3.158
Gaus mean : 27.3931
Gaus Width: 3.8366
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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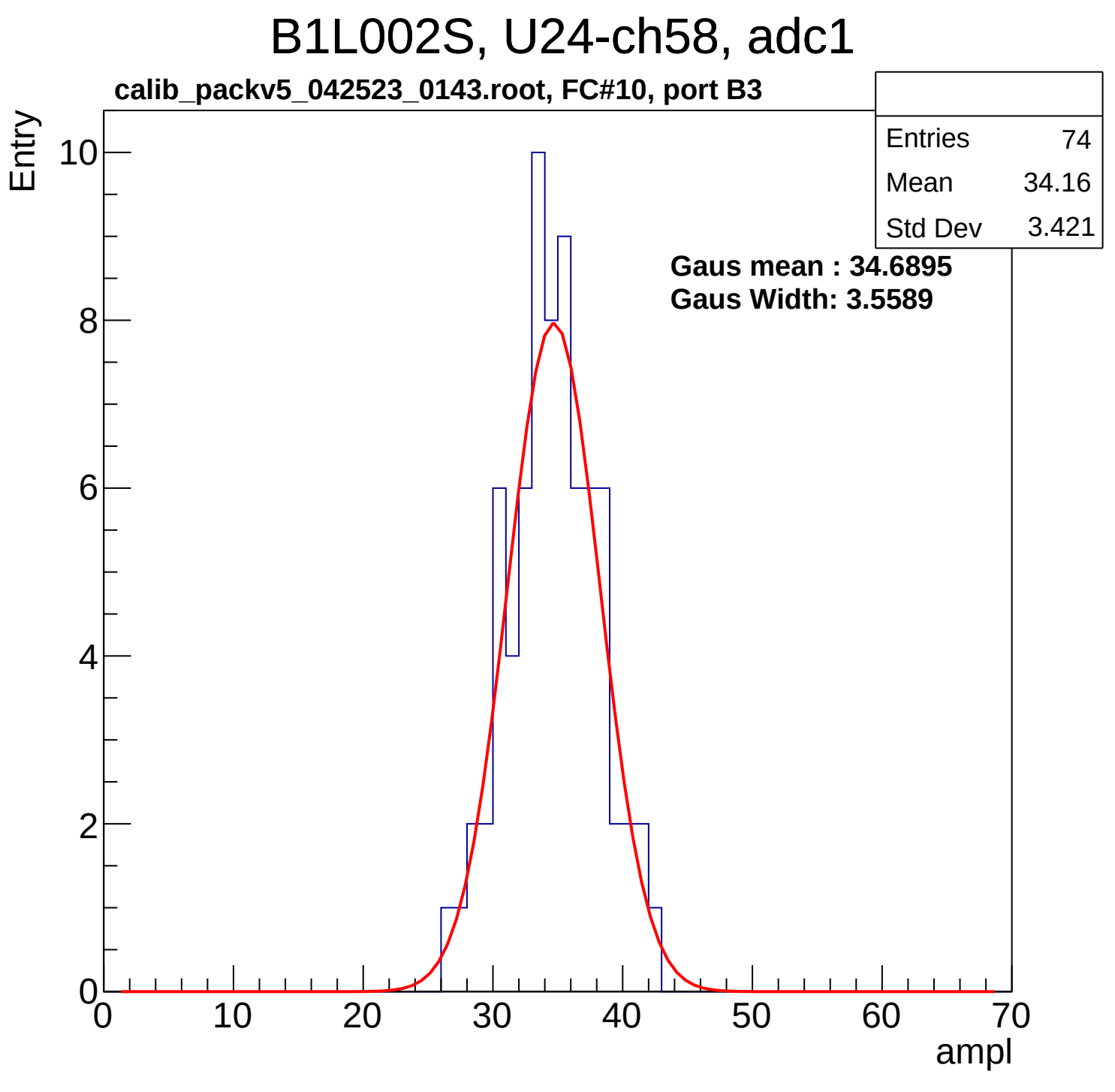
B1L002S, U24-ch58, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.16
Std Dev 3.421
Gaus mean : 34.6895
Gaus Width: 3.5589
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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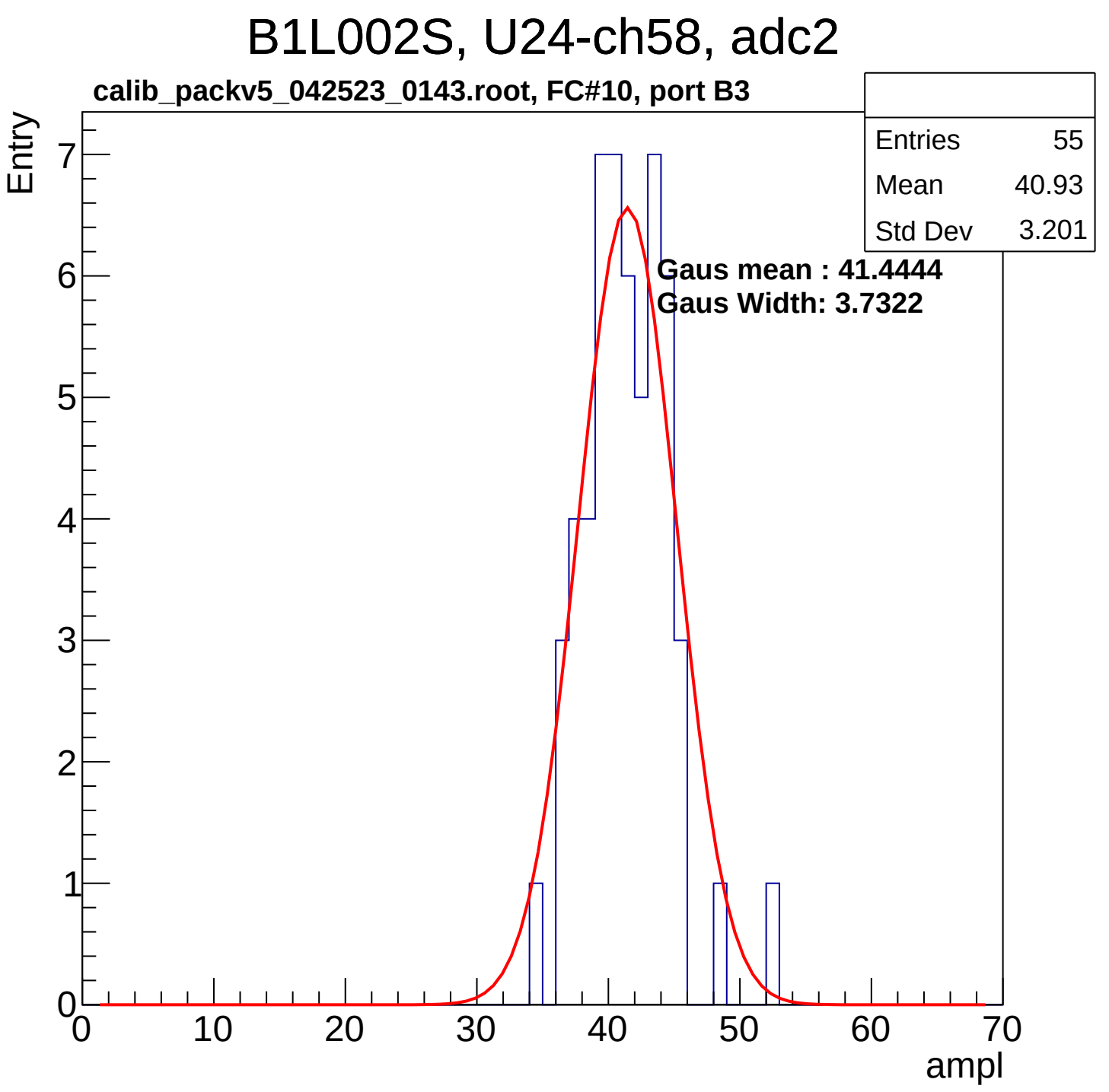
B1L002S, U24-ch58, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.93
Std Dev 3.201
Gaus mean : 41.4444
Gaus Width: 3.7322
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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60
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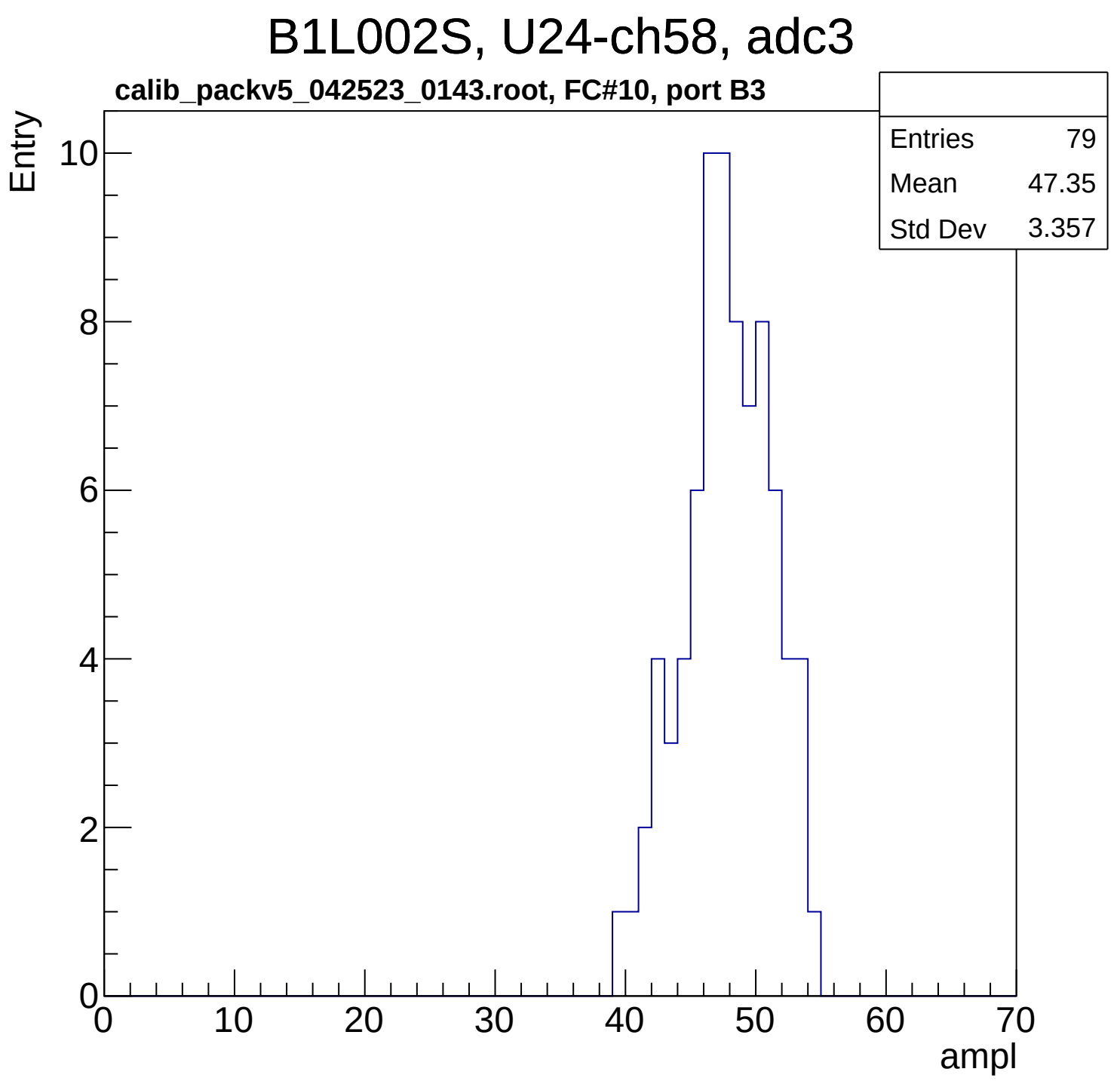
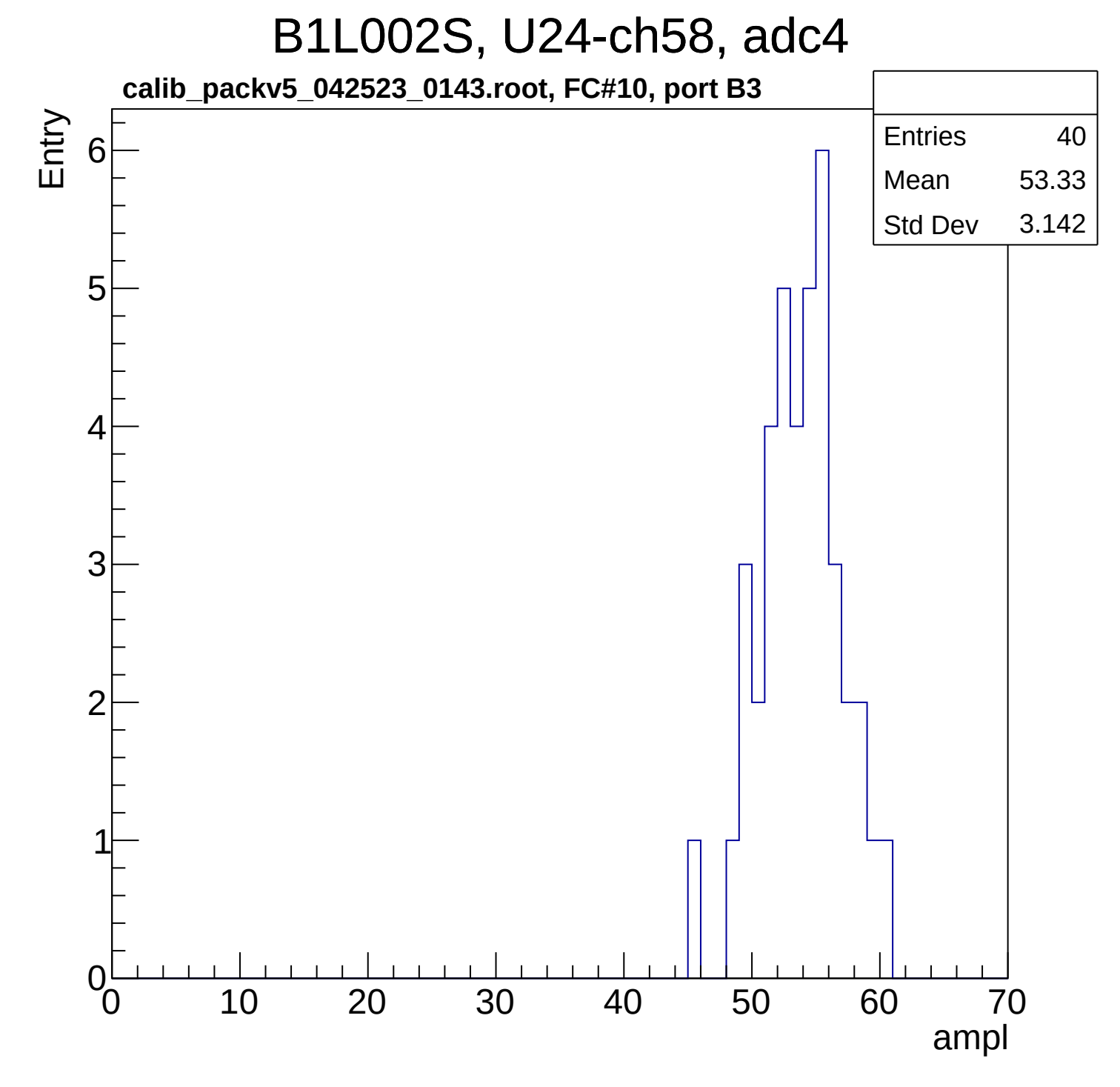
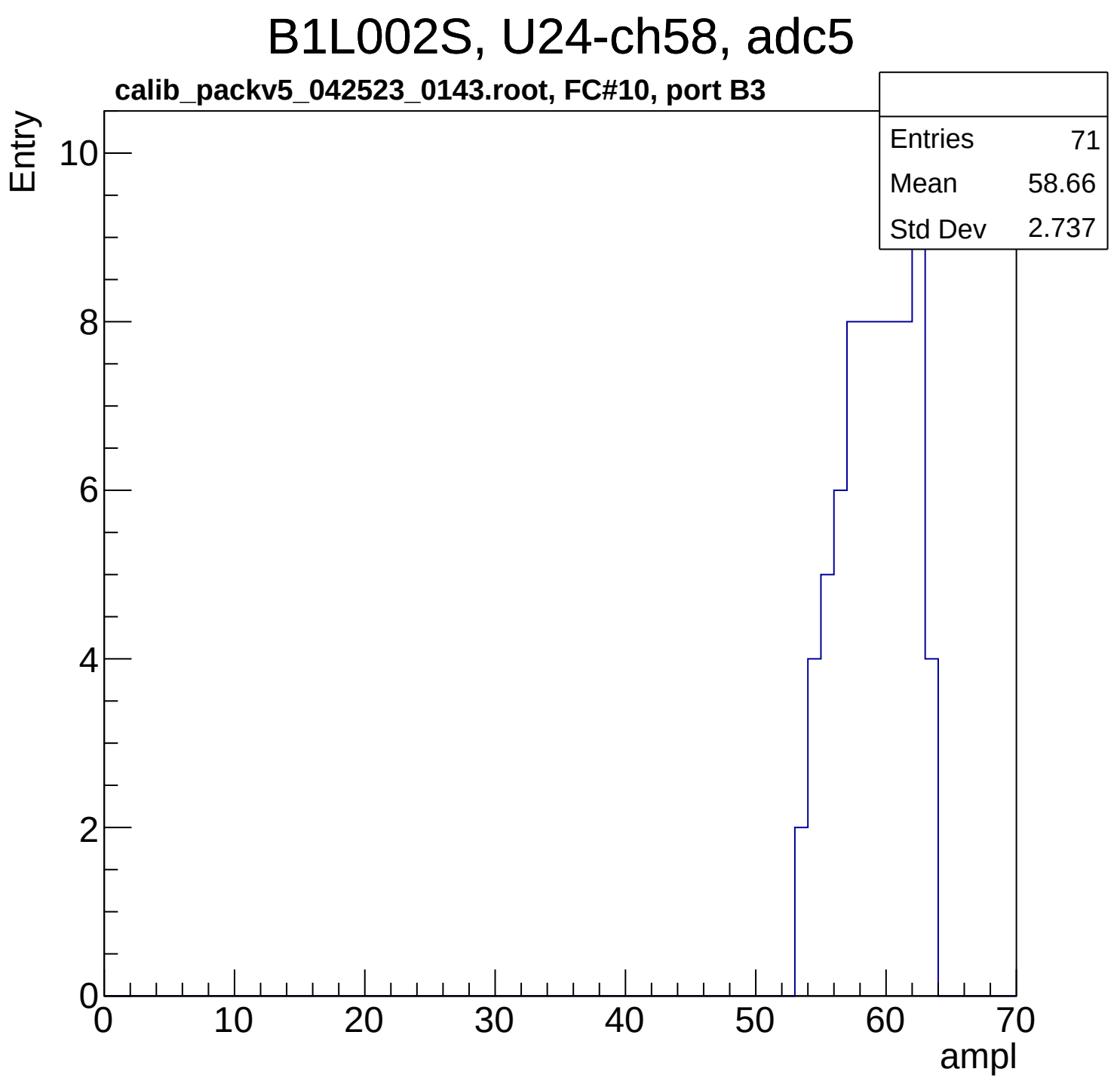
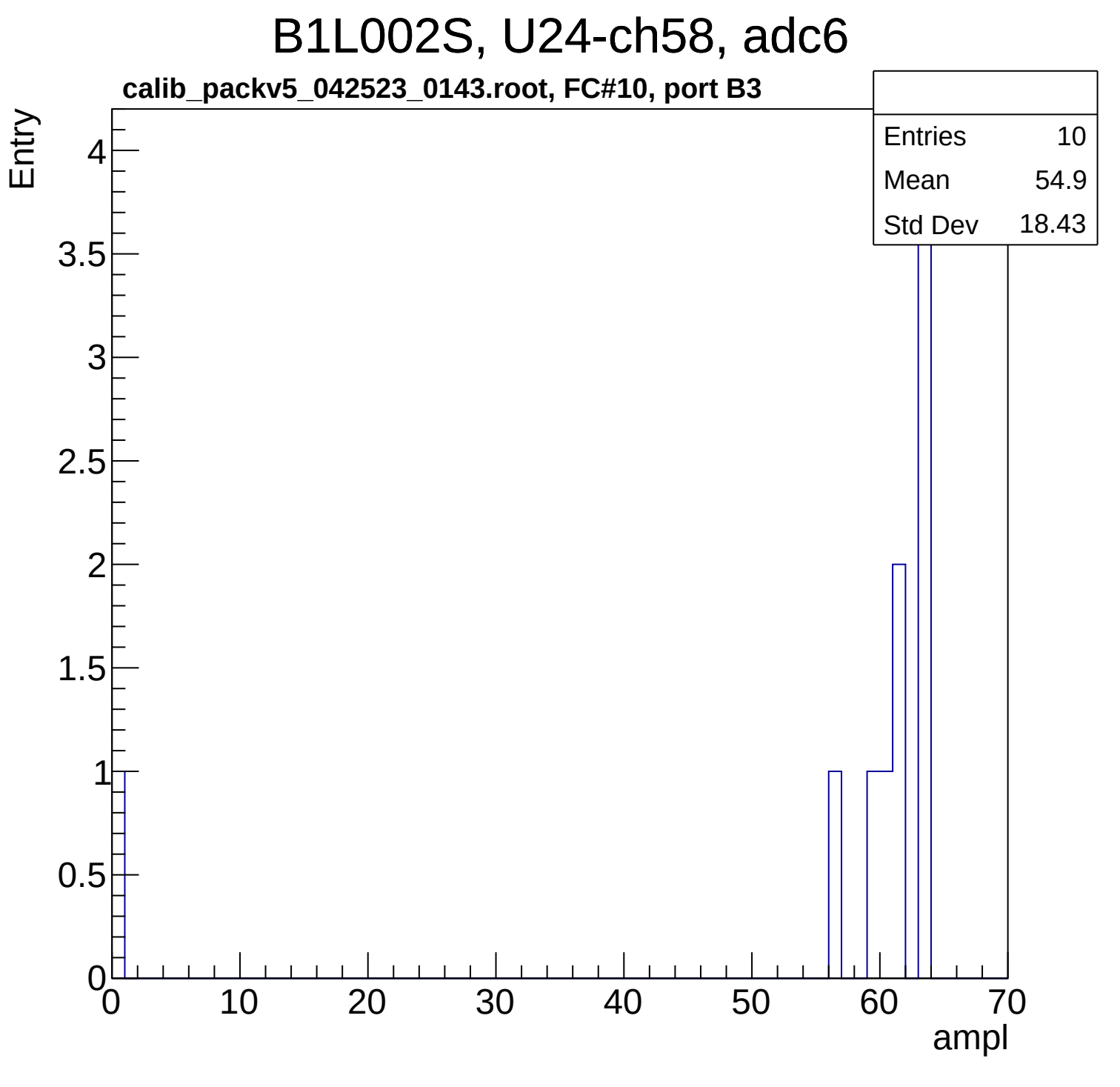

B1L002S, U24-ch58, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 47.35
Std Dev 3.357
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch58, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 53.33
Std Dev 3.142
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch58, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 58.66
Std Dev 2.737
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch58, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 54.9
Std Dev 18.43
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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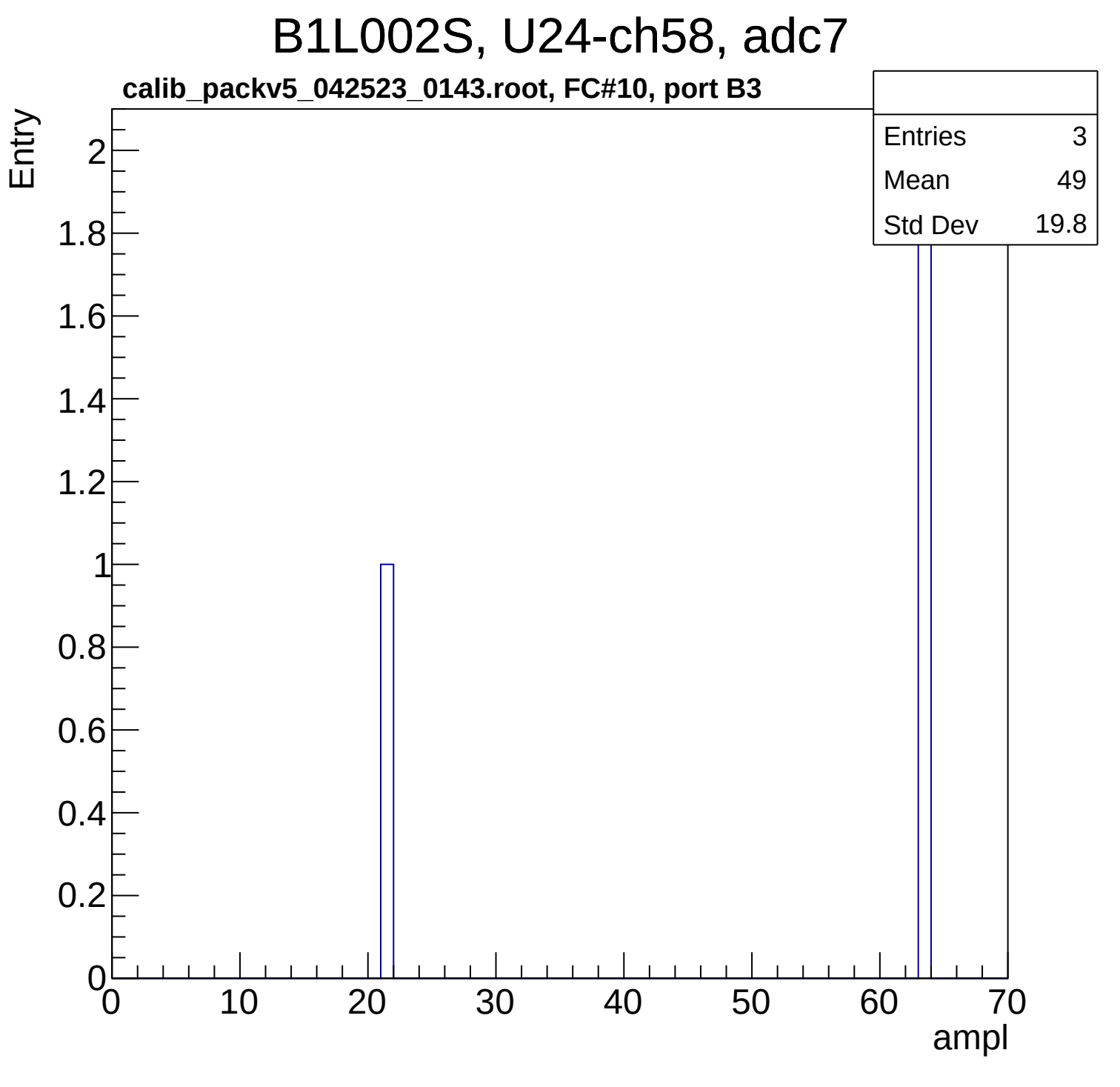
B1L002S, U24-ch58, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 49
Std Dev 19.8
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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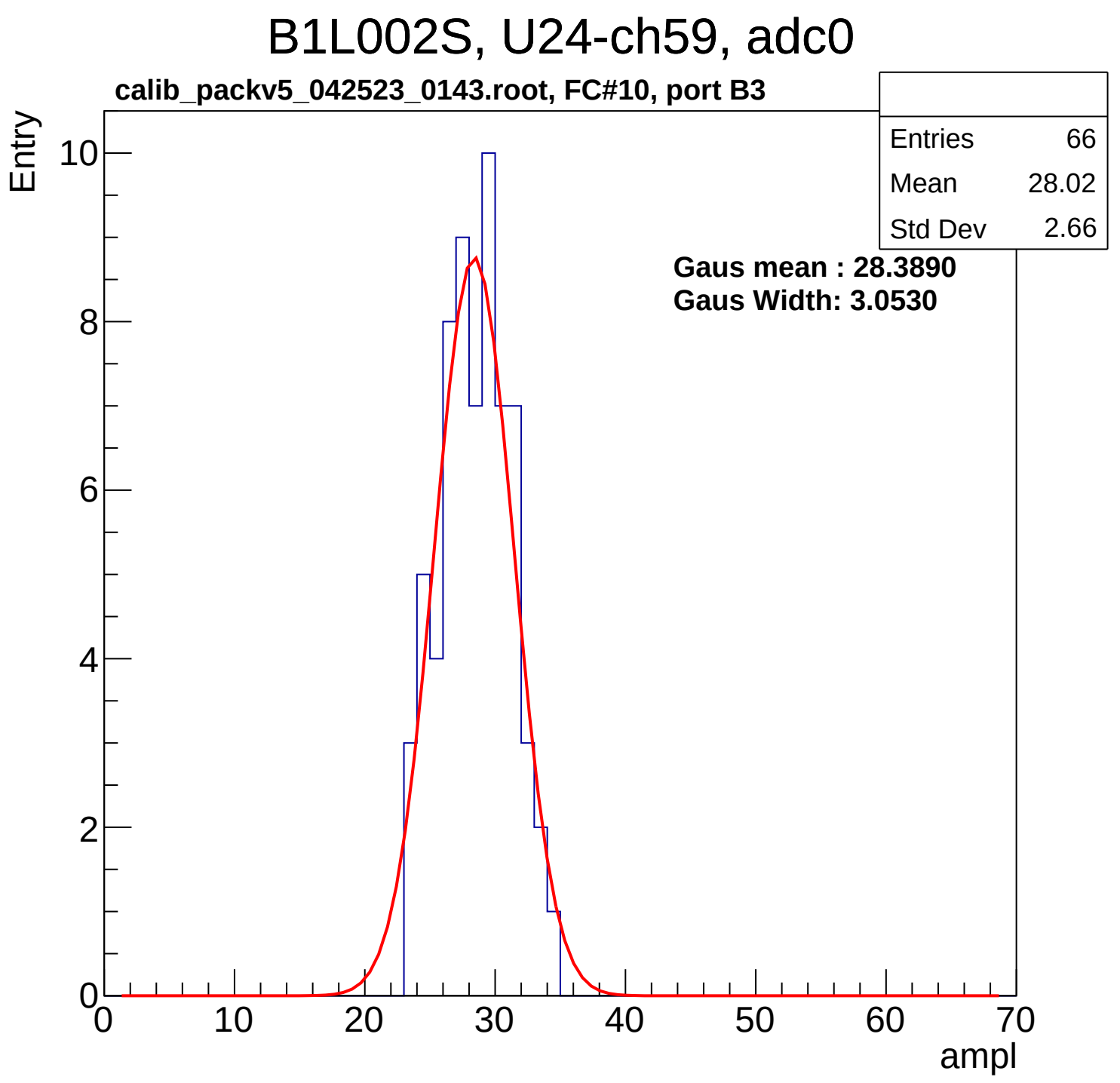
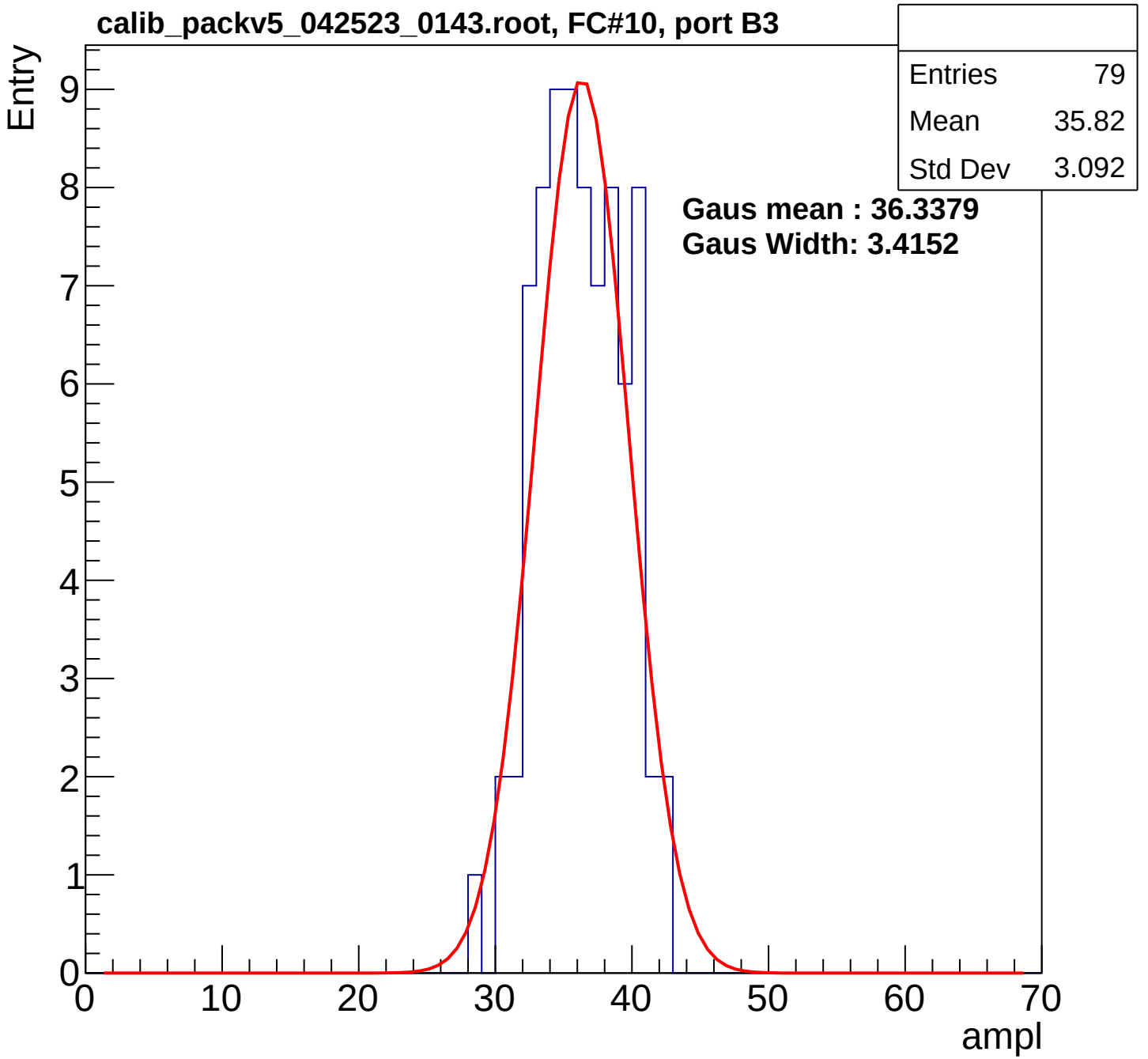
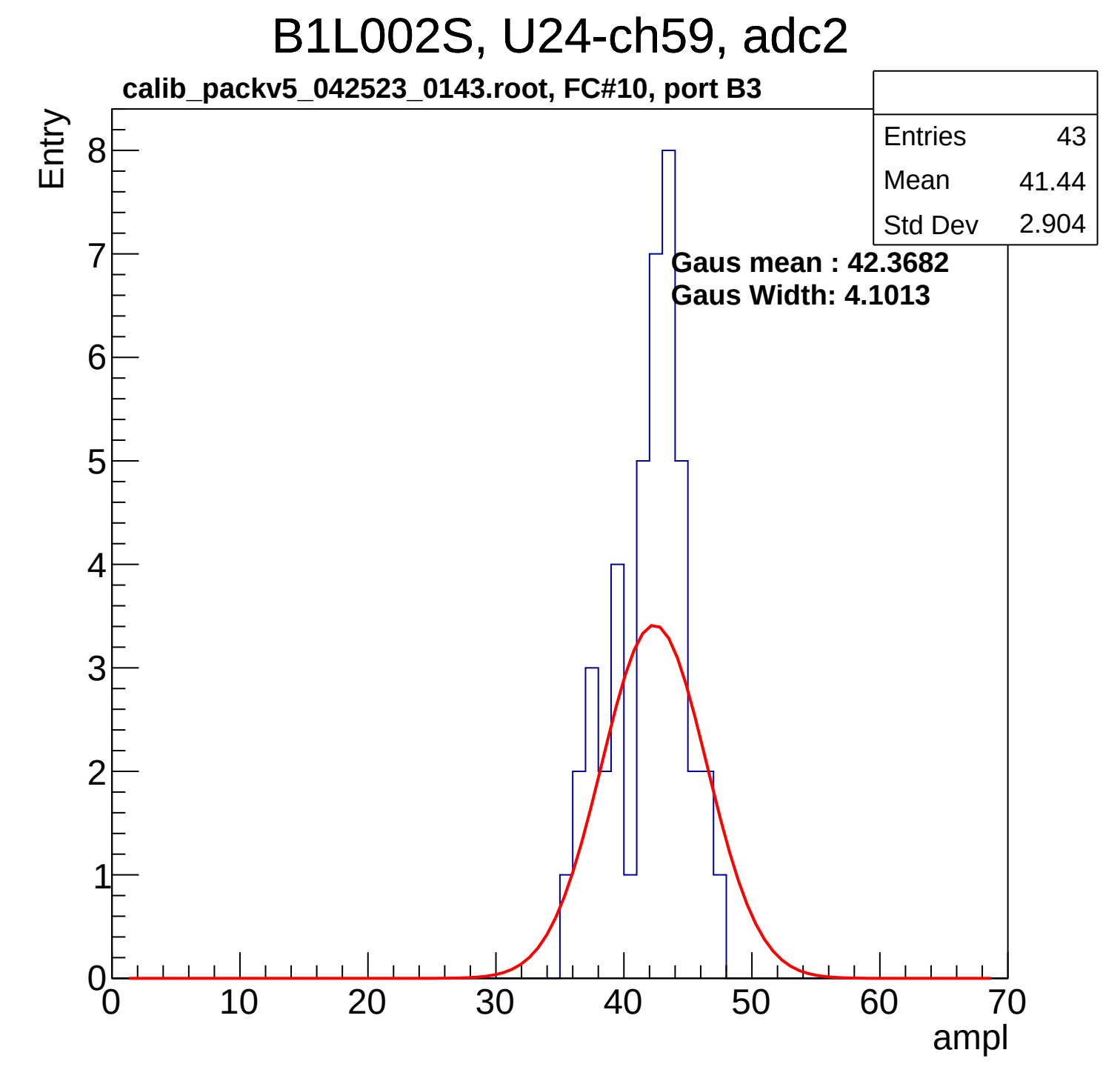
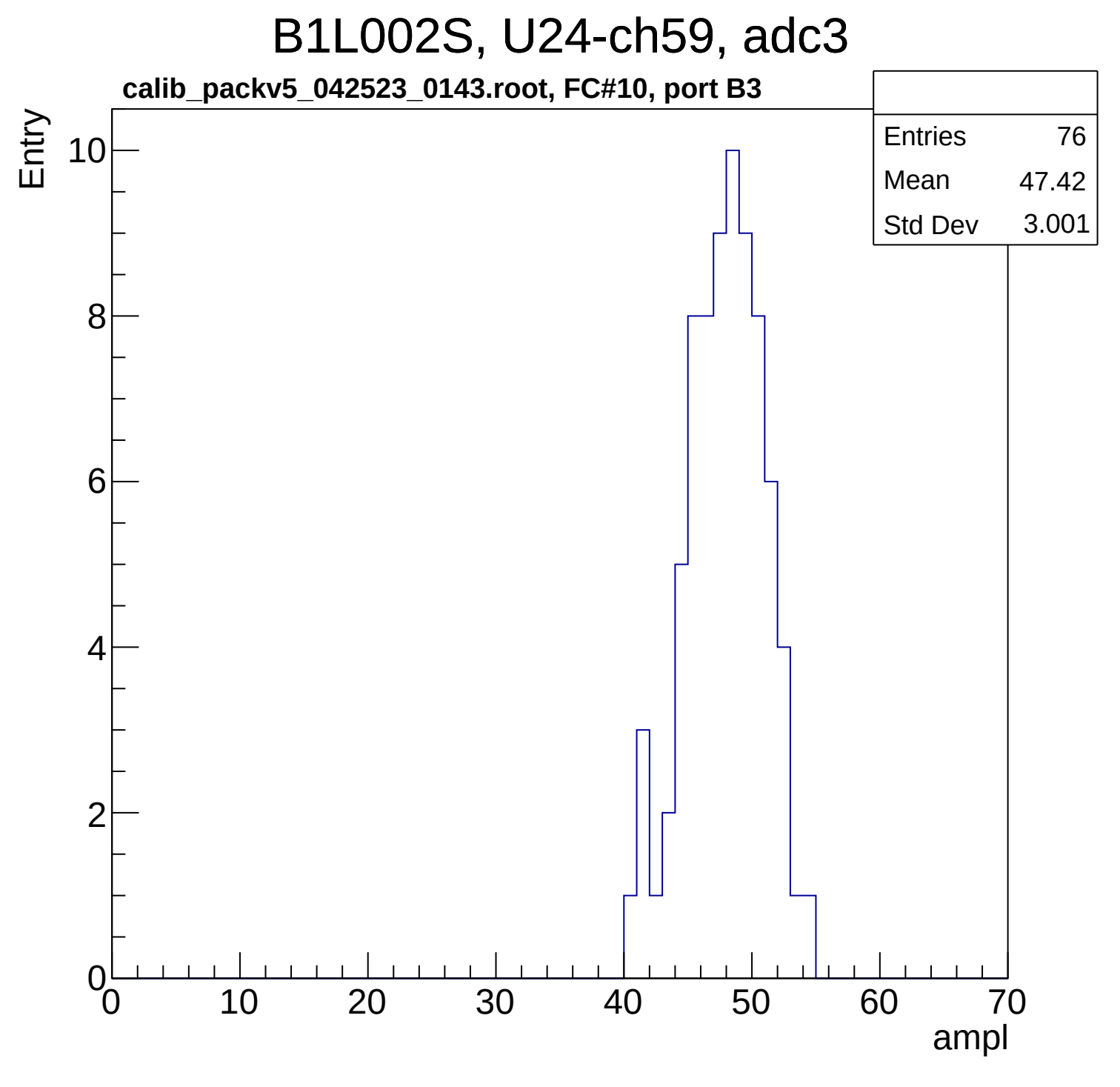
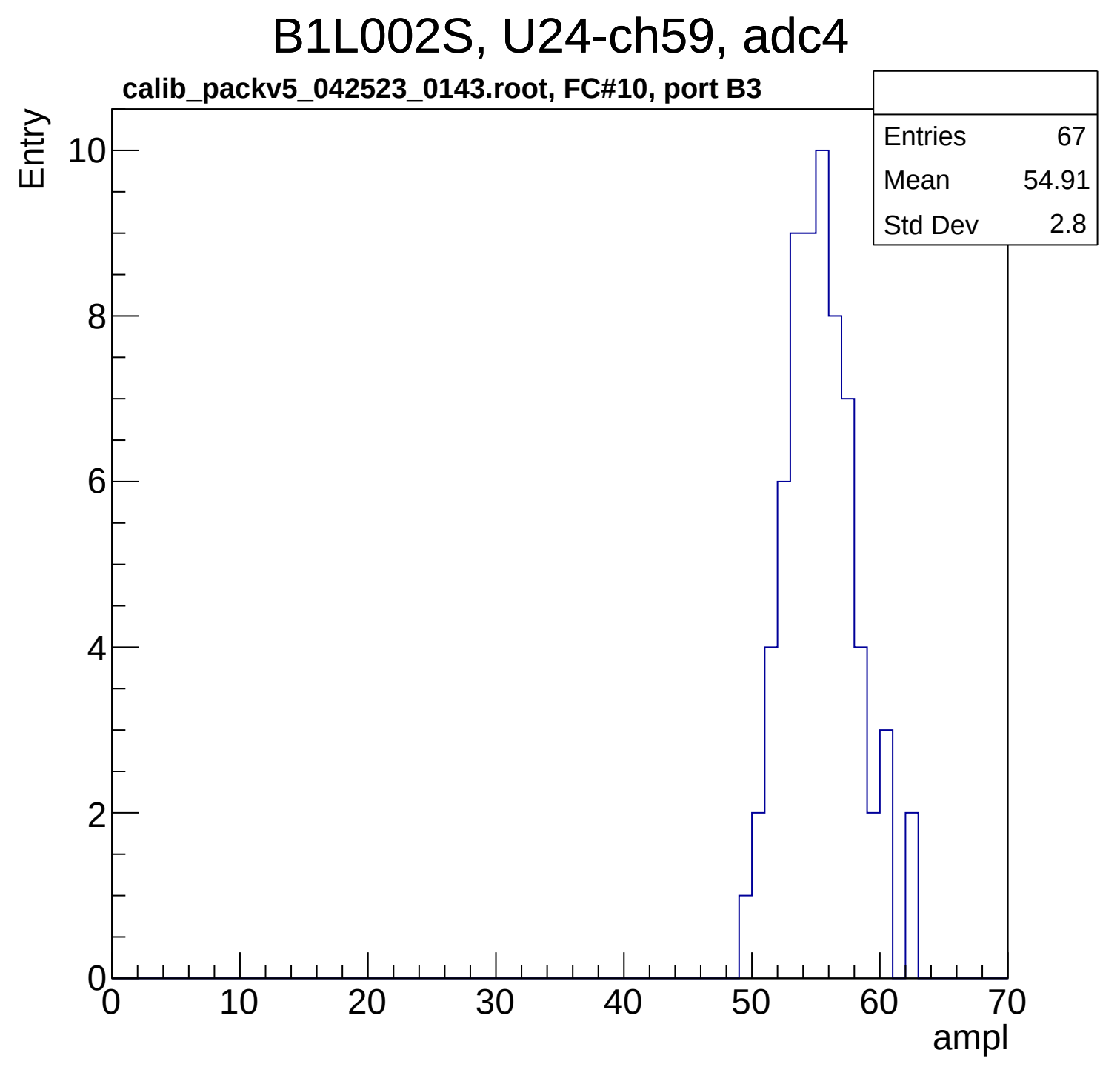
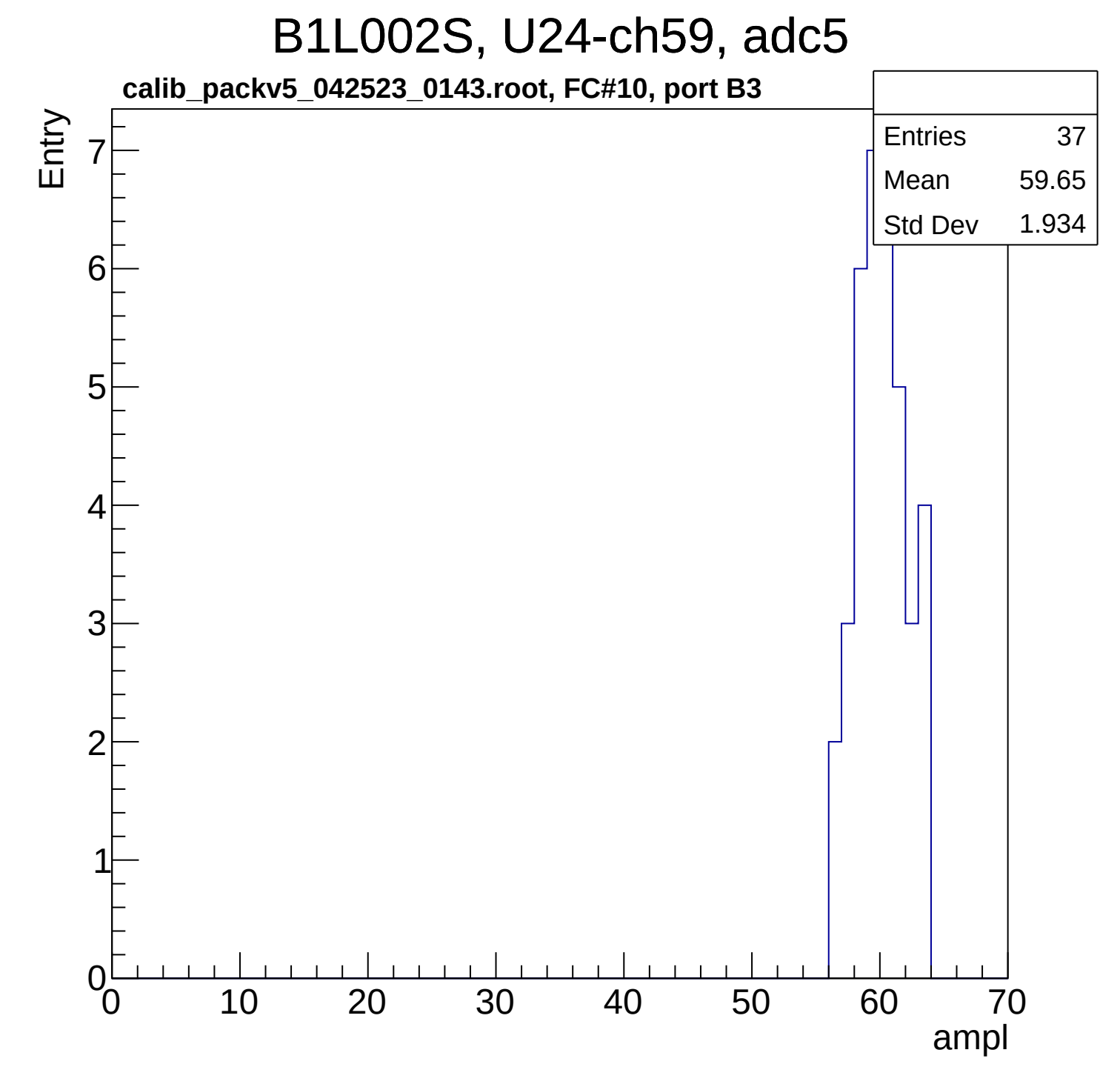

B1L002S, U24-ch59, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 28.02
Std Dev 2.66
Gaus mean : 28.3890
Gaus Width: 3.0530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

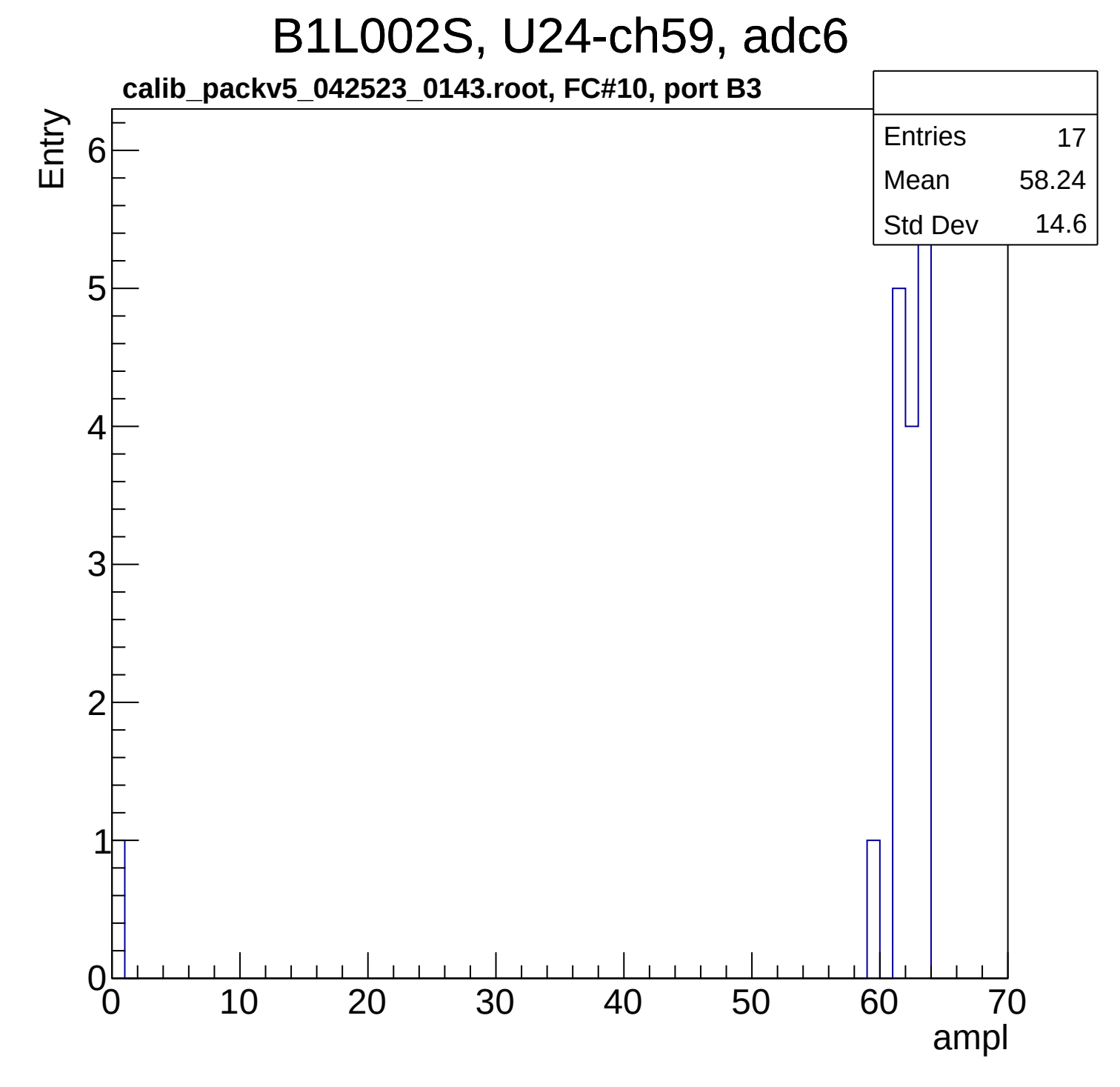

B1L002S, U24-ch59, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 35.82
Std Dev 3.092
Gaus mean : 36.3379
Gaus Width: 3.4152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

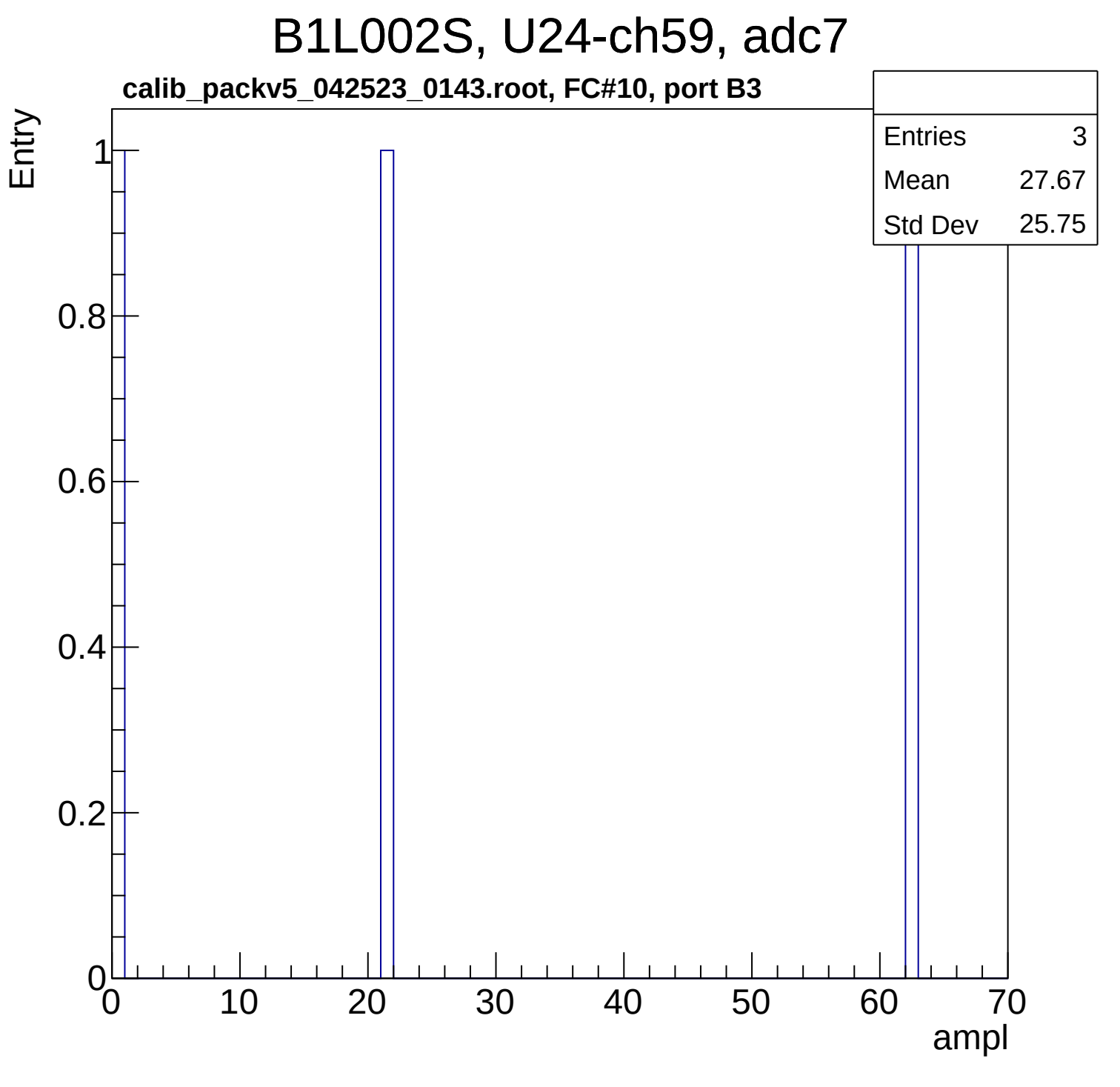

B1L002S, U24-ch59, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 41.44
Std Dev 2.904
Gaus mean : 42.3682
Gaus Width: 4.1013
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

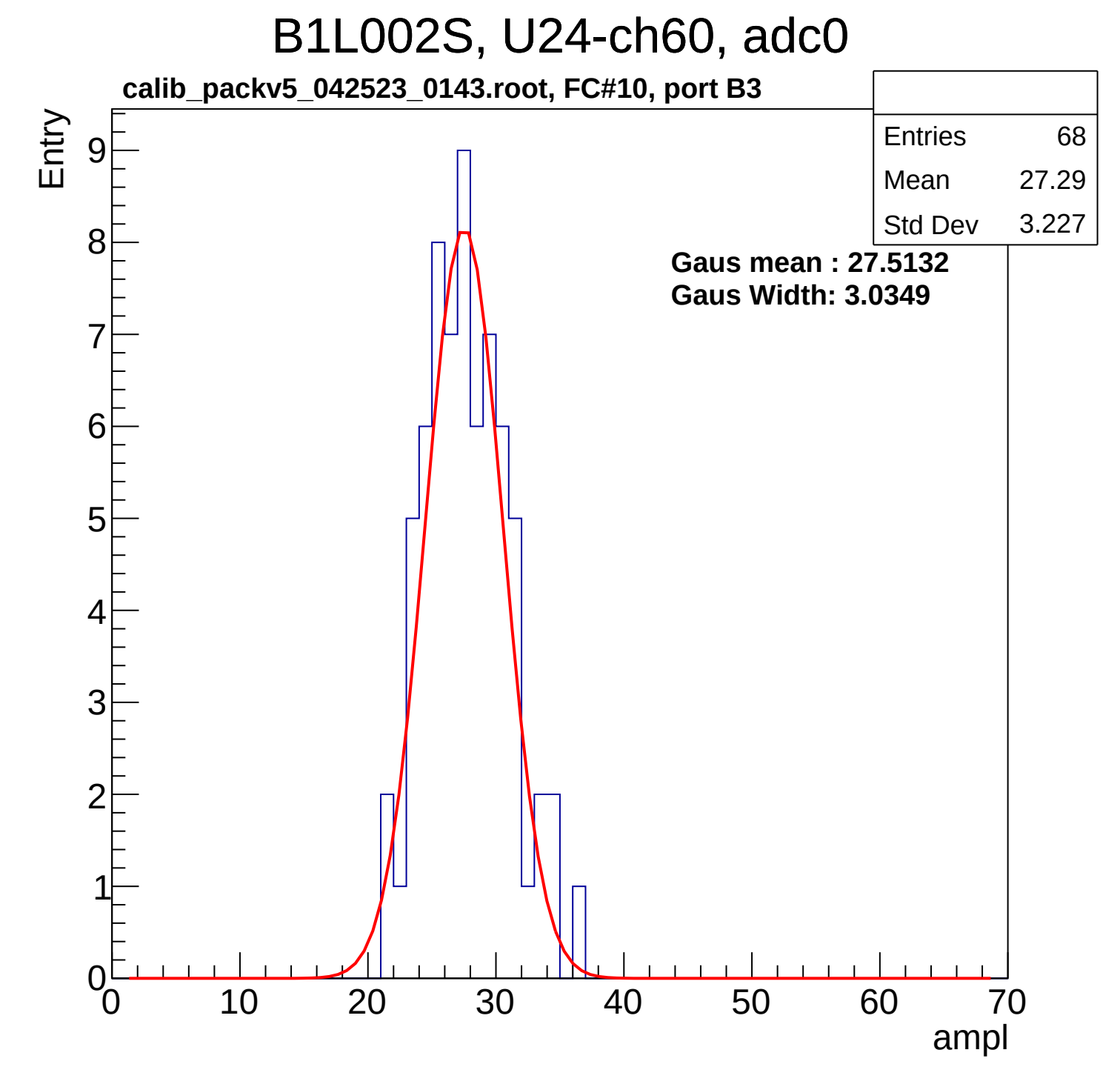

B1L002S, U24-ch59, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 47.42
Std Dev 3.001
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch59, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 54.91
Std Dev 2.8
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch59, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 59.65
Std Dev 1.934
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch59, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.24
Std Dev 14.6
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch59, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 27.67
Std Dev 25.75
Entry
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch60, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.29
Std Dev 3.227
Gaus mean : 27.5132
Gaus Width: 3.0349
Entry
ampl
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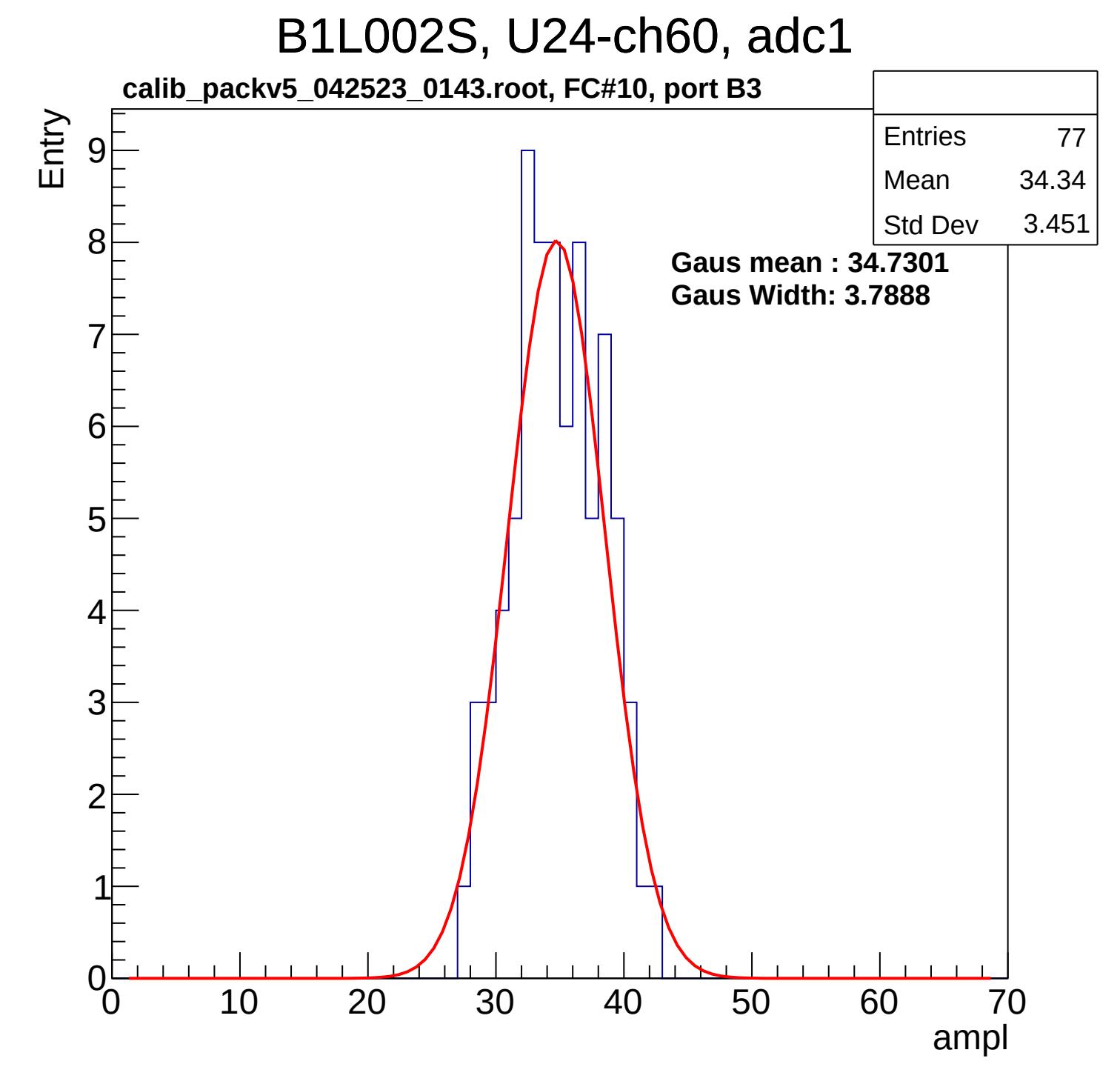
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch60, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 34.34
Std Dev 3.451
Gaus mean : 34.7301
Gaus Width: 3.7888
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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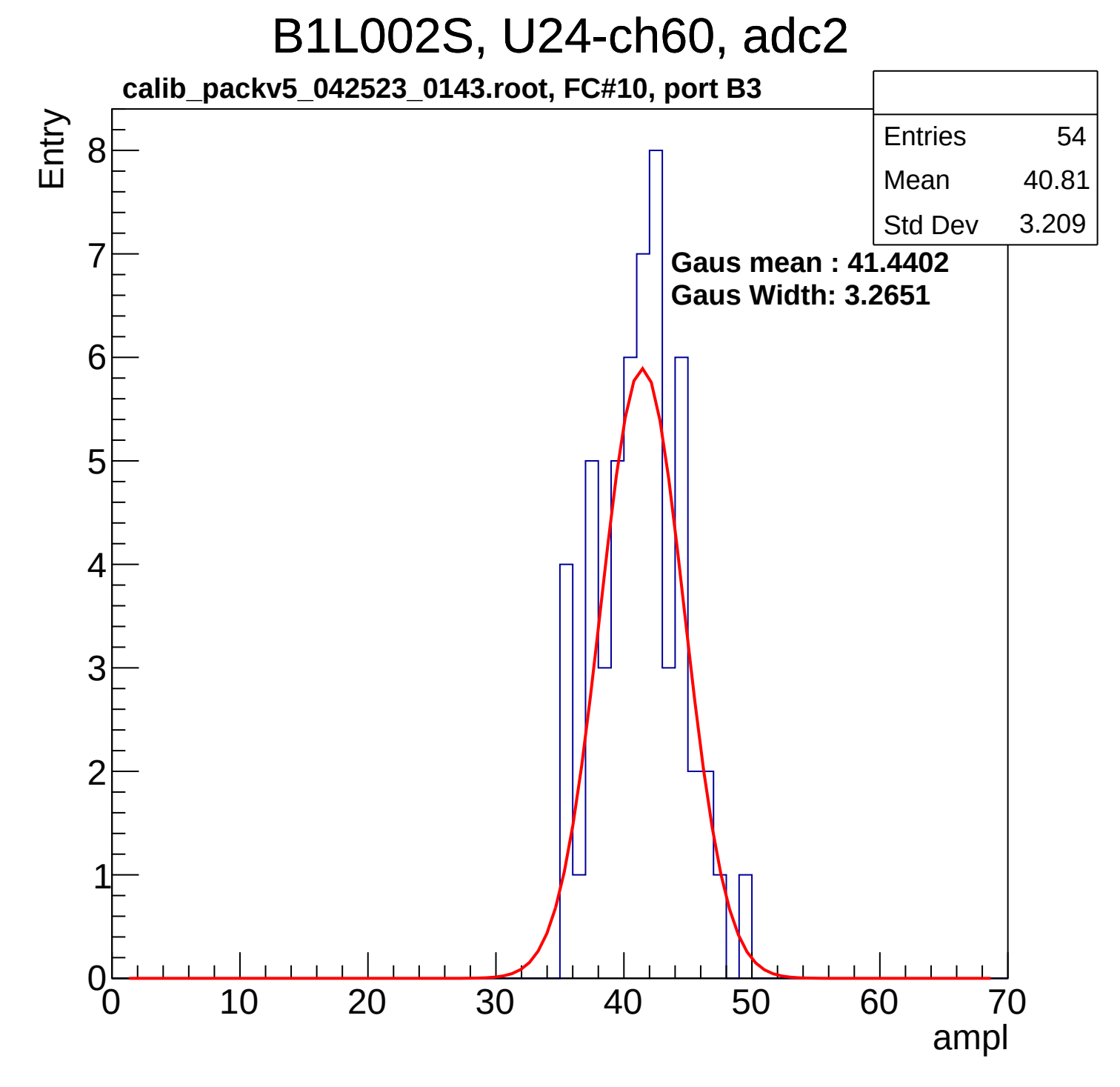
B1L002S, U24-ch60, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.81
Std Dev 3.209
Gaus mean : 41.4402
Gaus Width: 3.2651
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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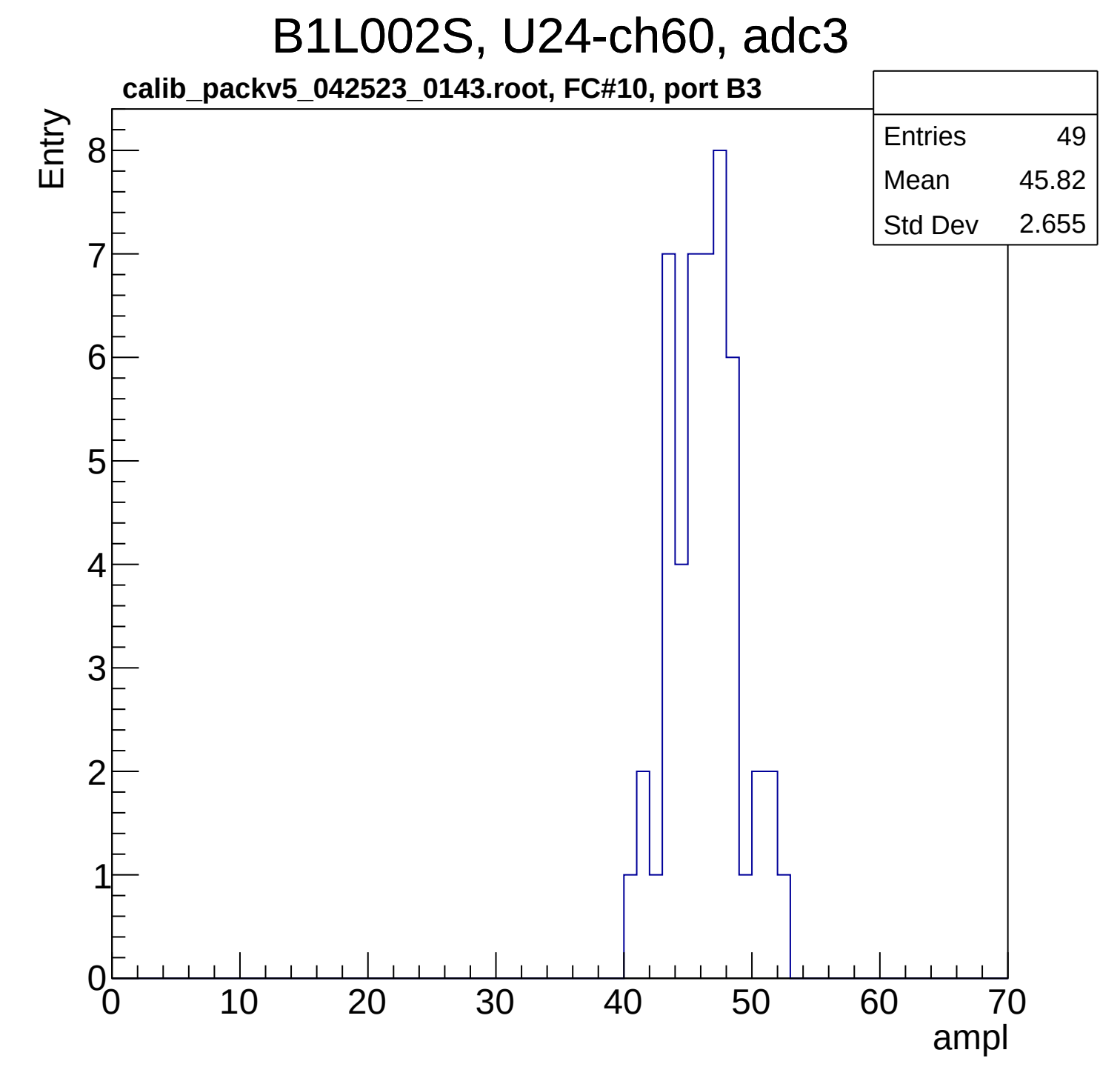
B1L002S, U24-ch60, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 45.82
Std Dev 2.655
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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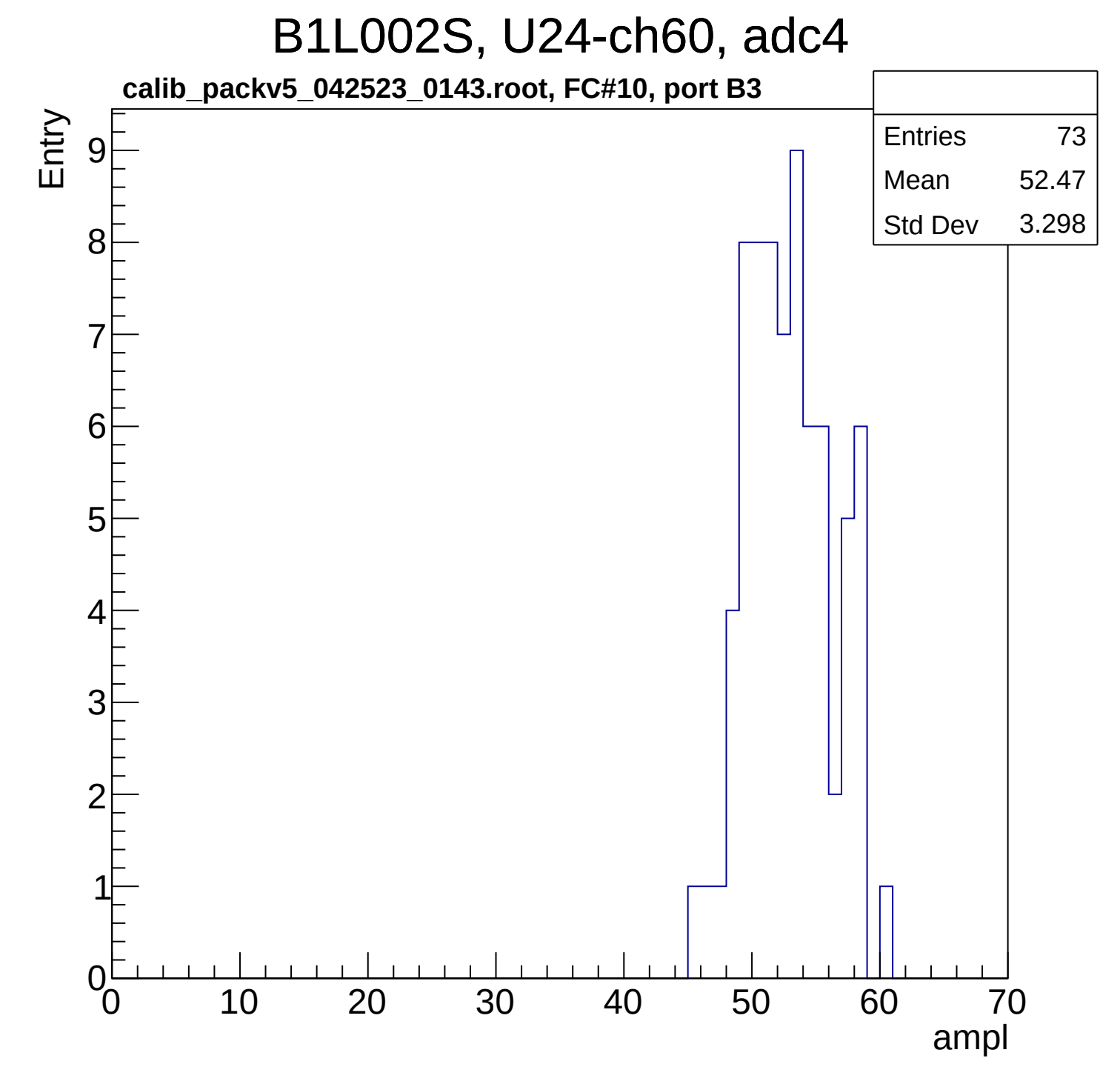
B1L002S, U24-ch60, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 52.47
Std Dev 3.298
Entry
ampl
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3
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5
6
7
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0
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20
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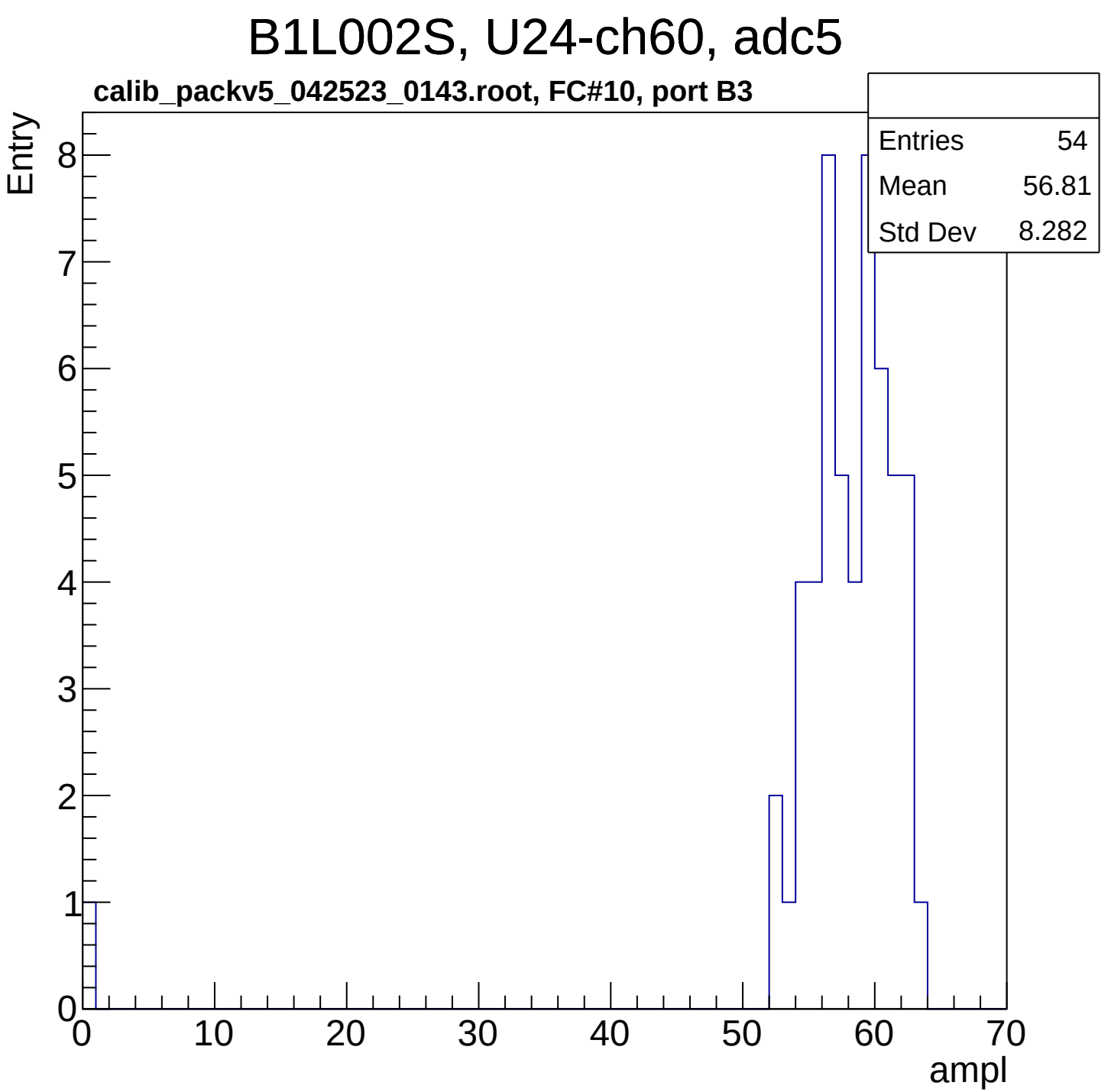
B1L002S, U24-ch60, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.81
Std Dev 8.282
Entry
ampl
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5
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7
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0
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20
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60
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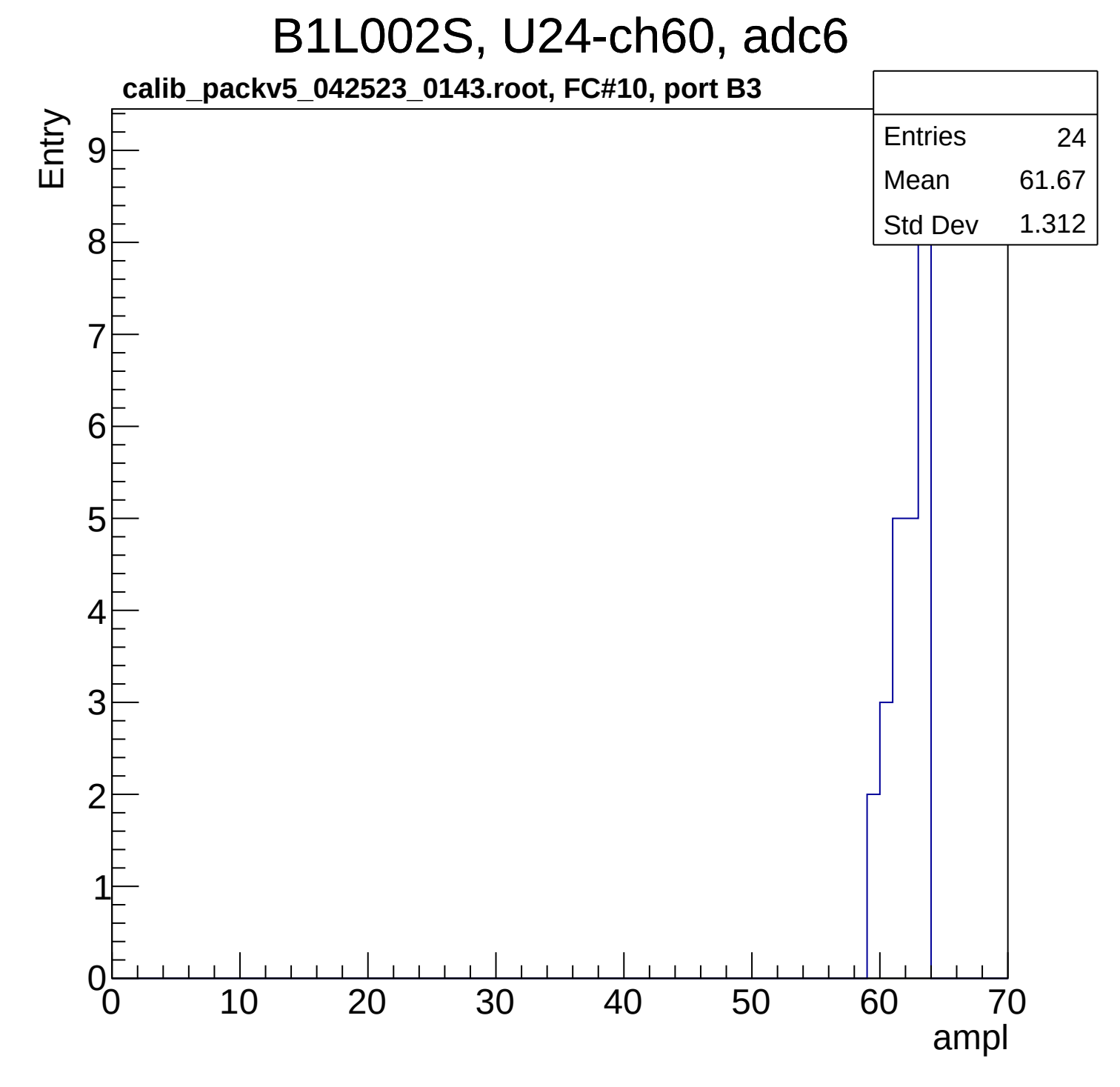
B1L002S, U24-ch60, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 61.67
Std Dev 1.312
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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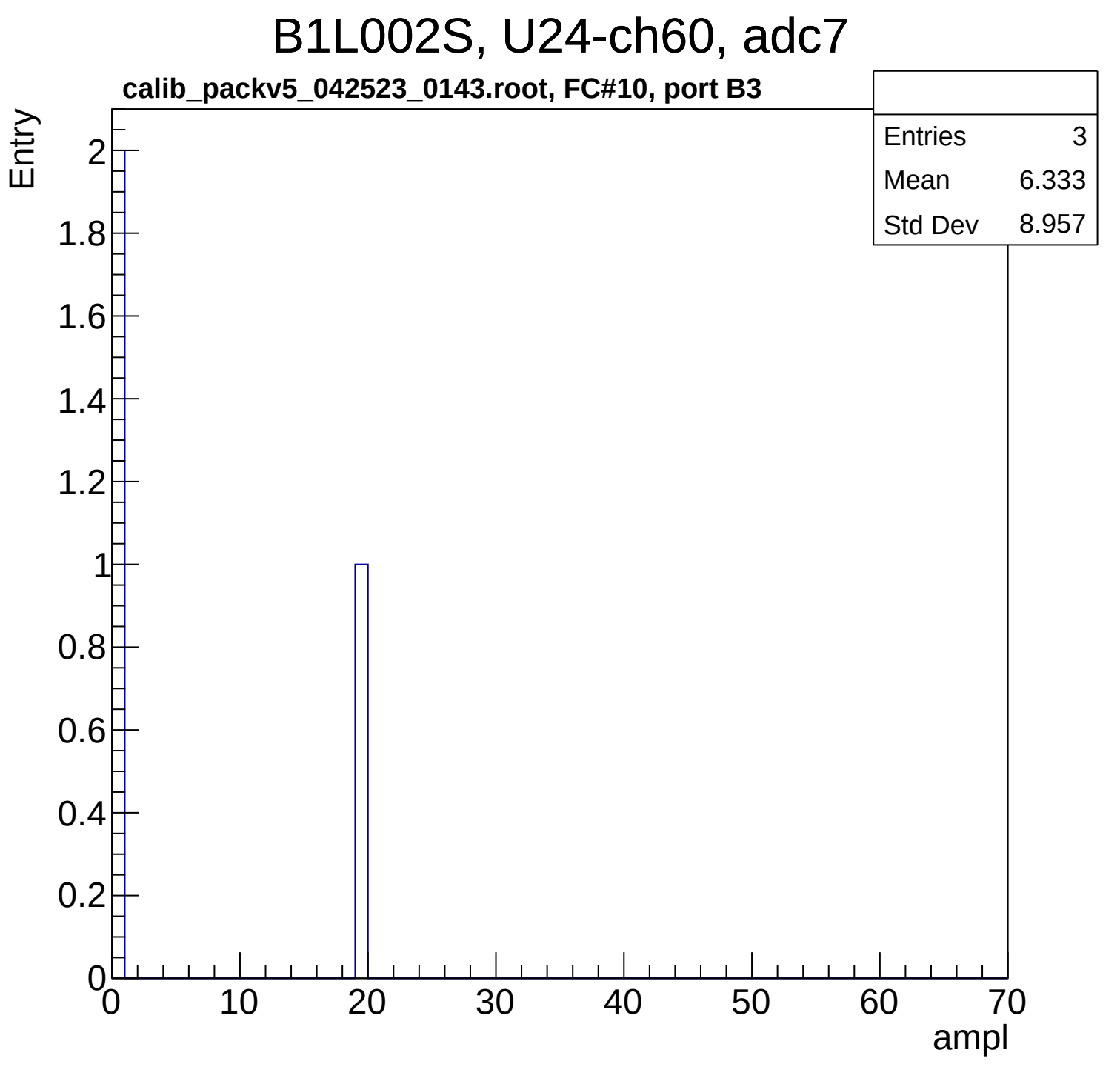
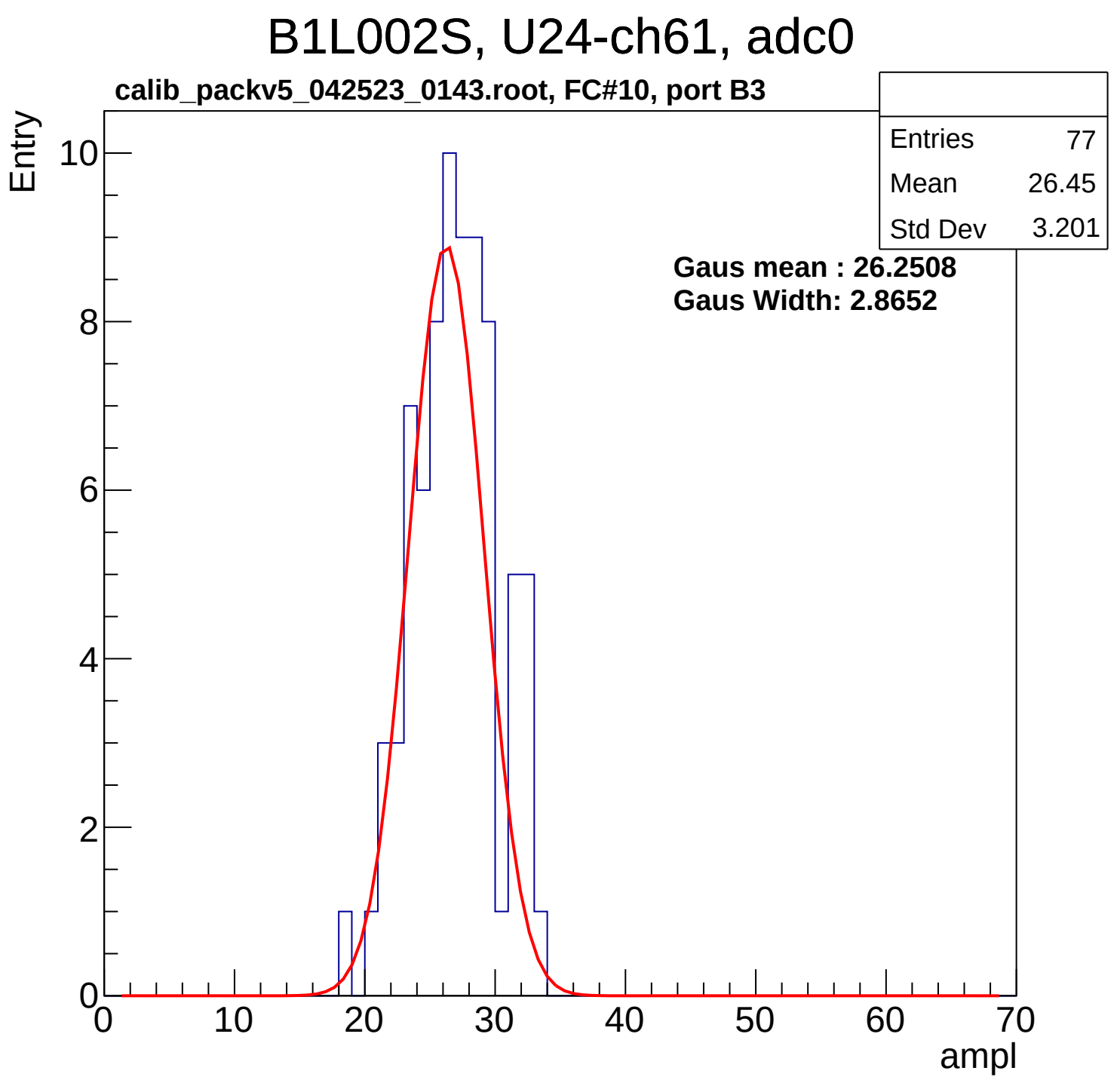
60
70


B1L002S, U24-ch60, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch61, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 26.45
Std Dev 3.201
Gaus mean : 26.2508
Gaus Width: 2.8652
Entry
ampl
0
2
4
6
8
10
0
10
20
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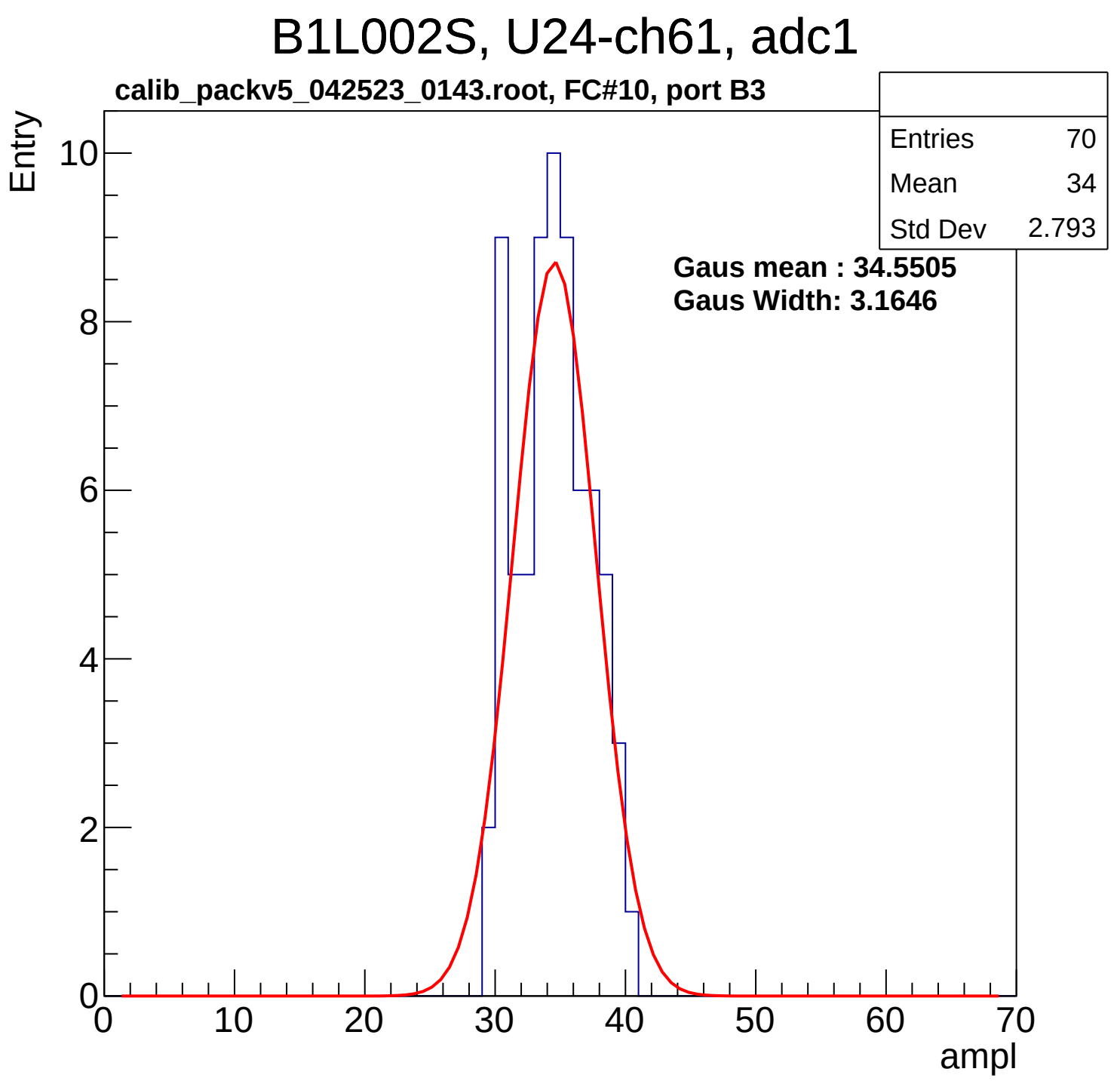
40
50
60
70


B1L002S, U24-ch61, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34
Std Dev 2.793
Gaus mean : 34.5505
Gaus Width: 3.1646
Entry
ampl
0
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4
6
8
10
0
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20
30
40
50
60
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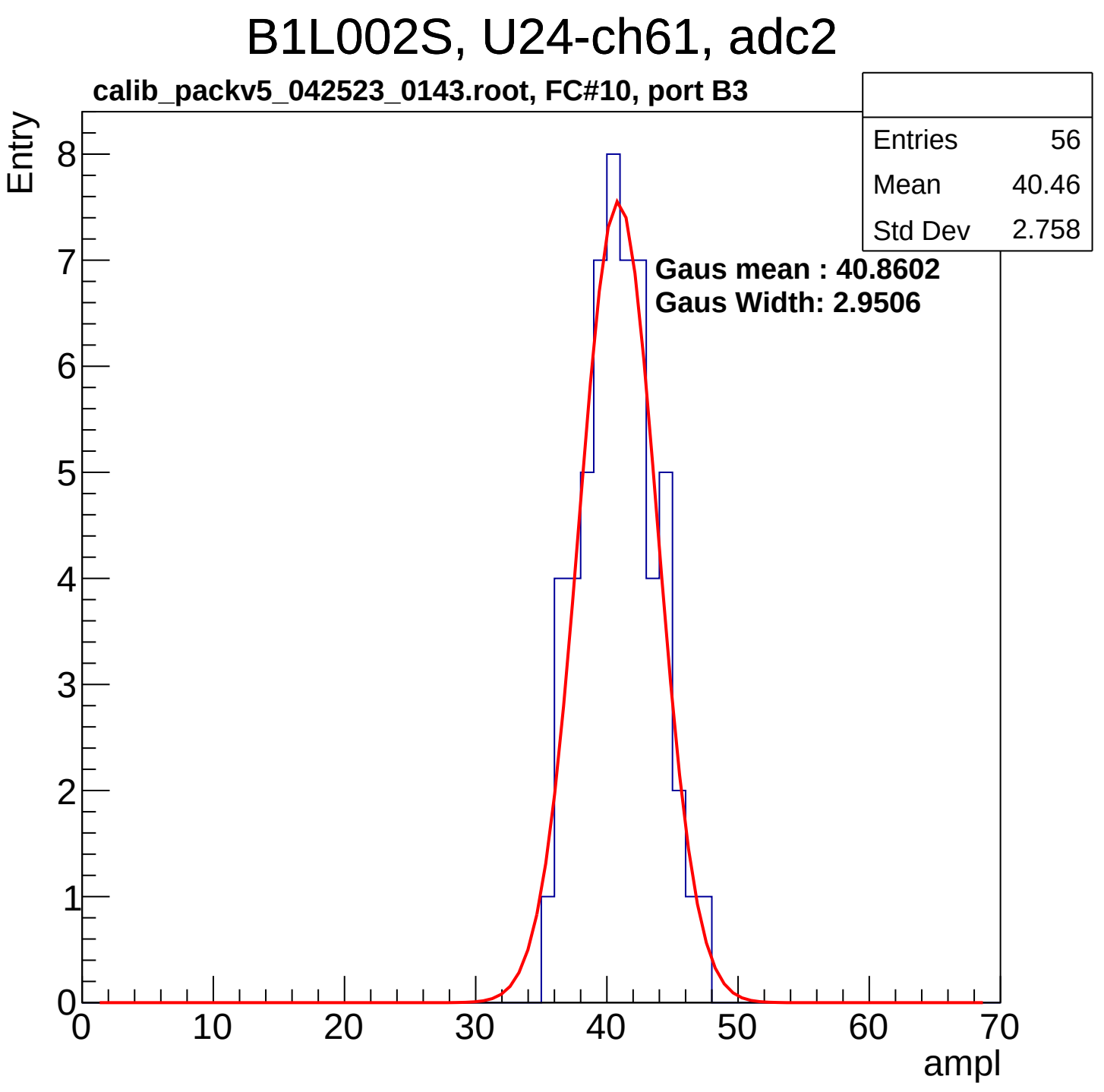
B1L002S, U24-ch61, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 40.46
Std Dev 2.758
Gaus mean : 40.8602
Gaus Width: 2.9506
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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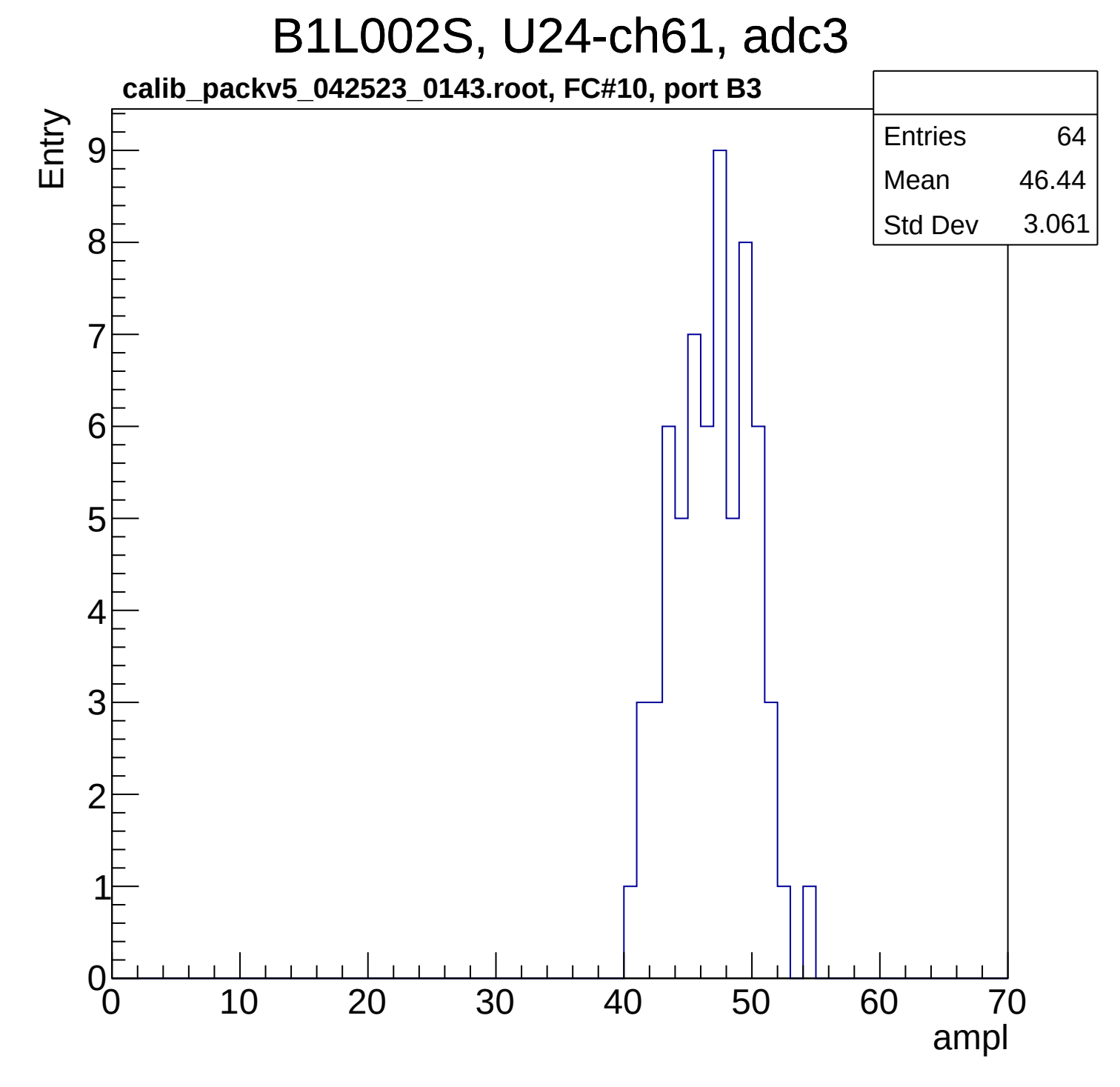
60
70


B1L002S, U24-ch61, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.44
Std Dev 3.061
Entry
ampl
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3
4
5
6
7
8
9
0
10
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60
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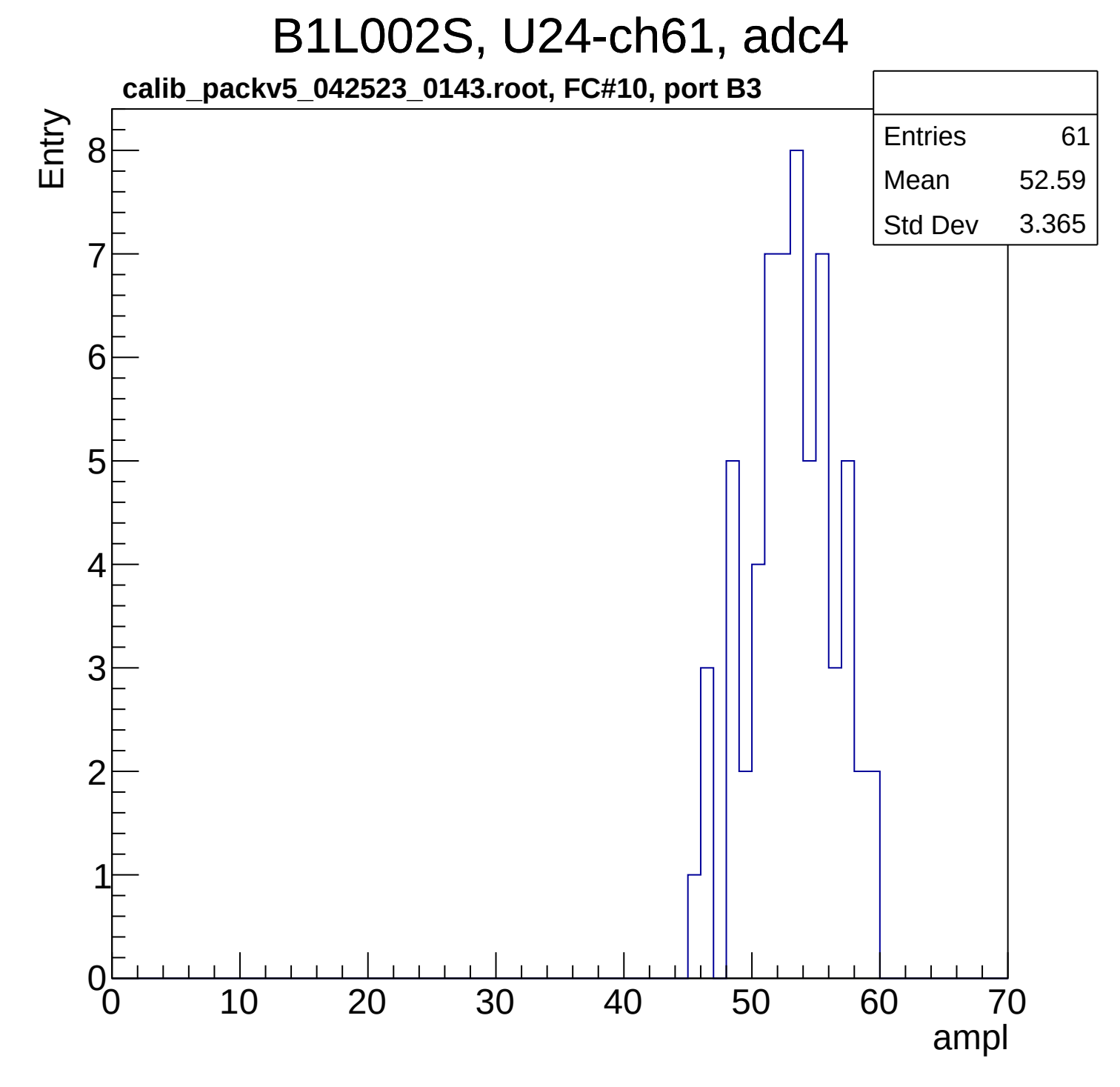
B1L002S, U24-ch61, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.59
Std Dev 3.365
Entry
ampl
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1
2
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4
5
6
7
8
0
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40
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60
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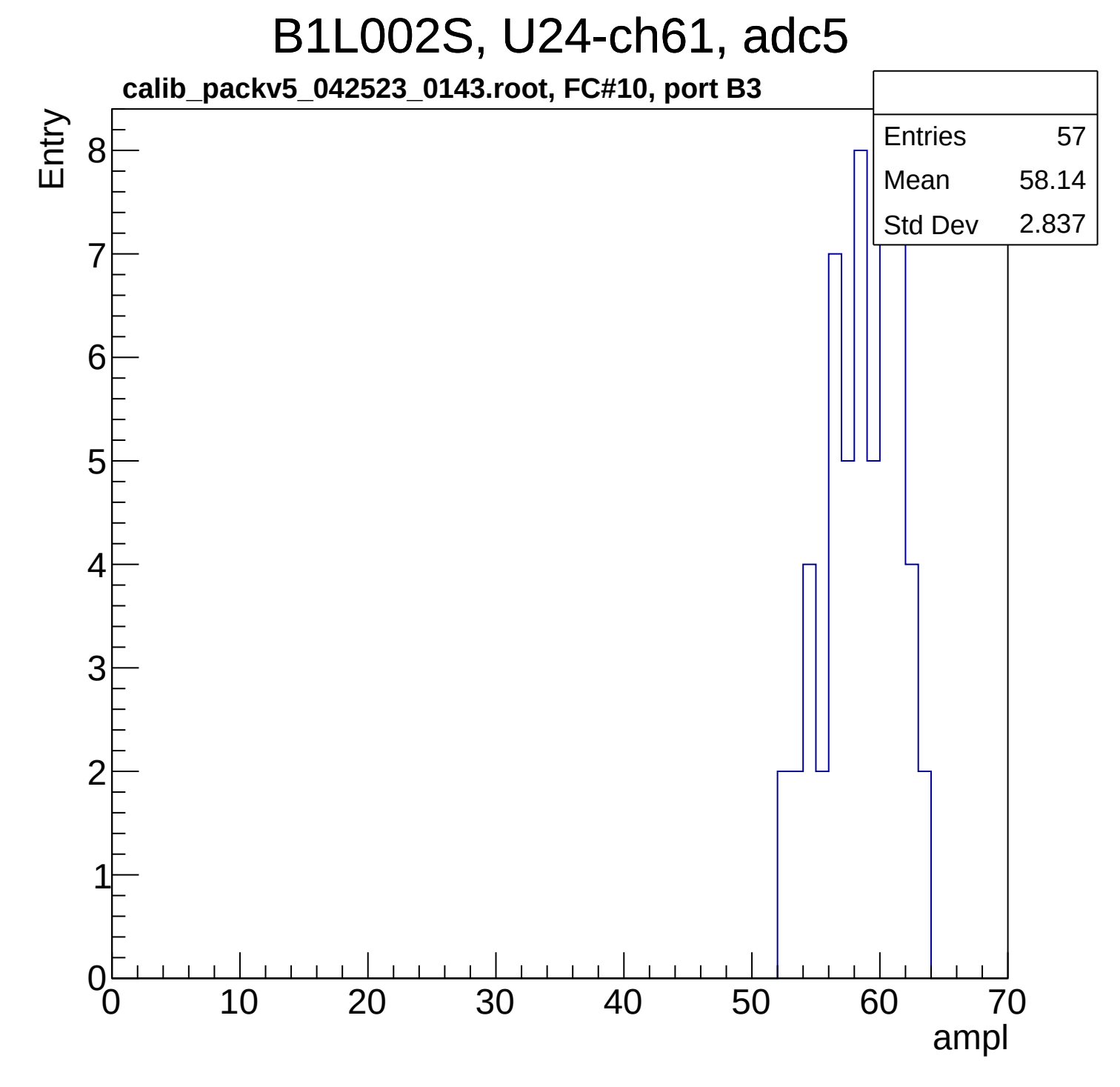
B1L002S, U24-ch61, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.14
Std Dev 2.837
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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60
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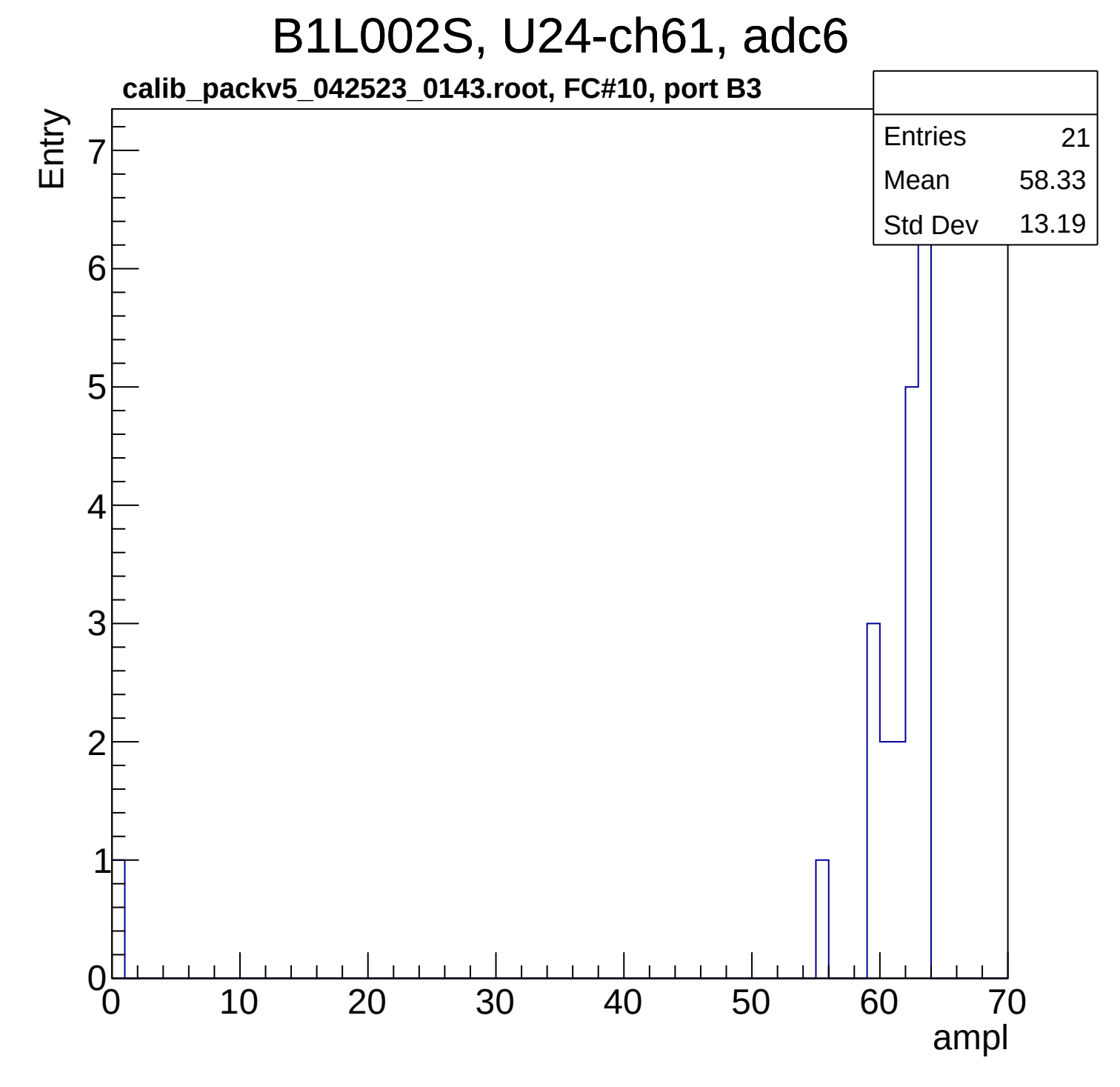
B1L002S, U24-ch61, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.33
Std Dev 13.19
Entry
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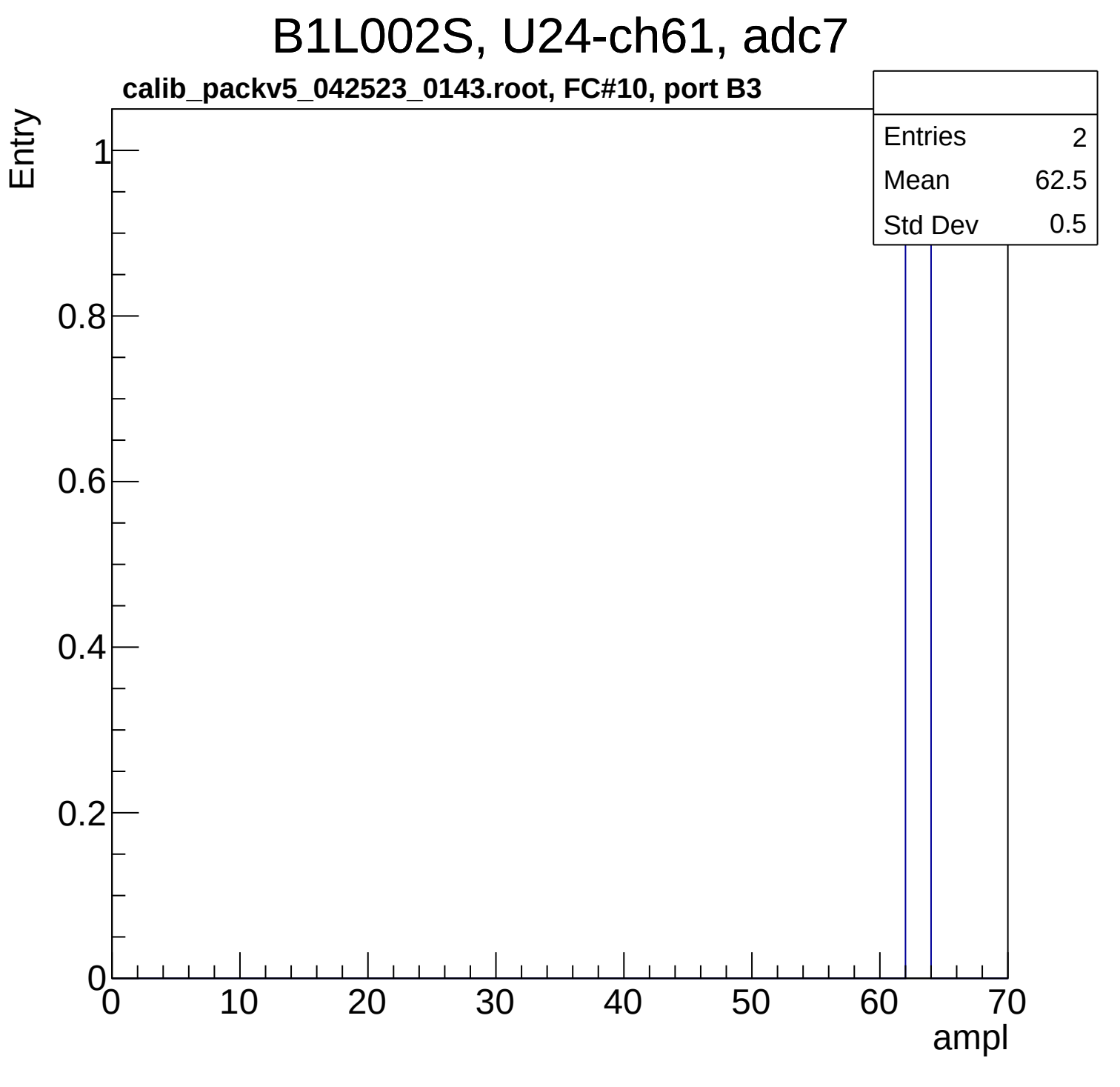
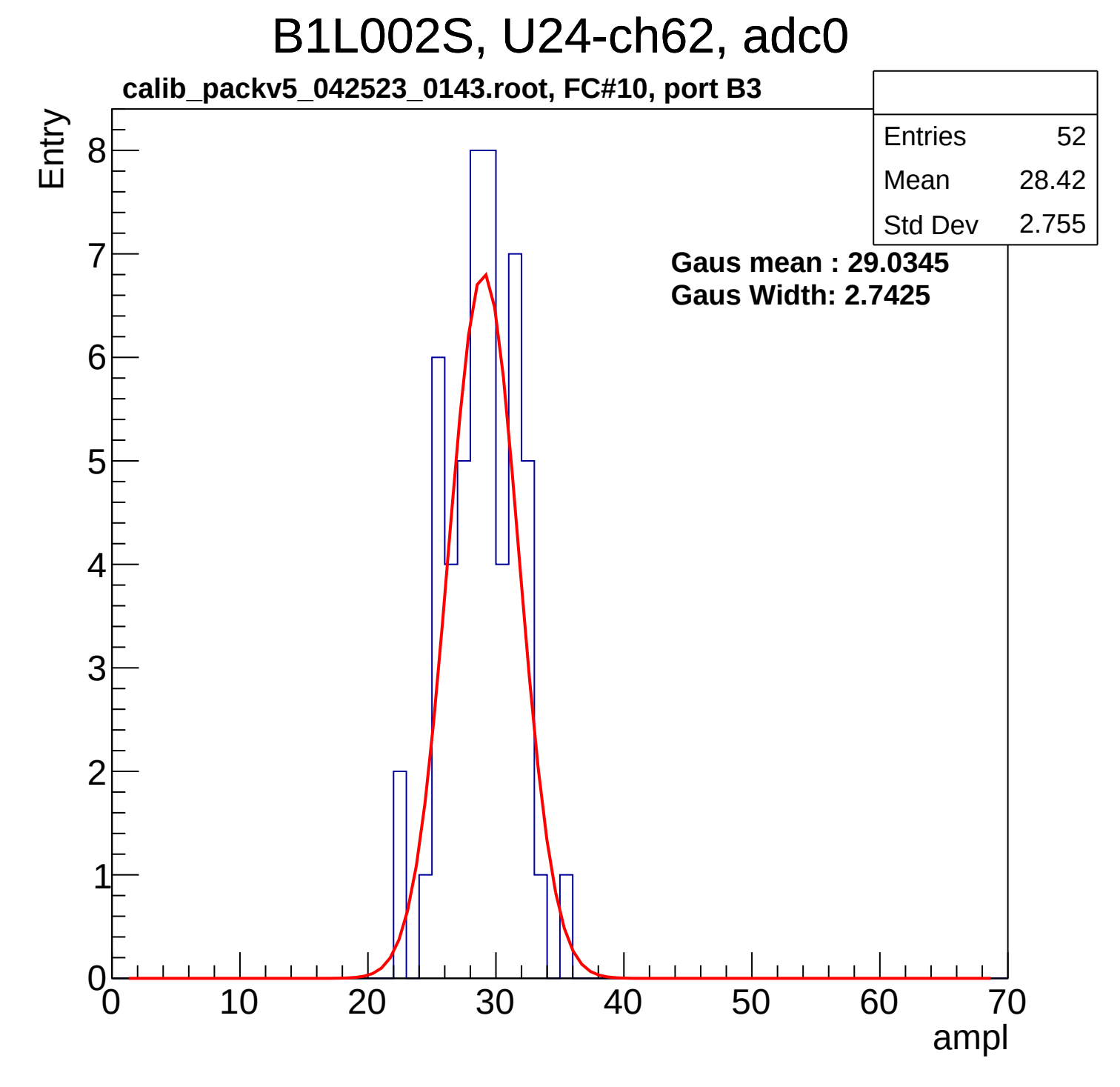
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch61, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch62, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 28.42
Std Dev 2.755
Gaus mean : 29.0345
Gaus Width: 2.7425
Entry
ampl
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1
2
3
4
5
6
7
8
0
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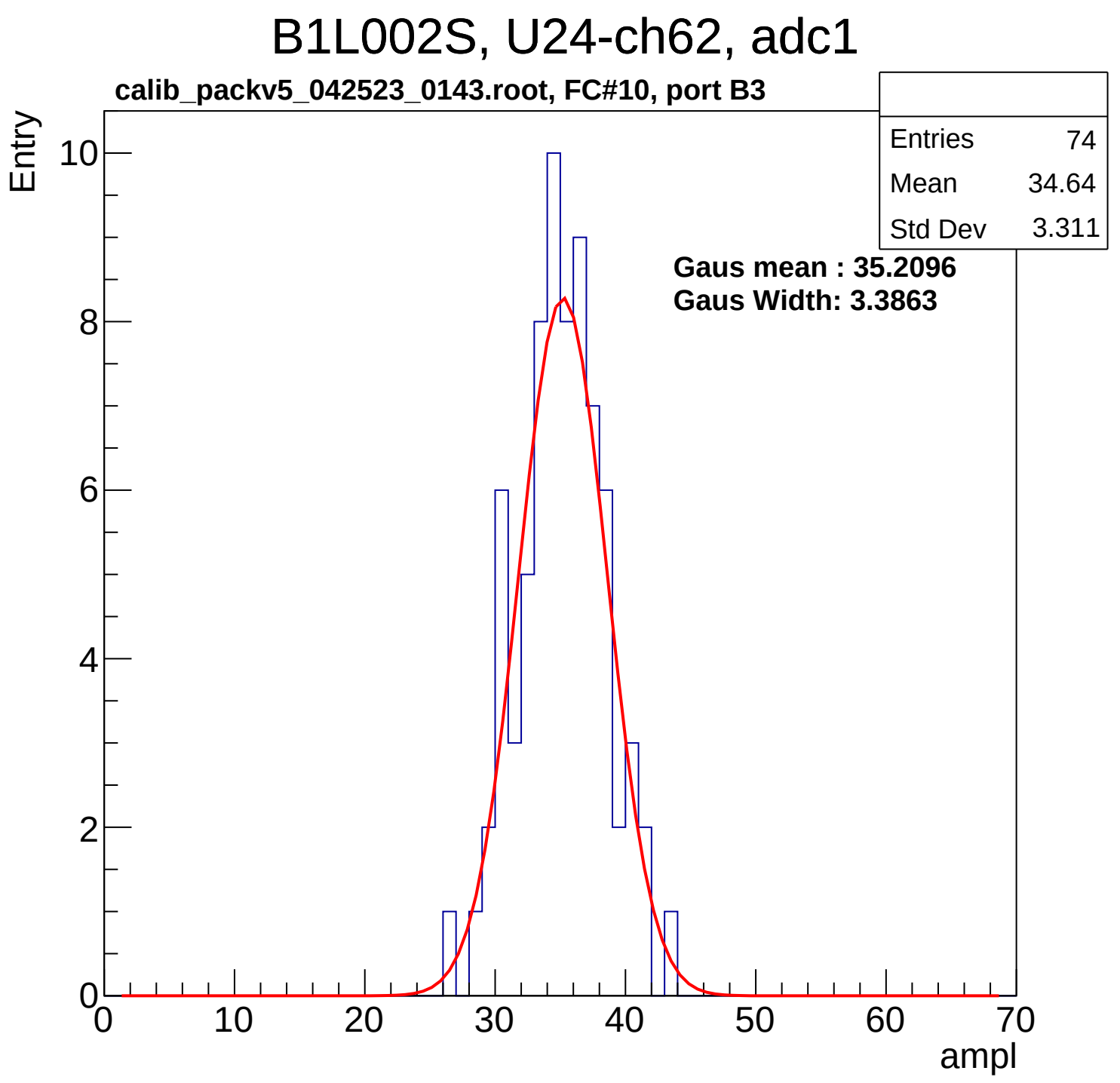
B1L002S, U24-ch62, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.64
Std Dev 3.311
Gaus mean : 35.2096
Gaus Width: 3.3863
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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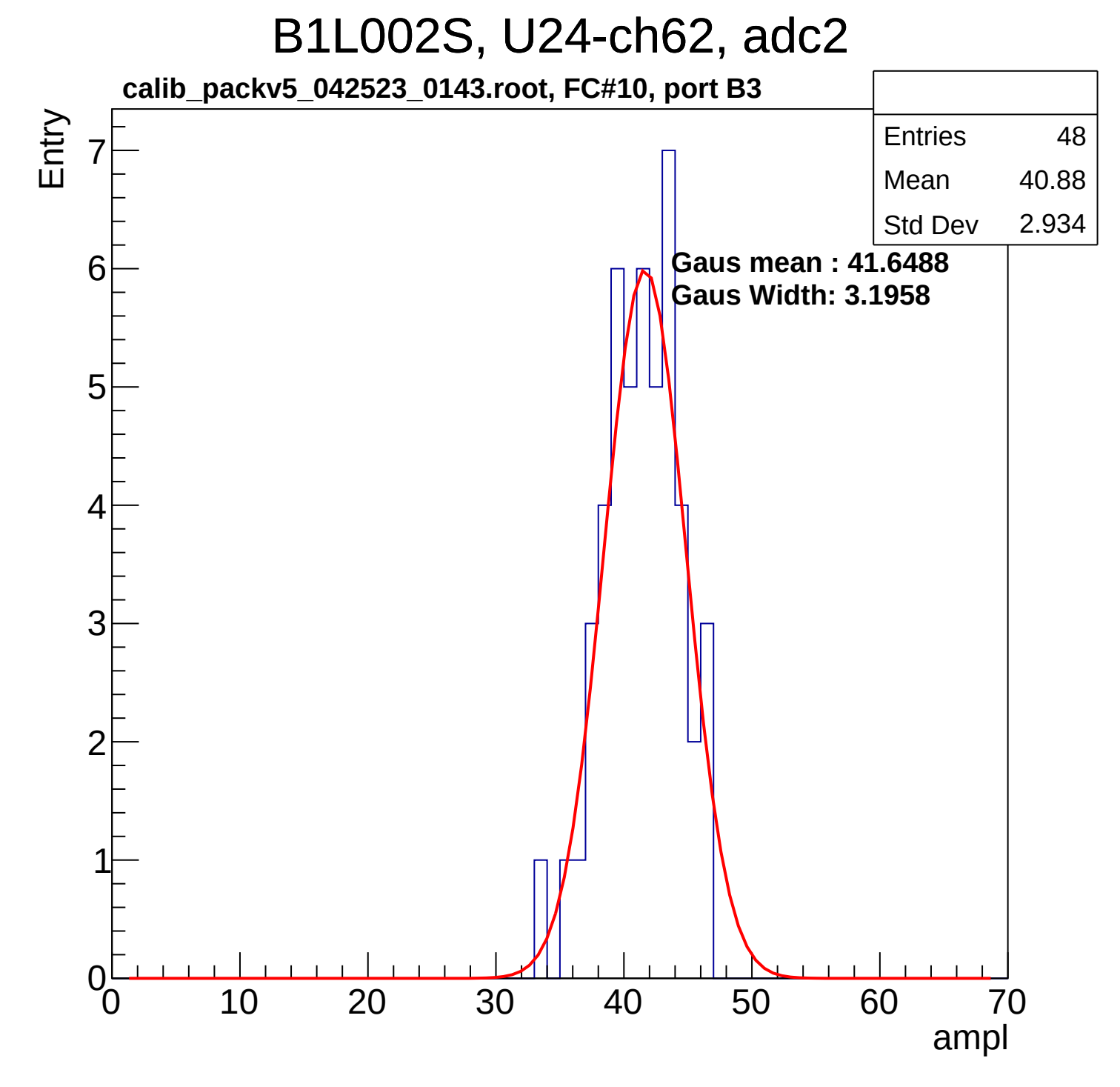
B1L002S, U24-ch62, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 40.88
Std Dev 2.934
Gaus mean : 41.6488
Gaus Width: 3.1958
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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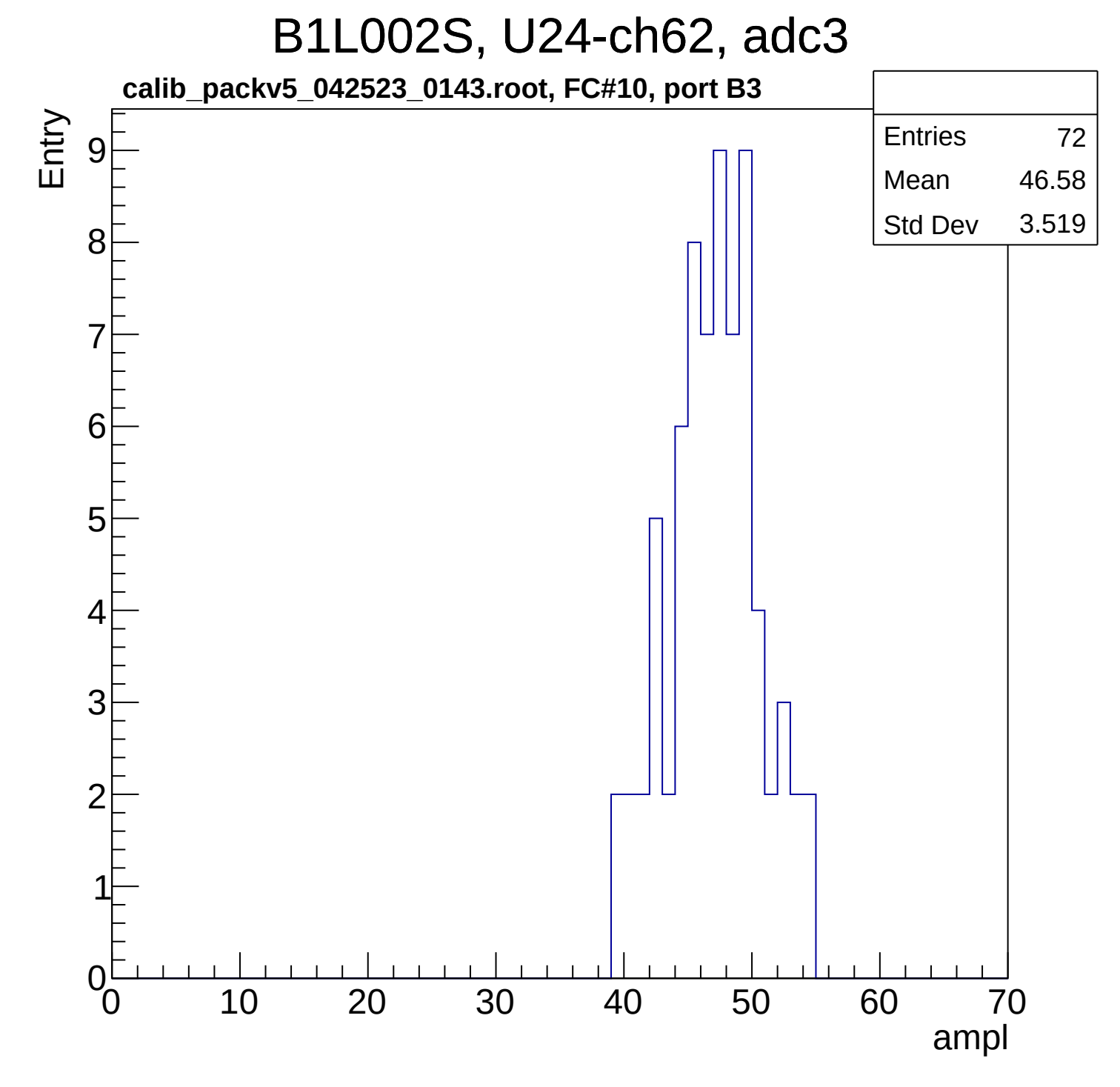
B1L002S, U24-ch62, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 46.58
Std Dev 3.519
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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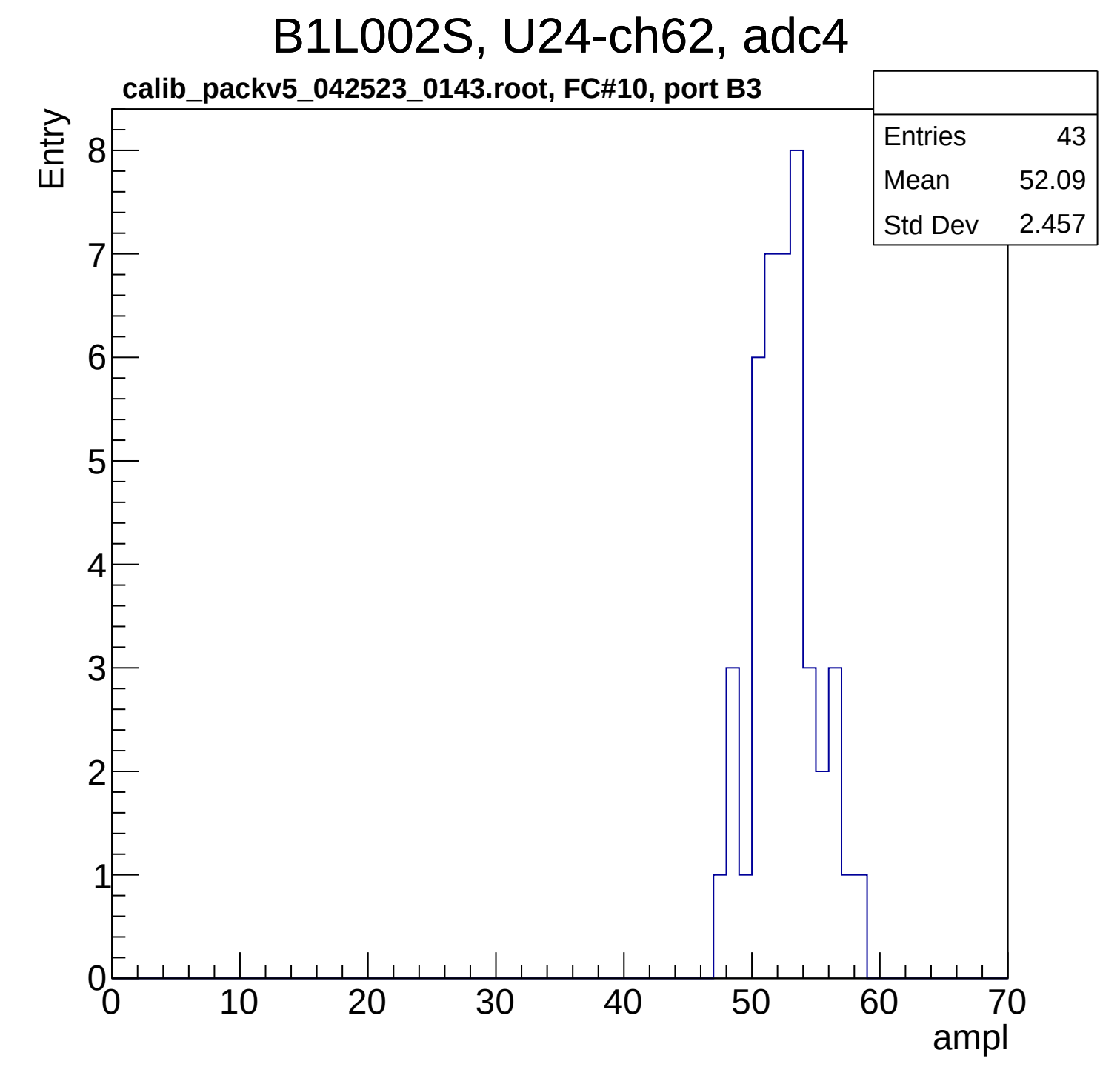
B1L002S, U24-ch62, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 52.09
Std Dev 2.457
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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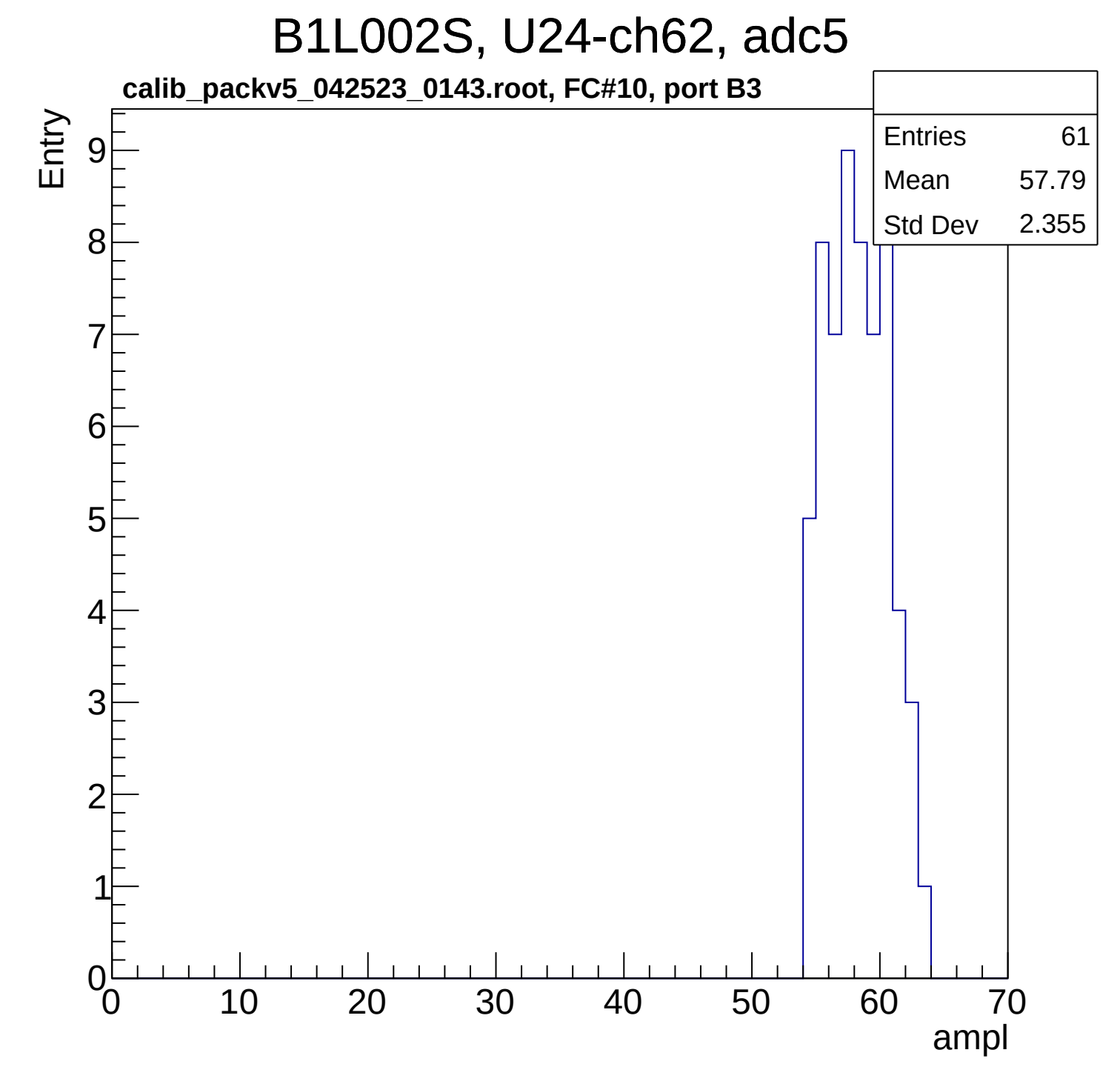
B1L002S, U24-ch62, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.79
Std Dev 2.355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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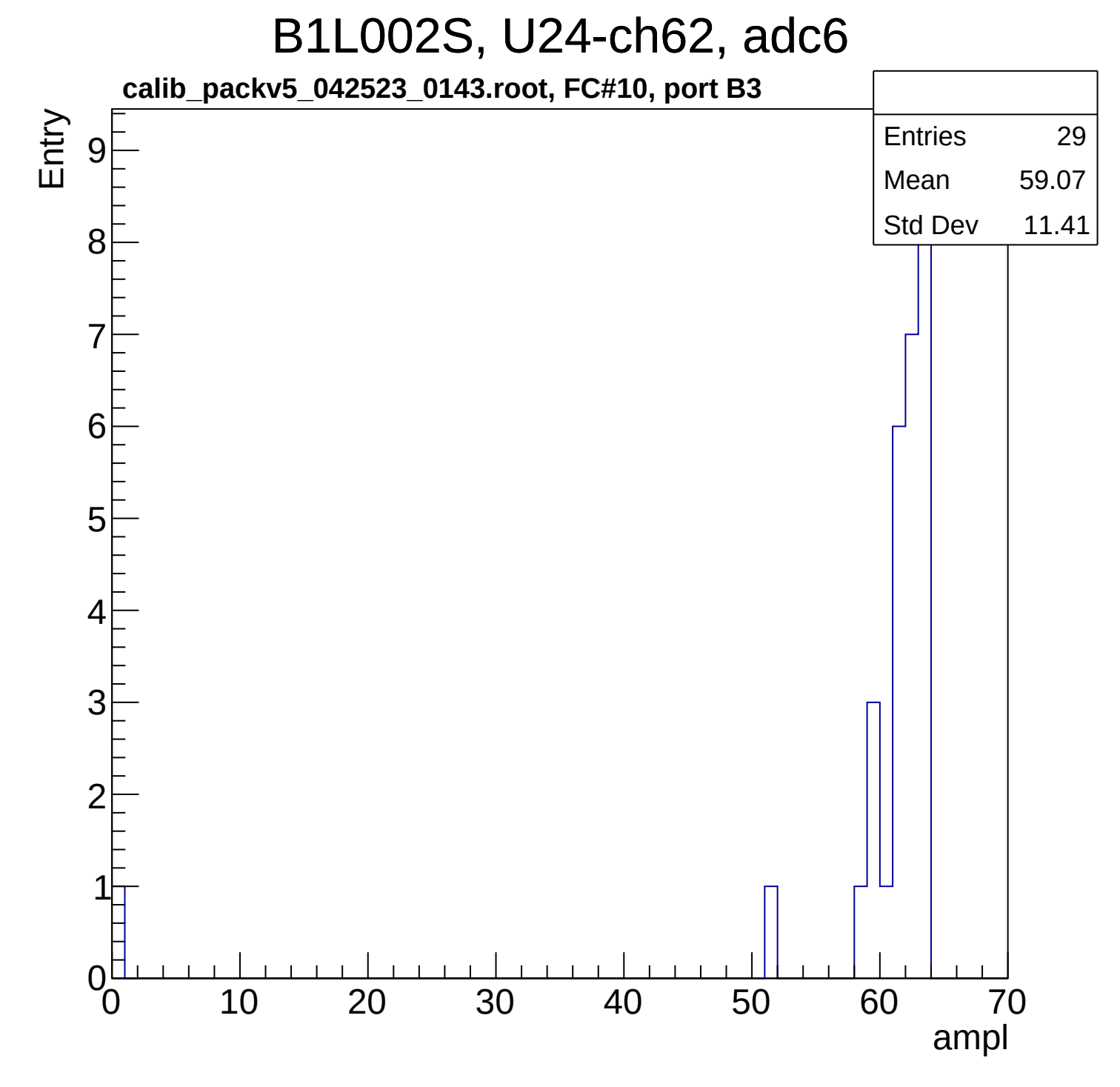
B1L002S, U24-ch62, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.07
Std Dev 11.41
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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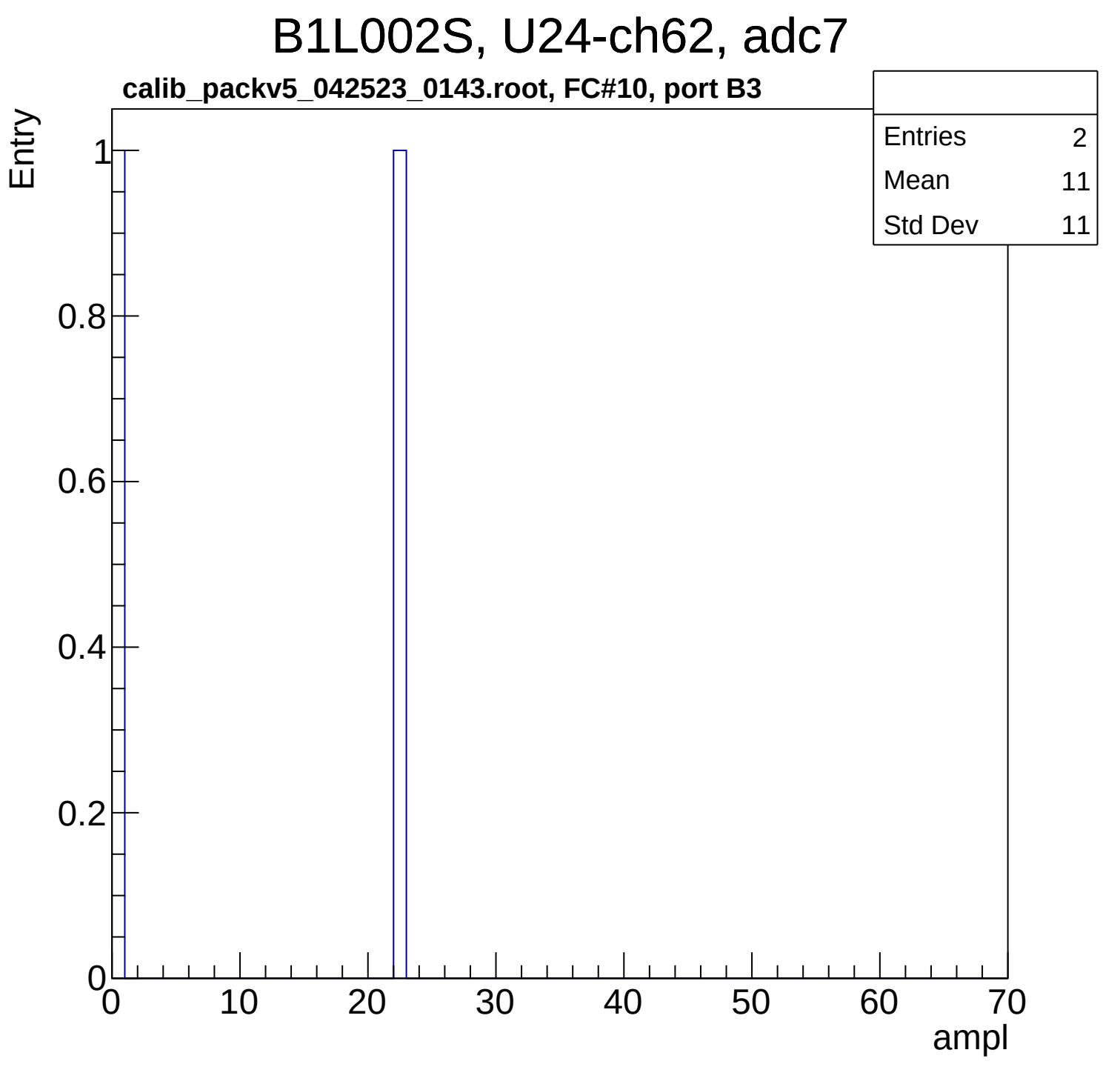
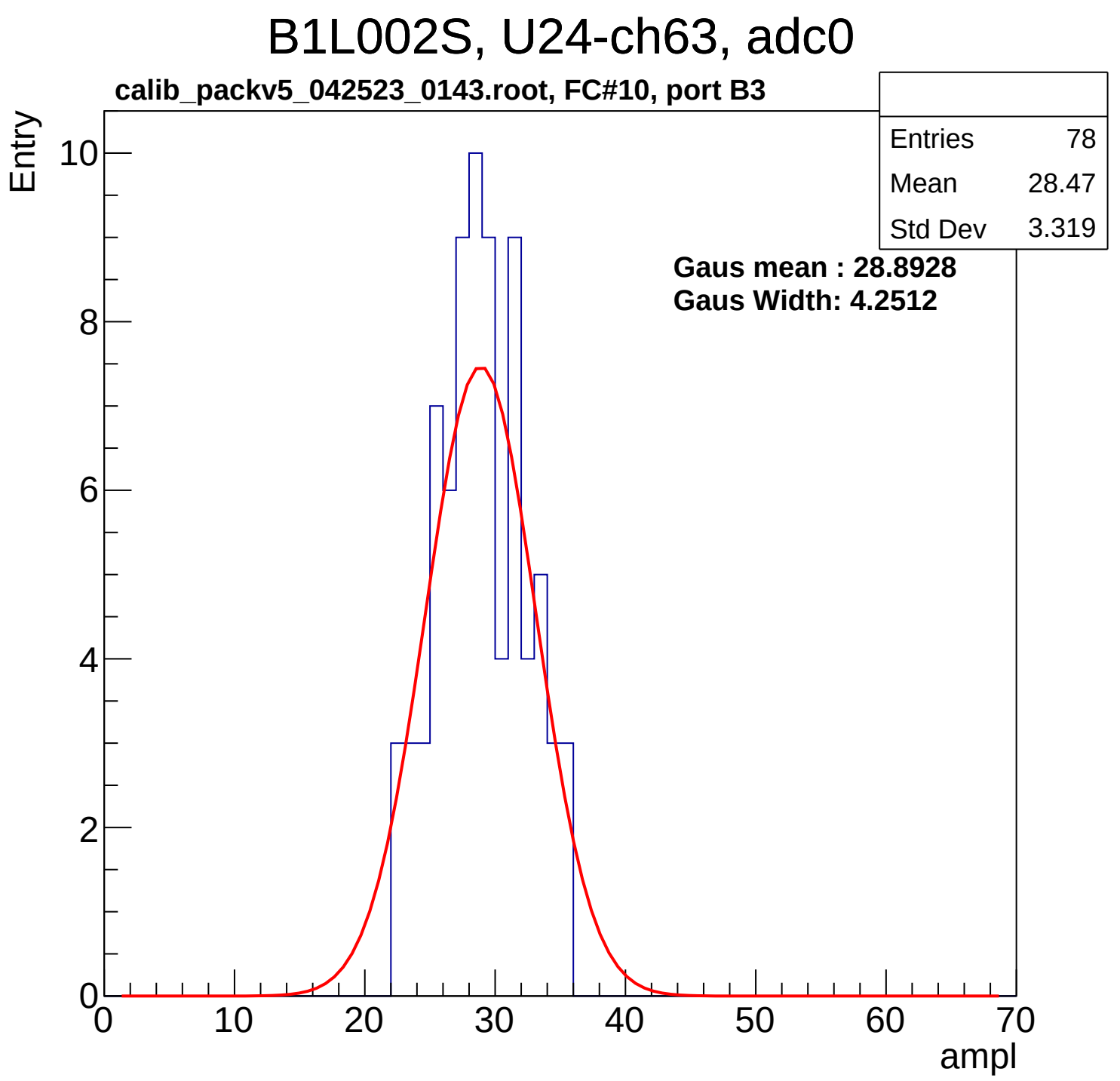
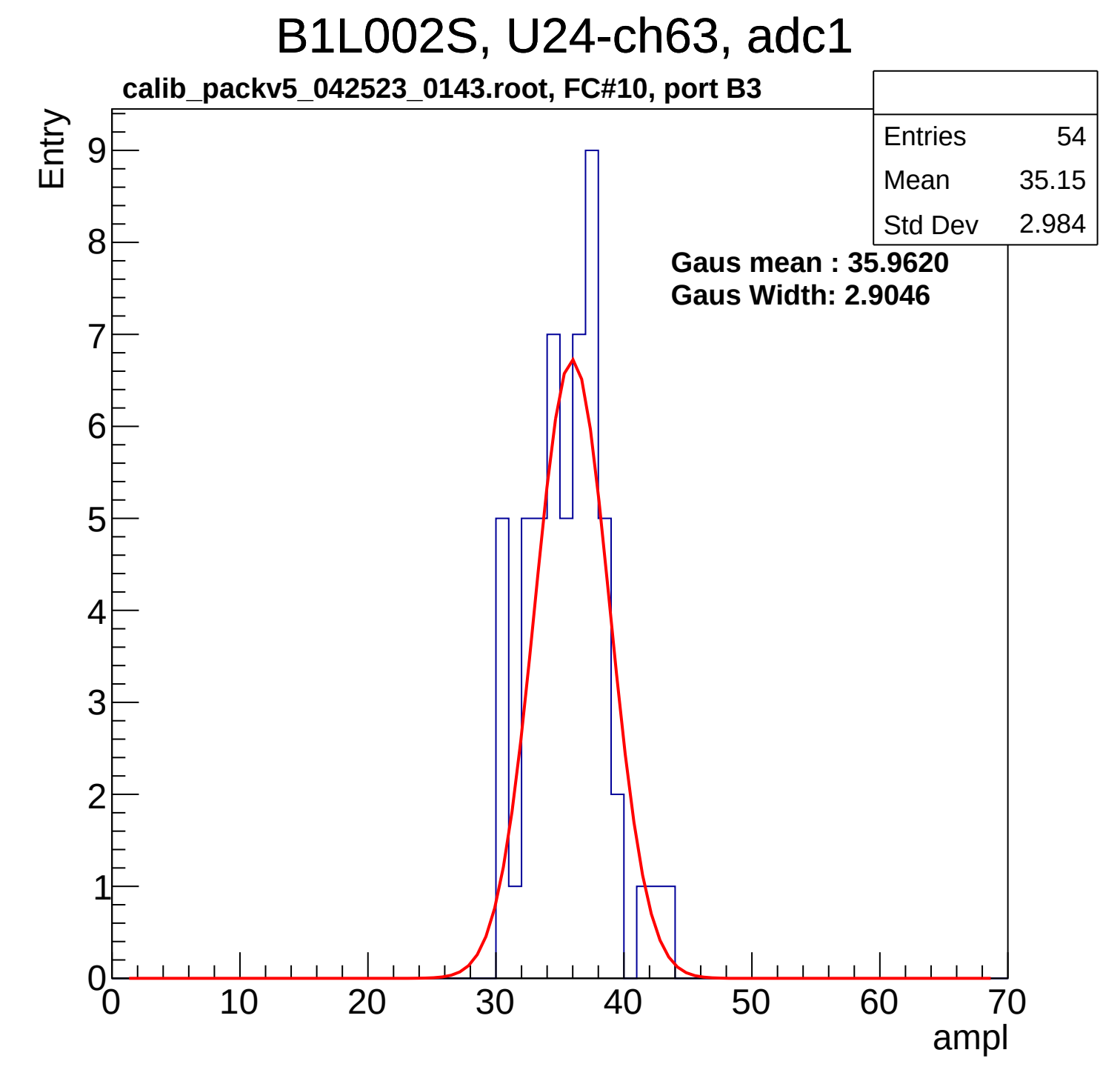

B1L002S, U24-ch62, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch63, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 28.47
Std Dev 3.319
Gaus mean : 28.8928
Gaus Width: 4.2512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch63, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 35.15
Std Dev 2.984
Gaus mean : 35.9620
Gaus Width: 2.9046
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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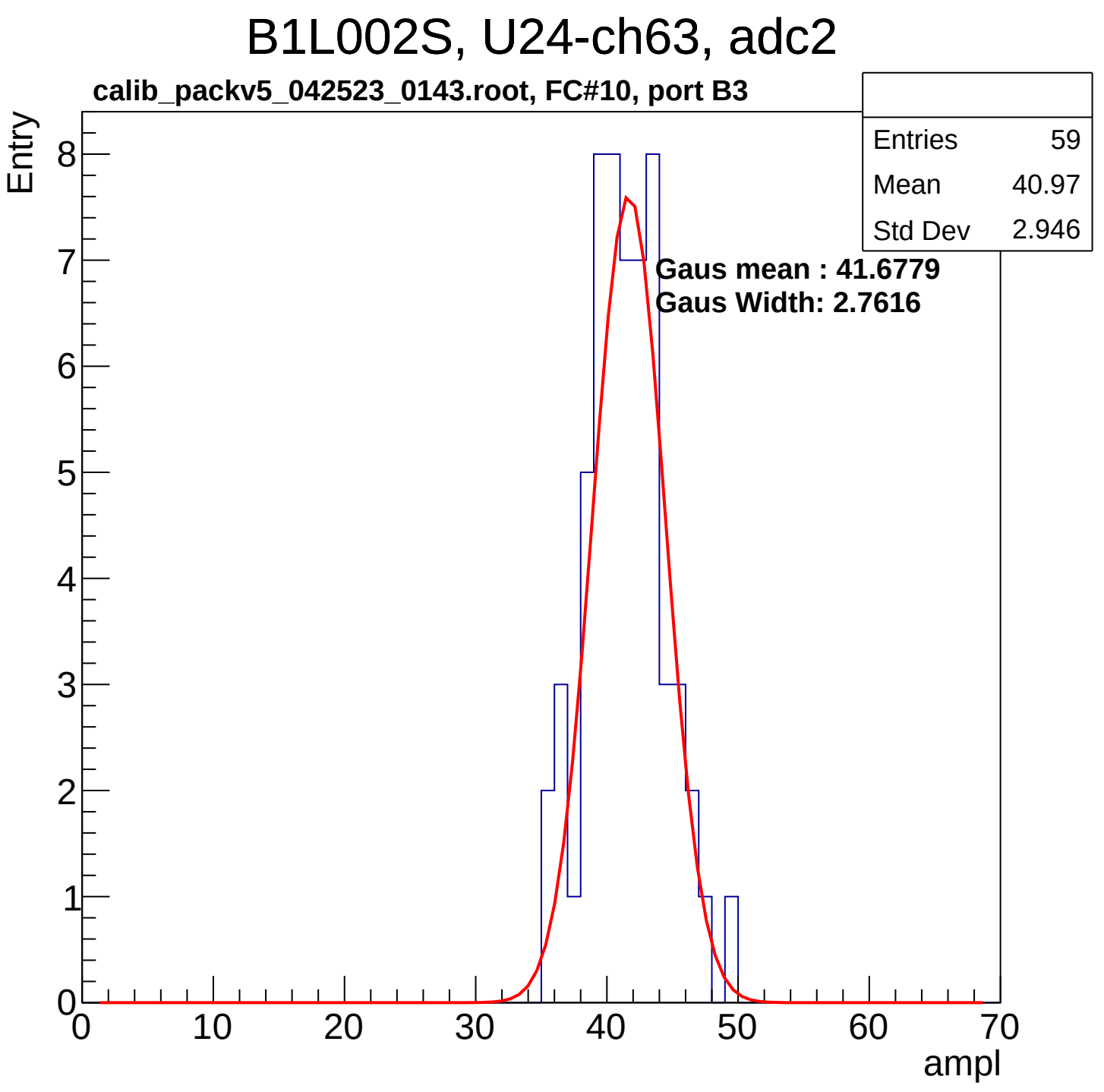
B1L002S, U24-ch63, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 40.97
Std Dev 2.946
Gaus mean : 41.6779
Gaus Width: 2.7616
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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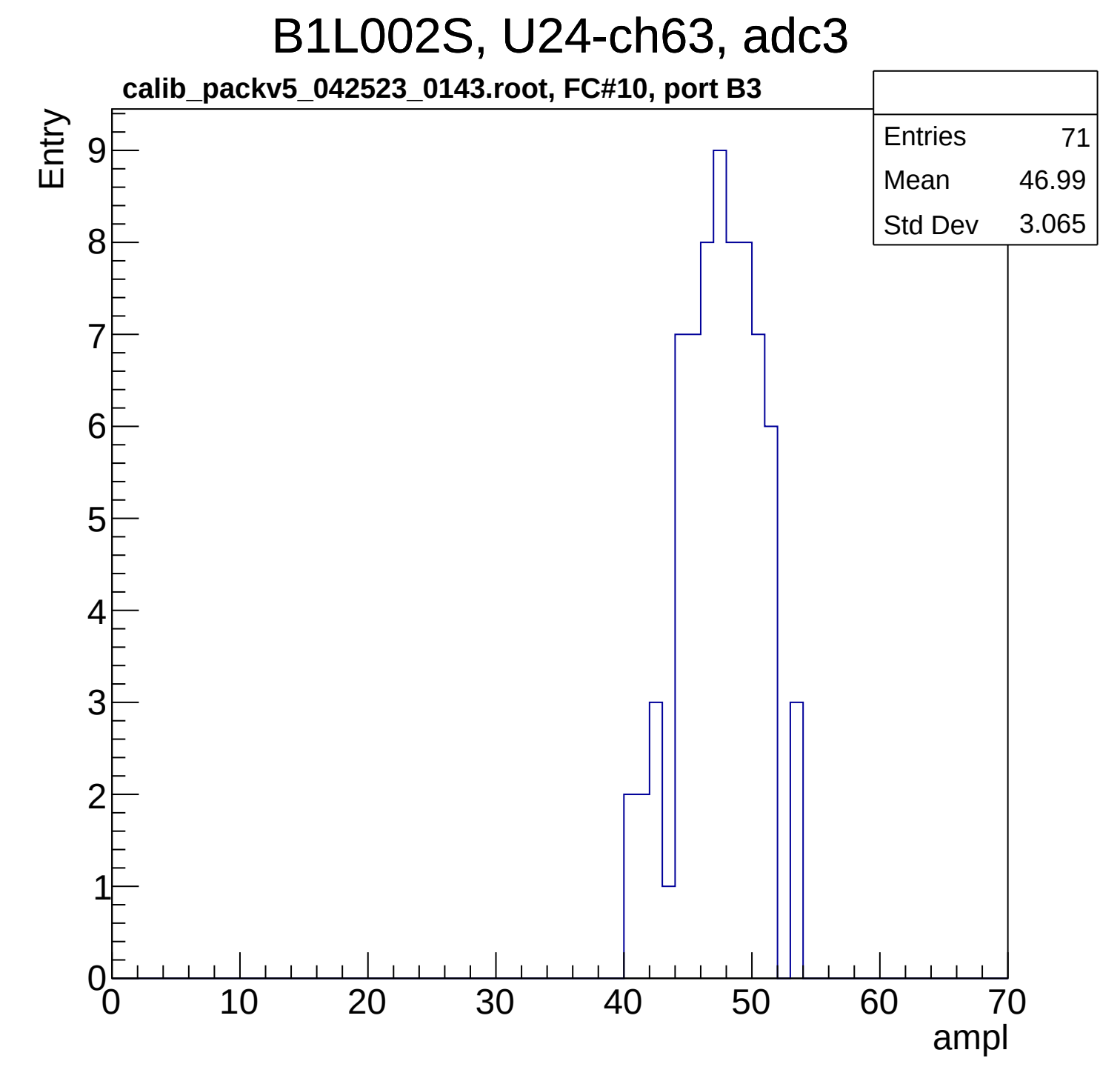
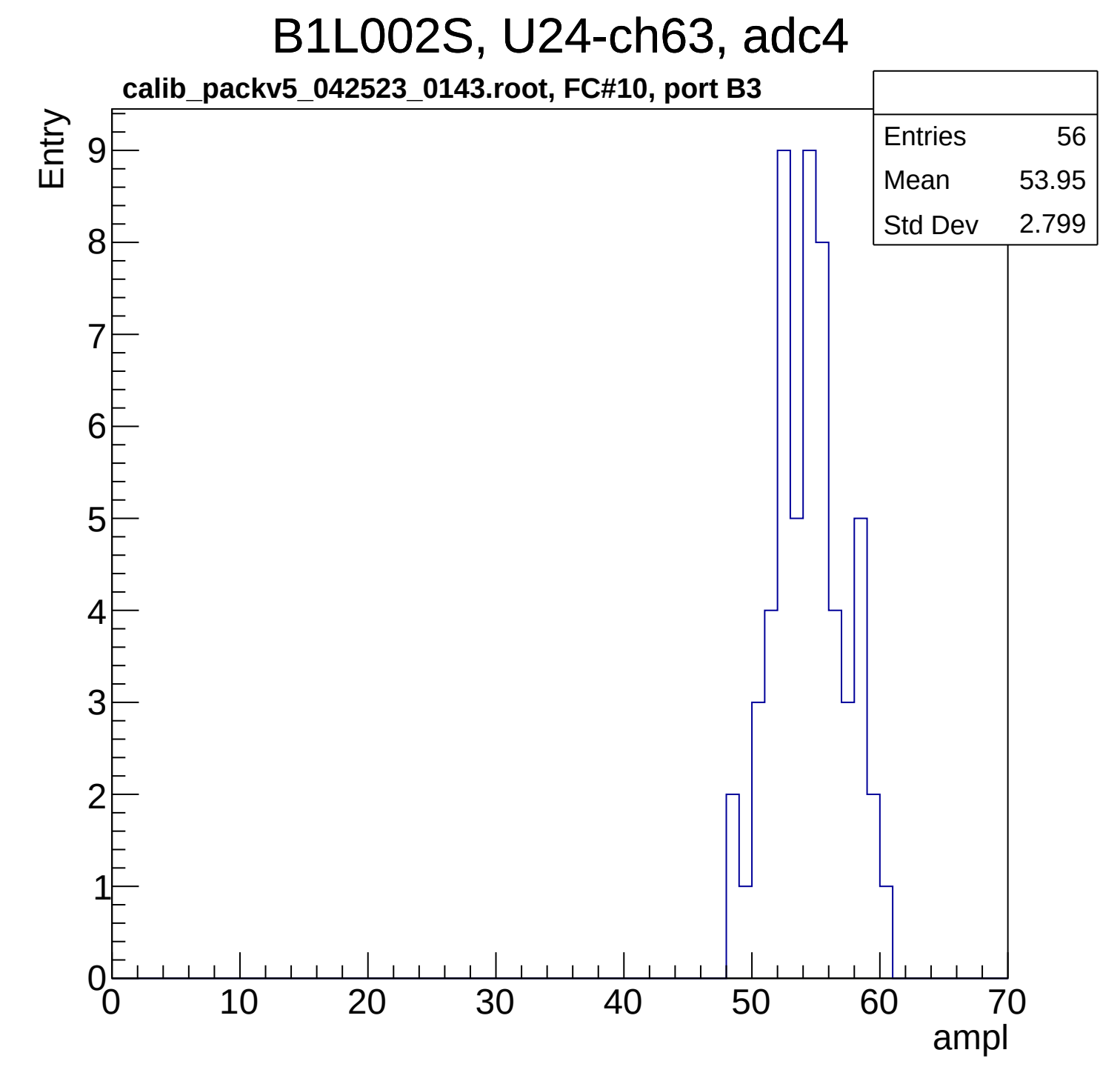
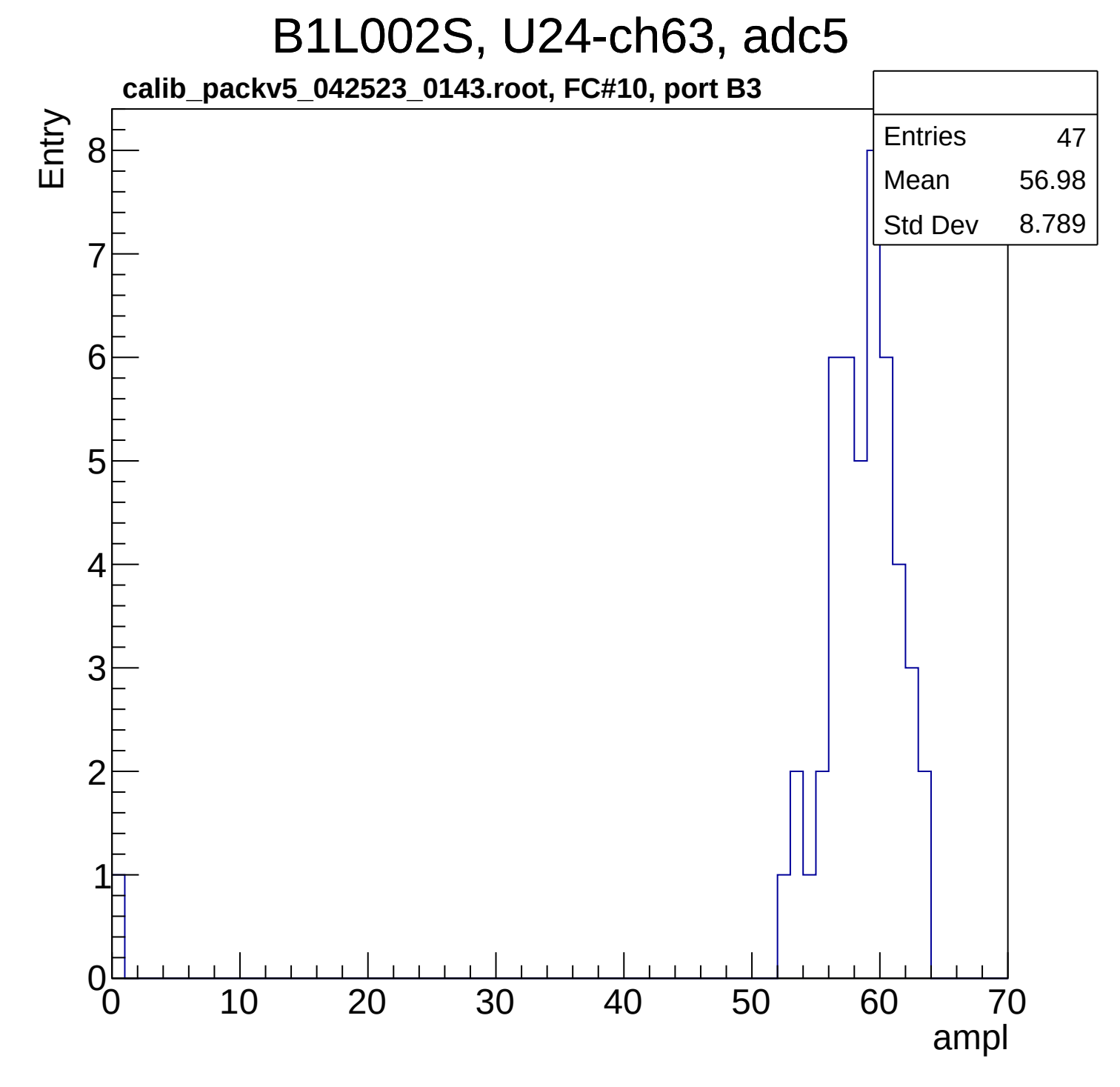

B1L002S, U24-ch63, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 46.99
Std Dev 3.065
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch63, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 53.95
Std Dev 2.799
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch63, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 56.98
Std Dev 8.789
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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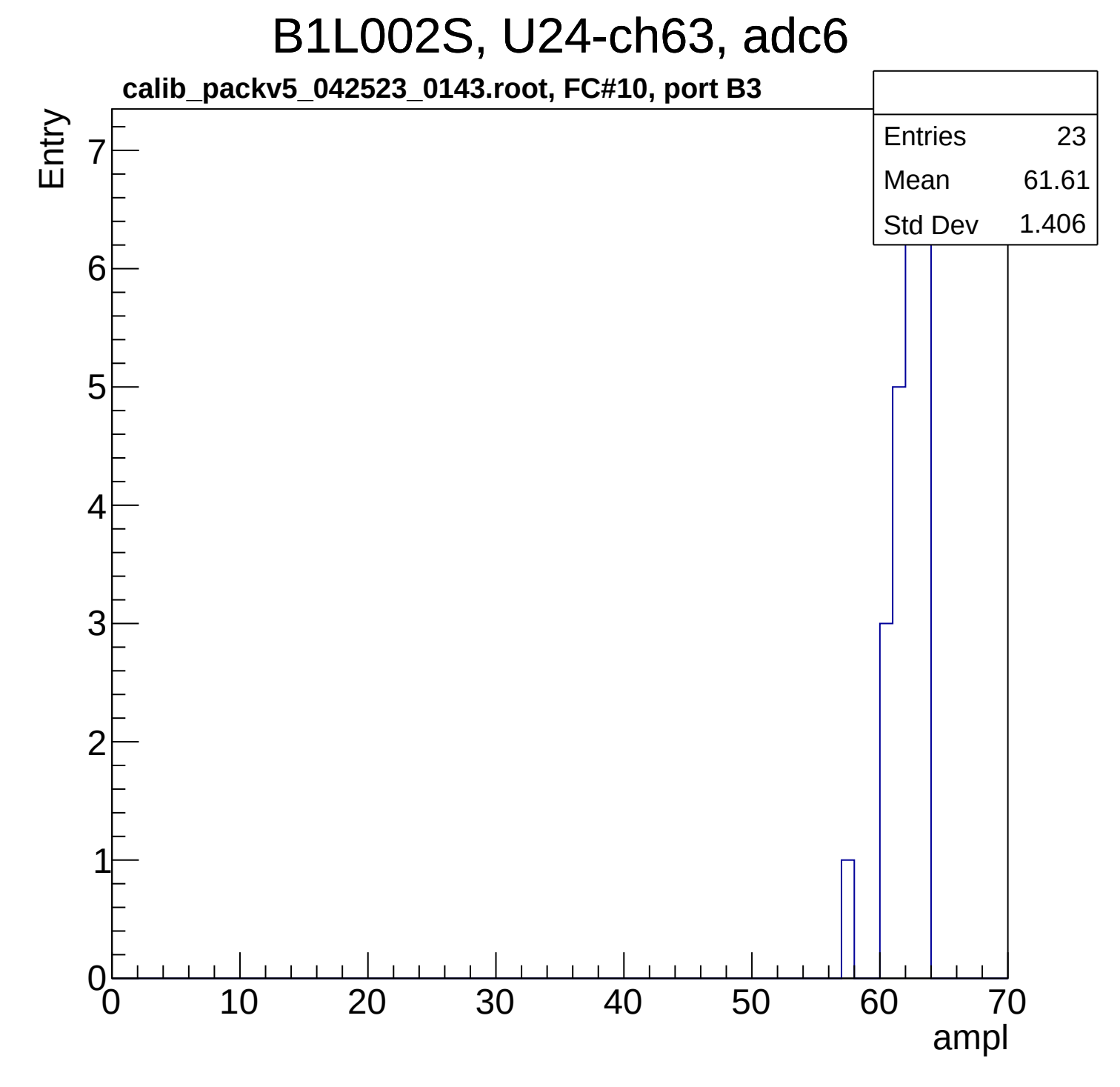
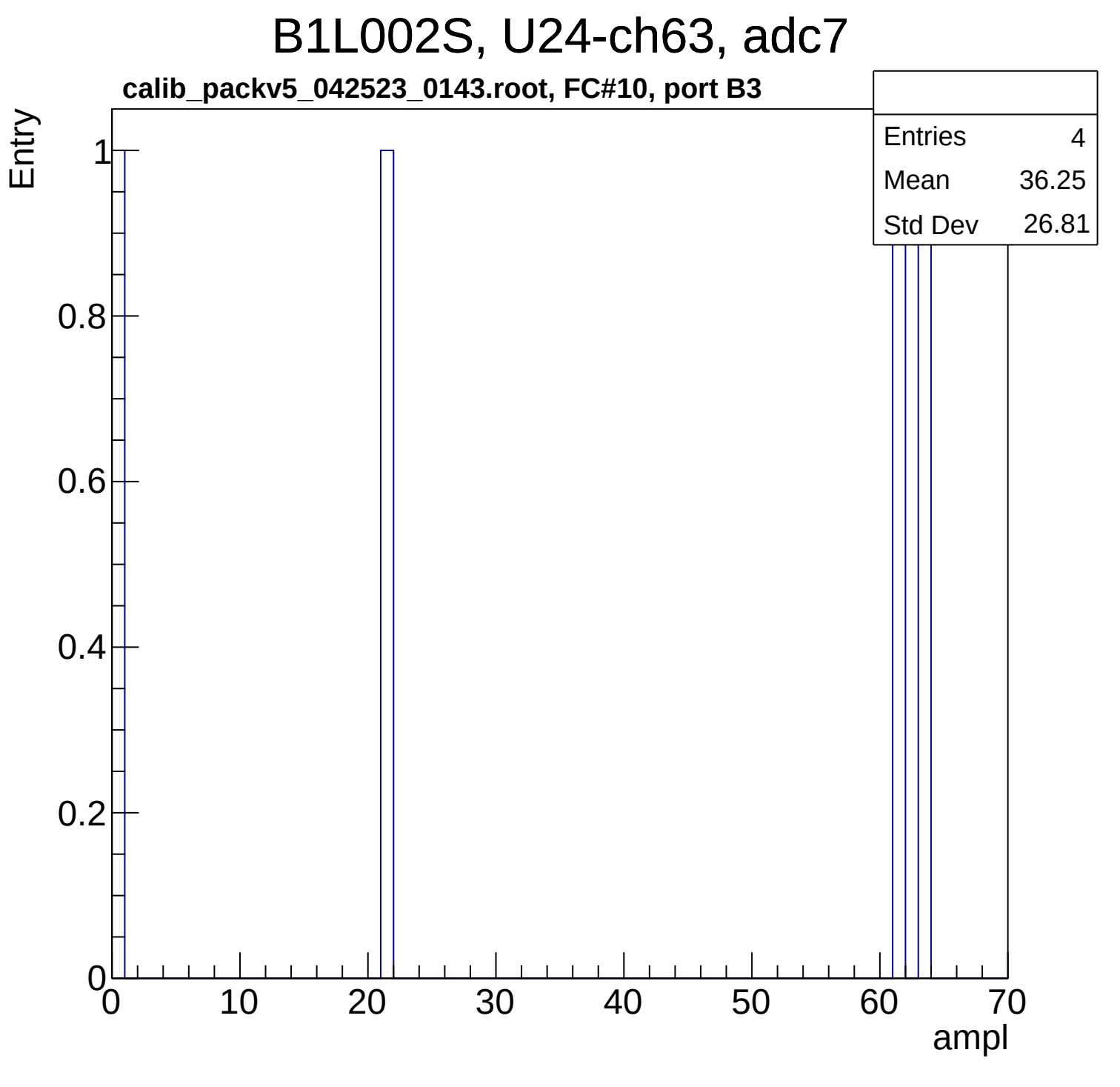
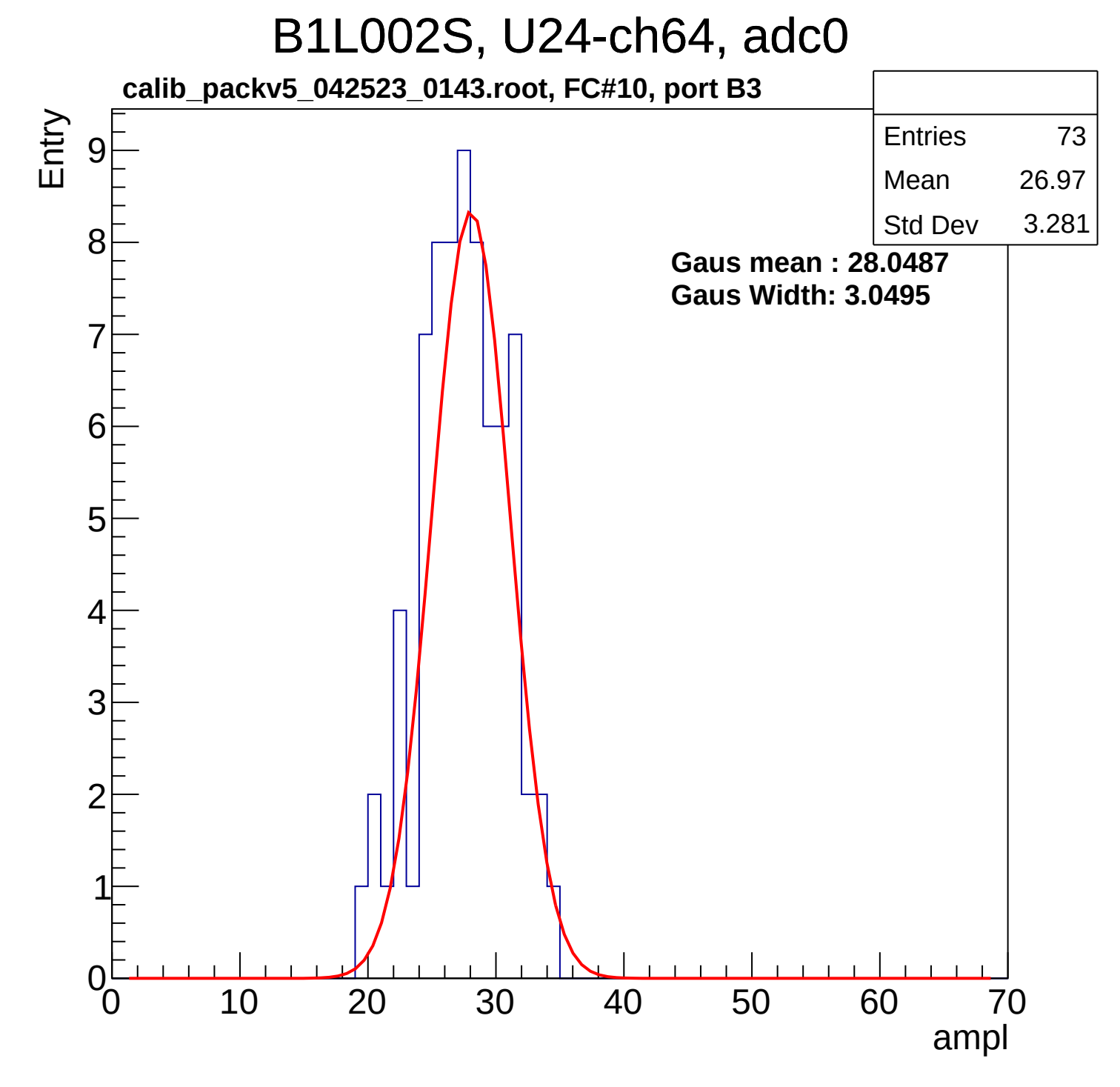
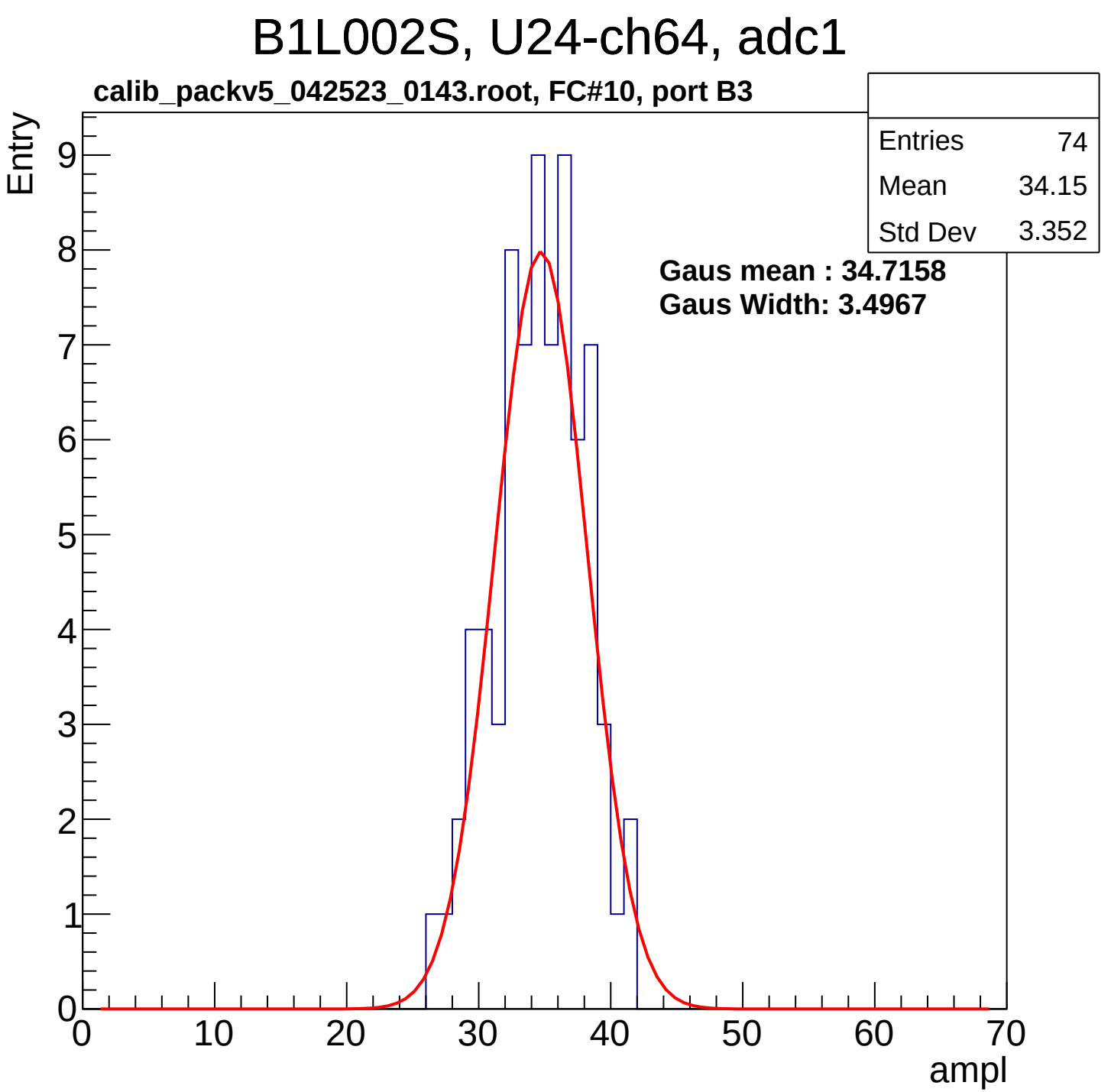
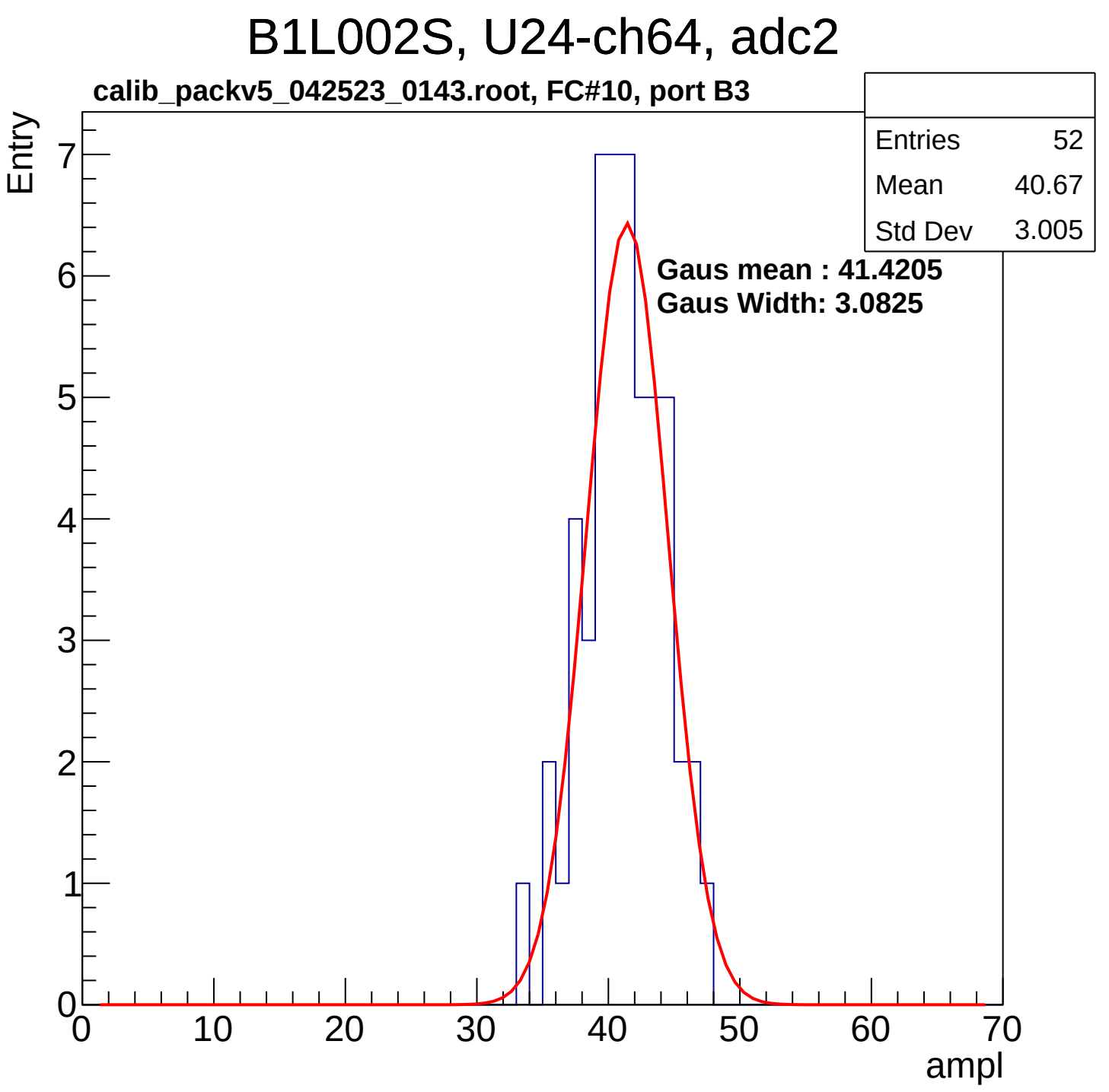
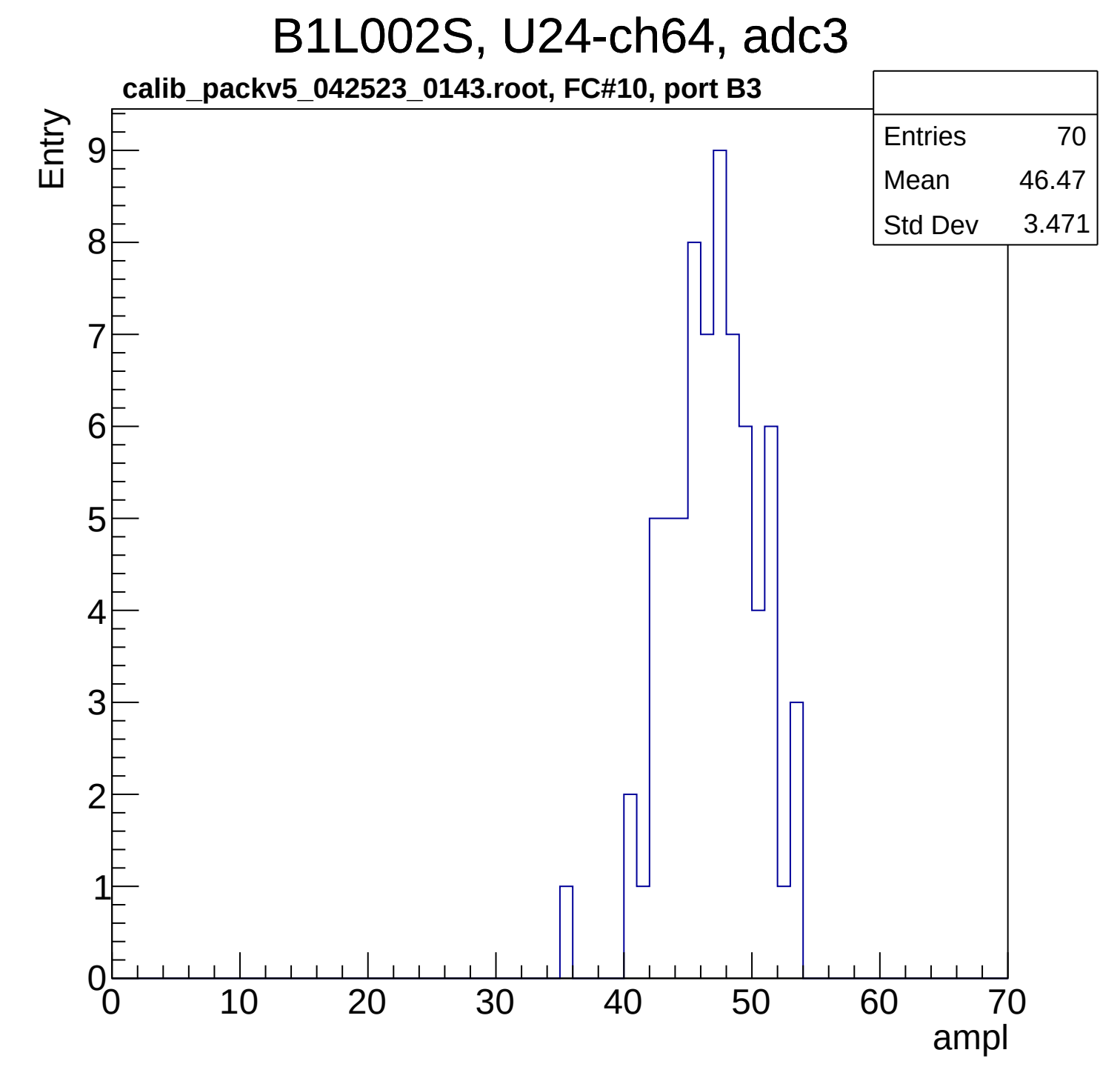

B1L002S, U24-ch63, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 23
Mean 61.61
Std Dev 1.406
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch63, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.25
Std Dev 26.81
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U24-ch64, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 26.97
Std Dev 3.281
Gaus mean : 28.0487
Gaus Width: 3.0495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch64, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 34.15
Std Dev 3.352
Gaus mean : 34.7158
Gaus Width: 3.4967
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch64, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.67
Std Dev 3.005
Gaus mean : 41.4205
Gaus Width: 3.0825
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch64, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.47
Std Dev 3.471
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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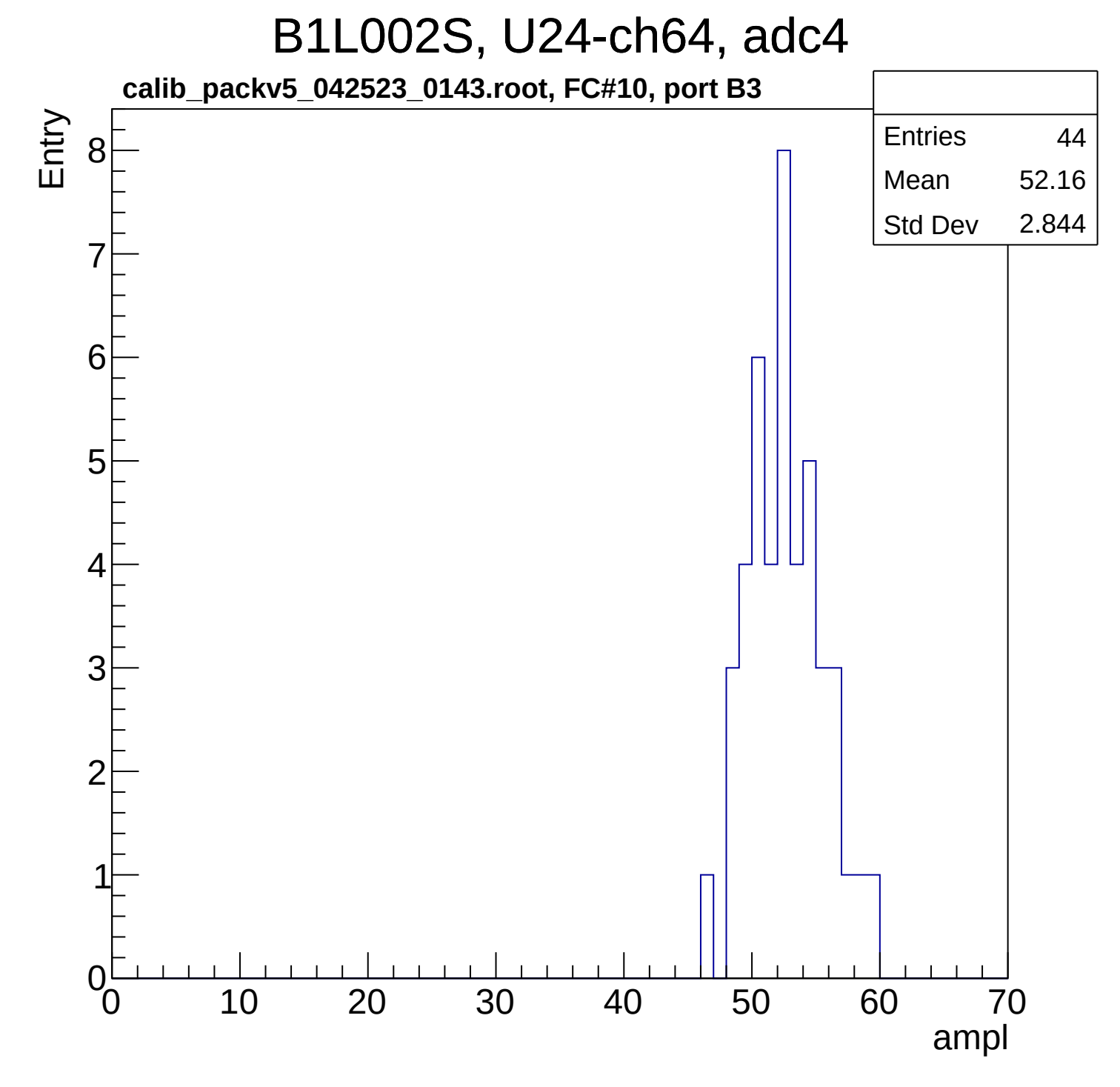
60
70


B1L002S, U24-ch64, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 52.16
Std Dev 2.844
Entry
ampl
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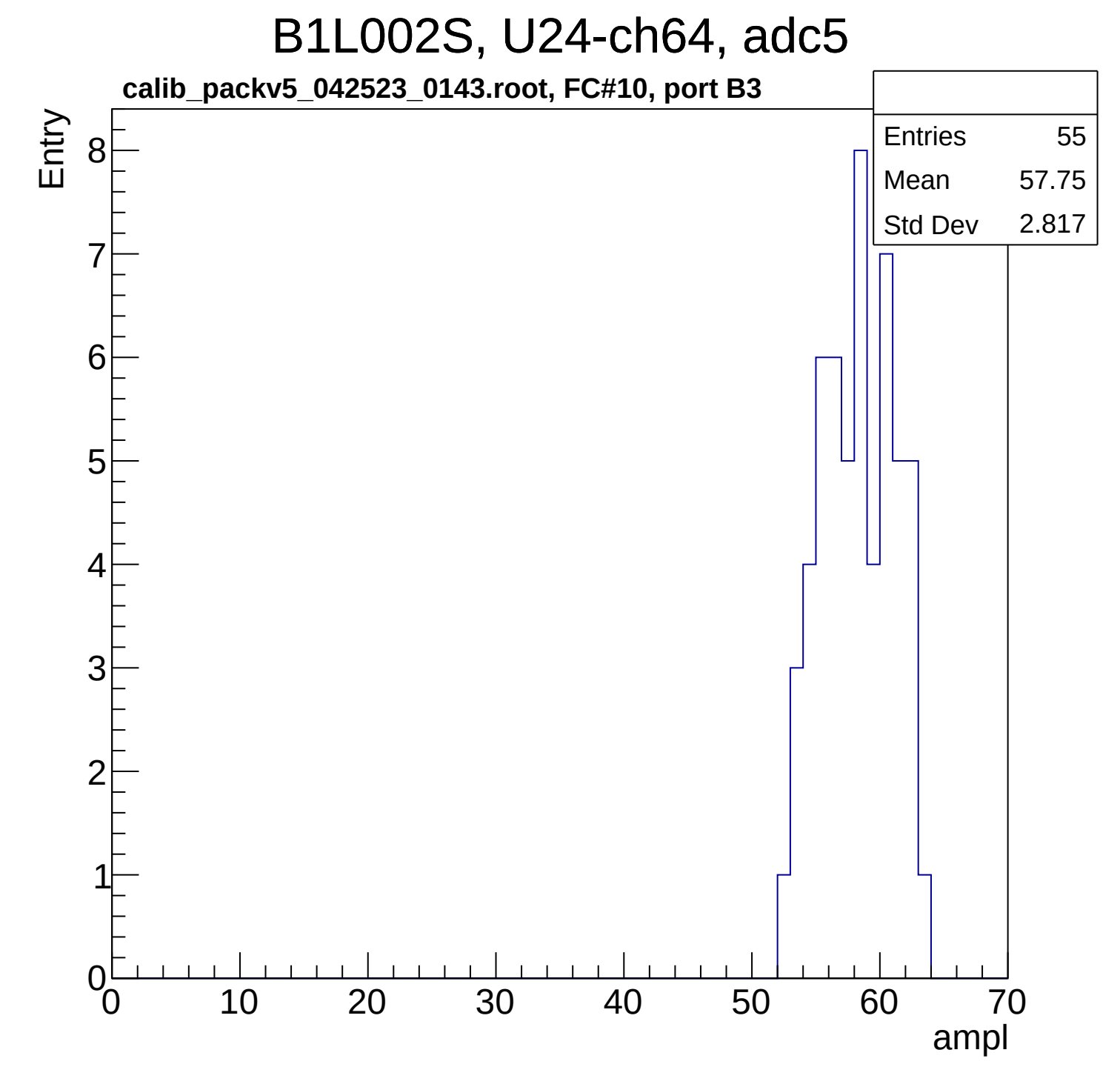
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch64, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.75
Std Dev 2.817
Entry
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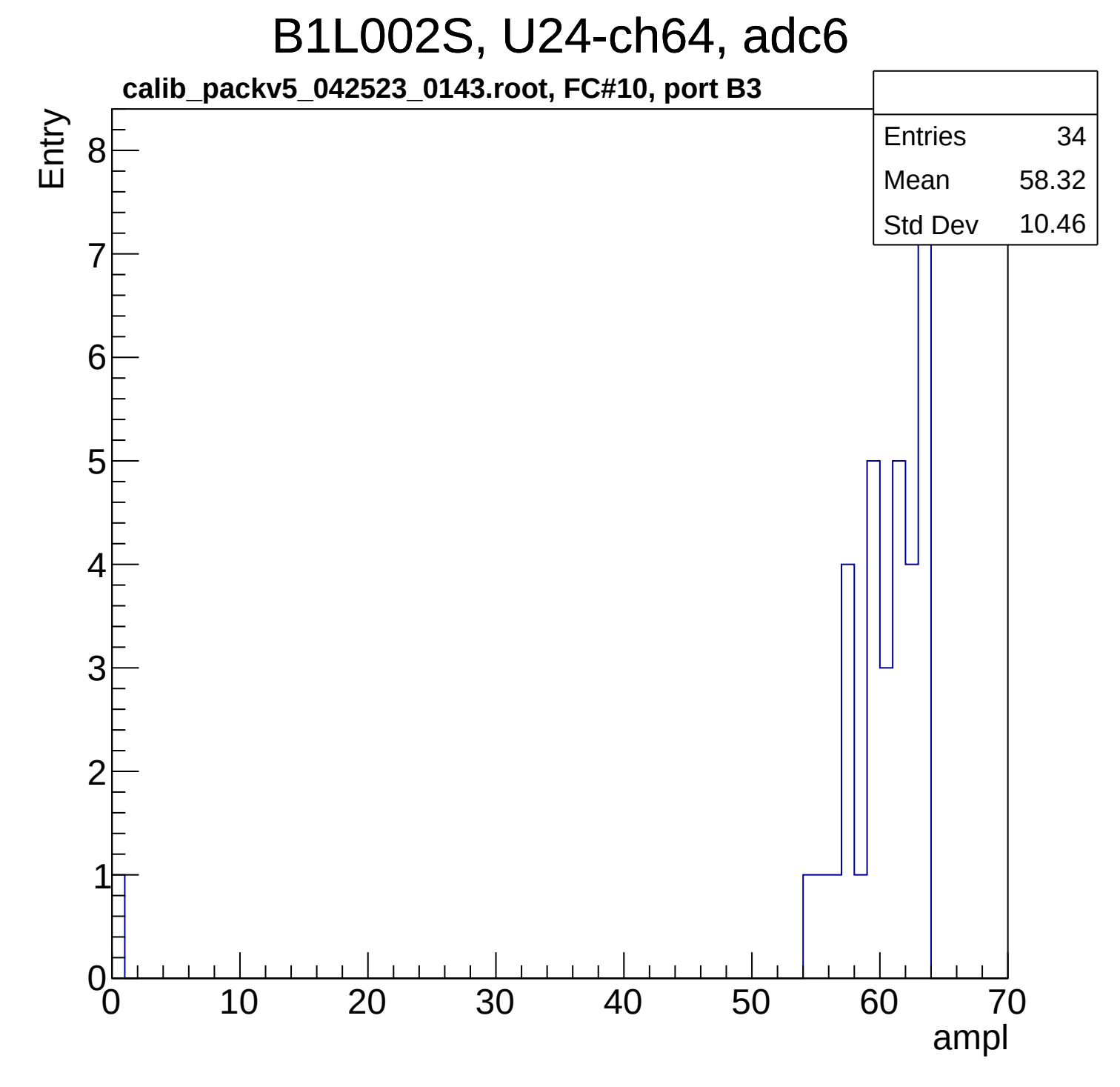
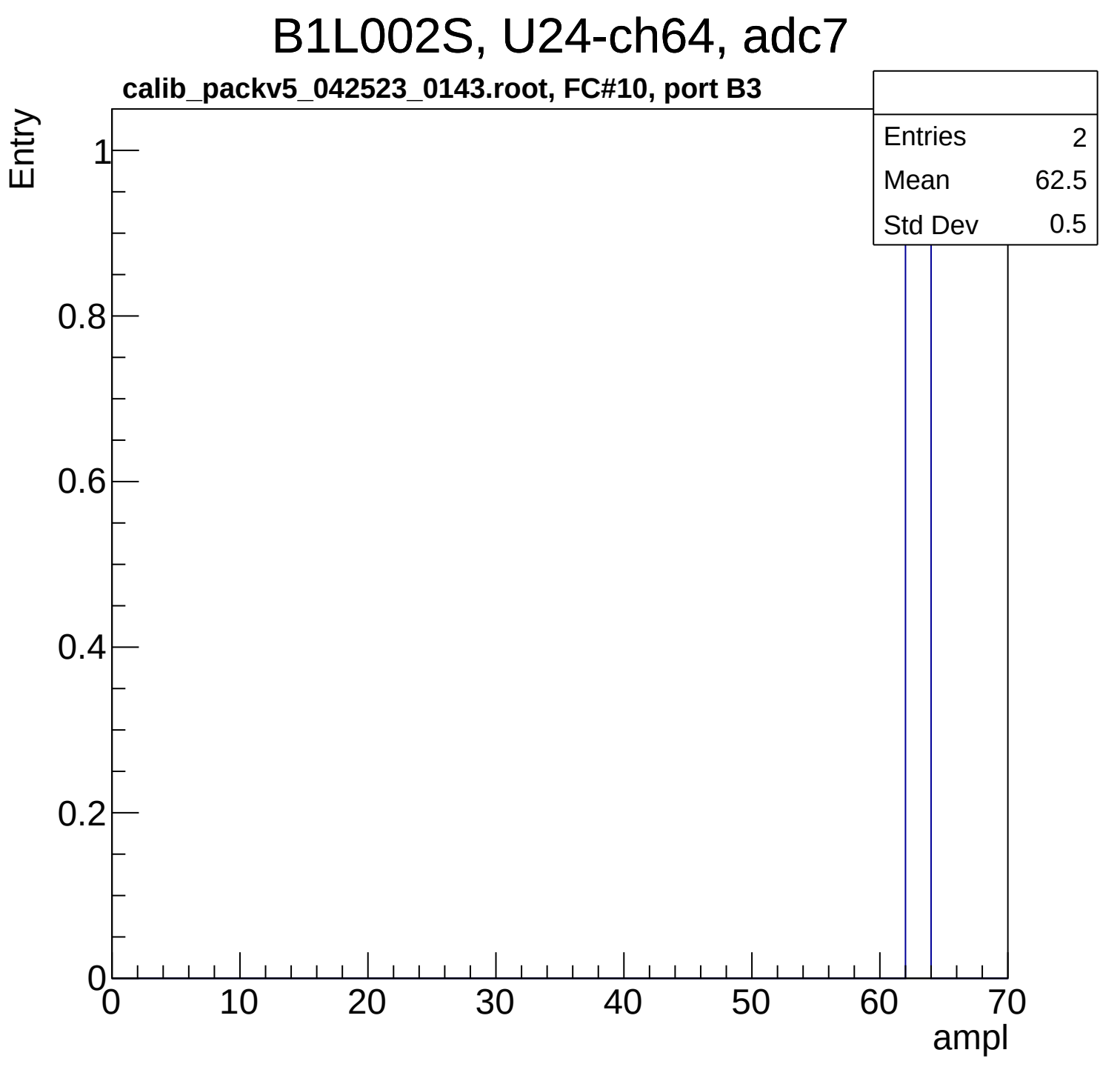
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch64, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.32
Std Dev 10.46
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch64, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 62.5
Std Dev 0.5
Entry
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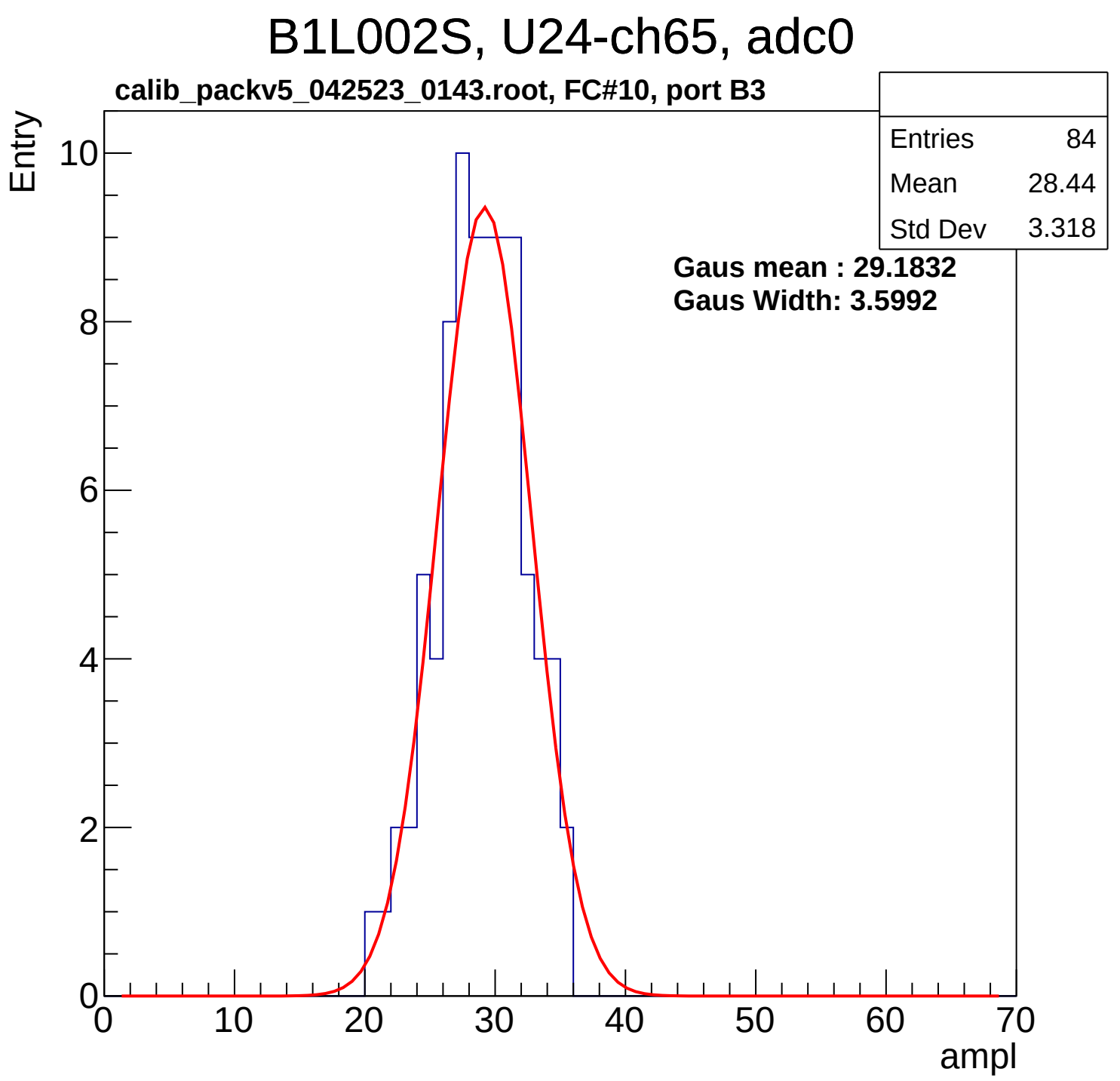
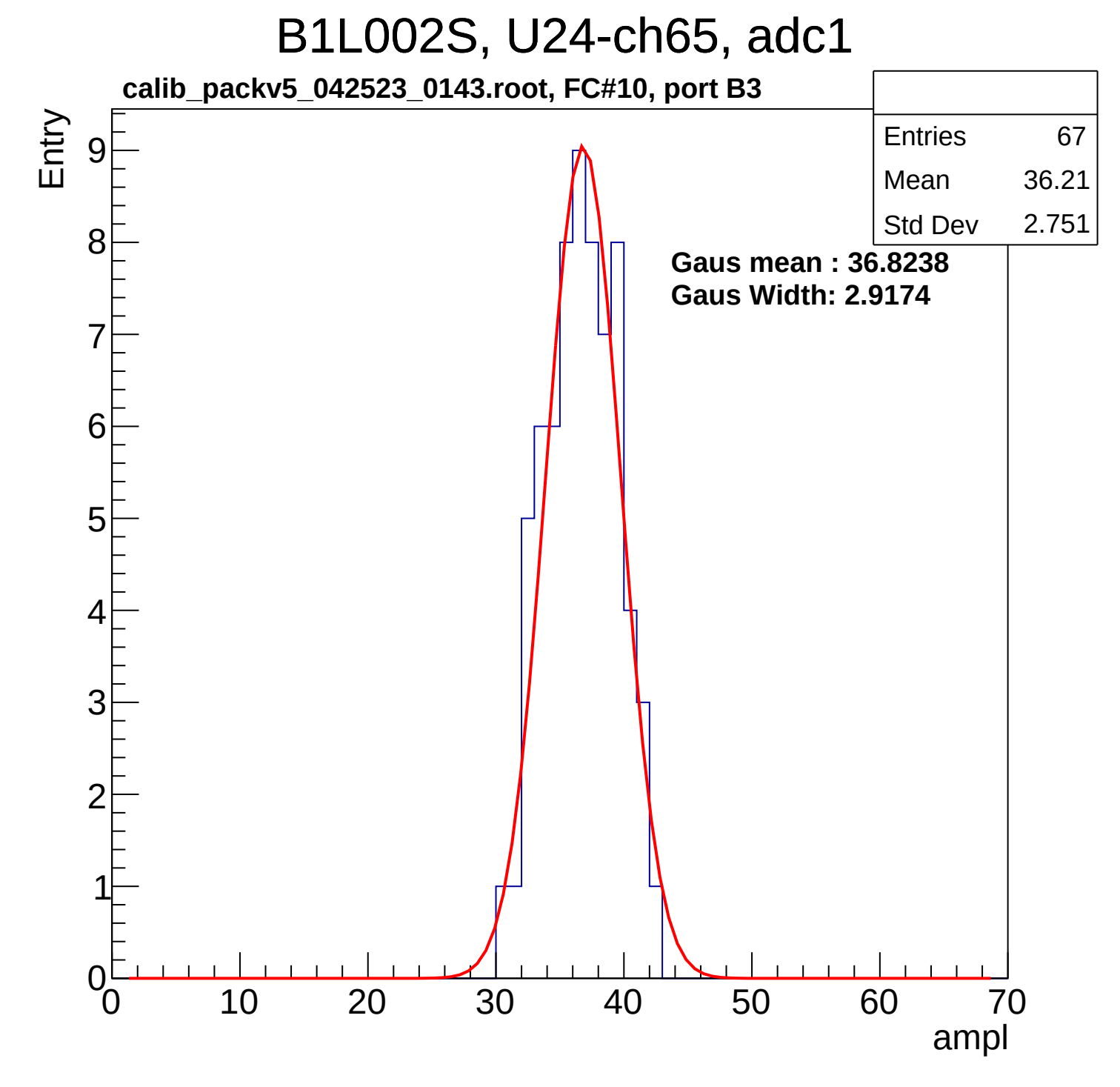
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch65, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 28.44
Std Dev 3.318
Gaus mean : 29.1832
Gaus Width: 3.5992
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch65, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 36.21
Std Dev 2.751
Gaus mean : 36.8238
Gaus Width: 2.9174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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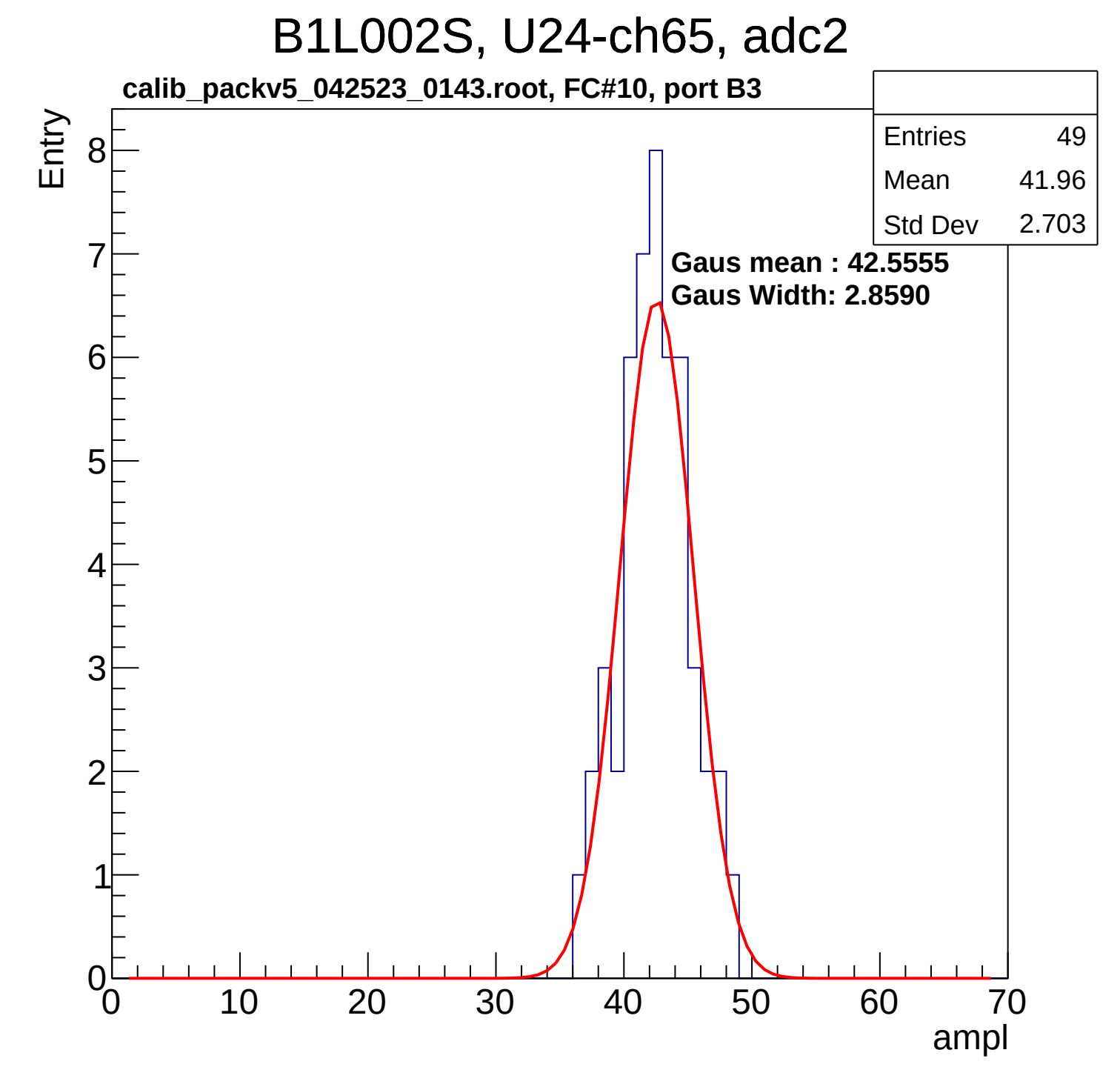
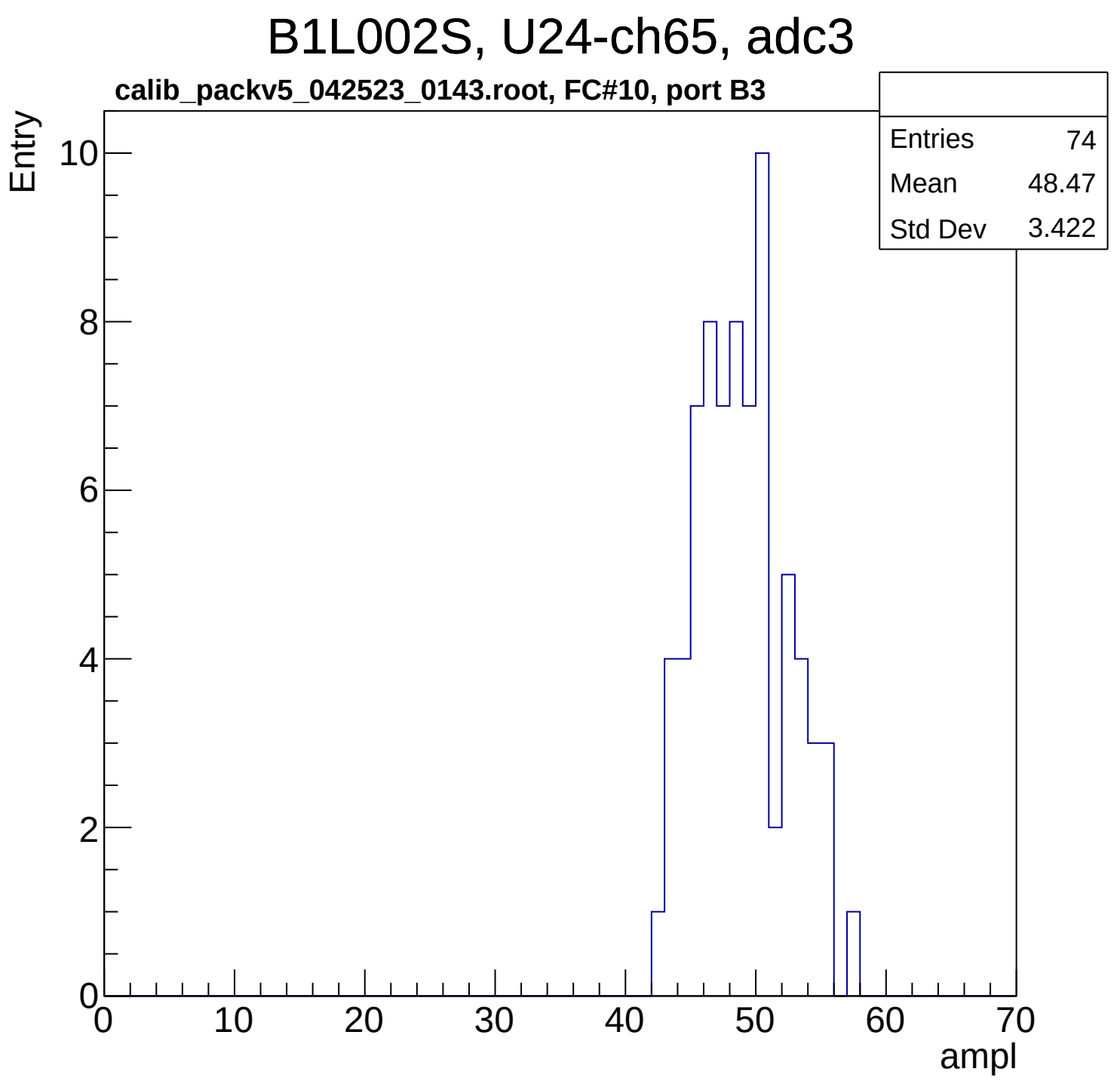
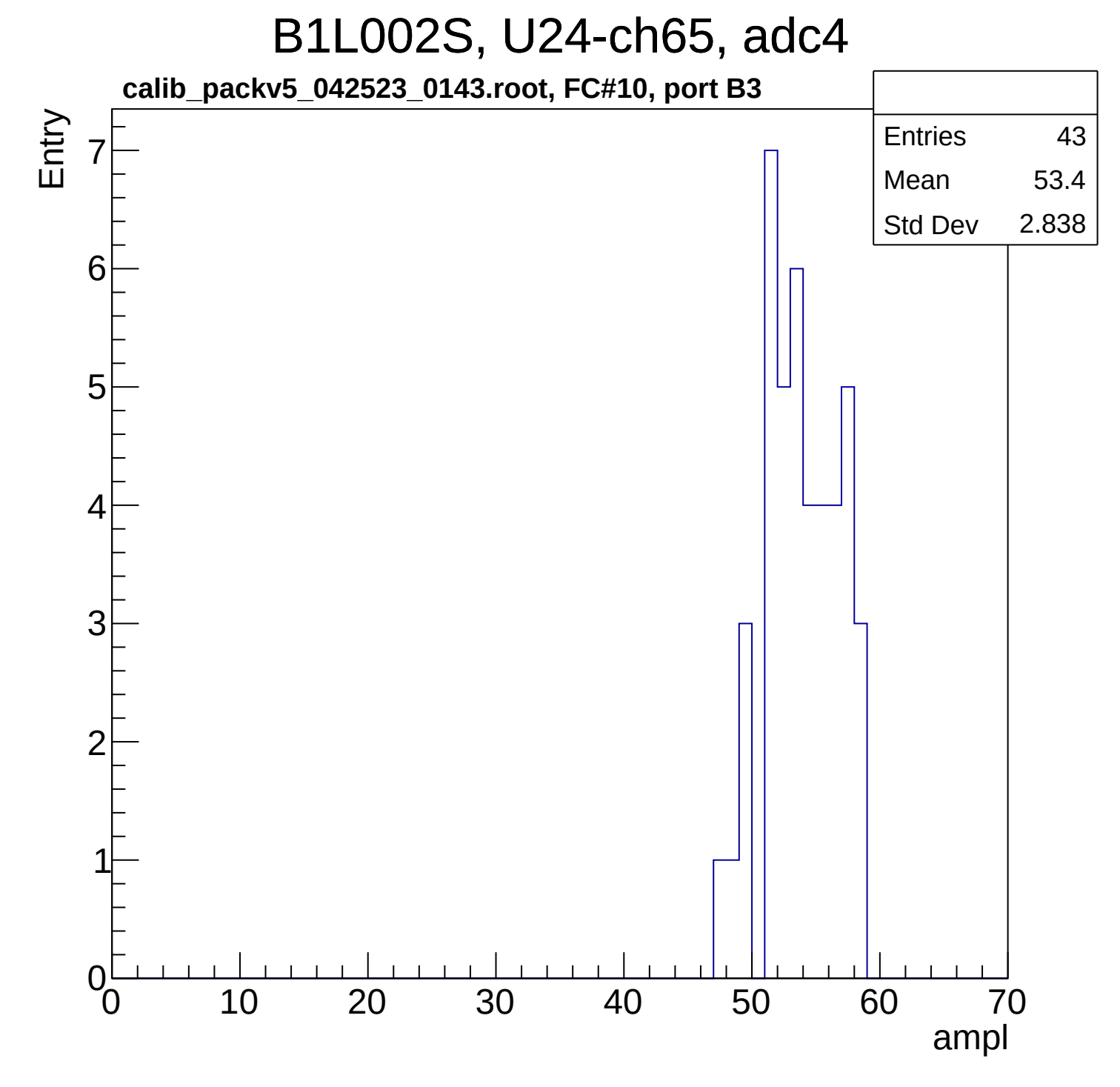

B1L002S, U24-ch65, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 41.96
Std Dev 2.703
Gaus mean : 42.5555
Gaus Width: 2.8590
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch65, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 48.47
Std Dev 3.422
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch65, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 53.4
Std Dev 2.838
Entry
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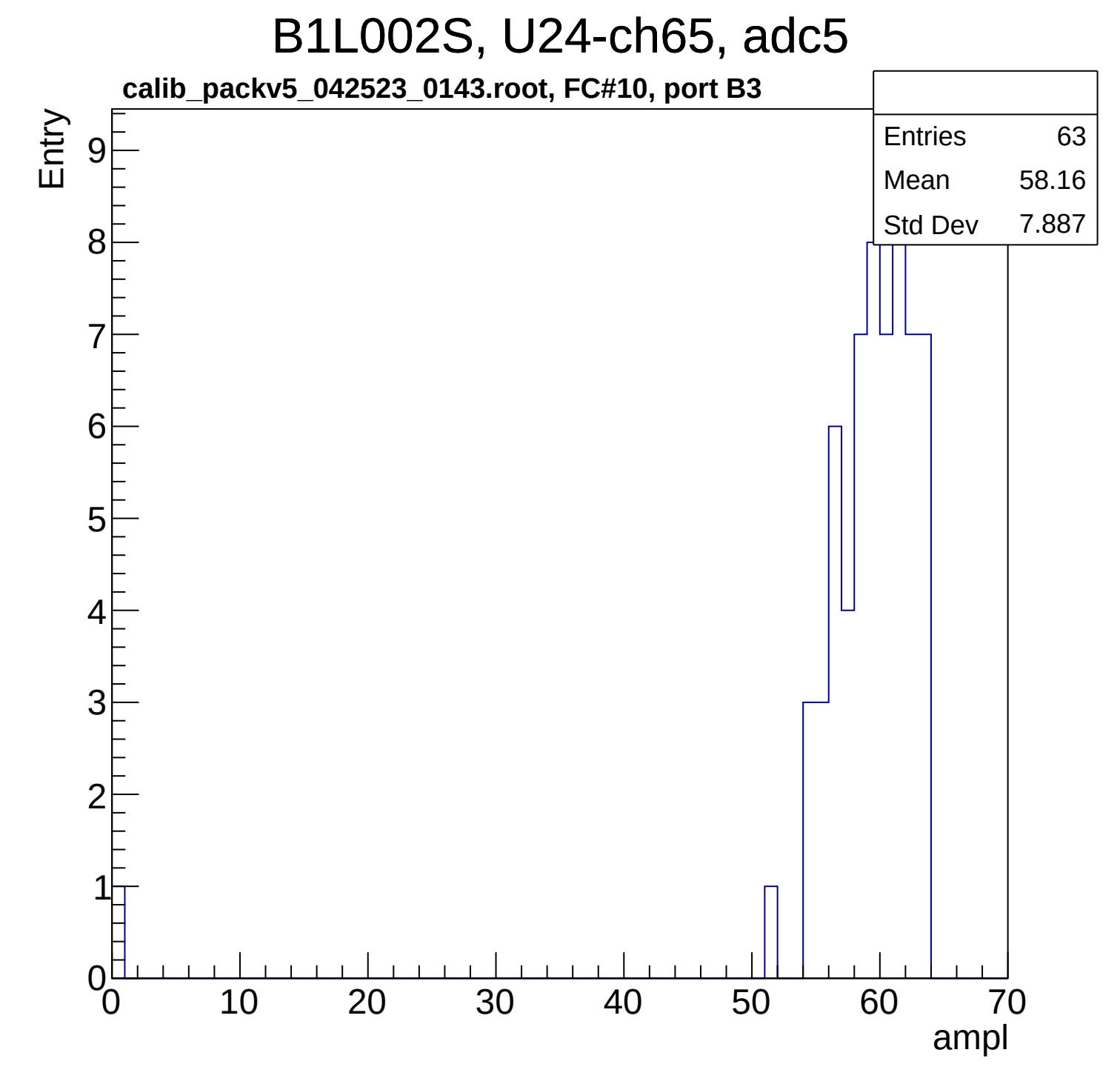
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch65, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 58.16
Std Dev 7.887
Entry
ampl
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3
4
5
6
7
8
9
0
10
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40
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60
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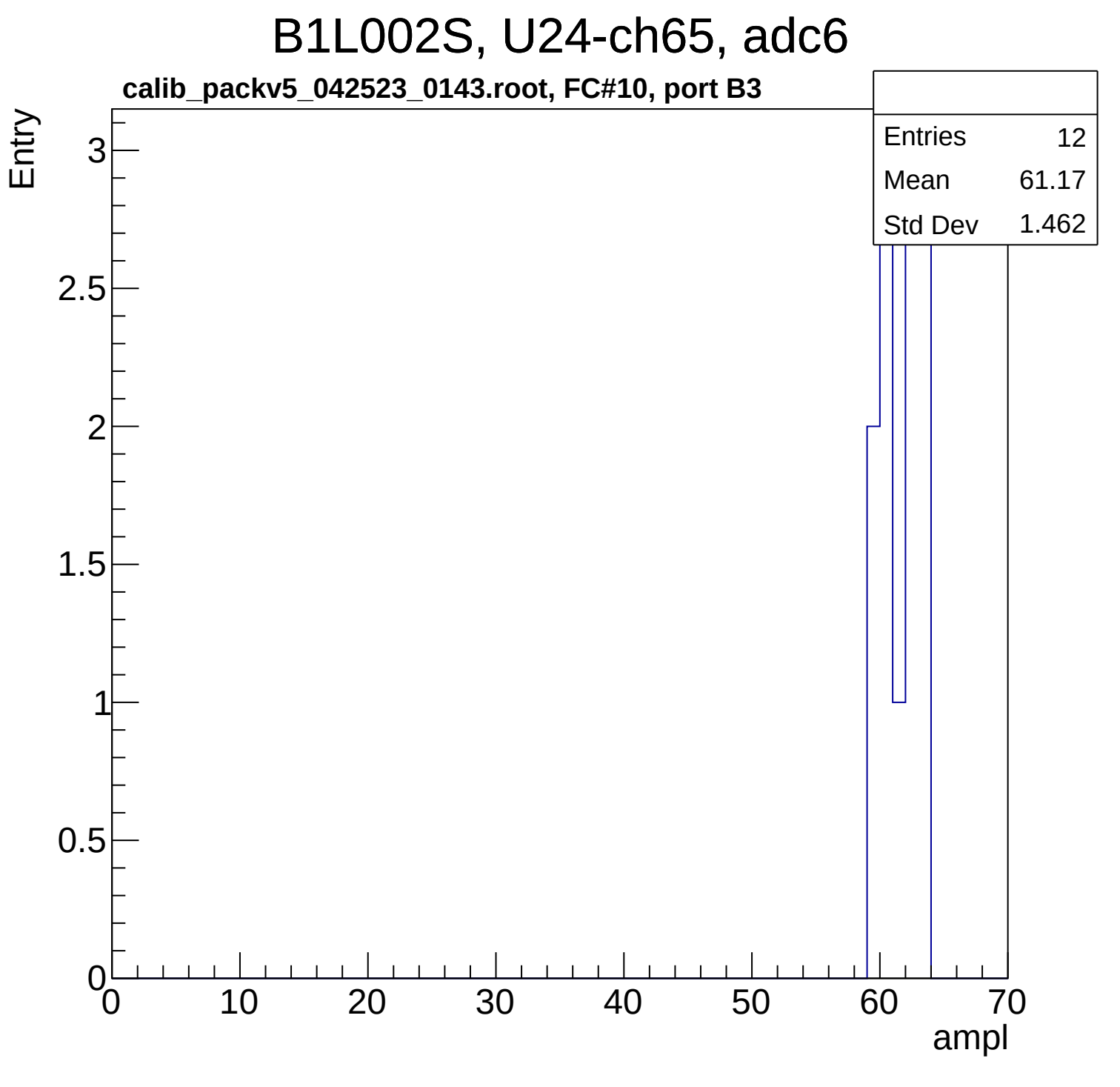
B1L002S, U24-ch65, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 61.17
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
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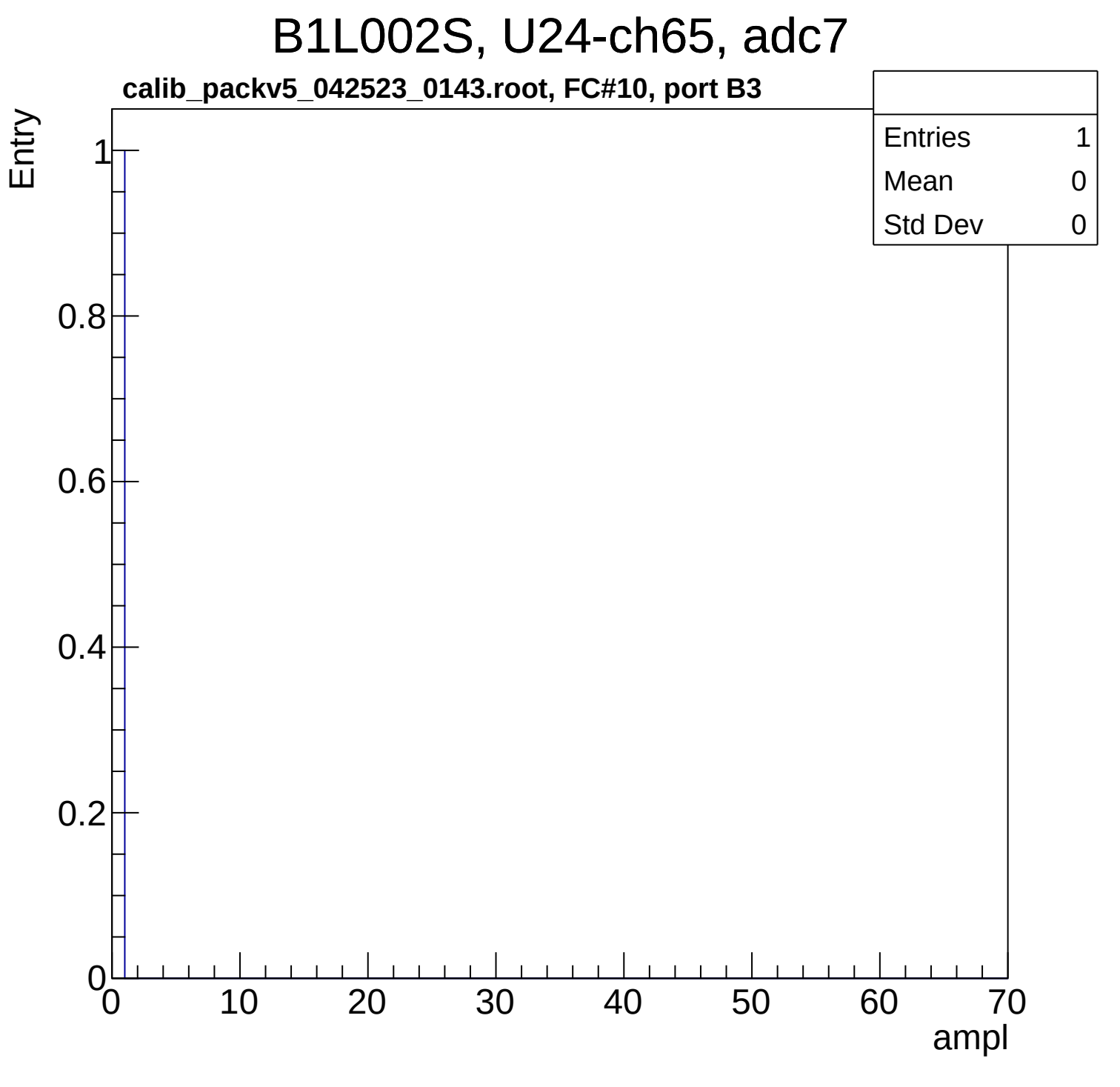
B1L002S, U24-ch65, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
0
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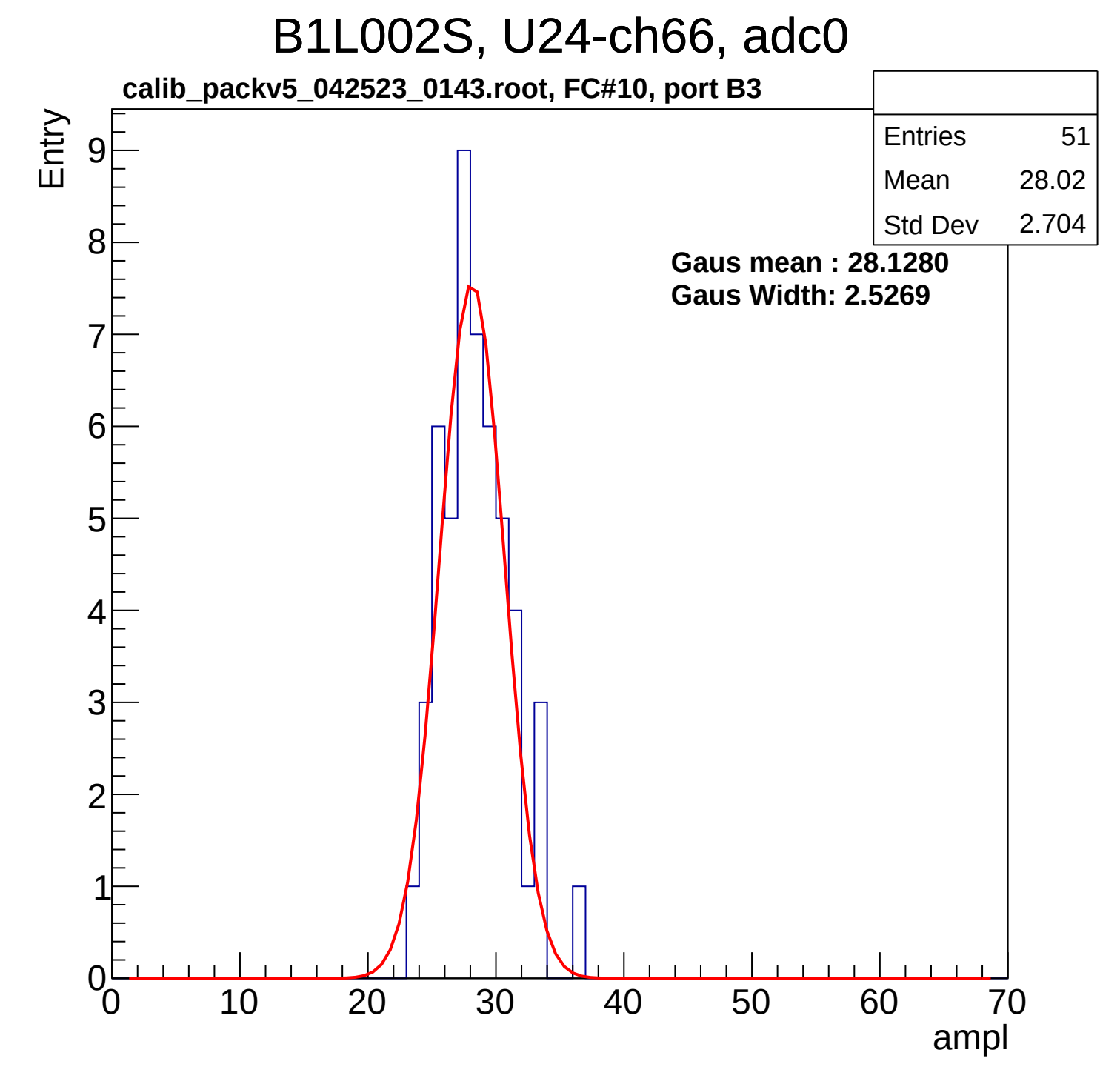
70
ampl


B1L002S, U24-ch66, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 28.02
Std Dev 2.704
Gaus mean : 28.1280
Gaus Width: 2.5269
Entry
ampl
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3
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6
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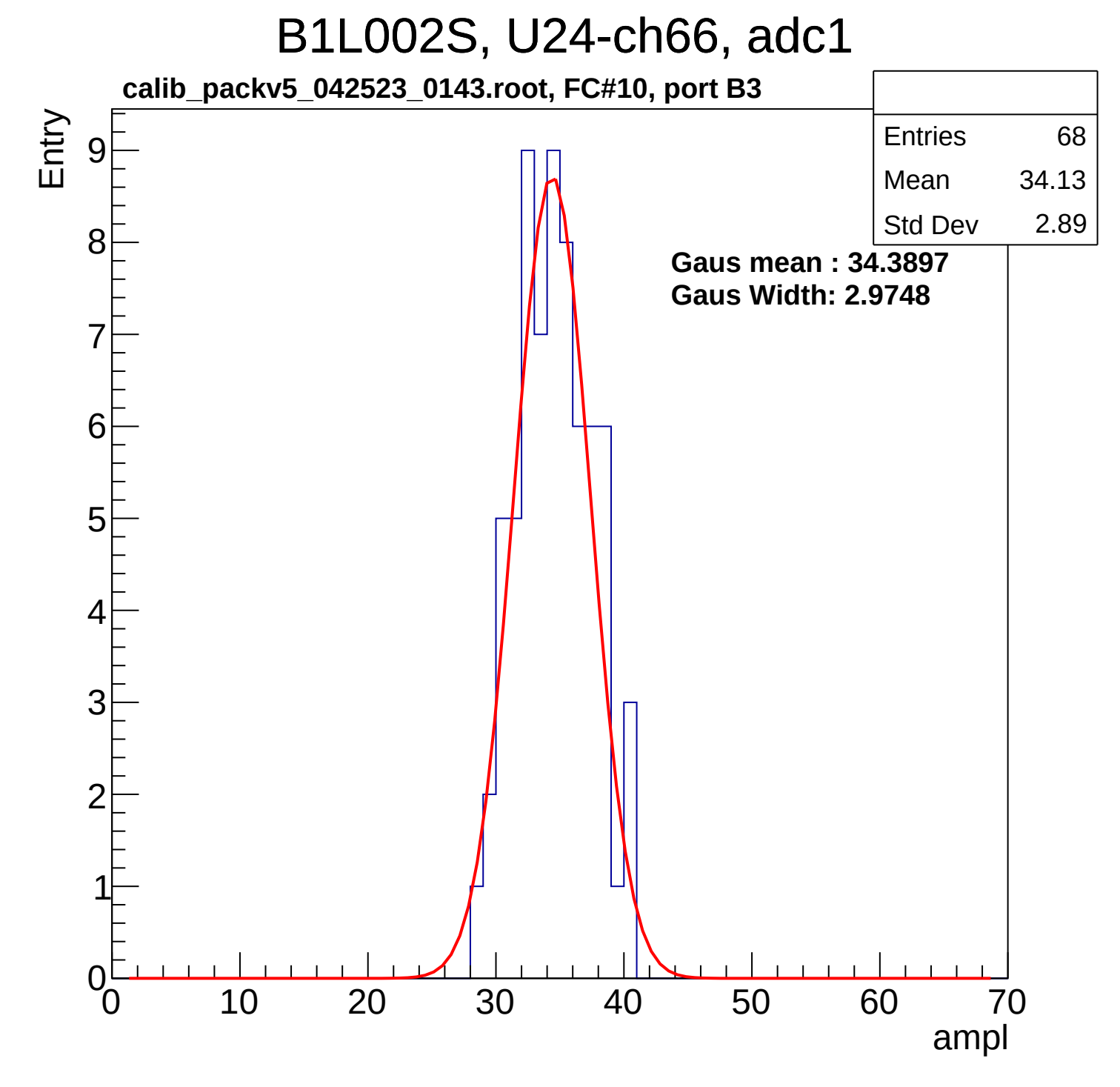
B1L002S, U24-ch66, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.13
Std Dev 2.89
Gaus mean : 34.3897
Gaus Width: 2.9748
Entry
ampl
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10
20
30
40
50
60
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2
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4
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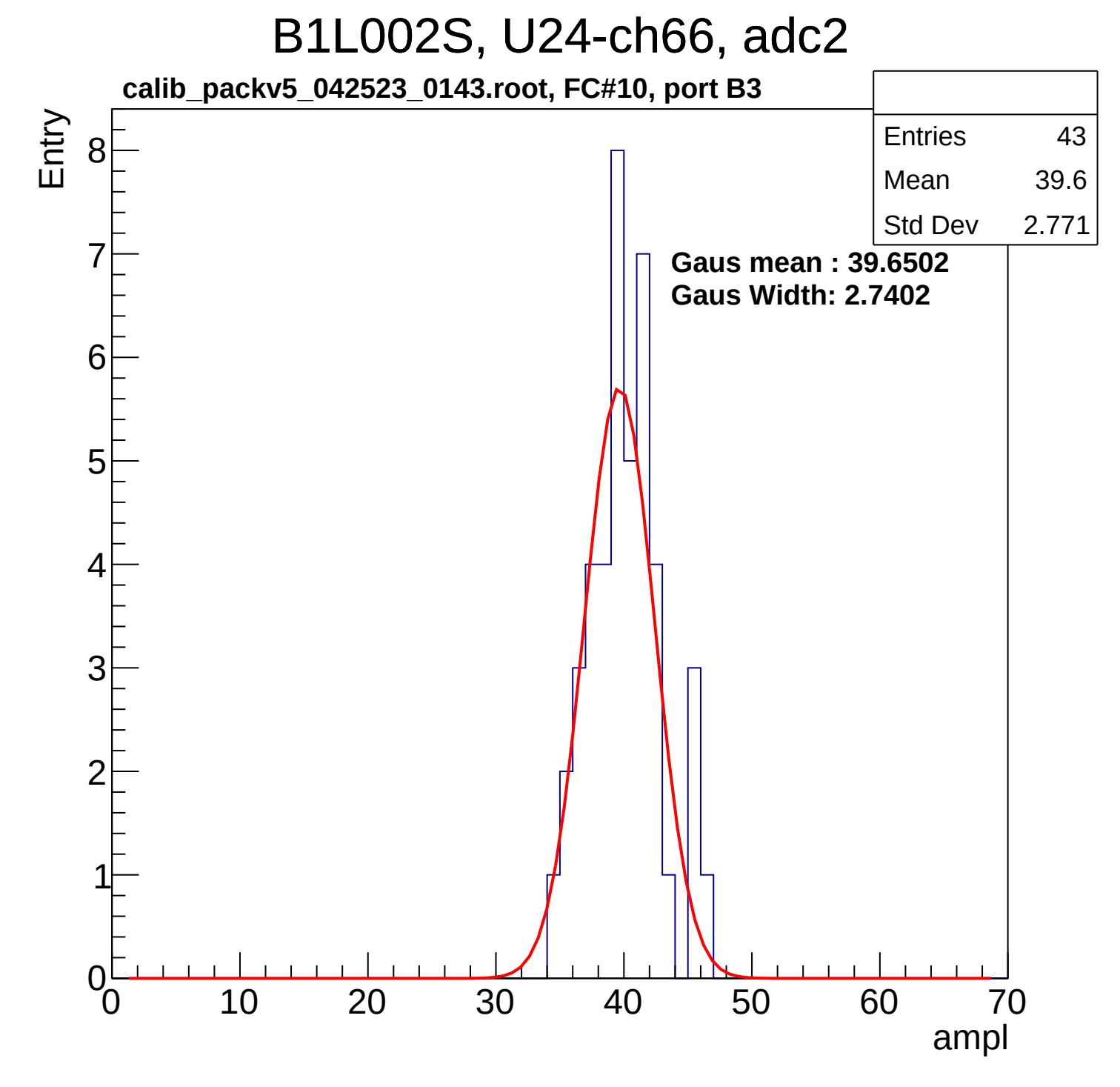
6
7
8
9


B1L002S, U24-ch66, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 39.6
Std Dev 2.771
Gaus mean : 39.6502
Gaus Width: 2.7402
Entry
ampl
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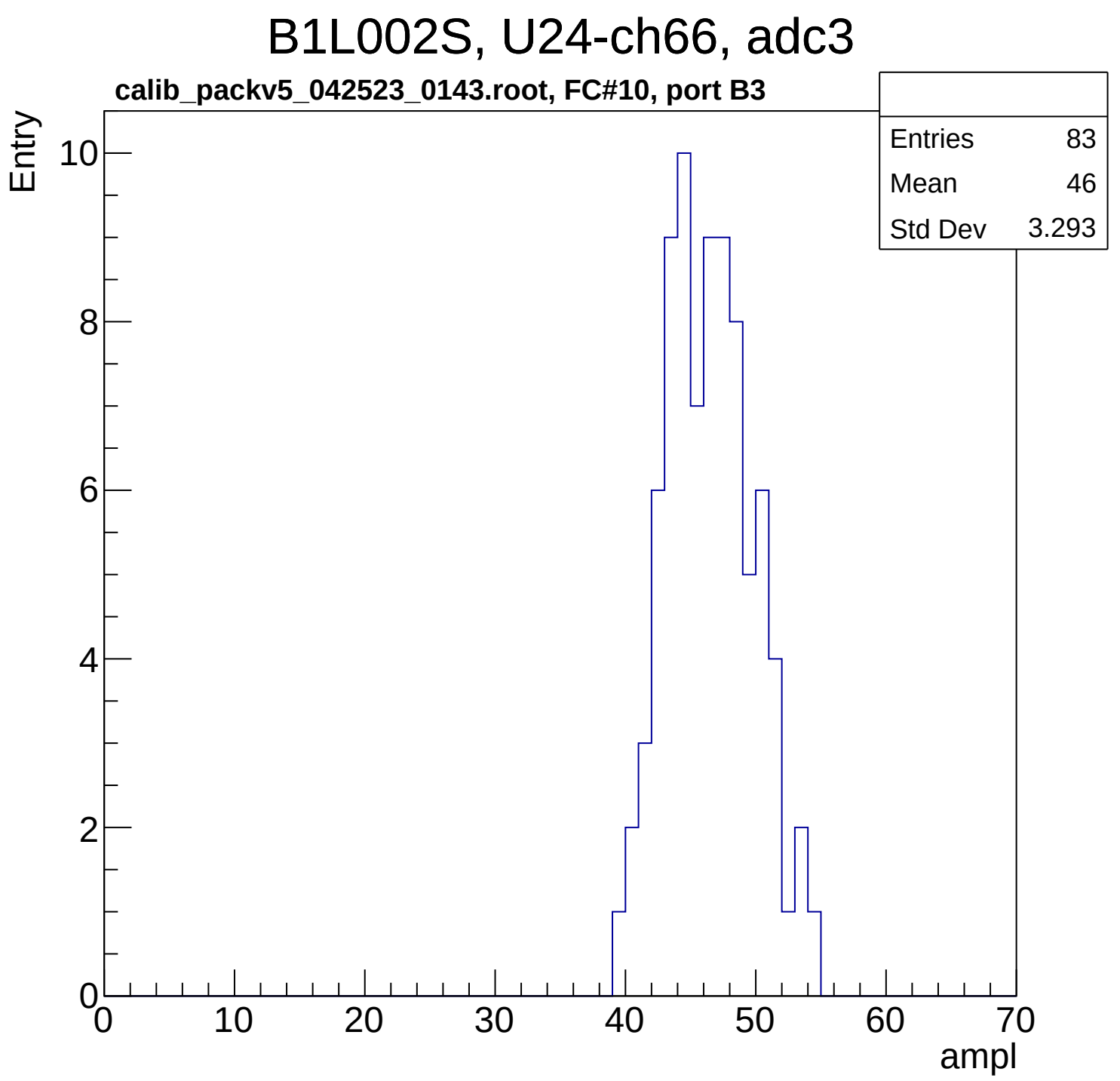
B1L002S, U24-ch66, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 46
Std Dev 3.293
Entry
0
2
4
6
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10
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10
20
30
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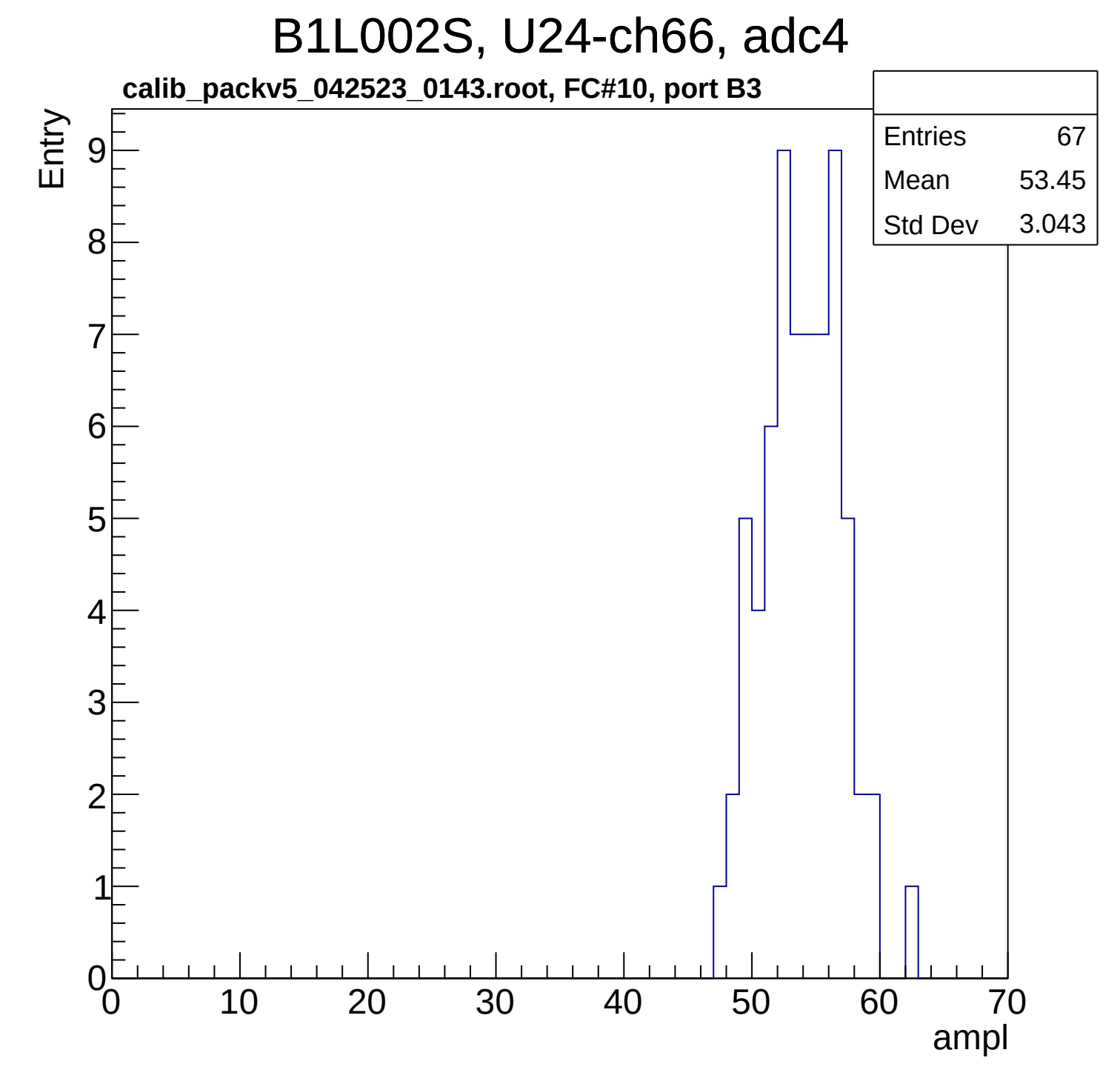
50
60
70
ampl


B1L002S, U24-ch66, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 53.45
Std Dev 3.043
Entry
ampl
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2
3
4
5
6
7
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9
0
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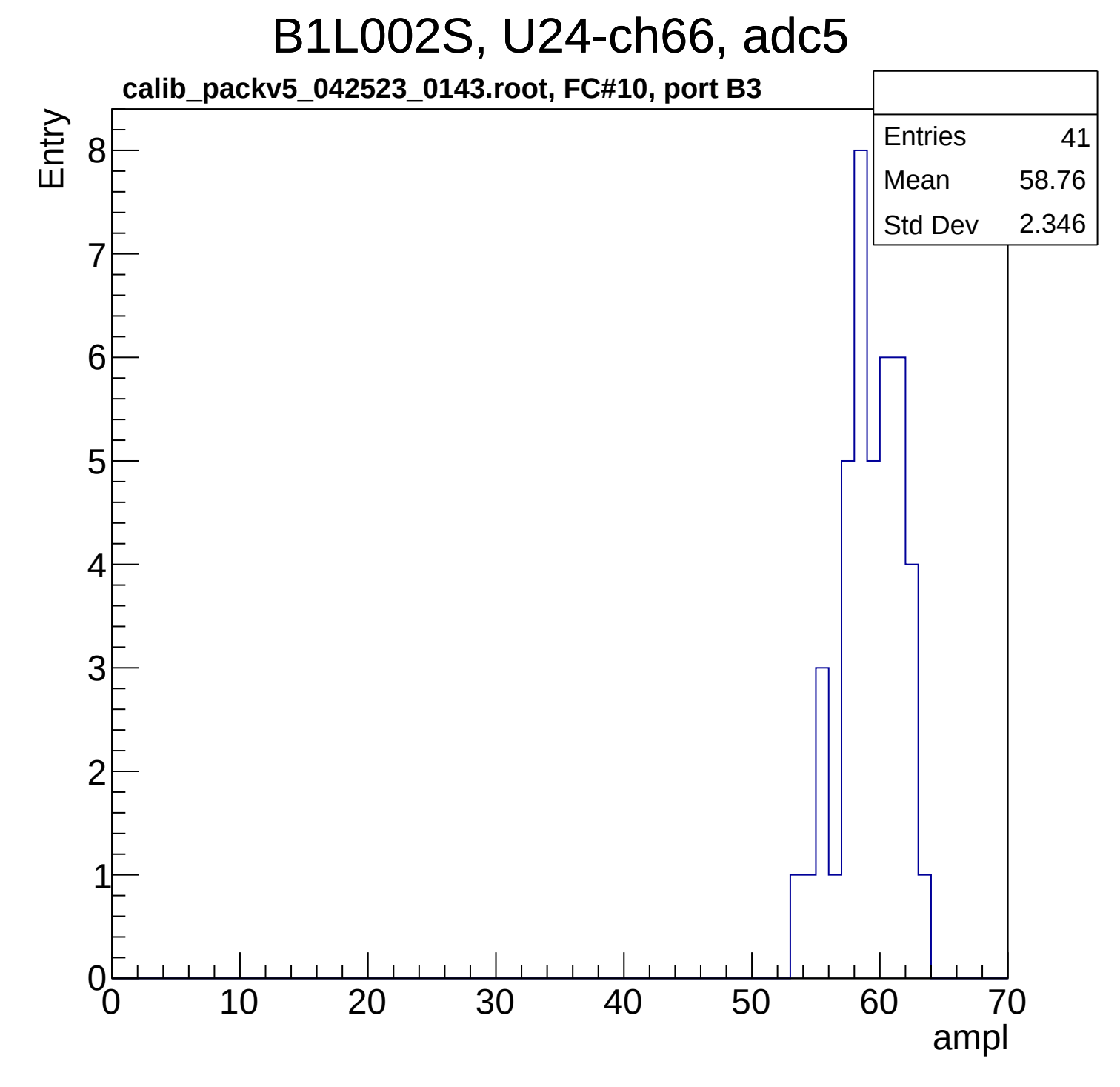
B1L002S, U24-ch66, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 58.76
Std Dev 2.346
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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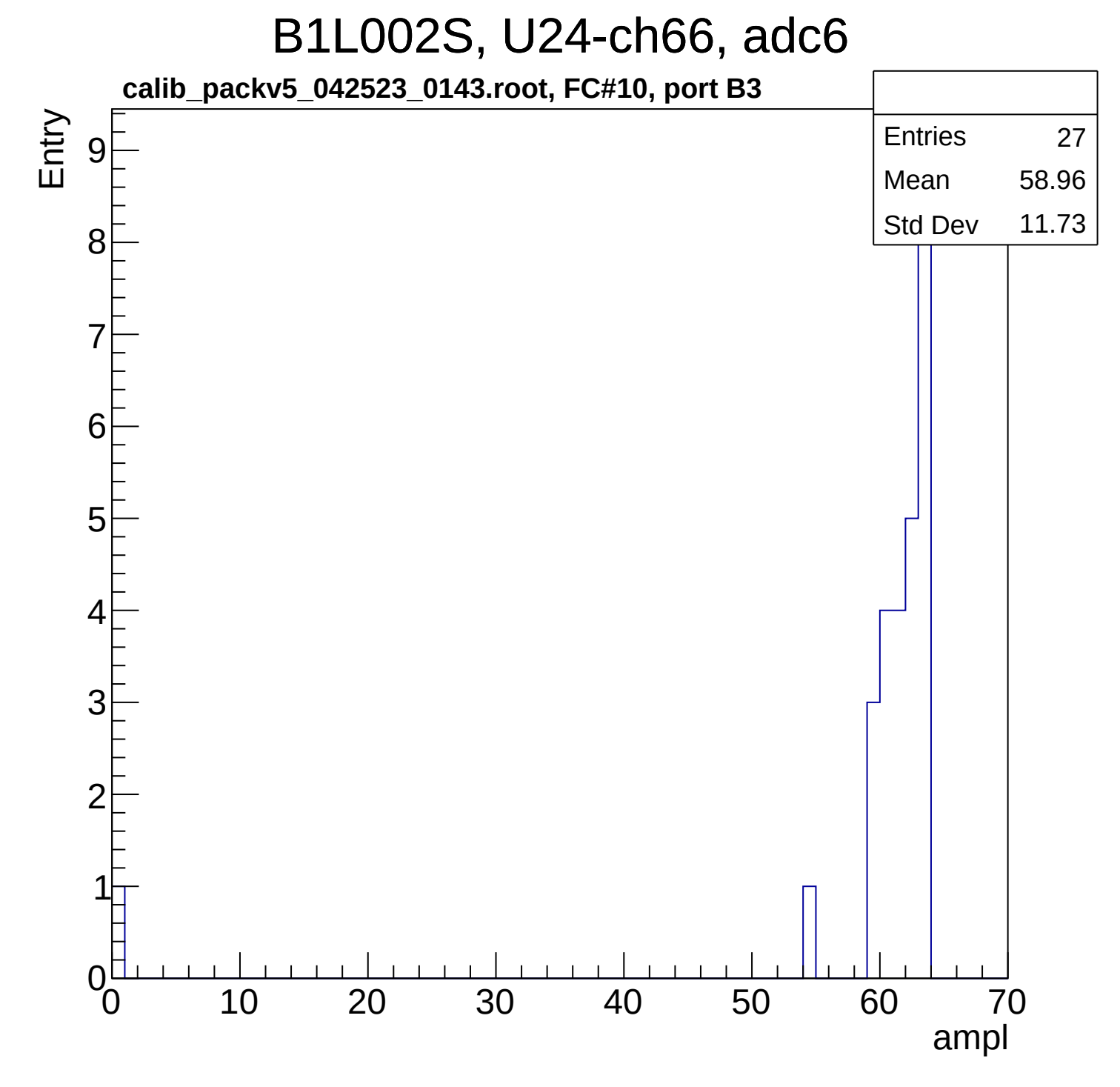
B1L002S, U24-ch66, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.96
Std Dev 11.73
Entry
ampl
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2
3
4
5
6
7
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0
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60
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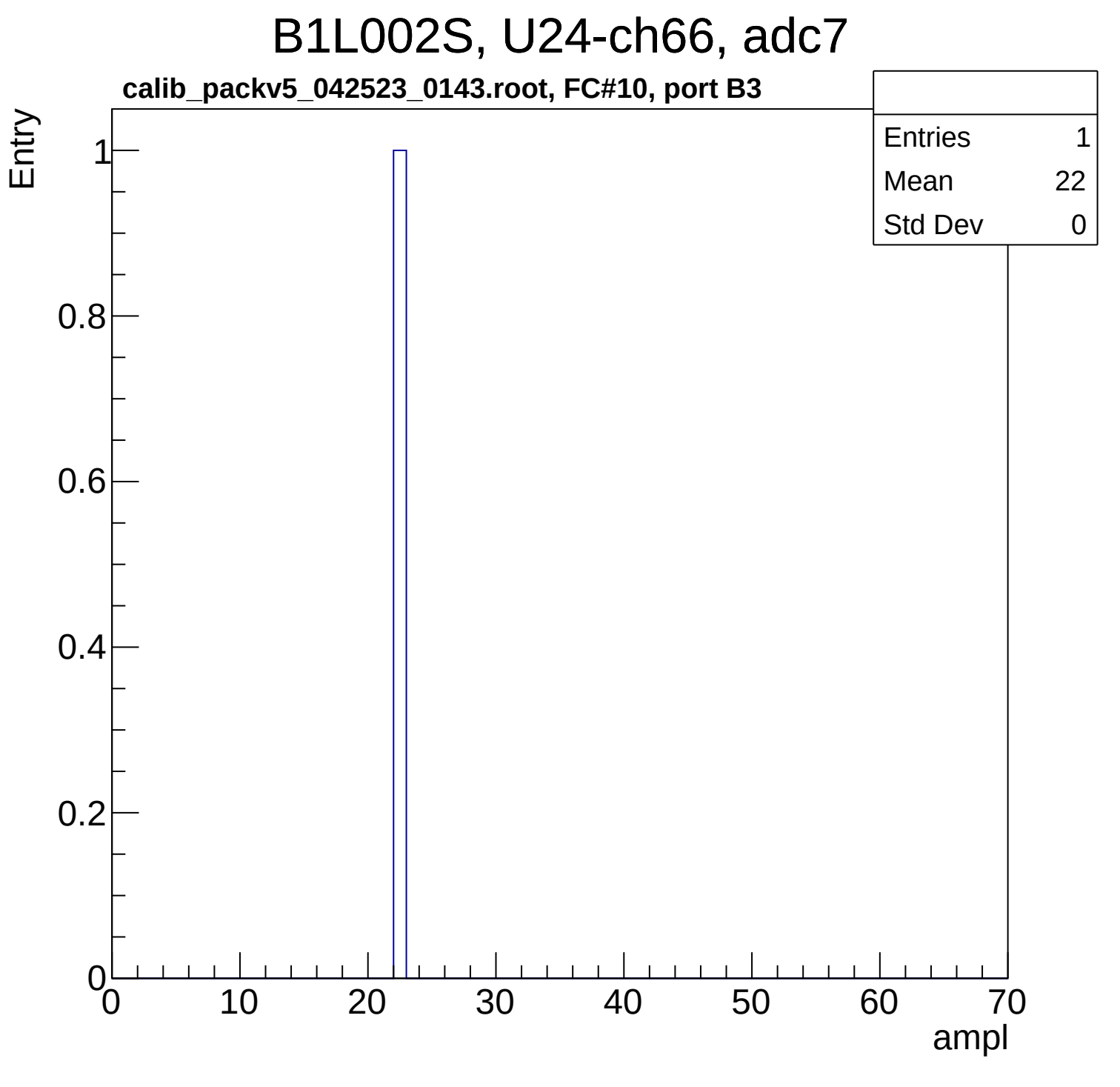
B1L002S, U24-ch66, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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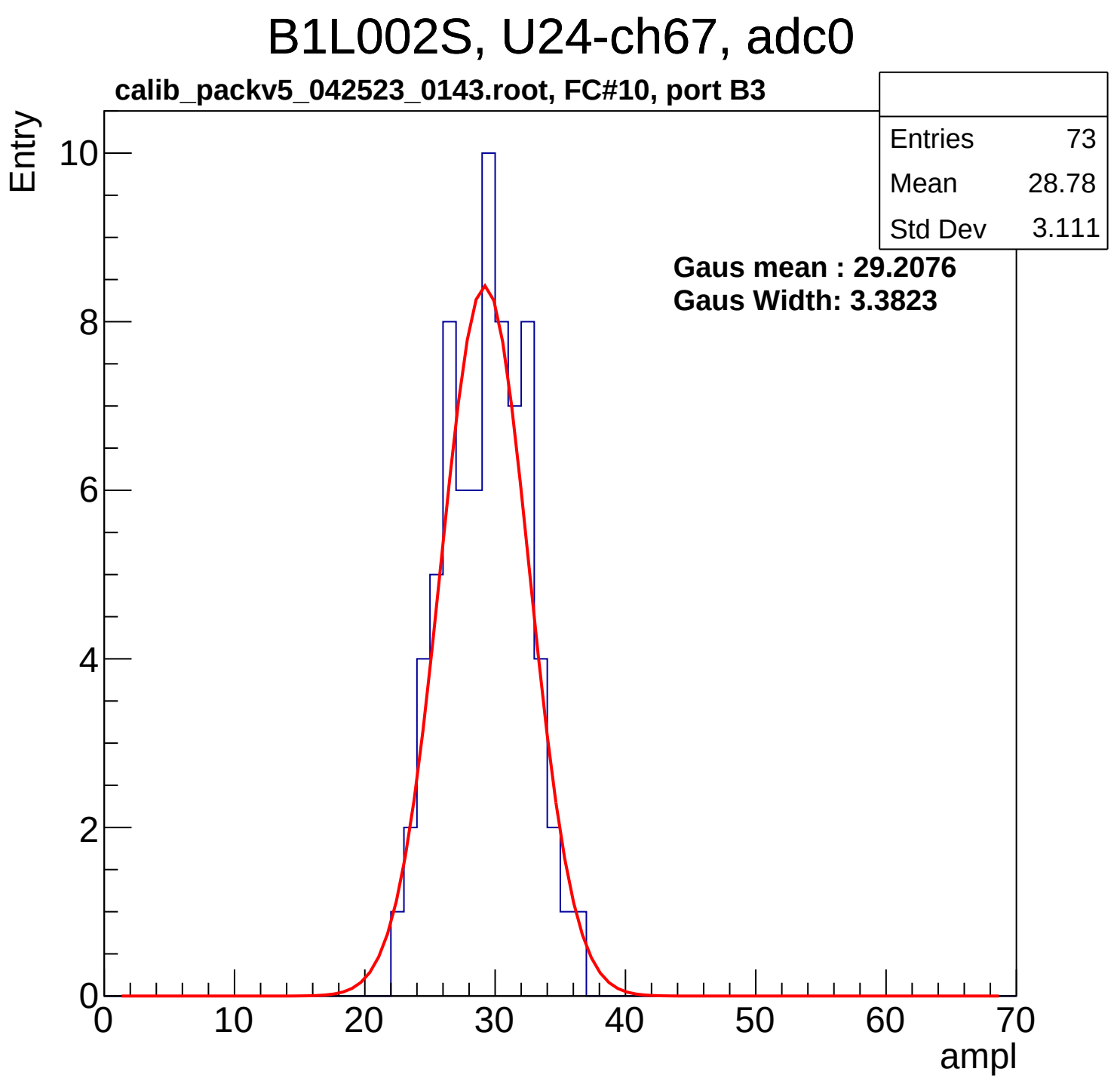
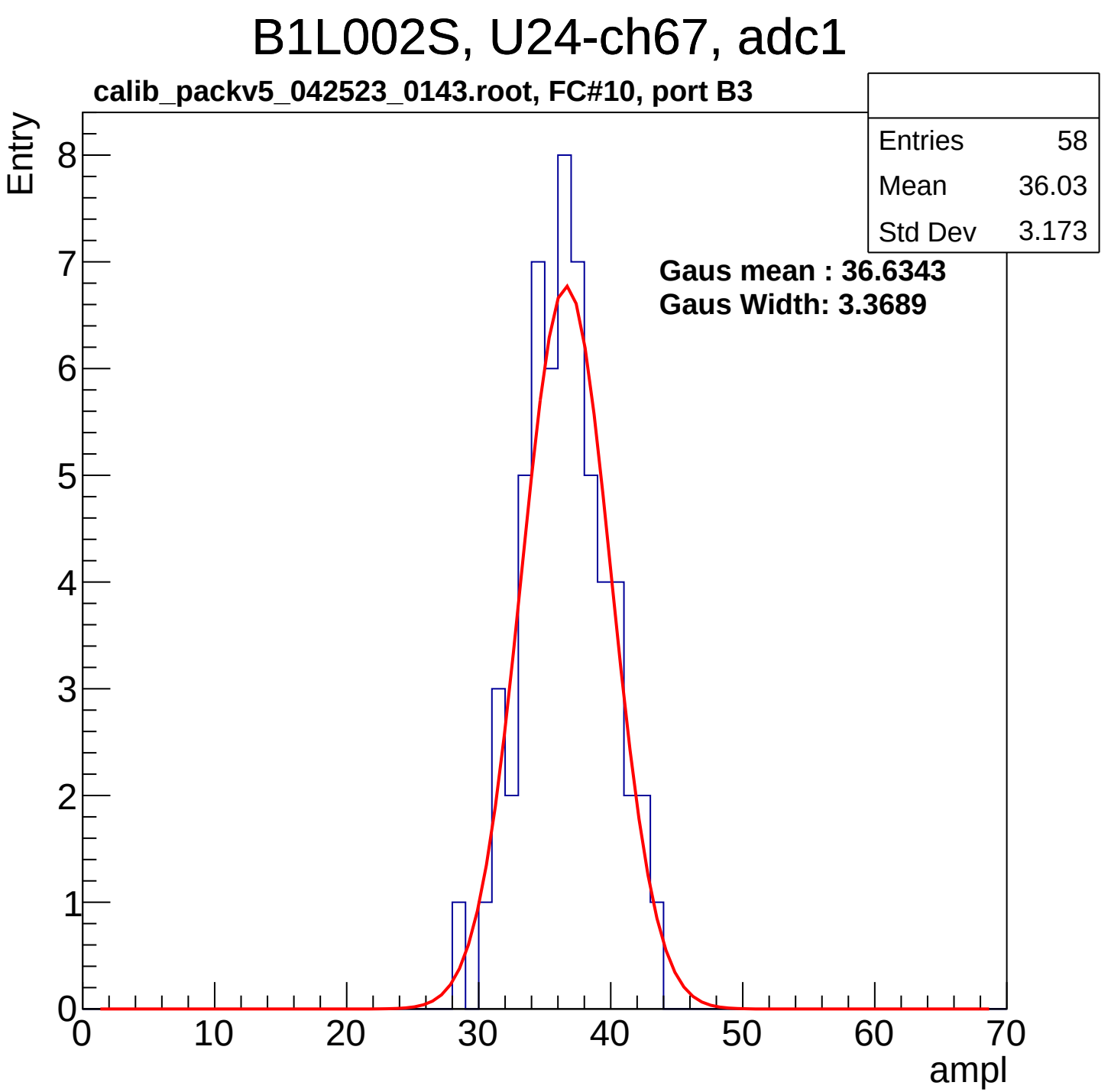
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch67, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.78
Std Dev 3.111
Gaus mean : 29.2076
Gaus Width: 3.3823
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch67, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 36.03
Std Dev 3.173
Gaus mean : 36.6343
Gaus Width: 3.3689
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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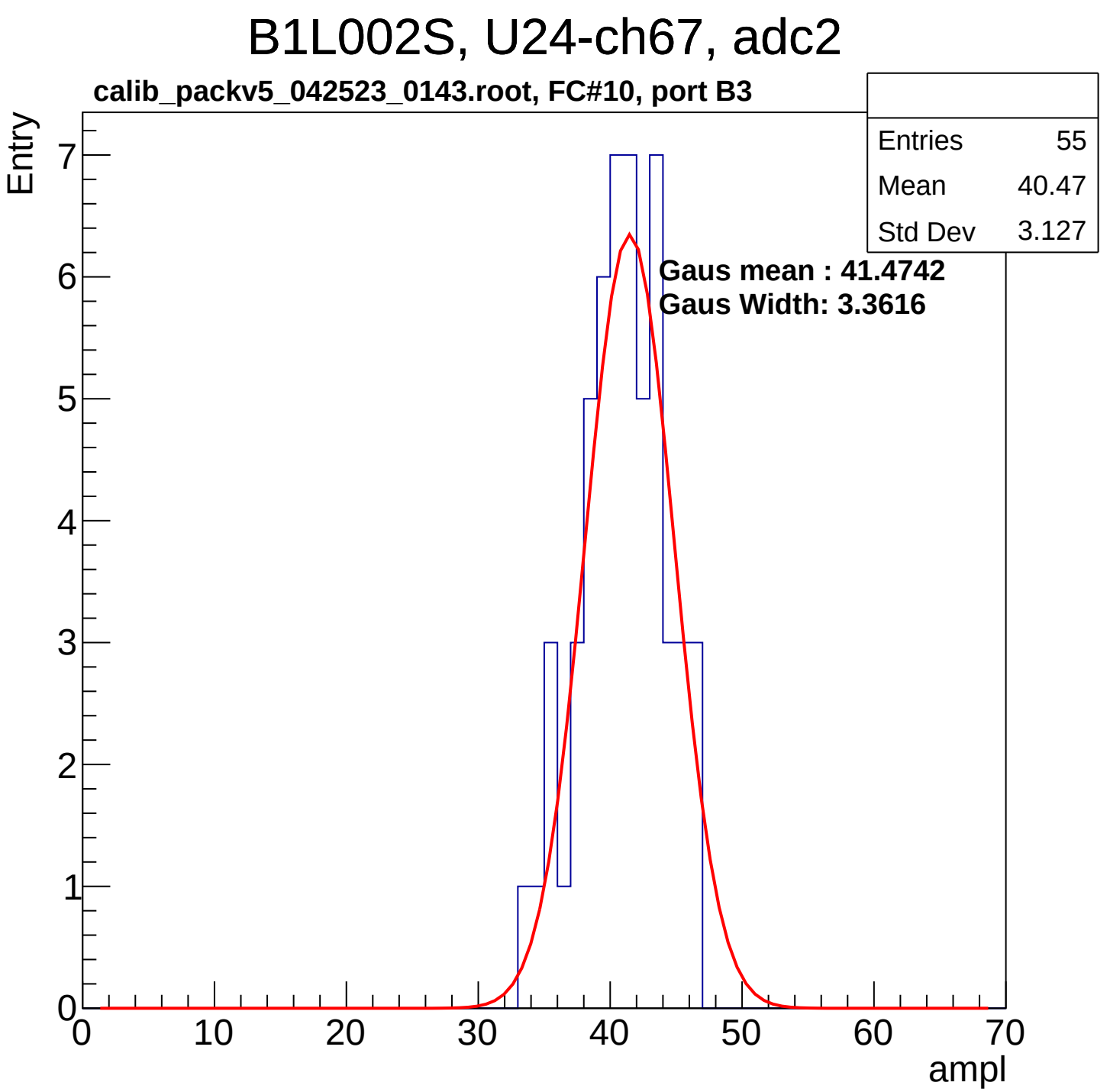
B1L002S, U24-ch67, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.47
Std Dev 3.127
Gaus mean : 41.4742
Gaus Width: 3.3616
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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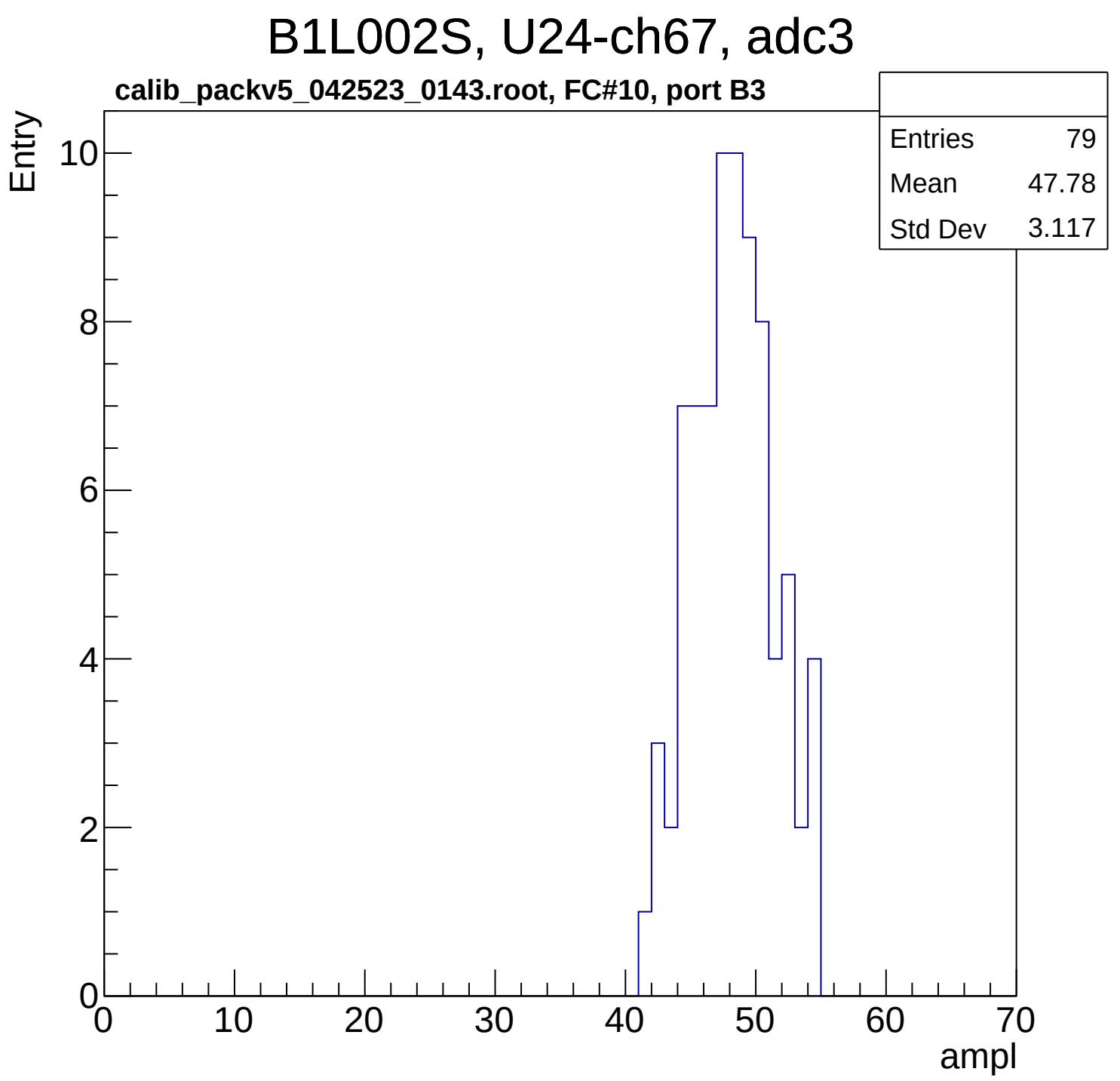
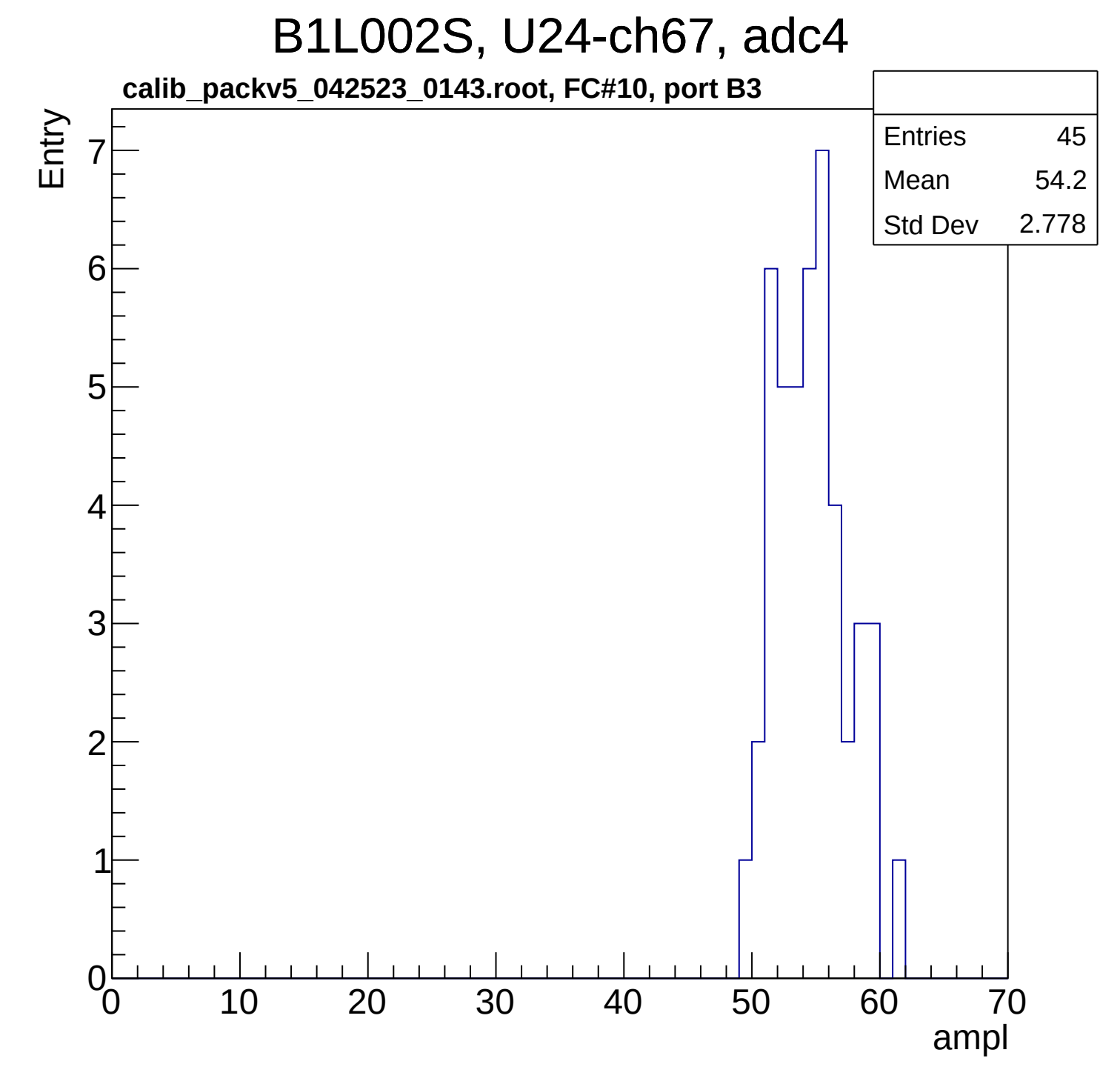
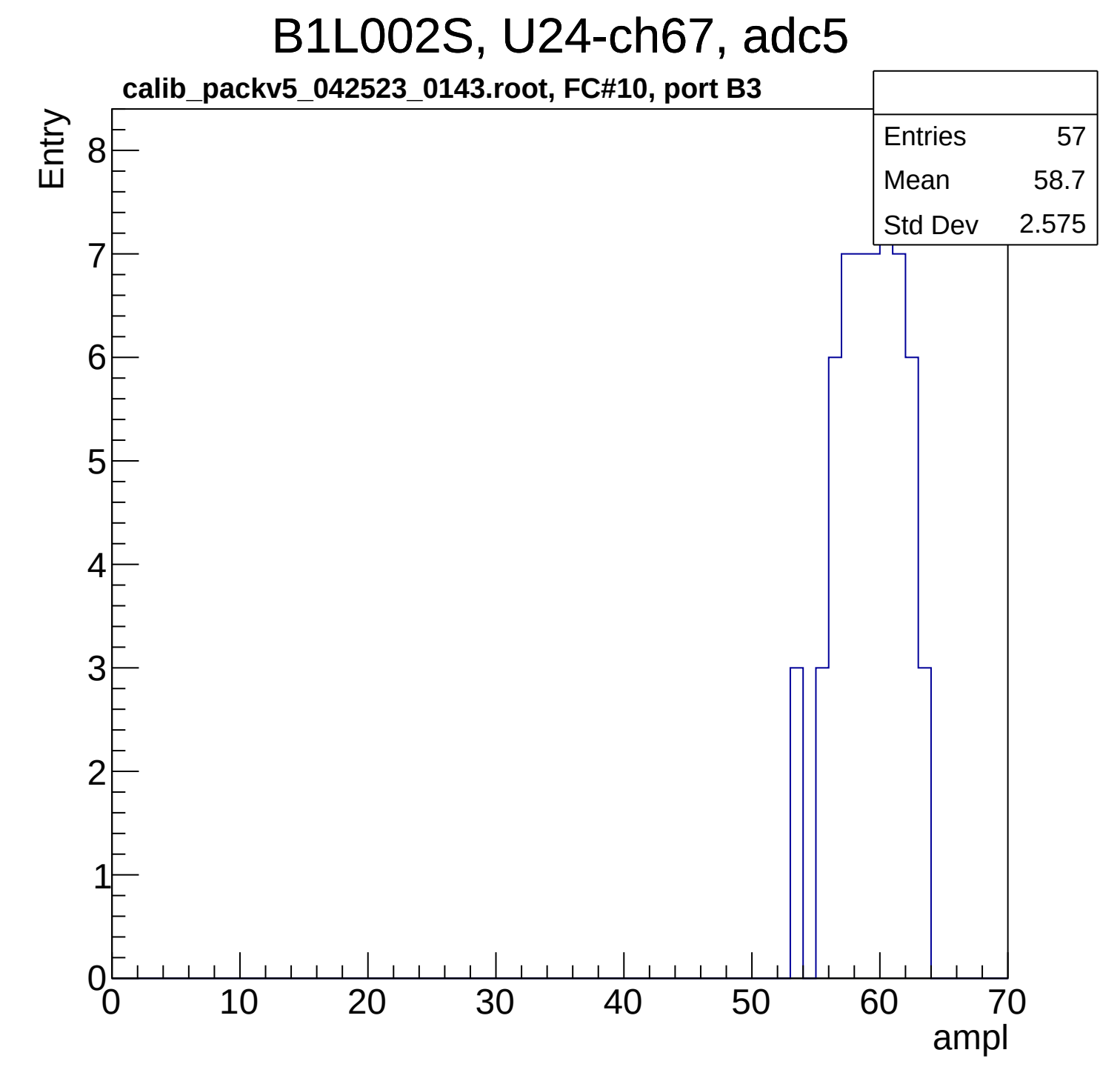

B1L002S, U24-ch67, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 47.78
Std Dev 3.117
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch67, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 54.2
Std Dev 2.778
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch67, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.7
Std Dev 2.575
Entry
ampl
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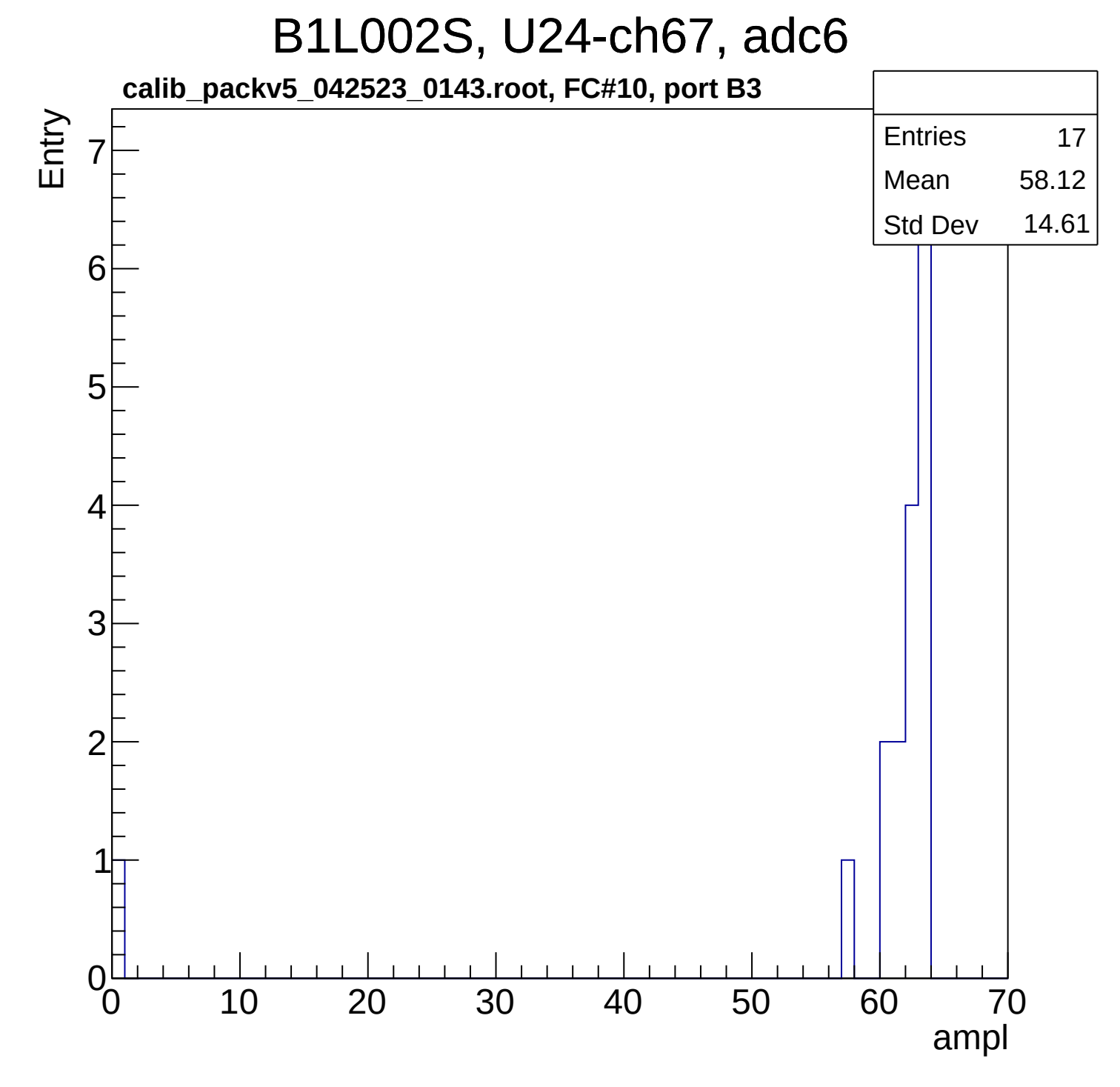
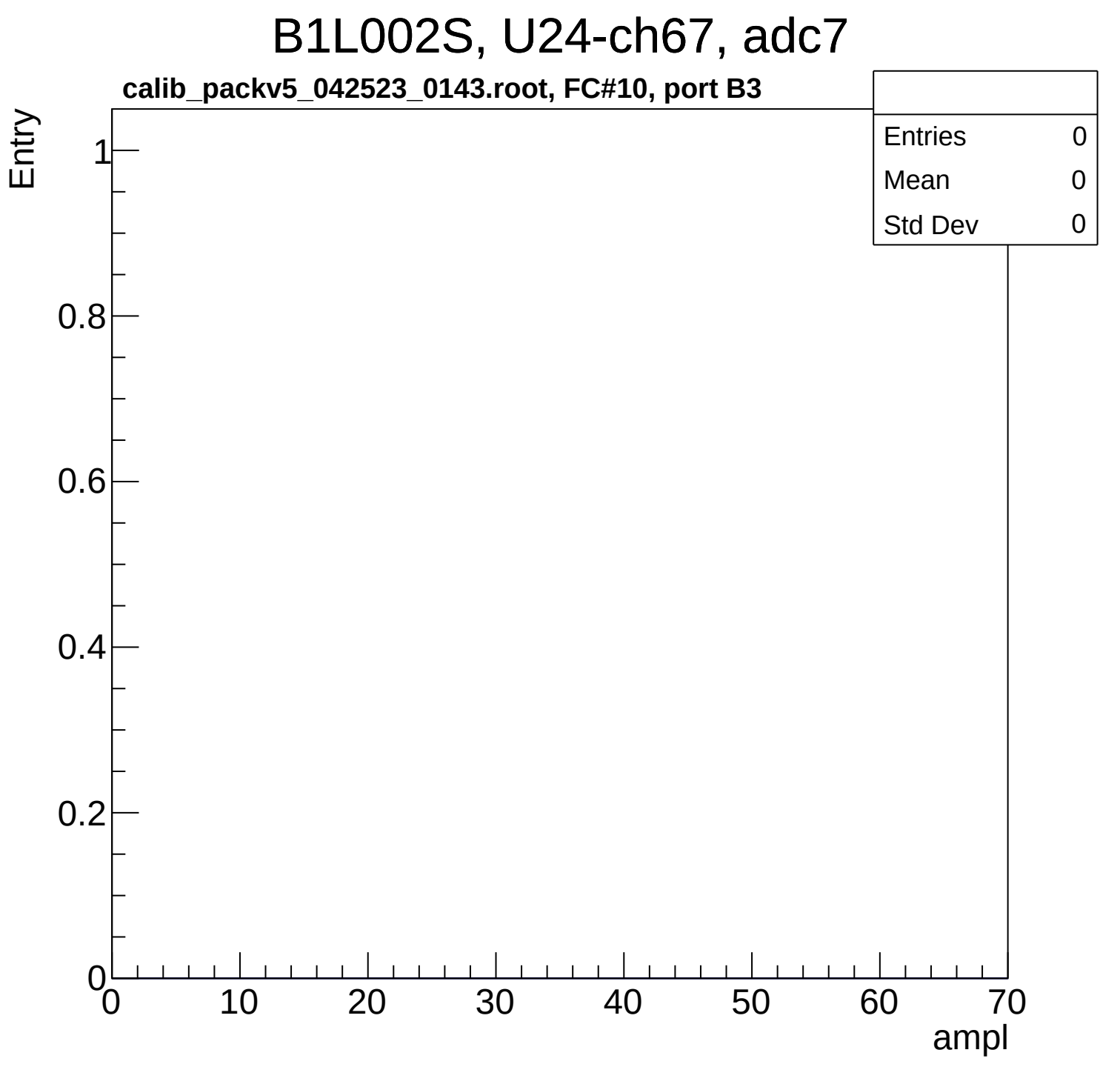
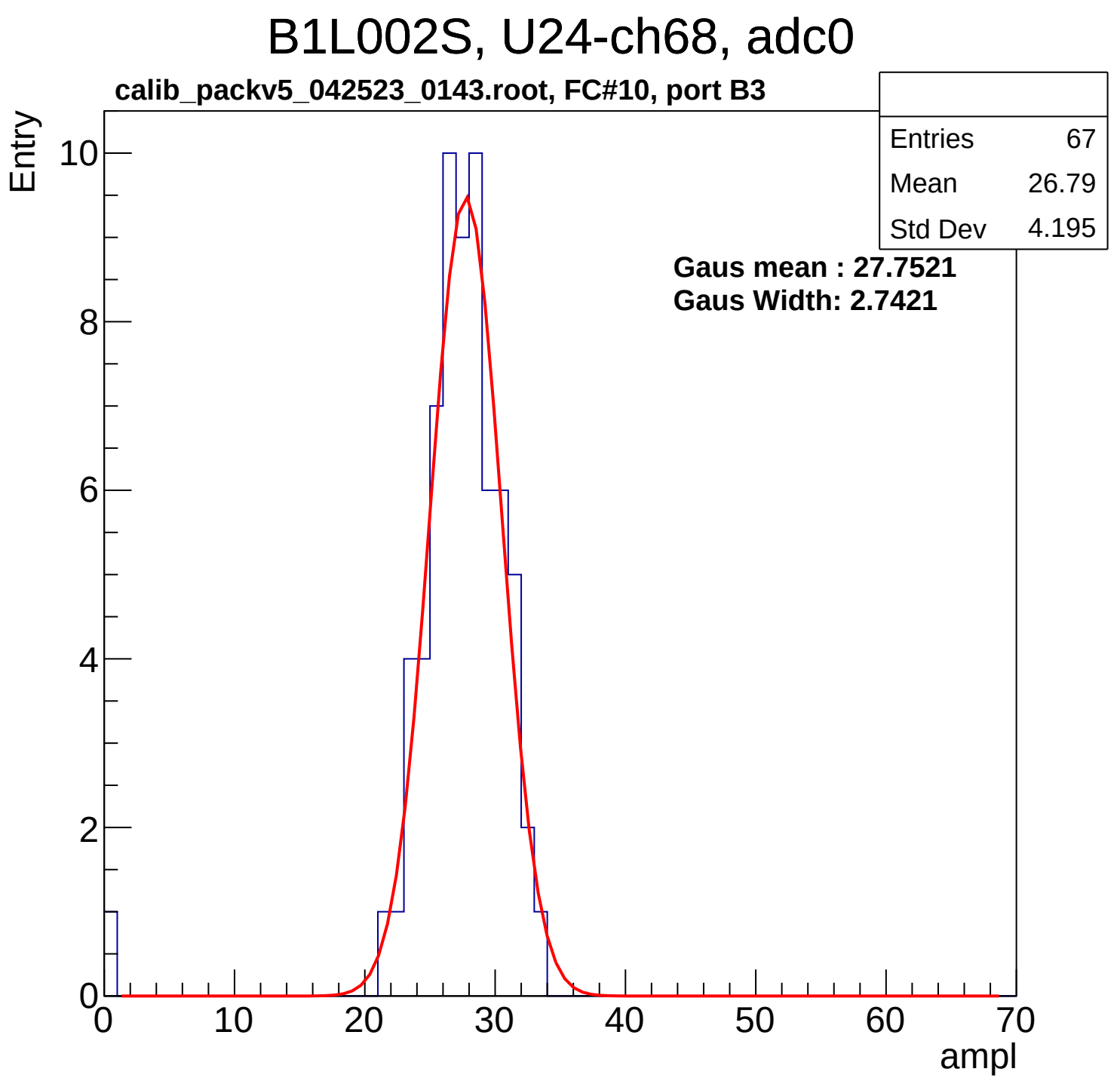
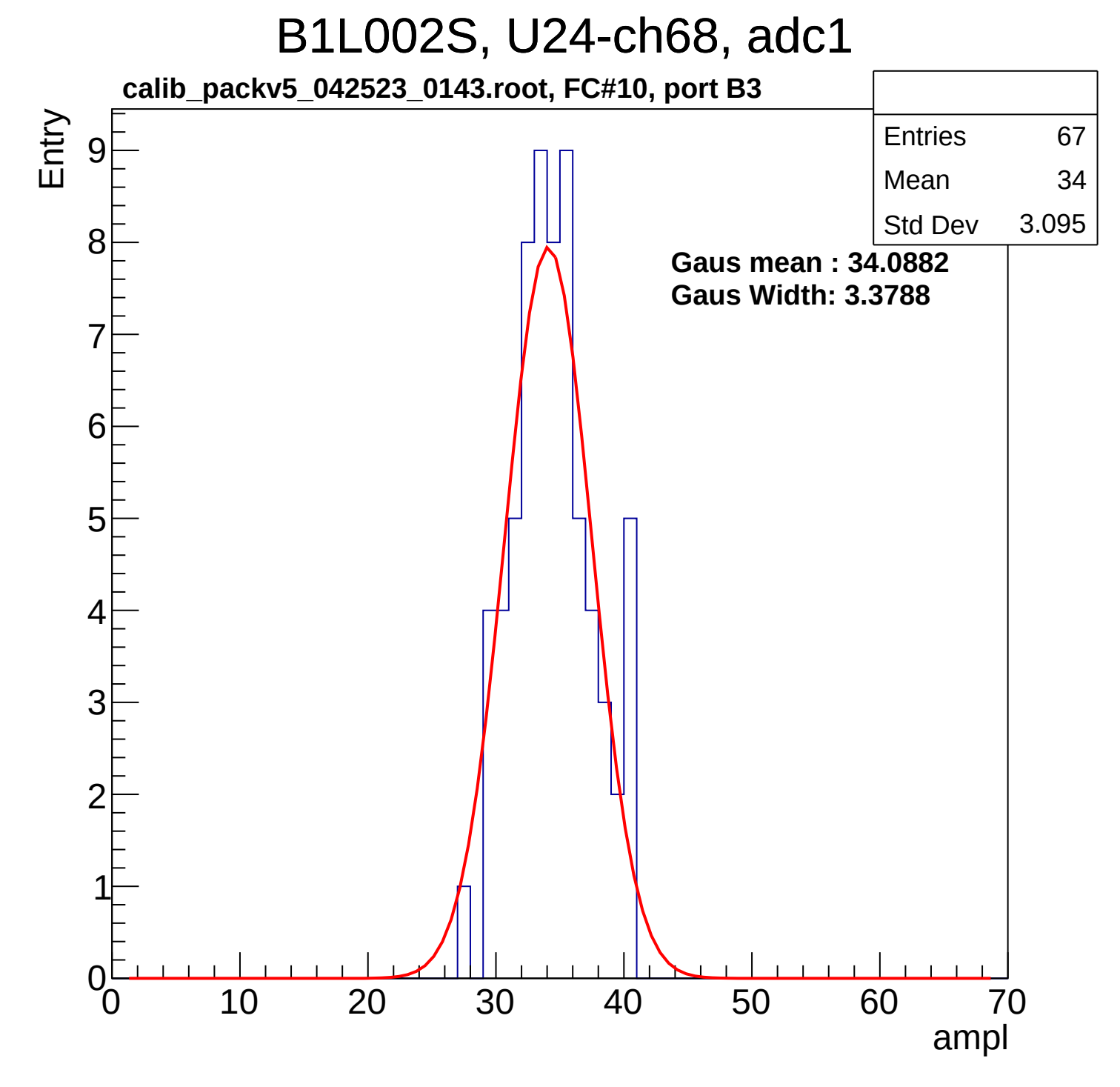
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch67, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.12
Std Dev 14.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch67, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch68, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 26.79
Std Dev 4.195
Gaus mean : 27.7521
Gaus Width: 2.7421
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch68, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 34
Std Dev 3.095
Gaus mean : 34.0882
Gaus Width: 3.3788
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
30
40
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60
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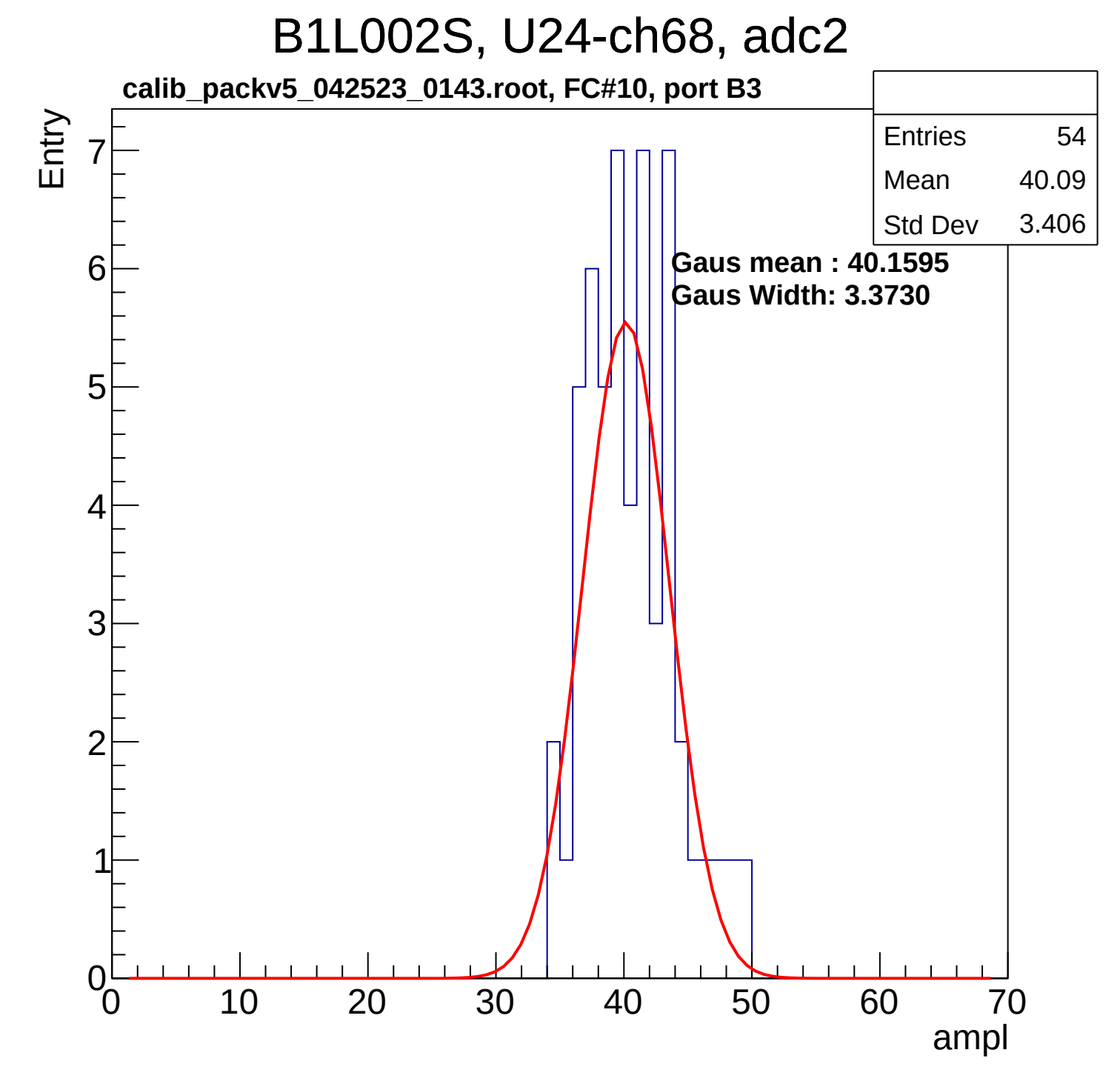
B1L002S, U24-ch68, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.09
Std Dev 3.406
Gaus mean : 40.1595
Gaus Width: 3.3730
Entry
ampl
0
1
2
3
4
5
6
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0
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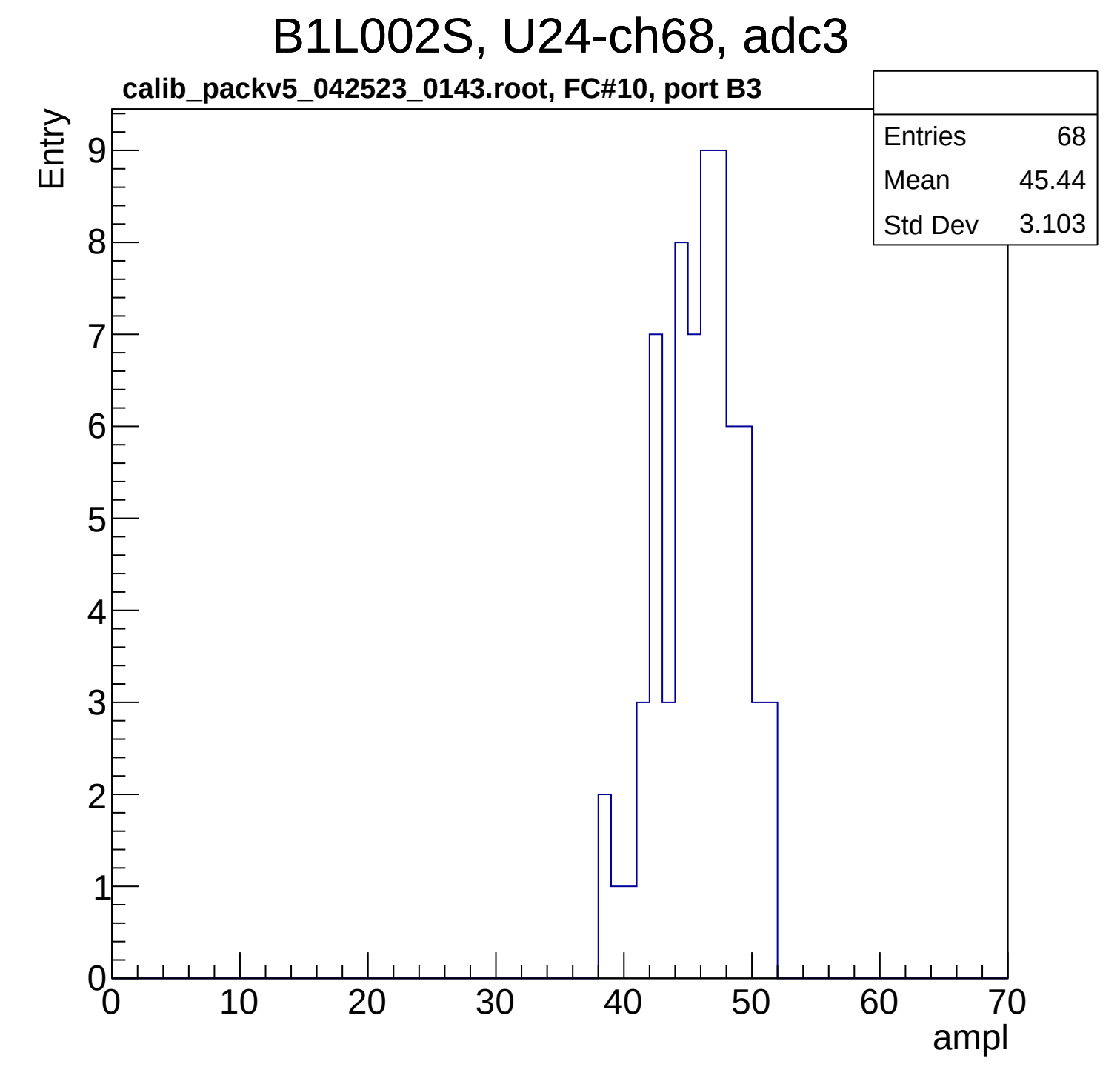
B1L002S, U24-ch68, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.44
Std Dev 3.103
Entry
ampl
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6
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0
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60
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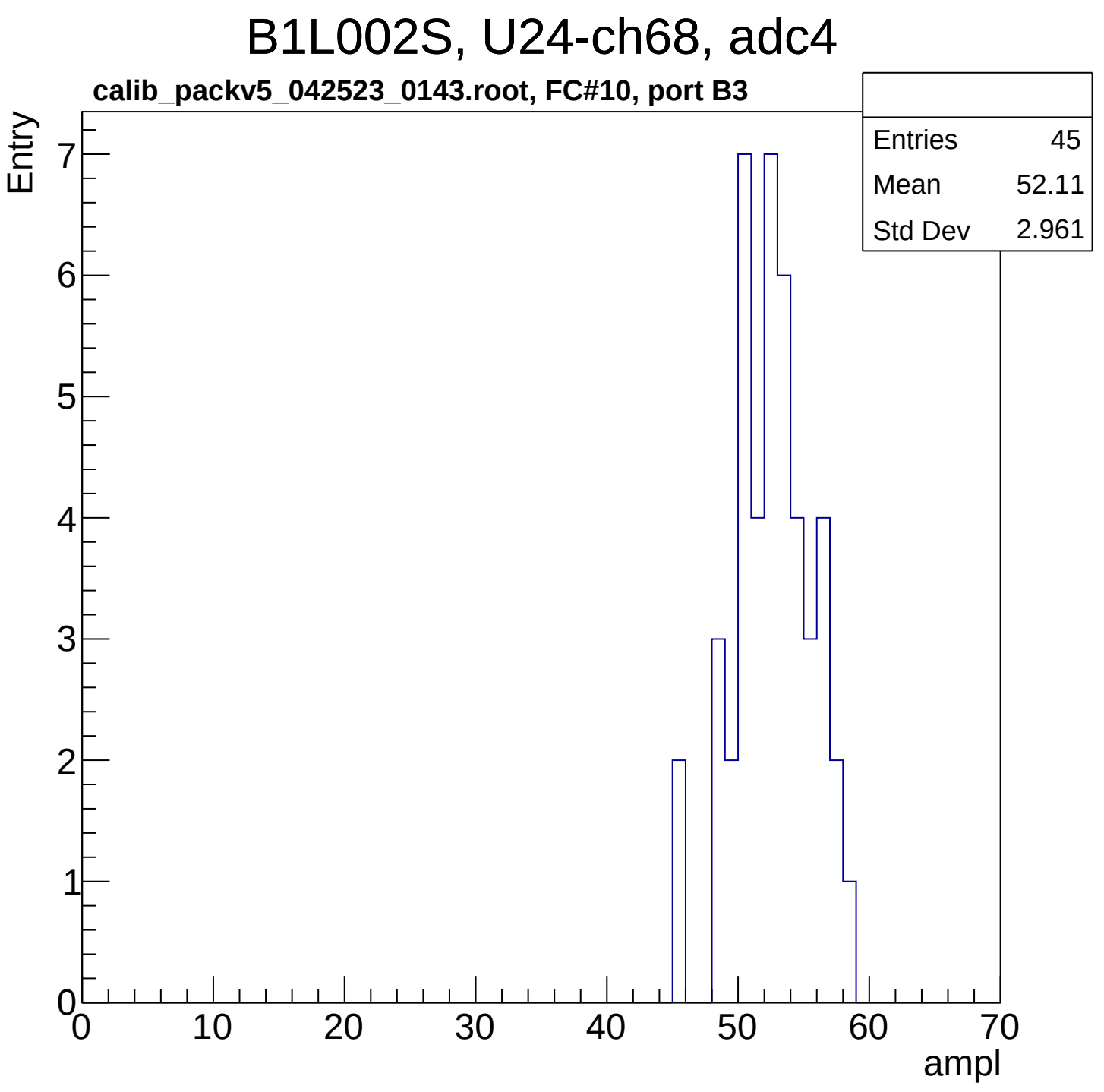
B1L002S, U24-ch68, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52.11
Std Dev 2.961
Entry
ampl
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0
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60
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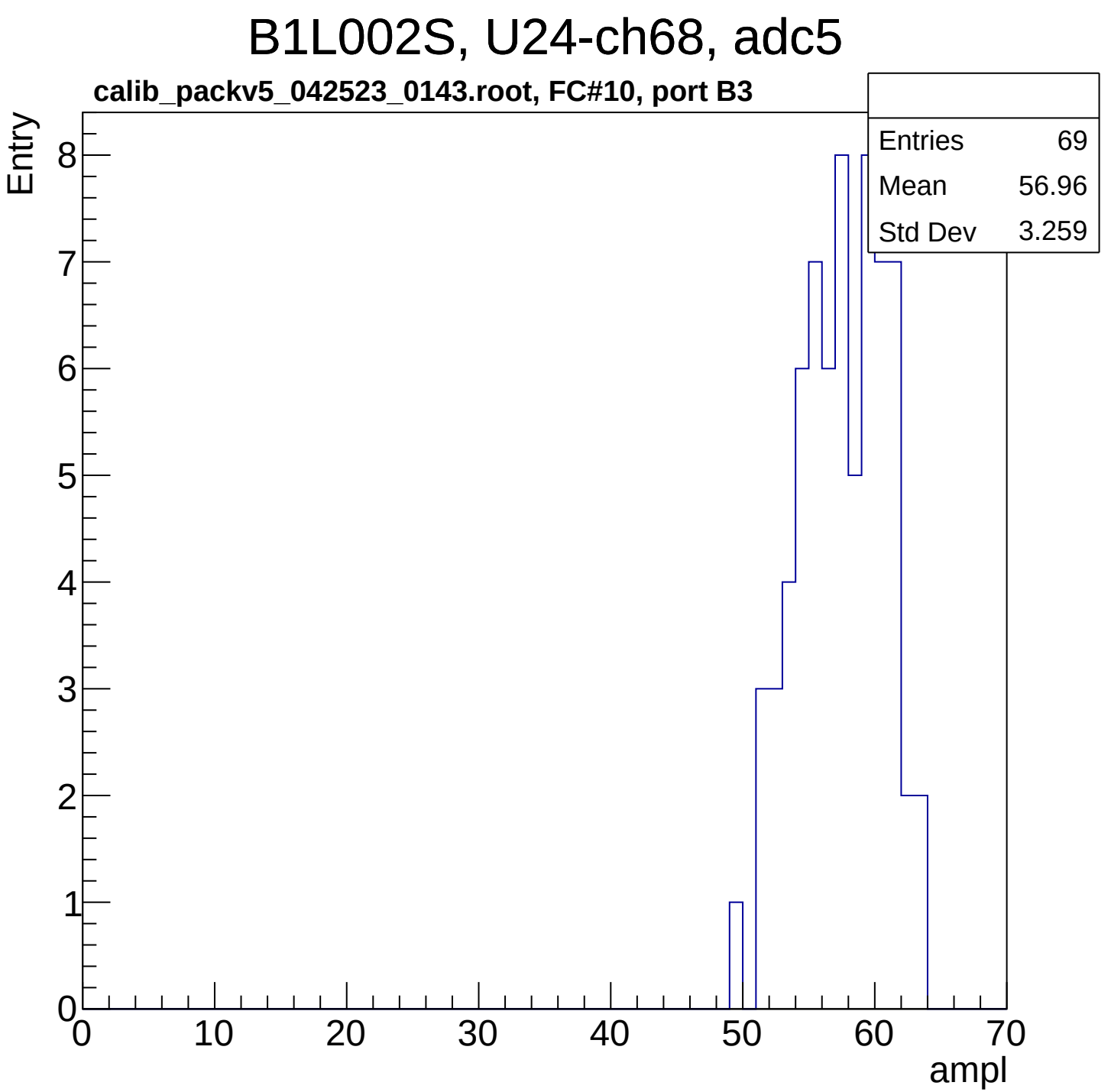
B1L002S, U24-ch68, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 56.96
Std Dev 3.259
Entry
ampl
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4
5
6
7
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0
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60
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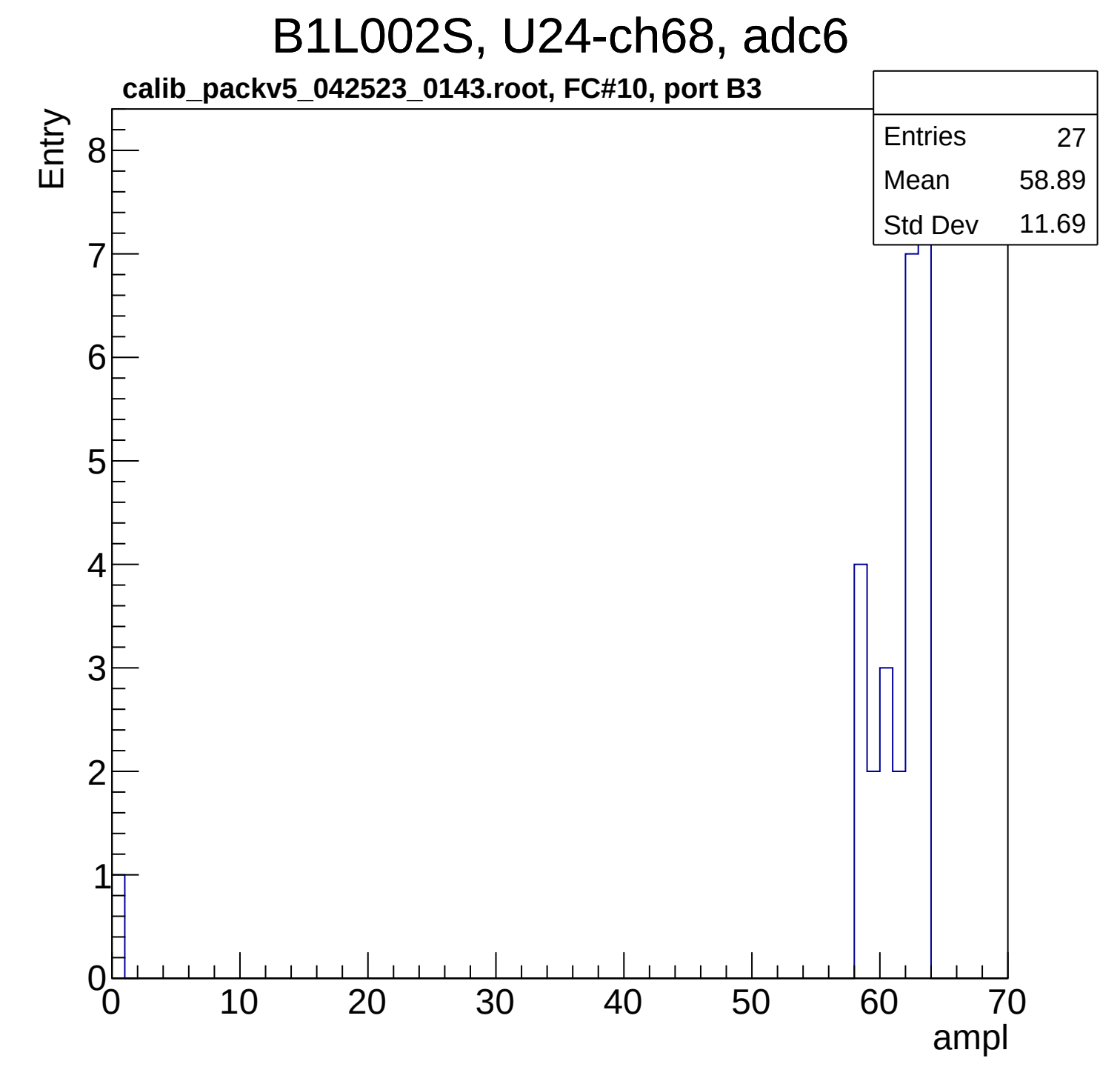
B1L002S, U24-ch68, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 58.89
Std Dev 11.69
Entry
ampl
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0
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60
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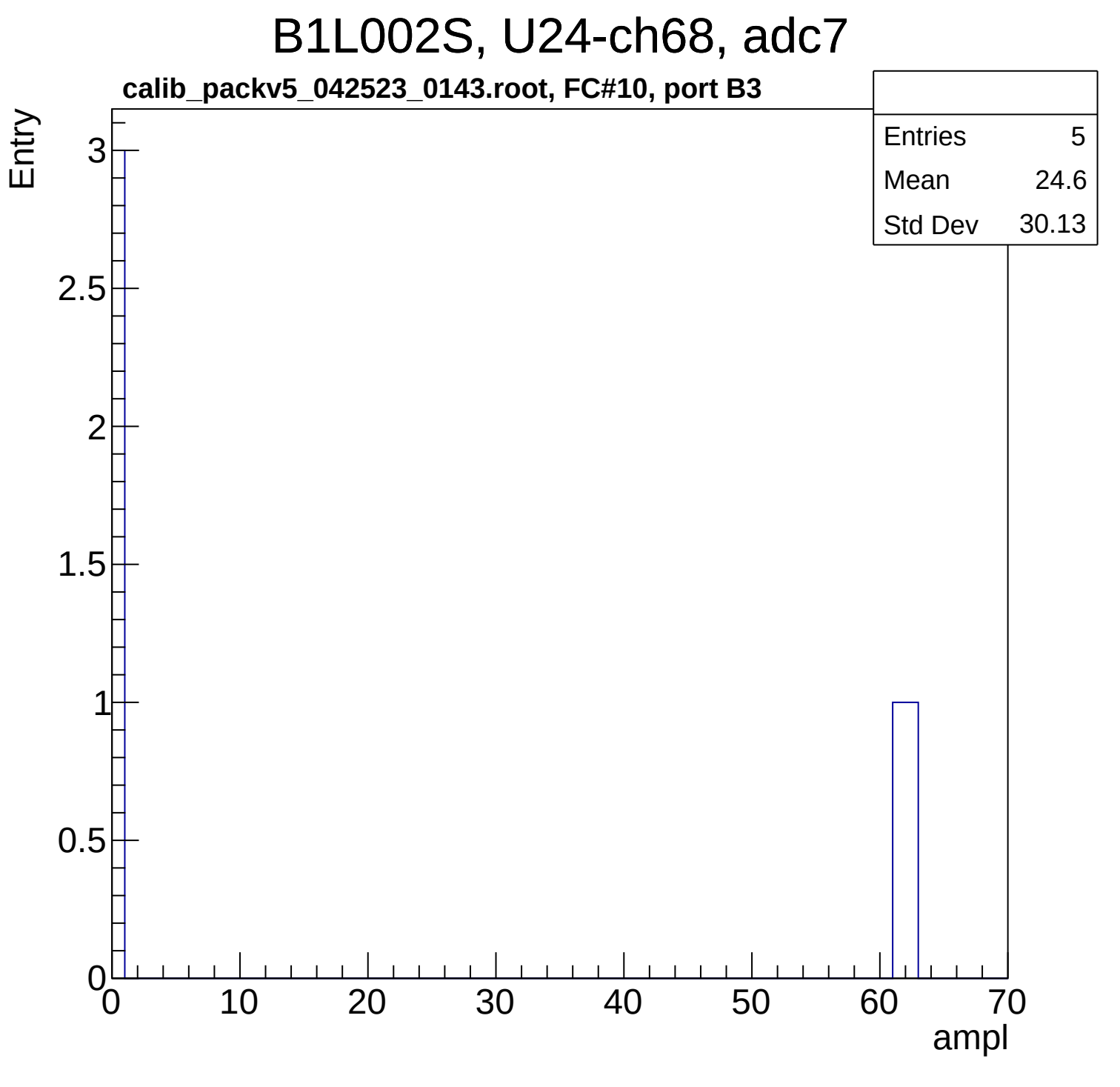
B1L002S, U24-ch68, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 24.6
Std Dev 30.13
Entry
ampl
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2
2.5
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0
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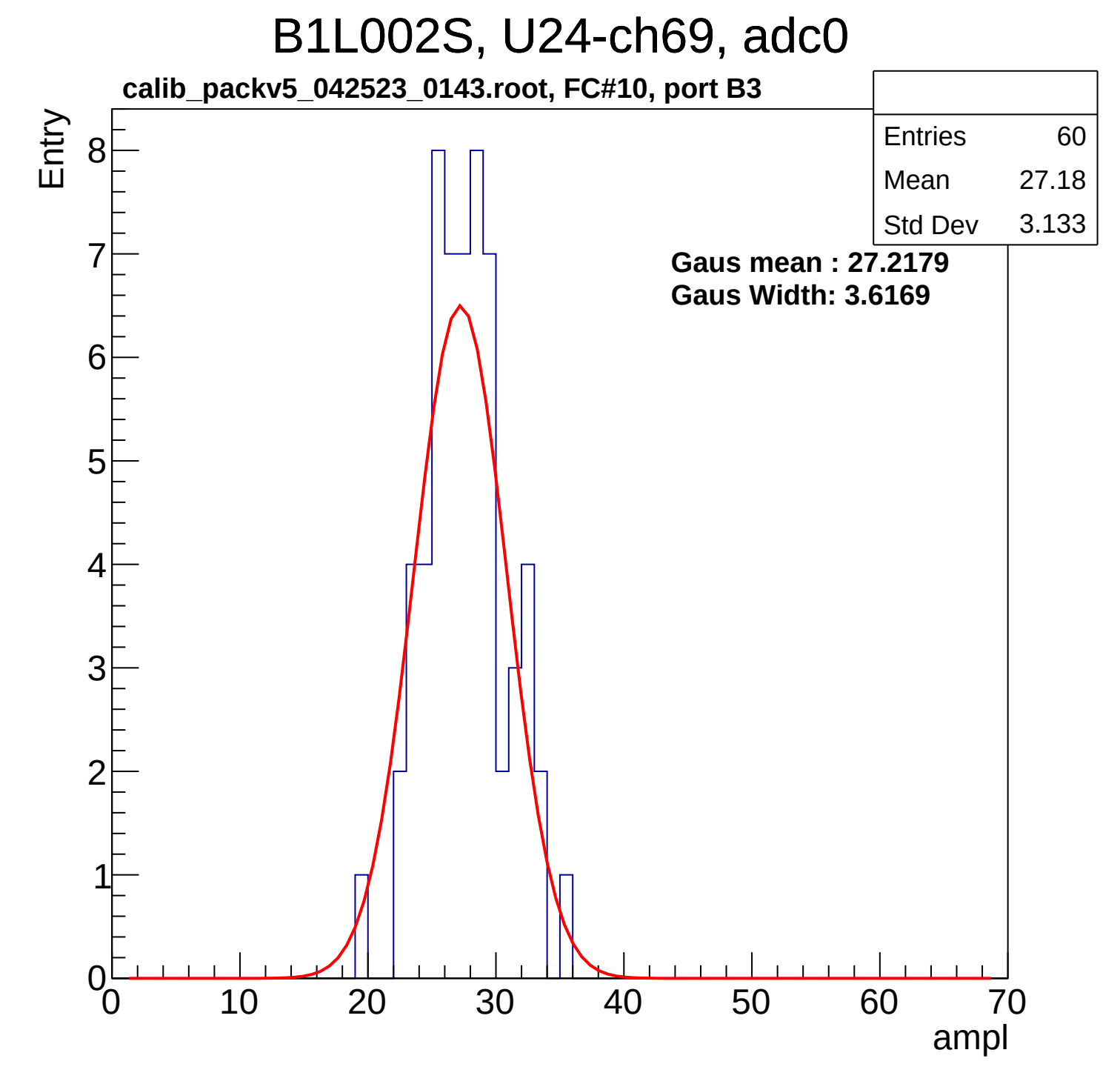
B1L002S, U24-ch69, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 27.18
Std Dev 3.133
Gaus mean : 27.2179
Gaus Width: 3.6169
Entry
ampl
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2
3
4
5
6
7
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0
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60
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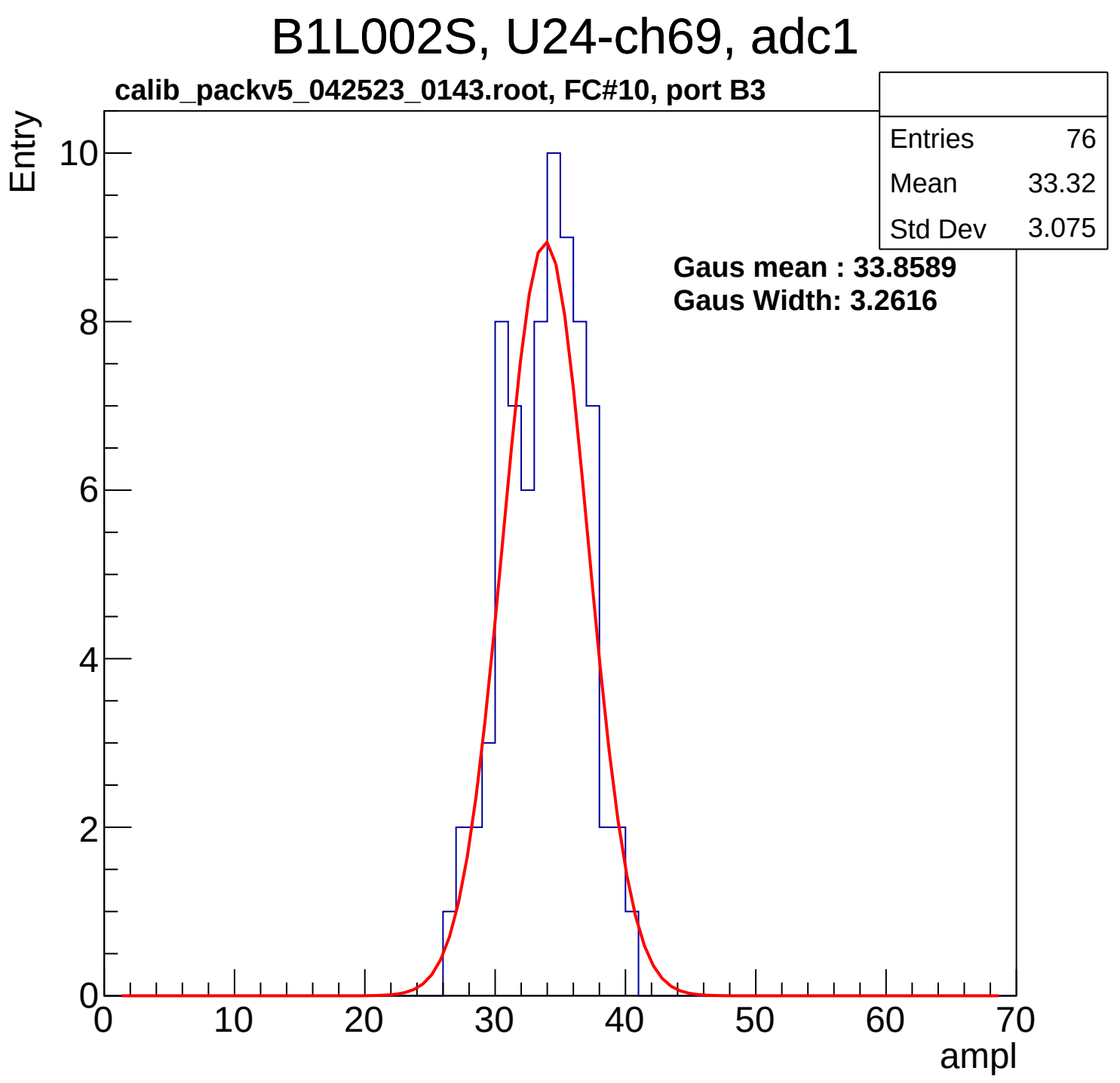
B1L002S, U24-ch69, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.32
Std Dev 3.075
Gaus mean : 33.8589
Gaus Width: 3.2616
Entry
ampl
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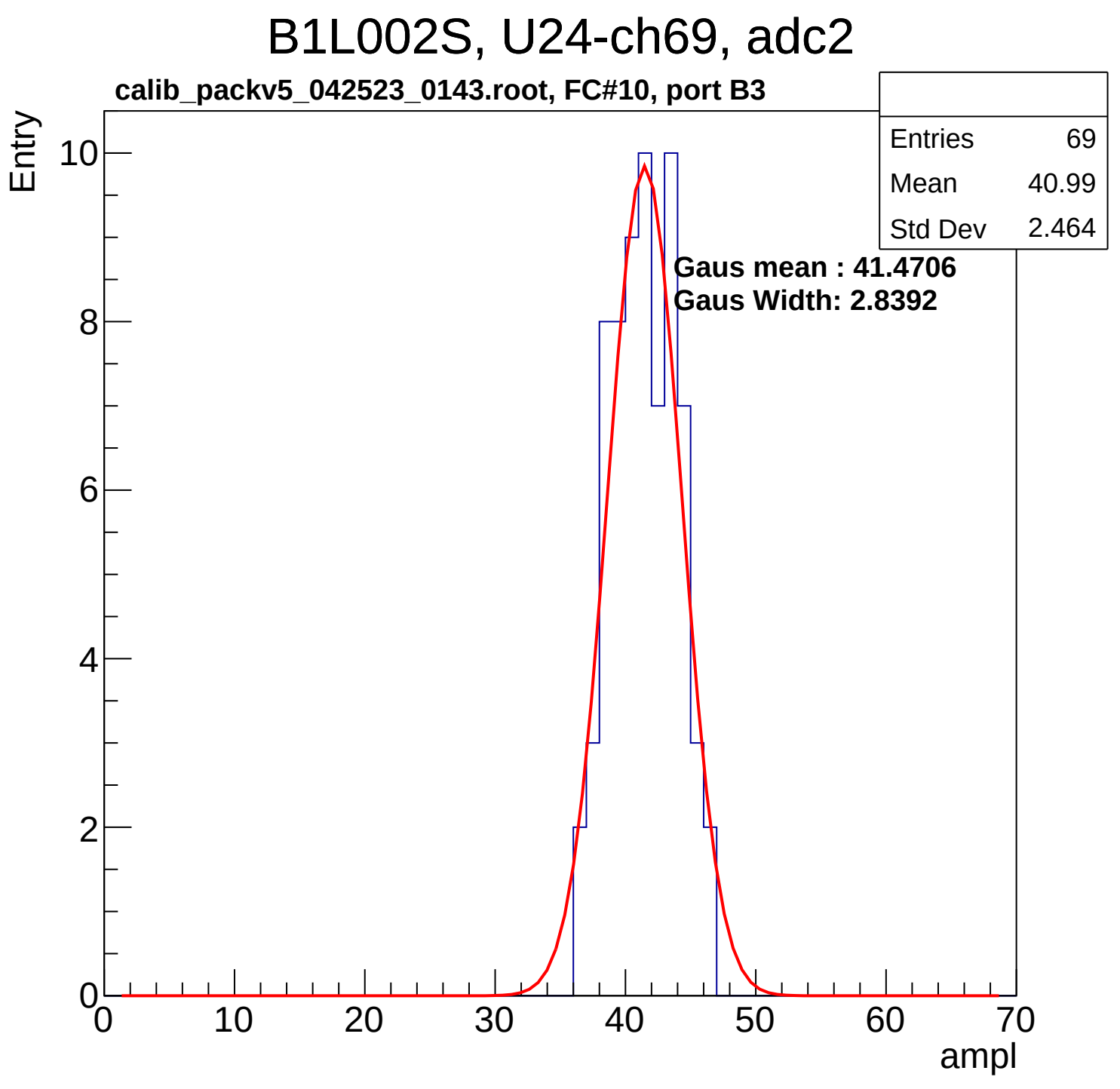
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch69, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.99
Std Dev 2.464
Gaus mean : 41.4706
Gaus Width: 2.8392
Entry
ampl
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6
8
10
0
10
20
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40
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60
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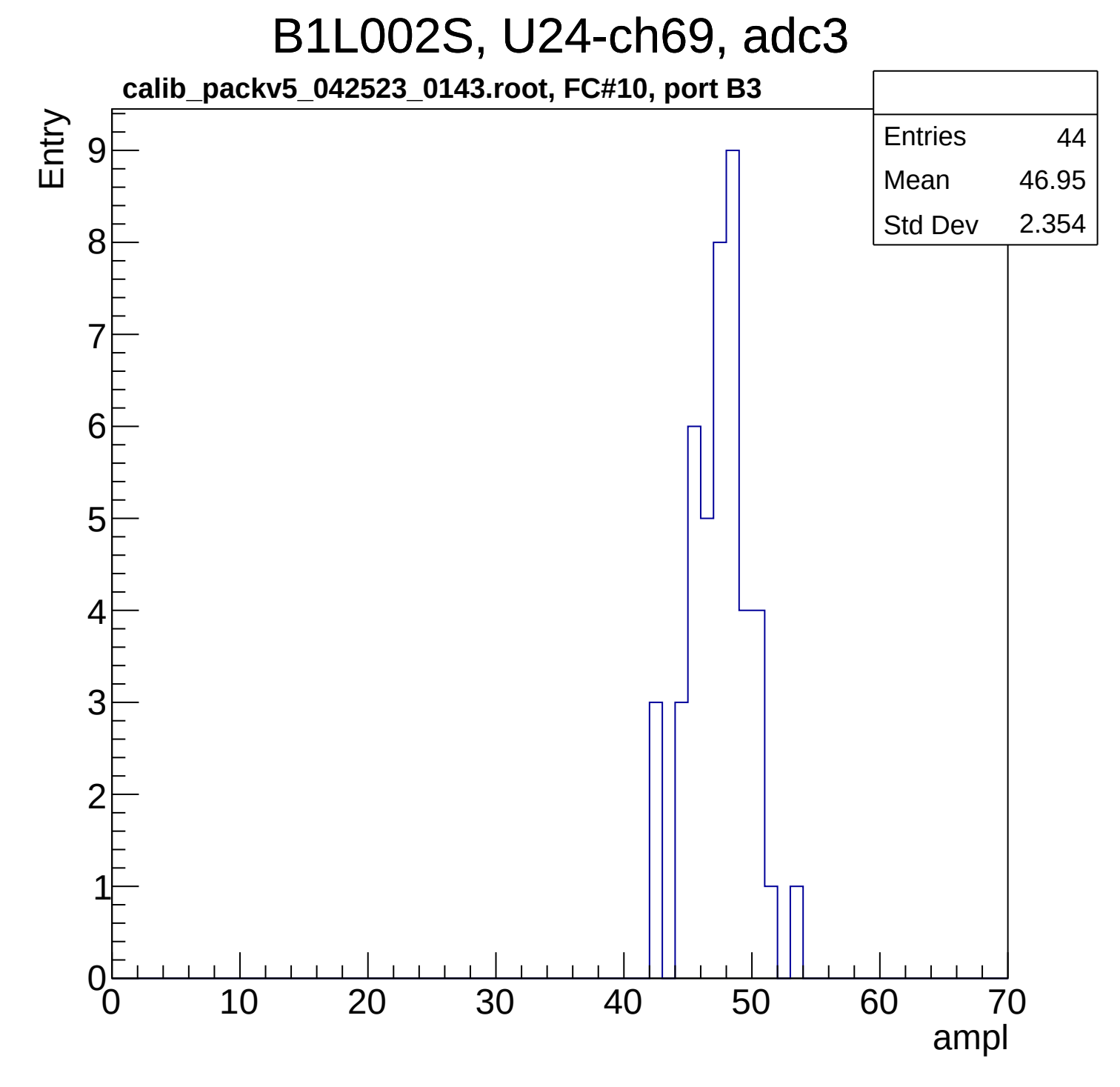
B1L002S, U24-ch69, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 46.95
Std Dev 2.354
Entry
ampl
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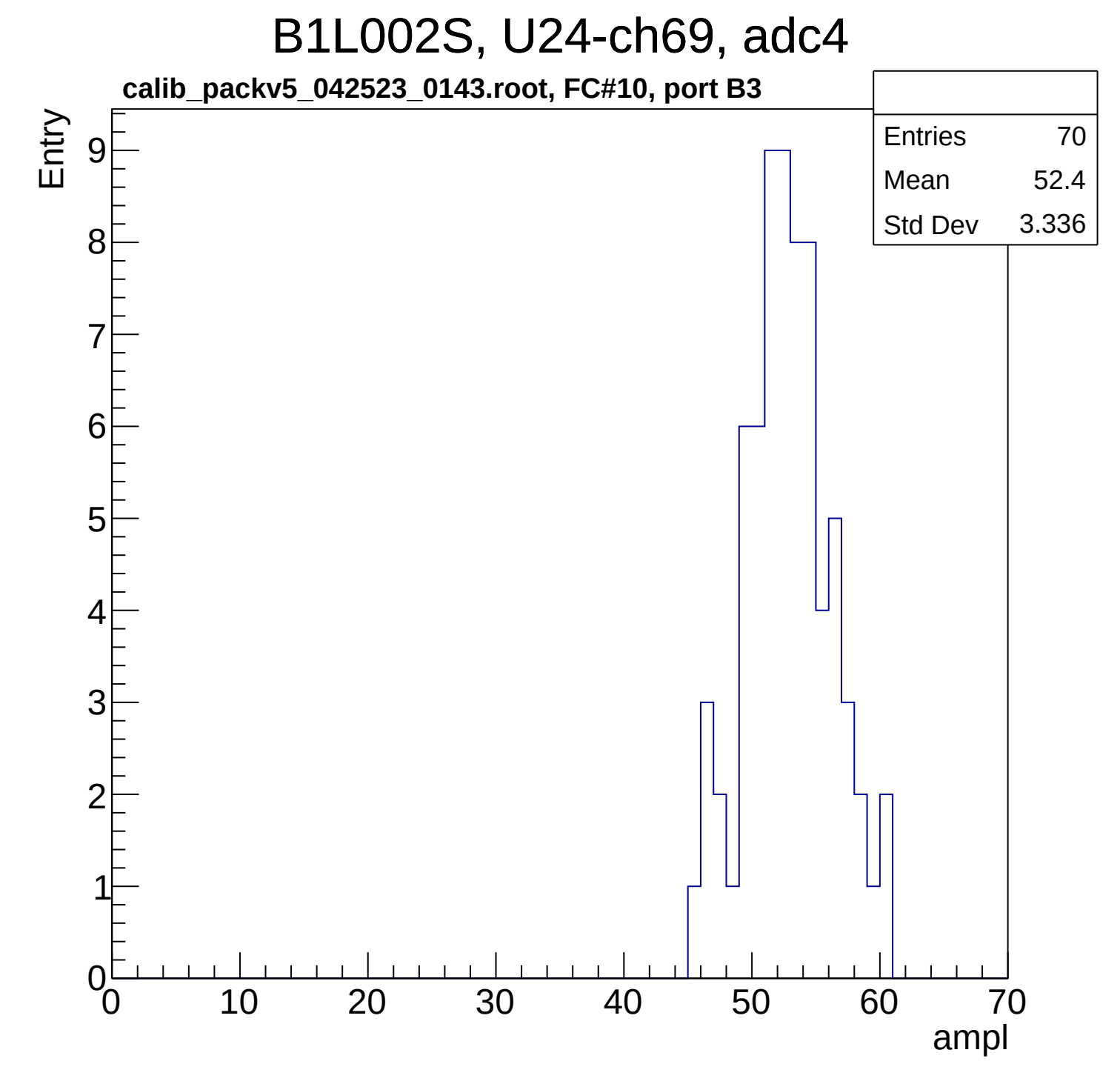
B1L002S, U24-ch69, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 52.4
Std Dev 3.336
Entry
ampl
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3
4
5
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9
0
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60
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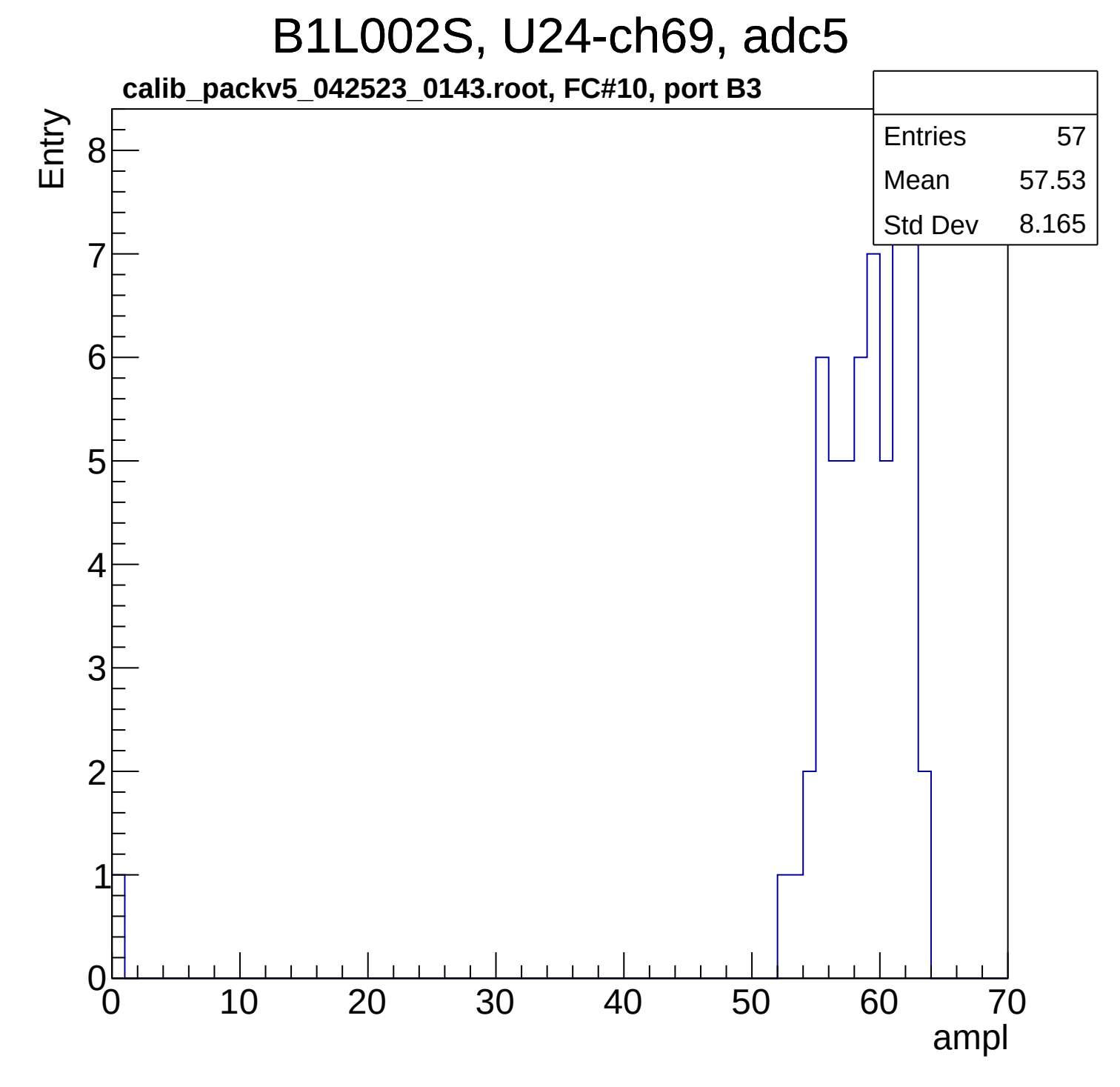
B1L002S, U24-ch69, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 57.53
Std Dev 8.165
Entry
ampl
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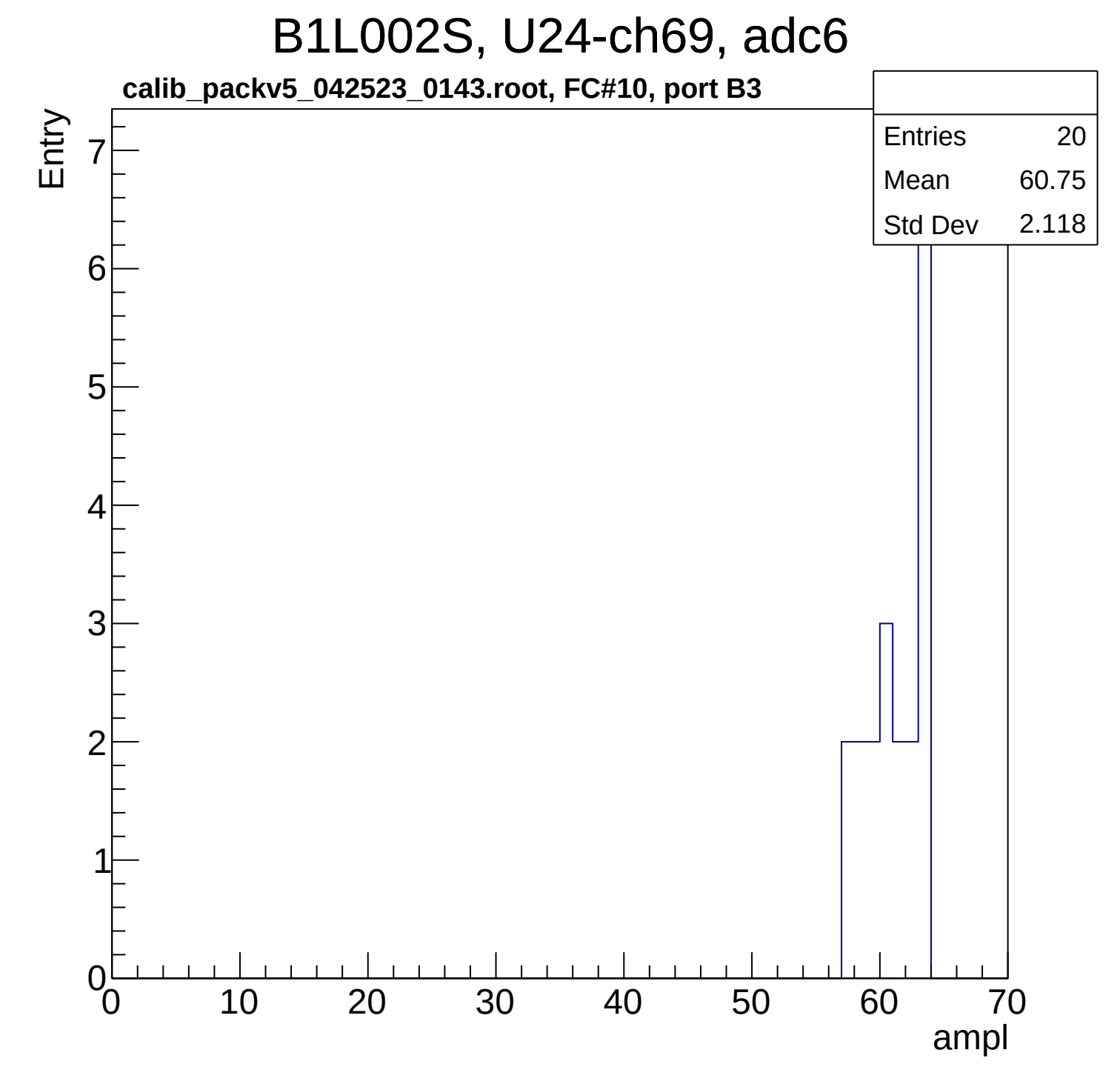
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch69, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 60.75
Std Dev 2.118
Entry
0
1
2
3
4
5
6
7
0
10
20
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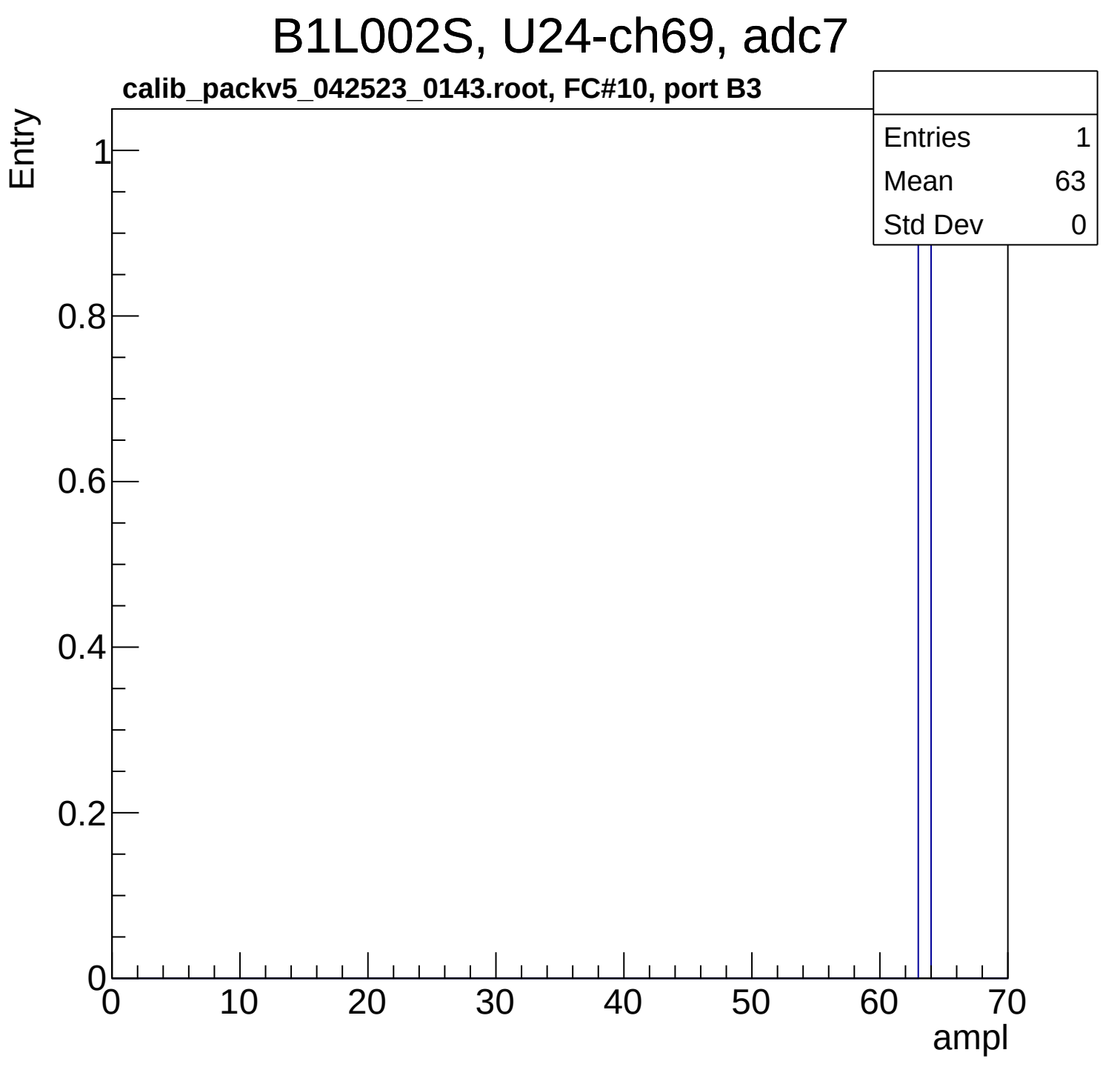
60
70
ampl


B1L002S, U24-ch69, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
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0.8
0.6
0.4
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0
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10
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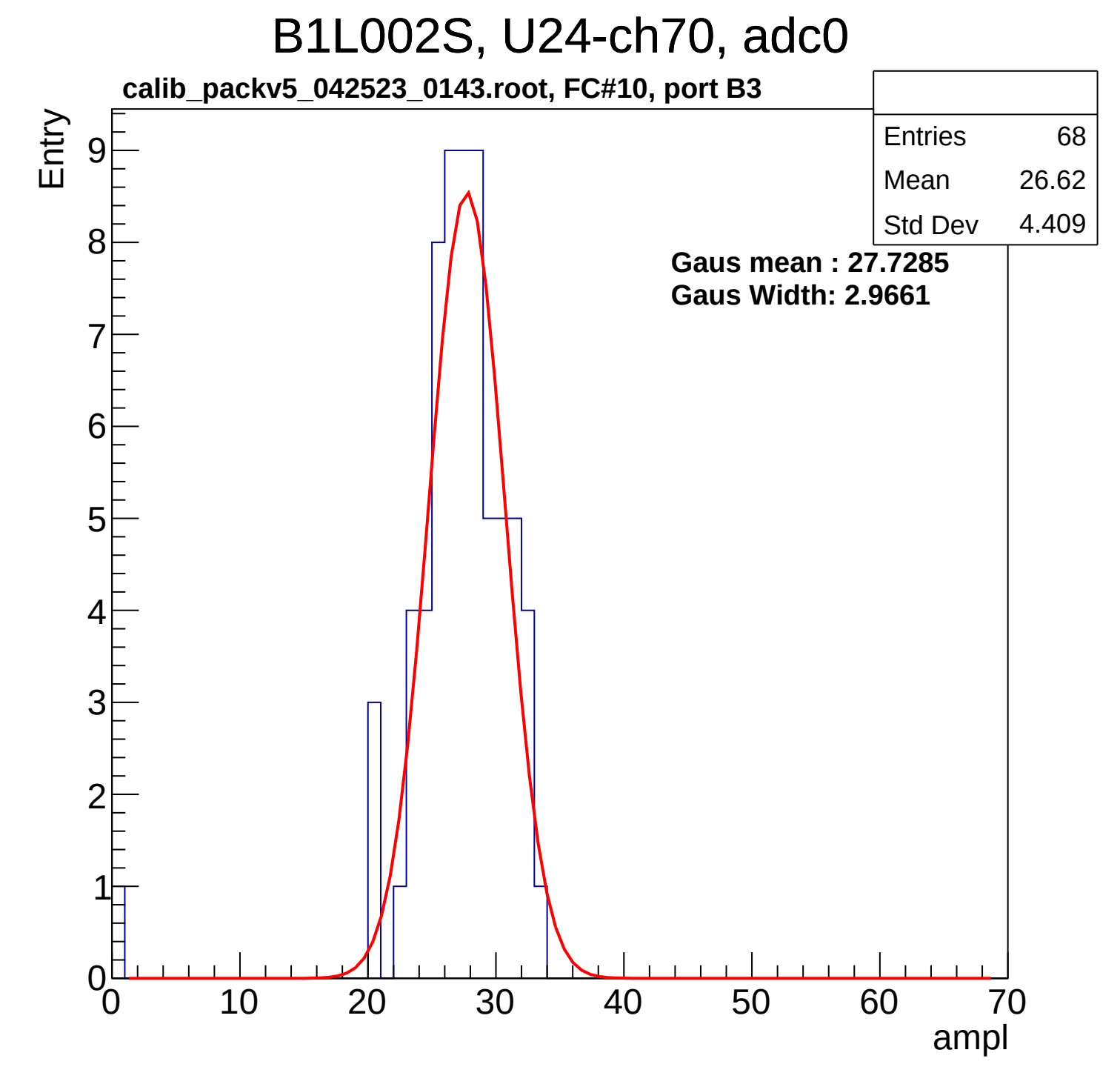
40
50
60
70
ampl


B1L002S, U24-ch70, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 26.62
Std Dev 4.409
Gaus mean : 27.7285
Gaus Width: 2.9661
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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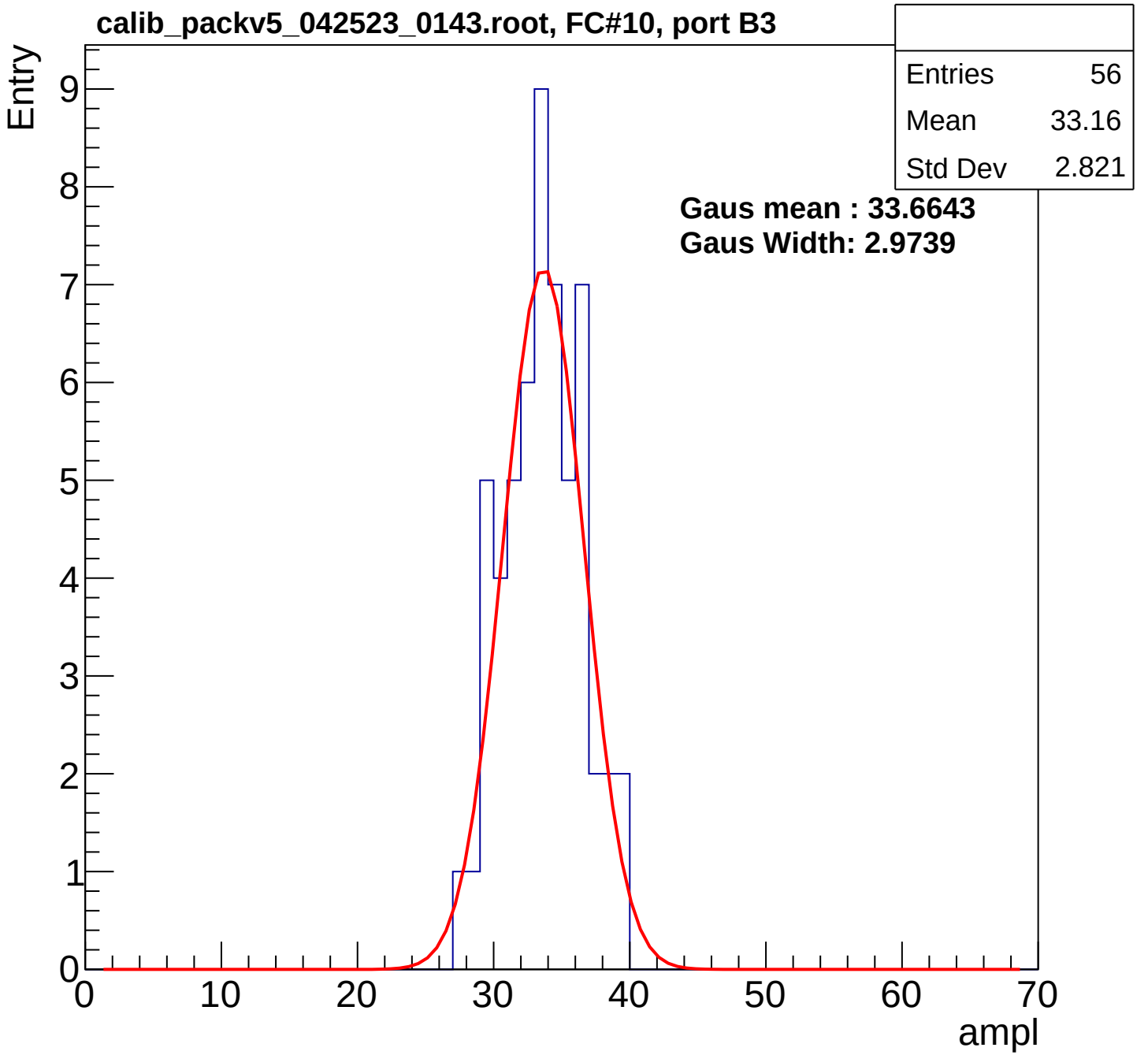
B1L002S, U24-ch70, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 33.16
Std Dev 2.821
Gaus mean : 33.6643
Gaus Width: 2.9739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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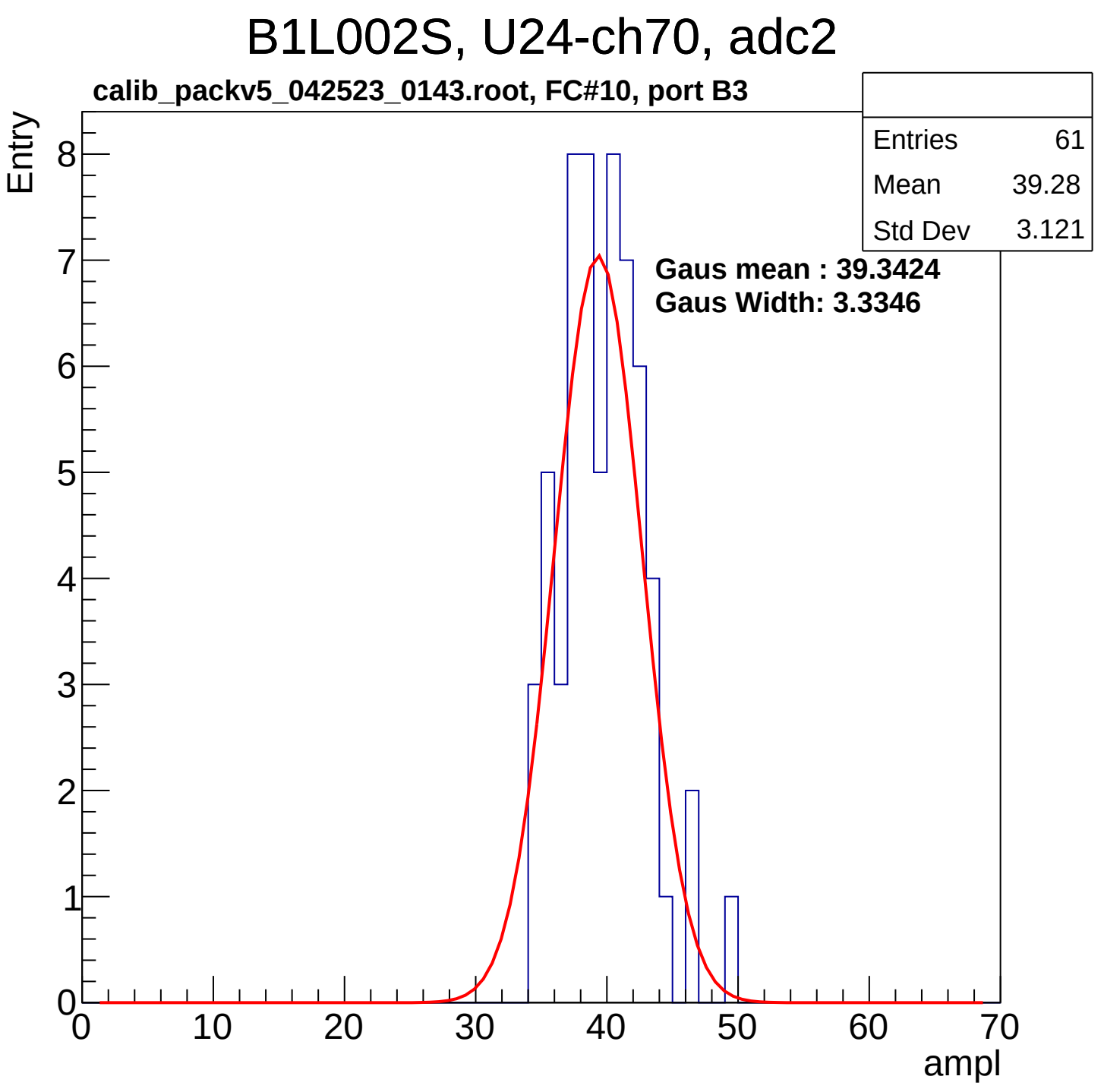
B1L002S, U24-ch70, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 39.28
Std Dev 3.121
Gaus mean : 39.3424
Gaus Width: 3.3346
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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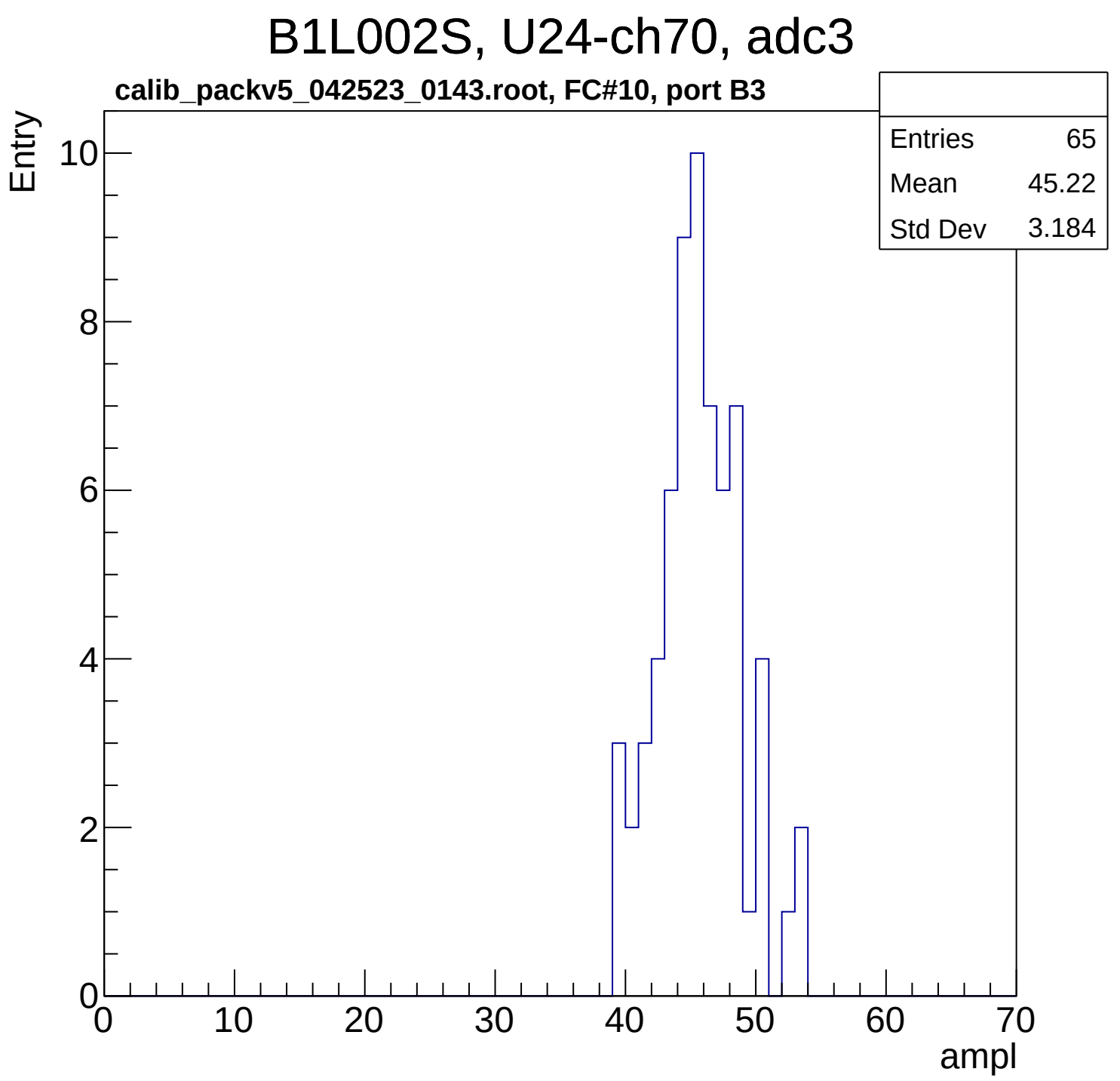
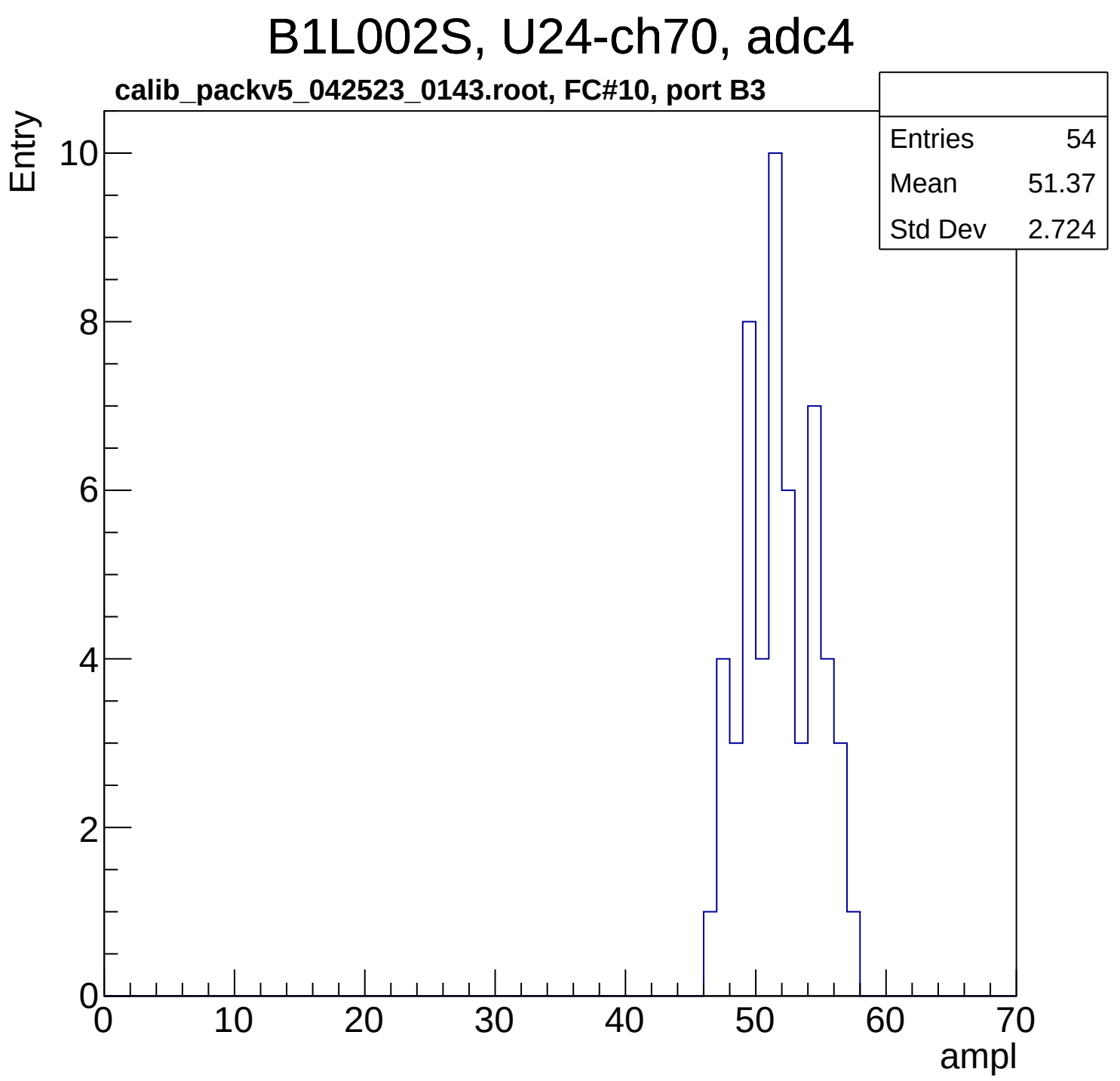
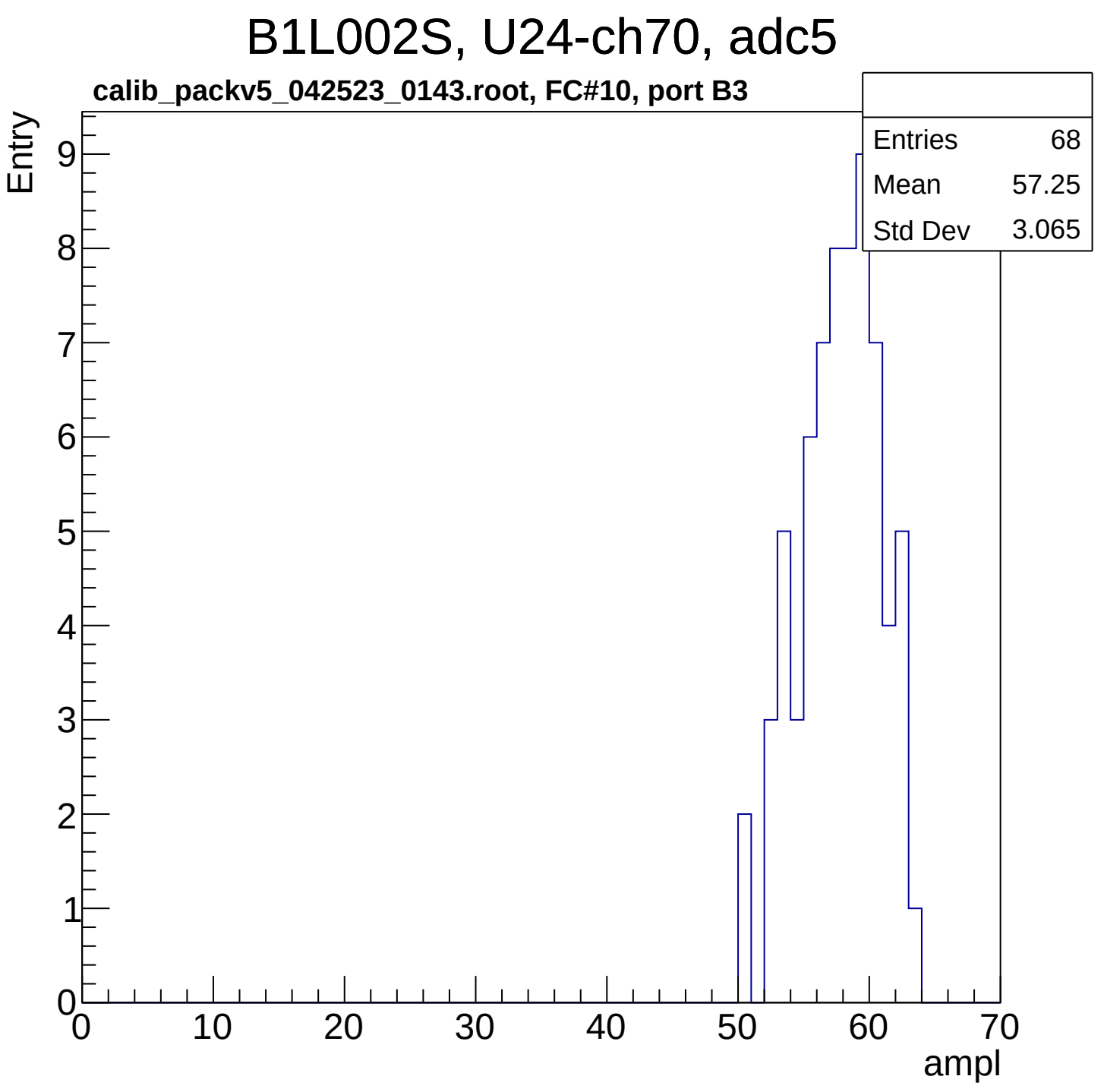

B1L002S, U24-ch70, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 45.22
Std Dev 3.184
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch70, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 51.37
Std Dev 2.724
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch70, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.25
Std Dev 3.065
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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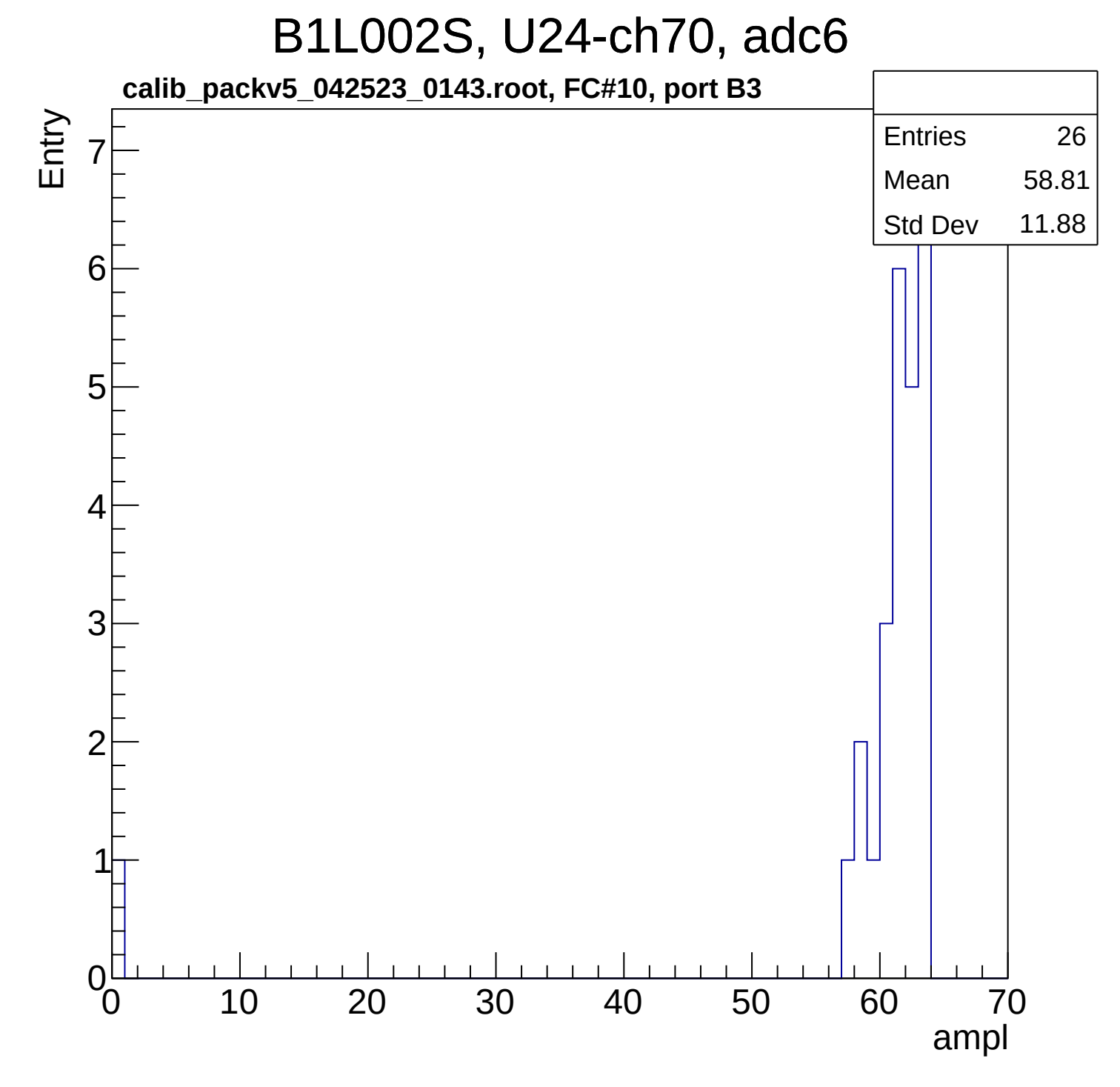
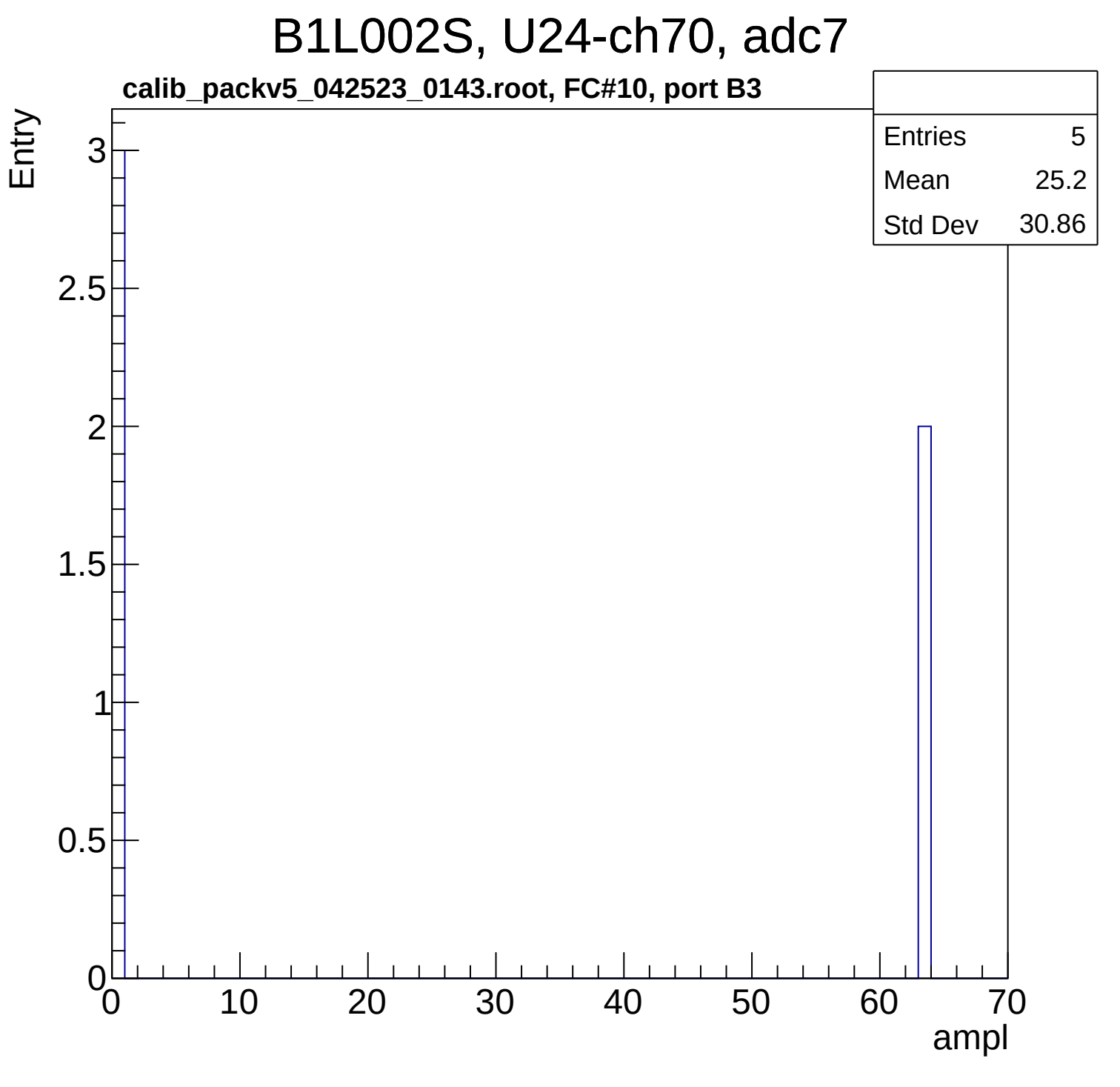
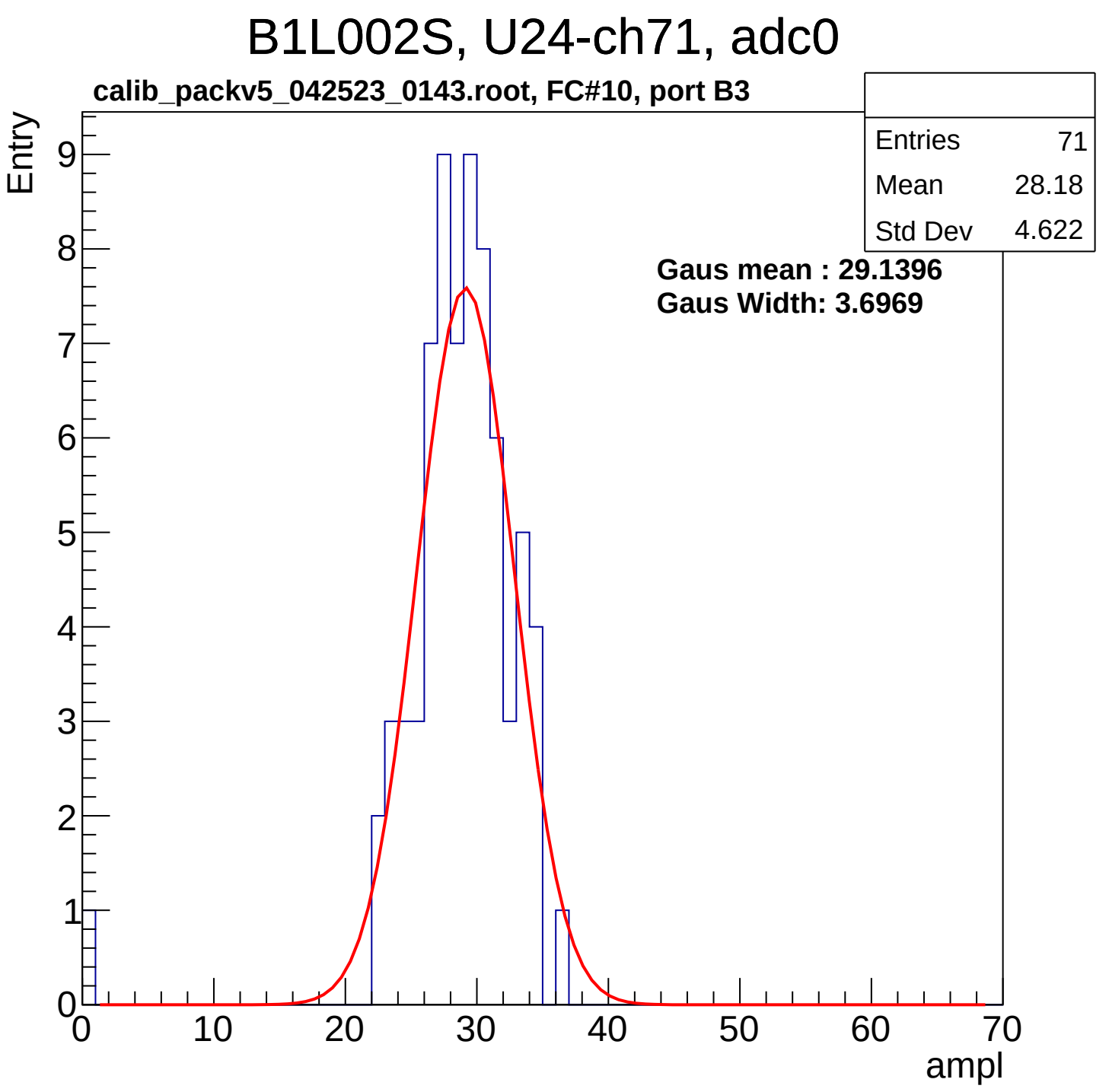

B1L002S, U24-ch70, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.81
Std Dev 11.88
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch70, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 25.2
Std Dev 30.86
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch71, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 28.18
Std Dev 4.622
Gaus mean : 29.1396
Gaus Width: 3.6969
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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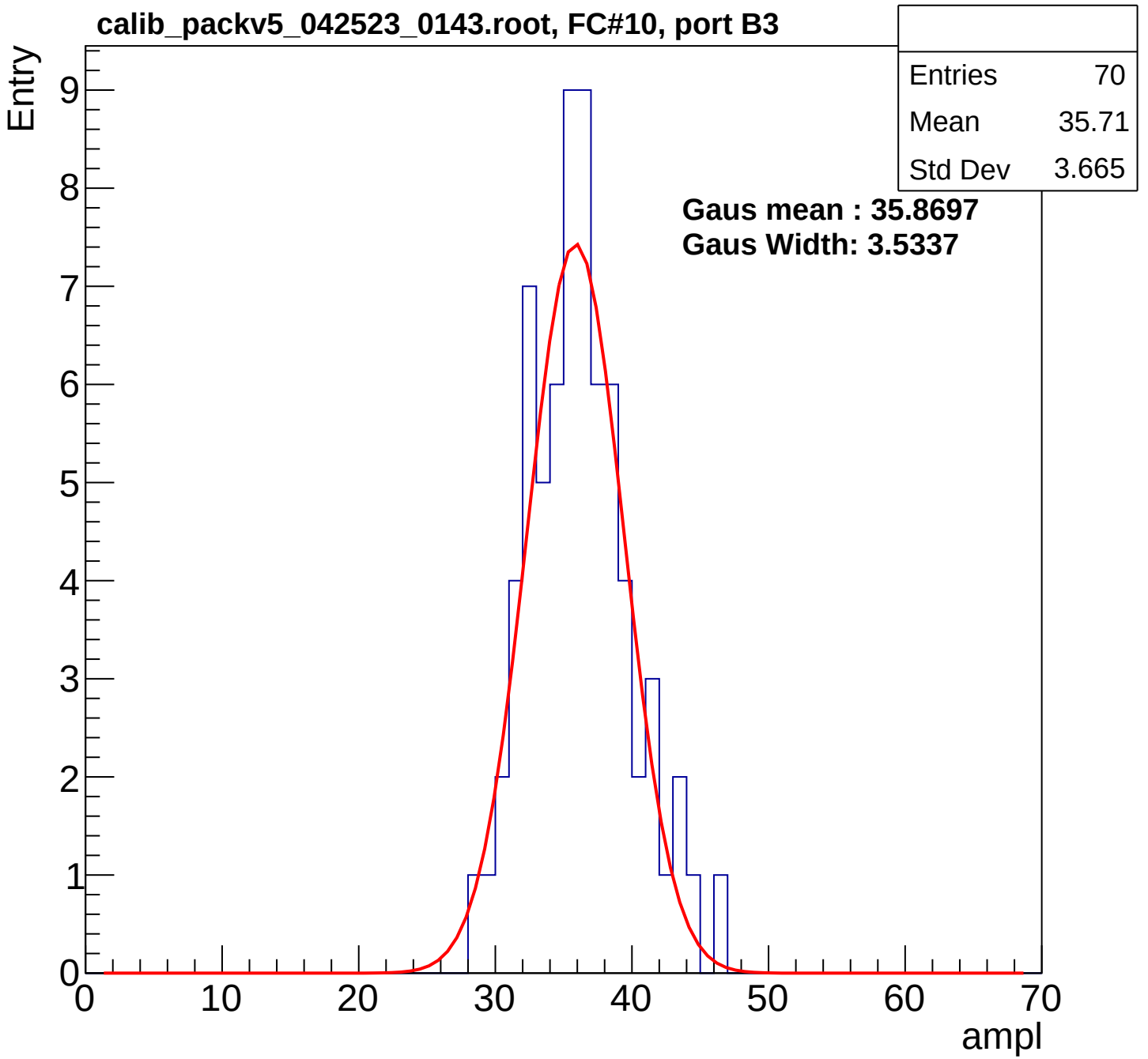
B1L002S, U24-ch71, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 35.71
Std Dev 3.665
Gaus mean : 35.8697
Gaus Width: 3.5337
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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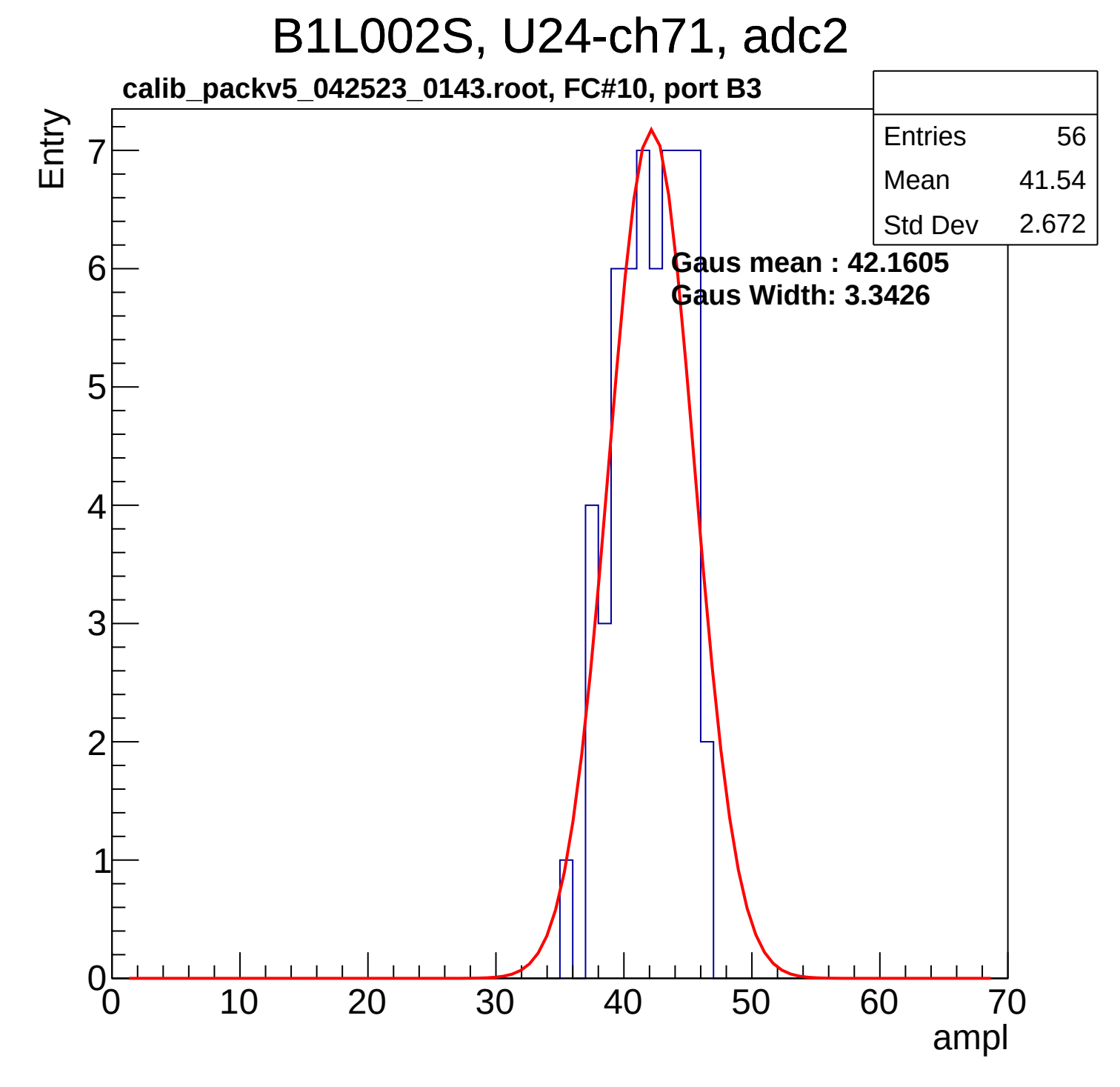
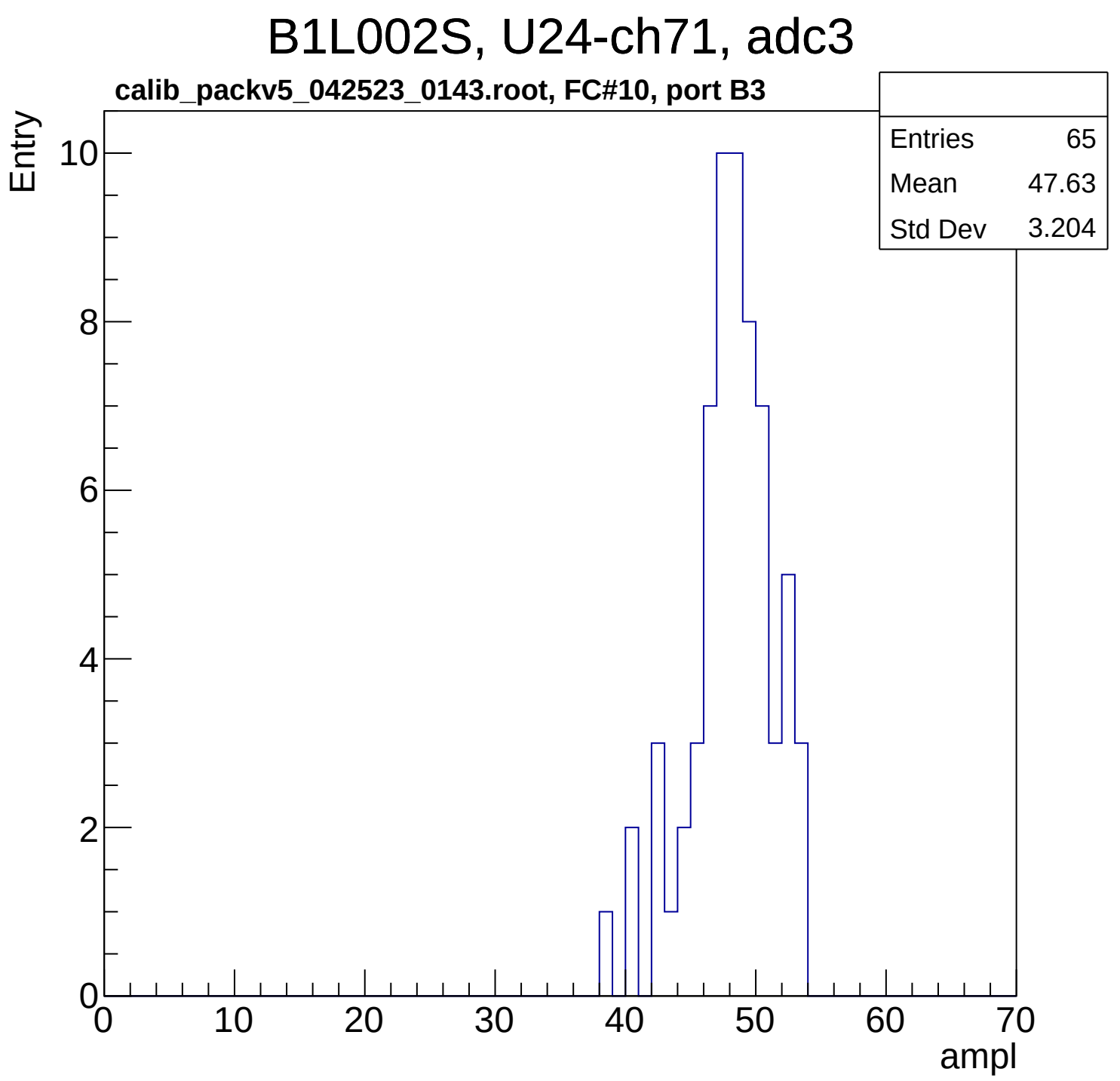
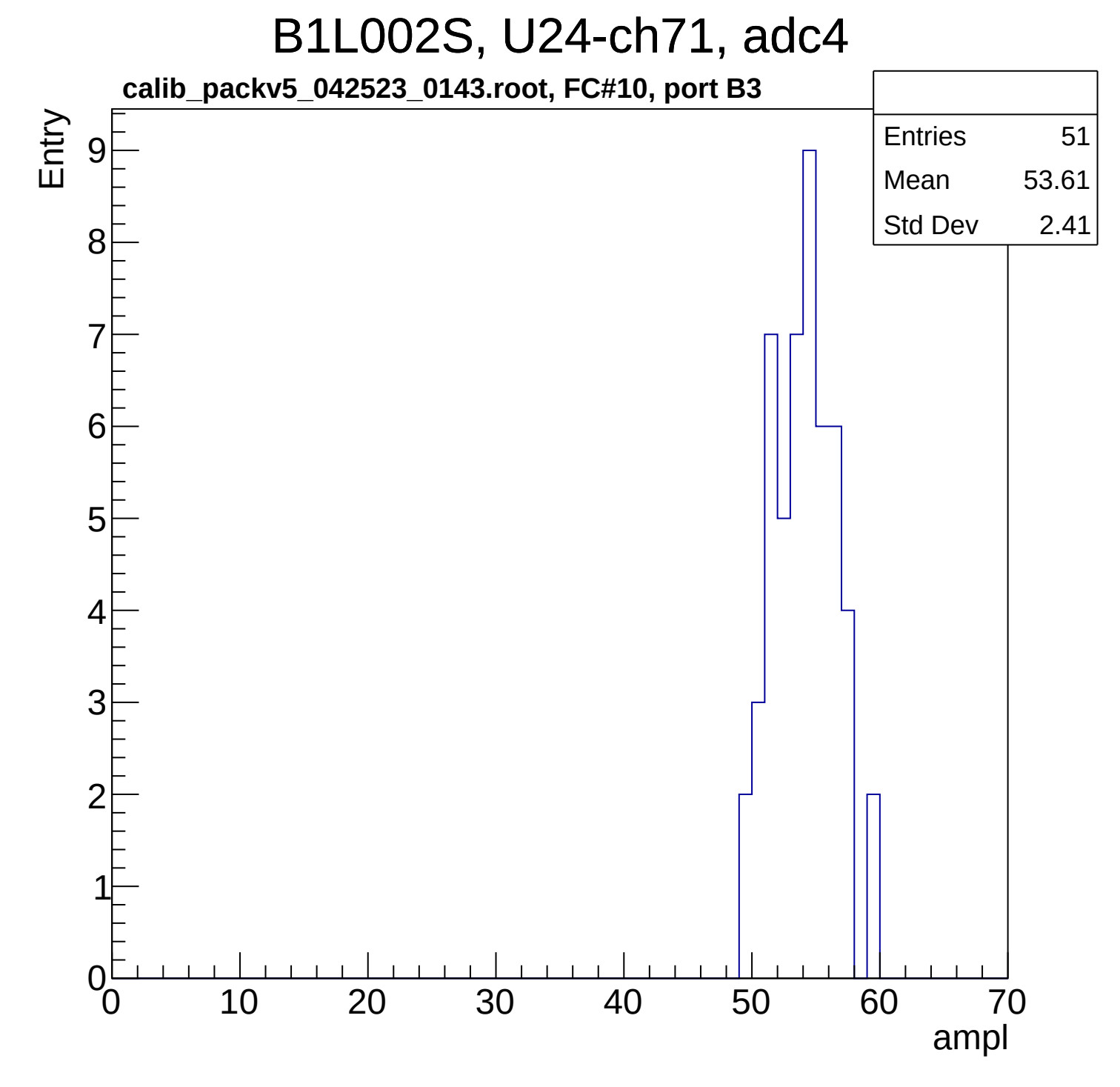
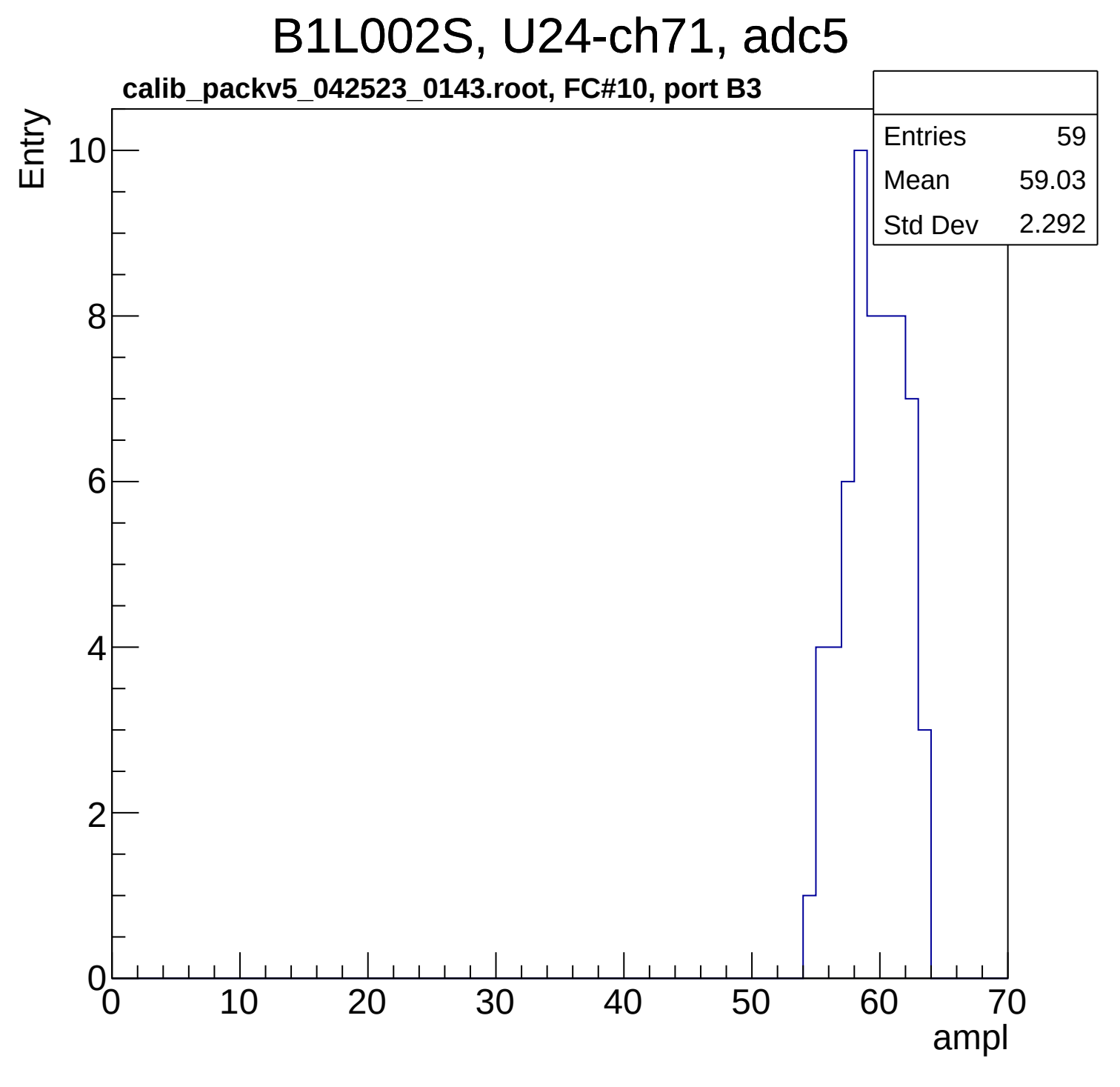
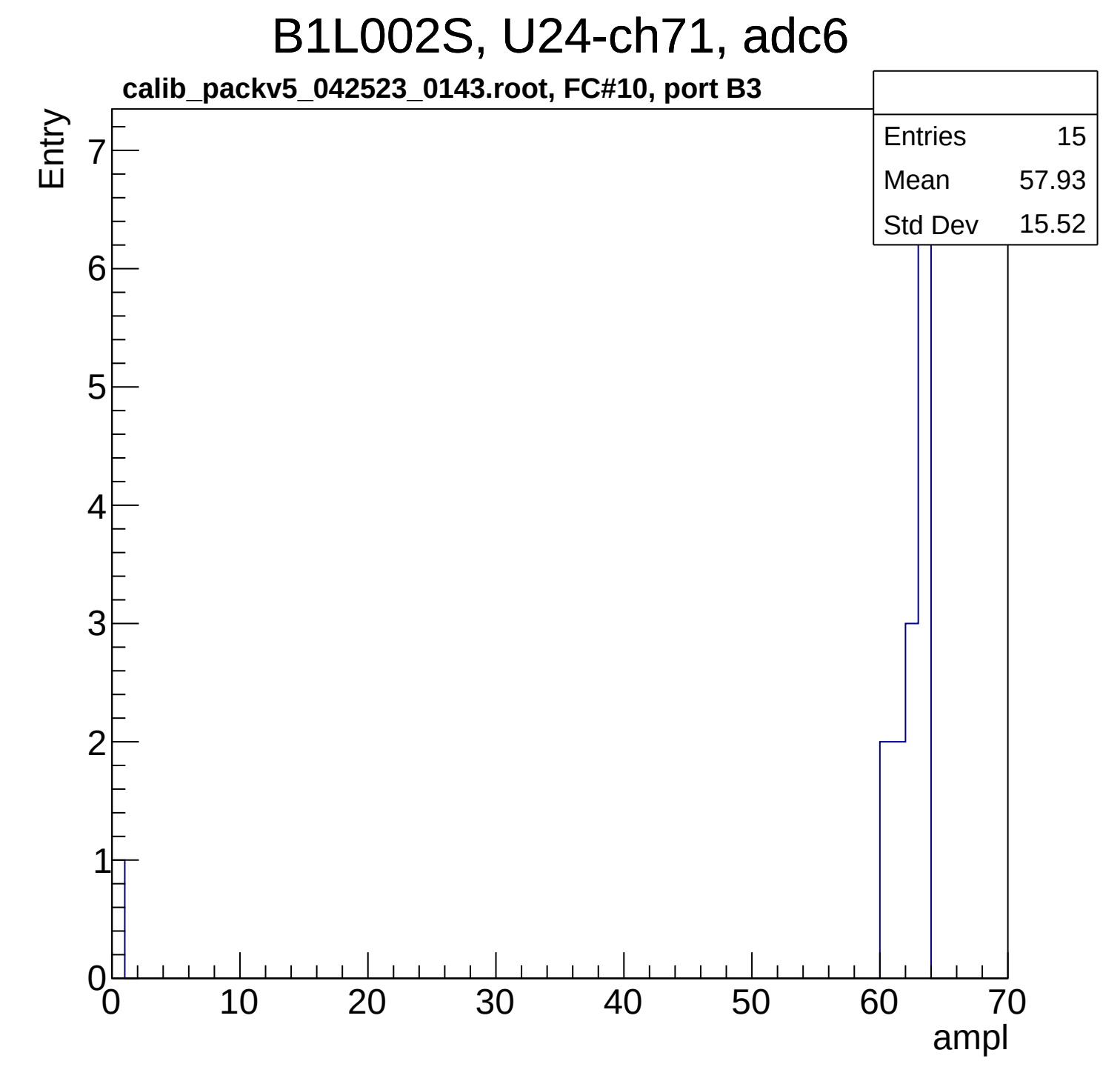
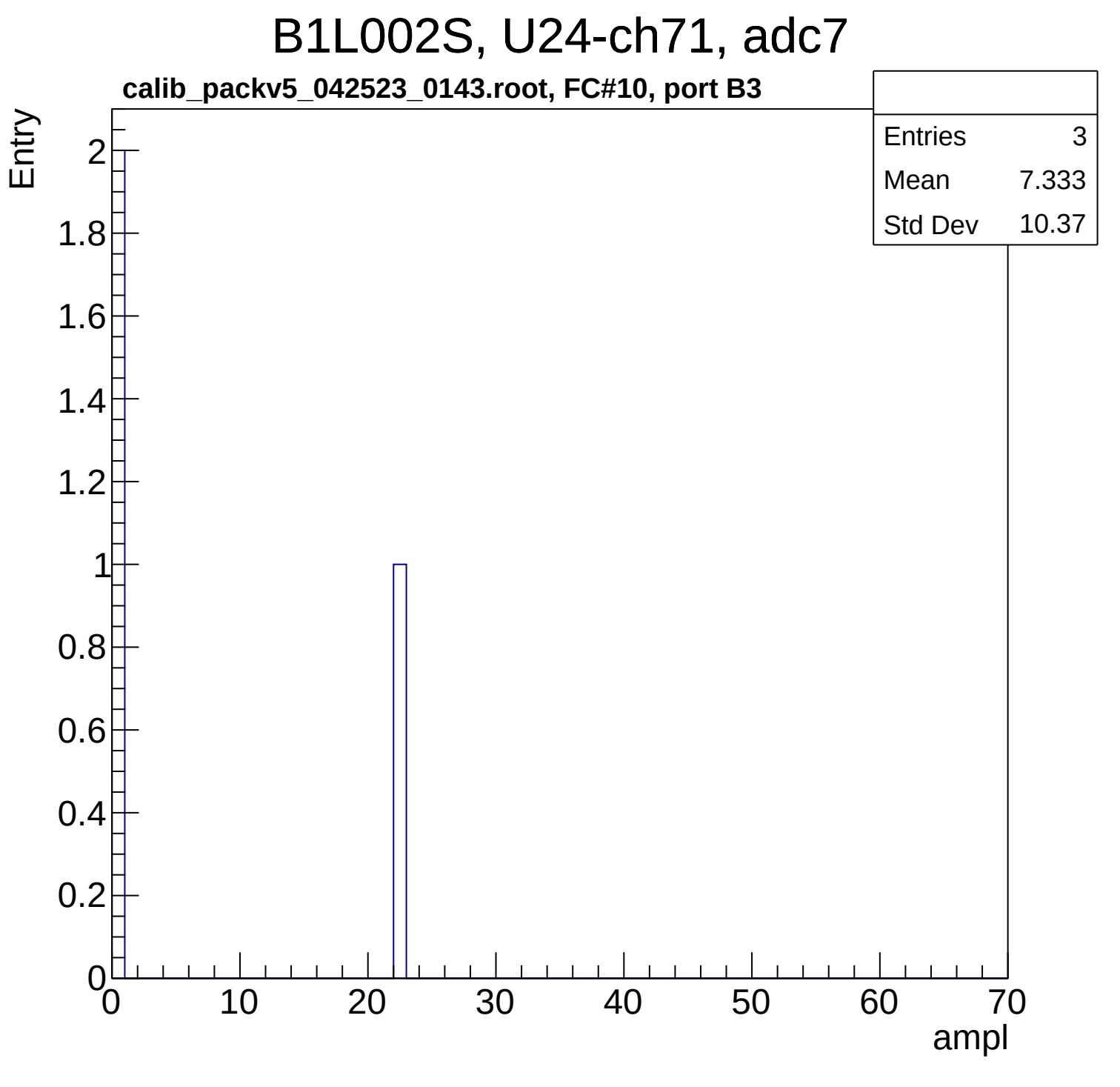

B1L002S, U24-ch71, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 41.54
Std Dev 2.672
Gaus mean : 42.1605
Gaus Width: 3.3426
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

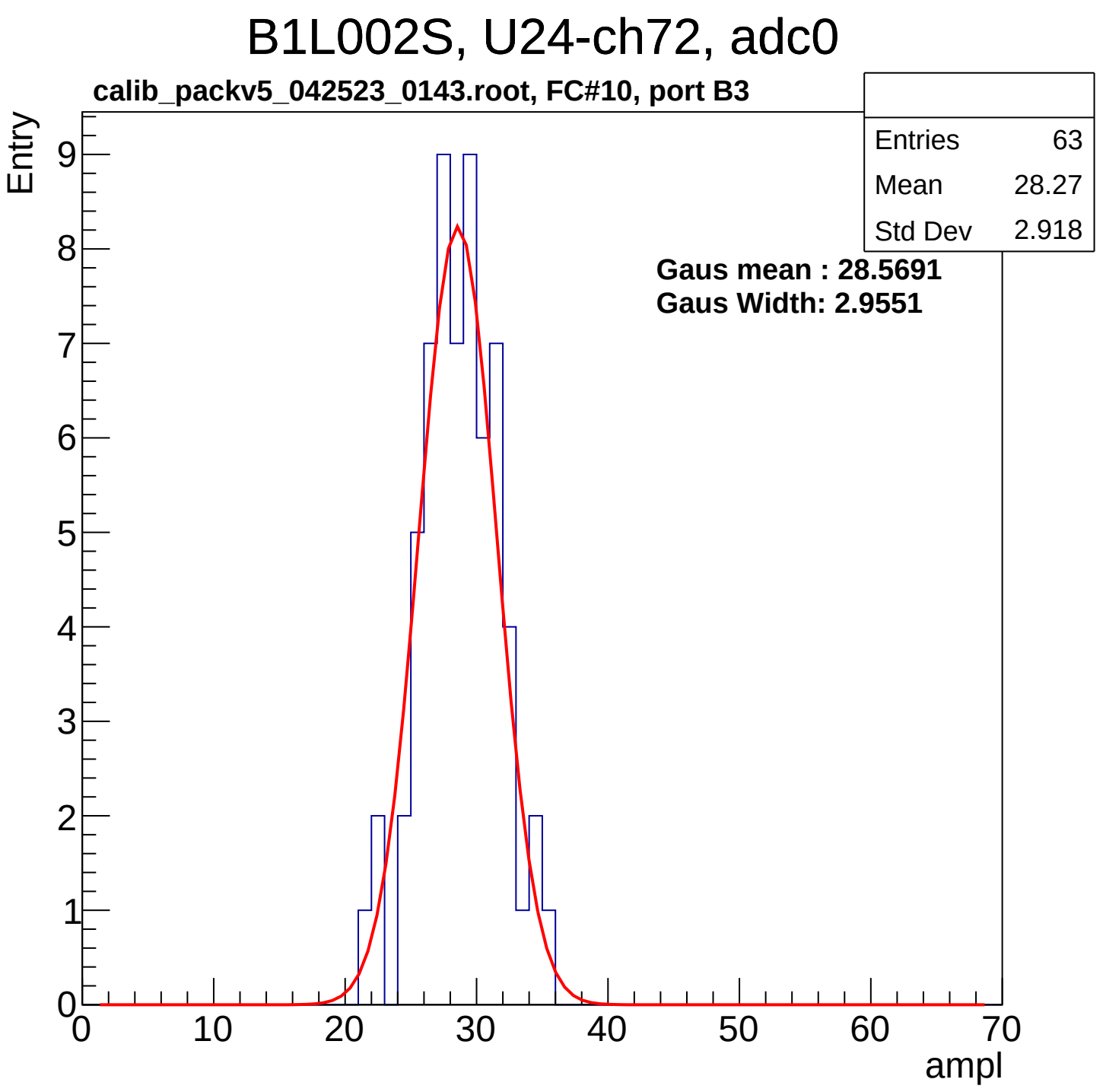

B1L002S, U24-ch71, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 47.63
Std Dev 3.204
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

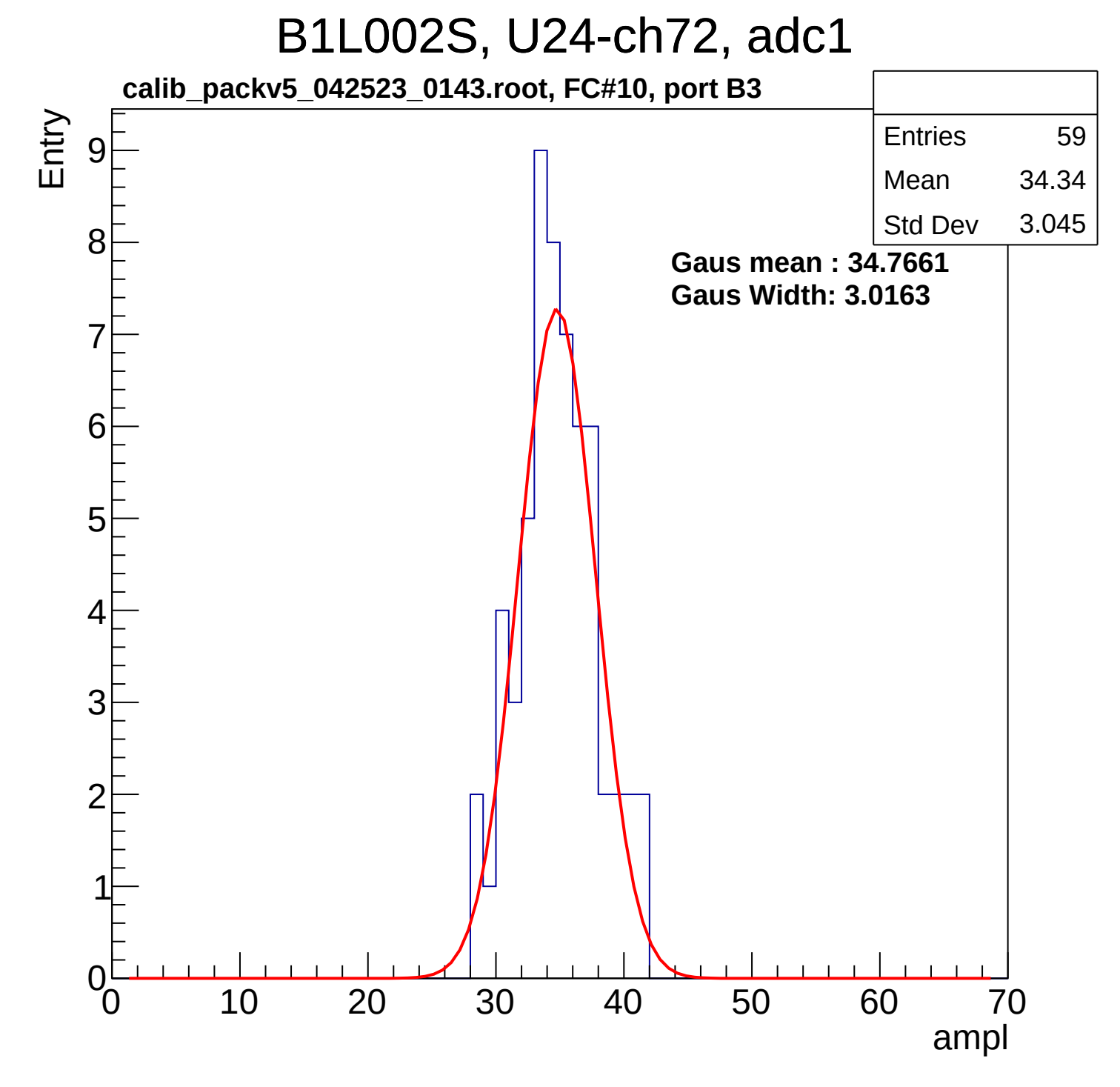

B1L002S, U24-ch71, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.61
Std Dev 2.41
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch71, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 59.03
Std Dev 2.292
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch71, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.93
Std Dev 15.52
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch71, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 7.333
Std Dev 10.37
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch72, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 28.27
Std Dev 2.918
Gaus mean : 28.5691
Gaus Width: 2.9551
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch72, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 34.34
Std Dev 3.045
Gaus mean : 34.7661
Gaus Width: 3.0163
Entry
ampl
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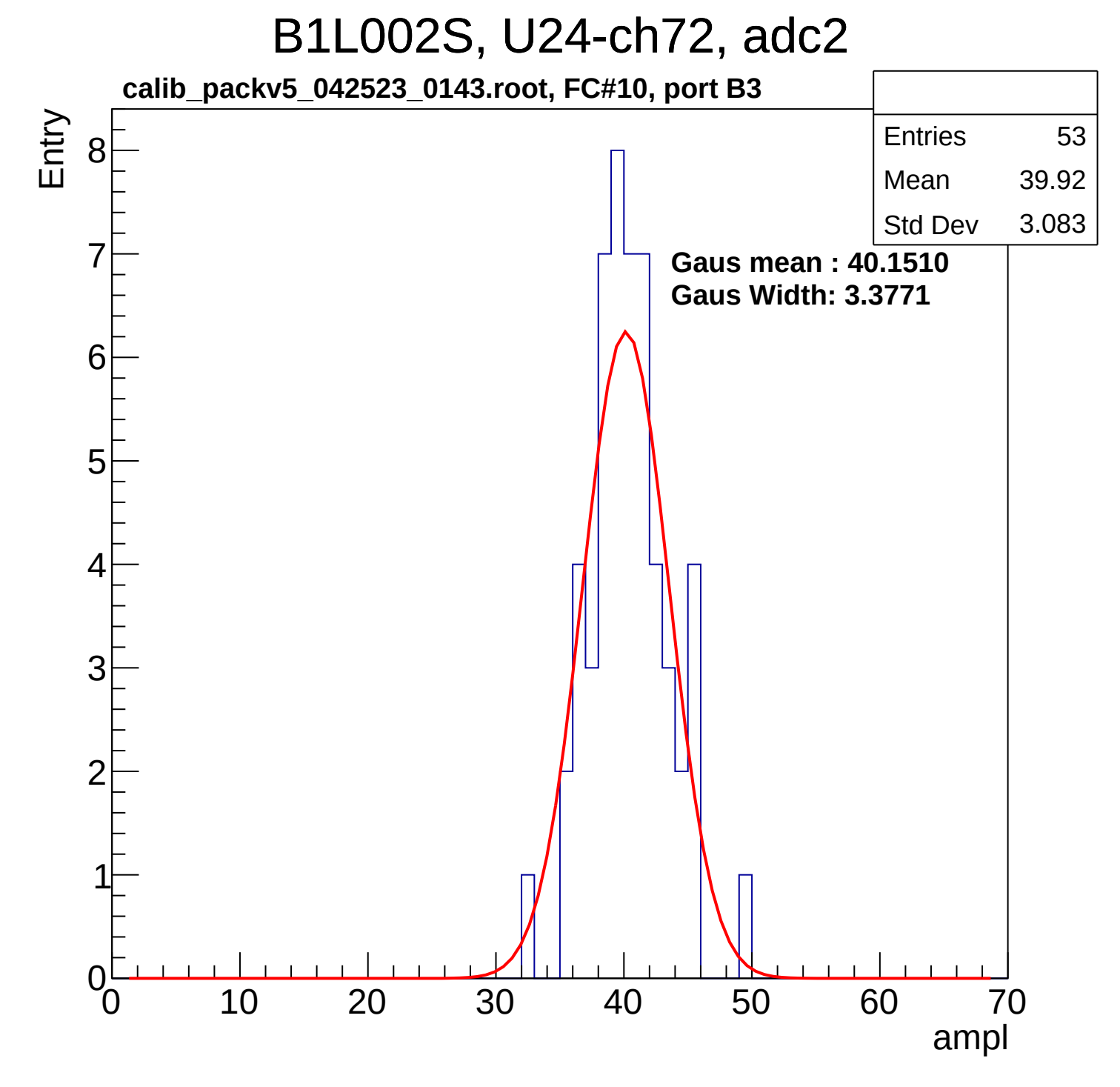
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch72, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 39.92
Std Dev 3.083
Gaus mean : 40.1510
Gaus Width: 3.3771
Entry
ampl
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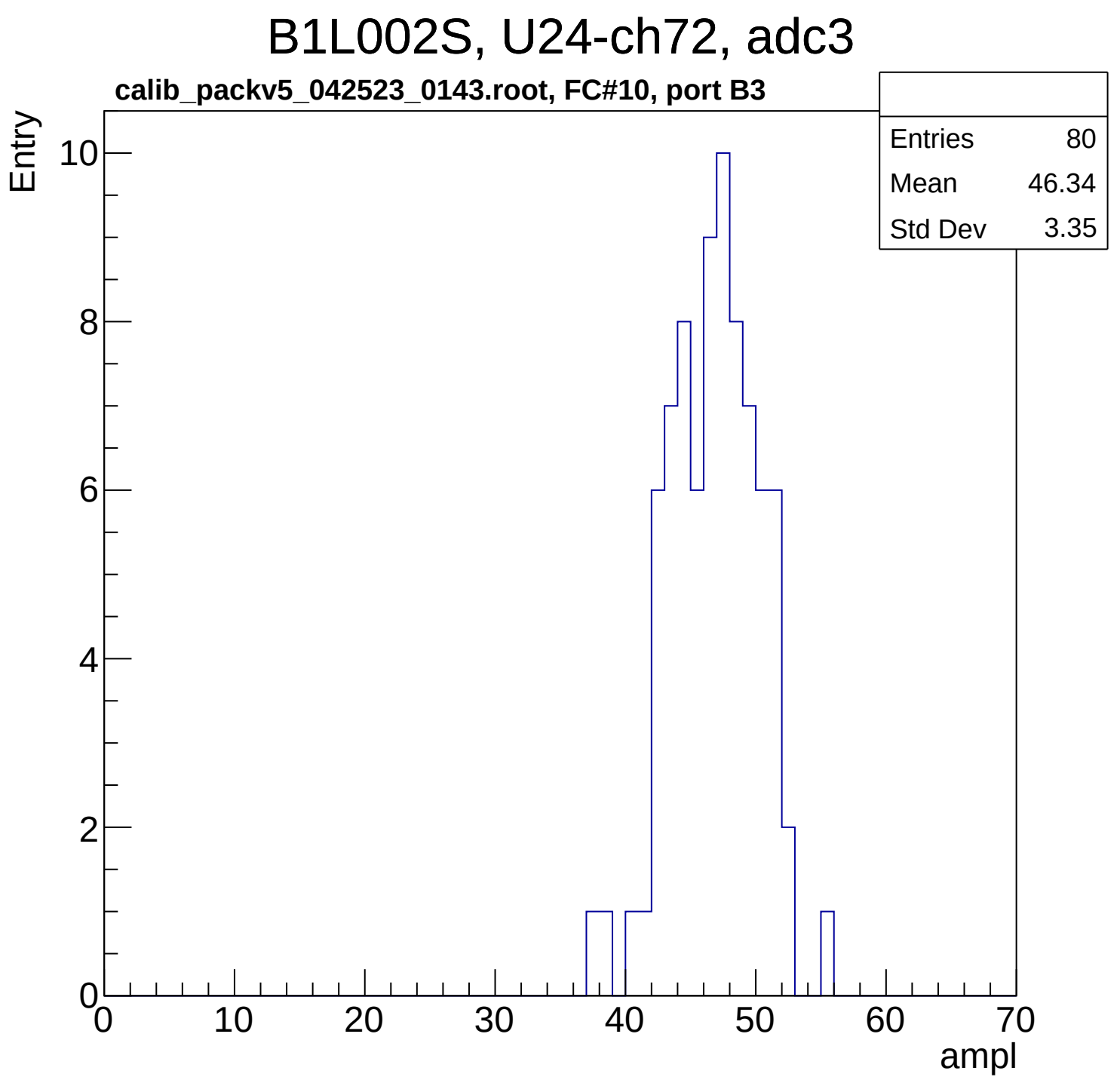
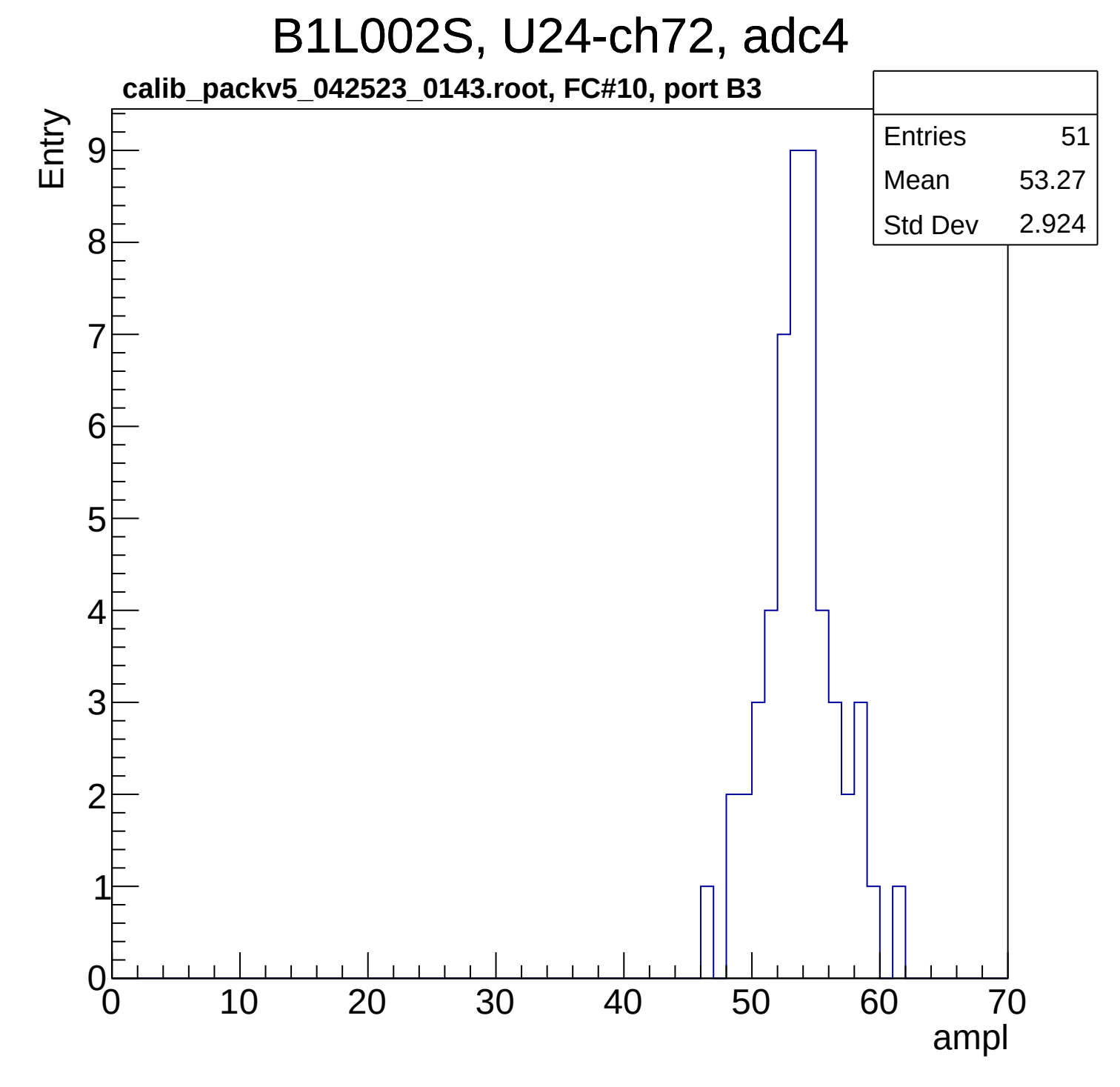
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch72, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 80
Mean 46.34
Std Dev 3.35
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch72, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 53.27
Std Dev 2.924
Entry
ampl
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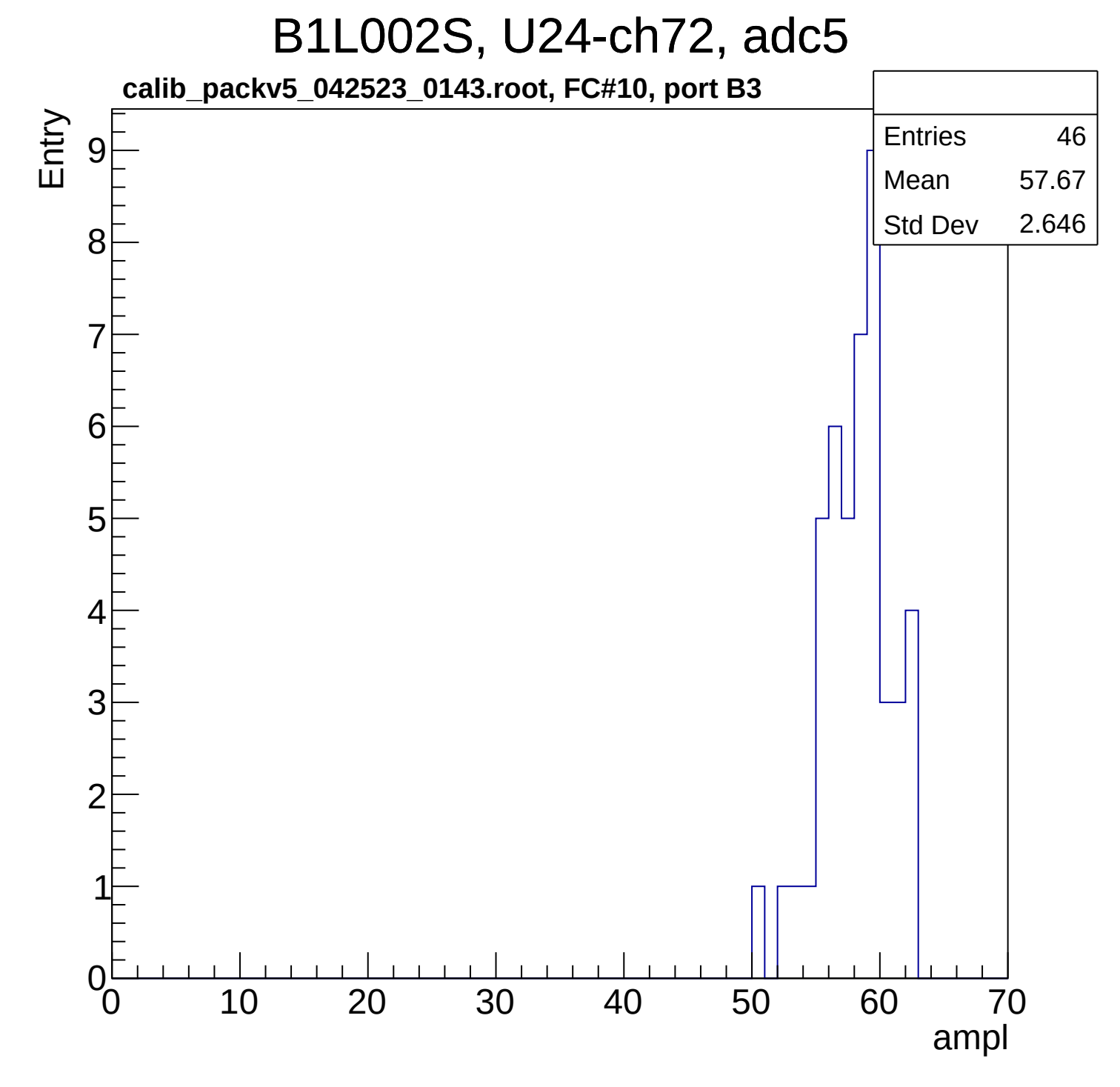
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch72, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 57.67
Std Dev 2.646
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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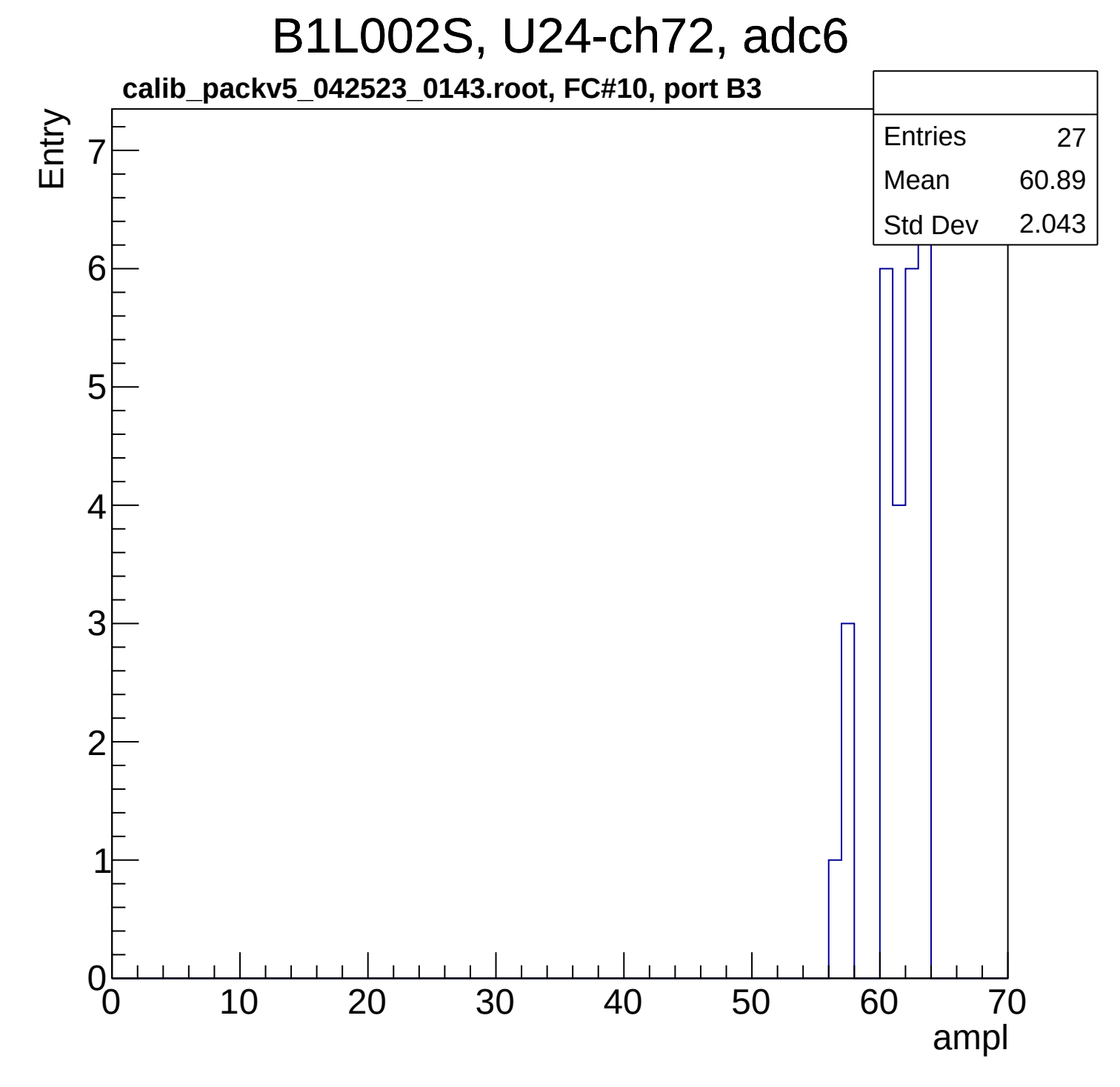
B1L002S, U24-ch72, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 27
Mean 60.89
Std Dev 2.043
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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40
50
60
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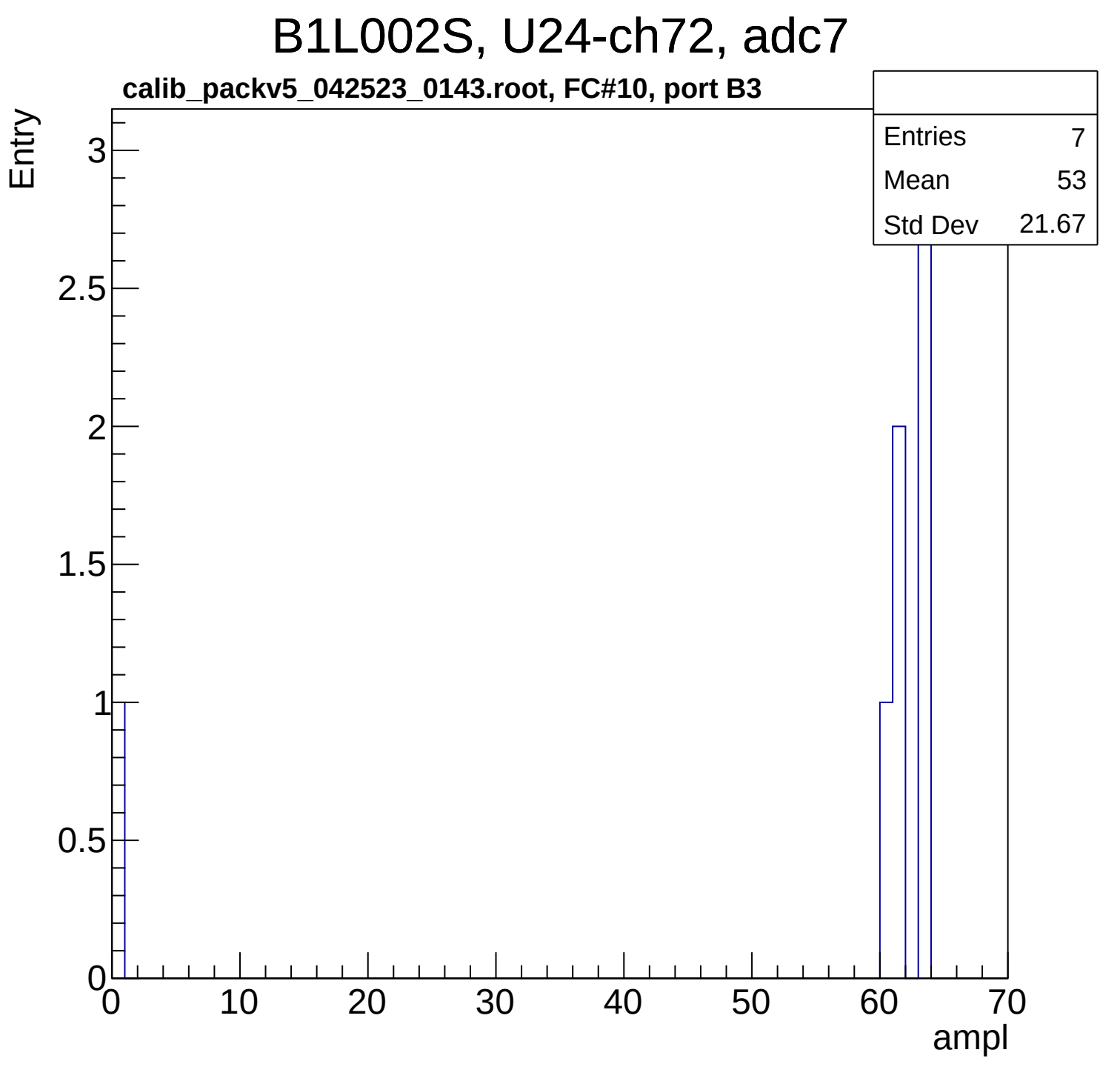
B1L002S, U24-ch72, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53
Std Dev 21.67
Entry
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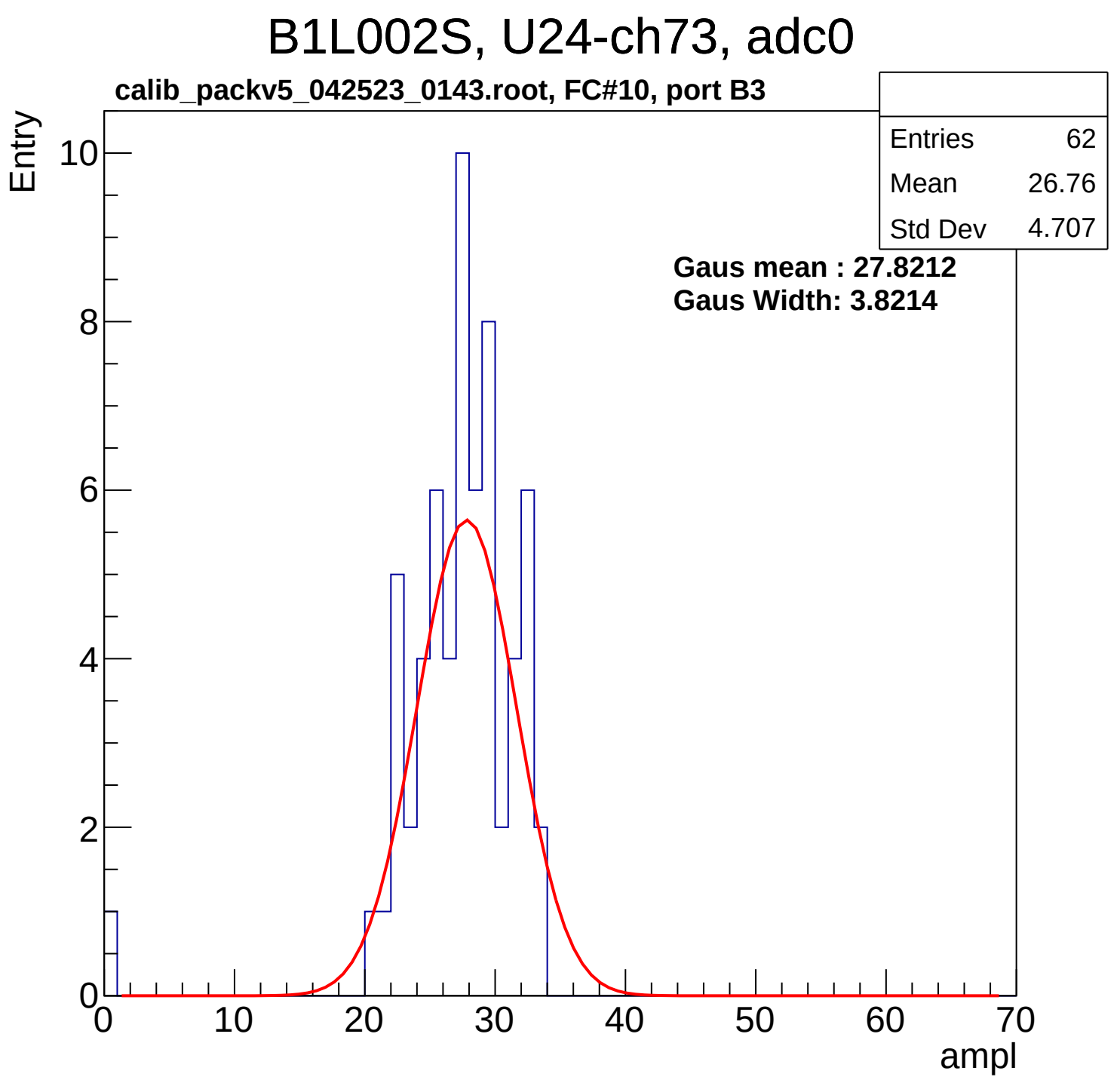
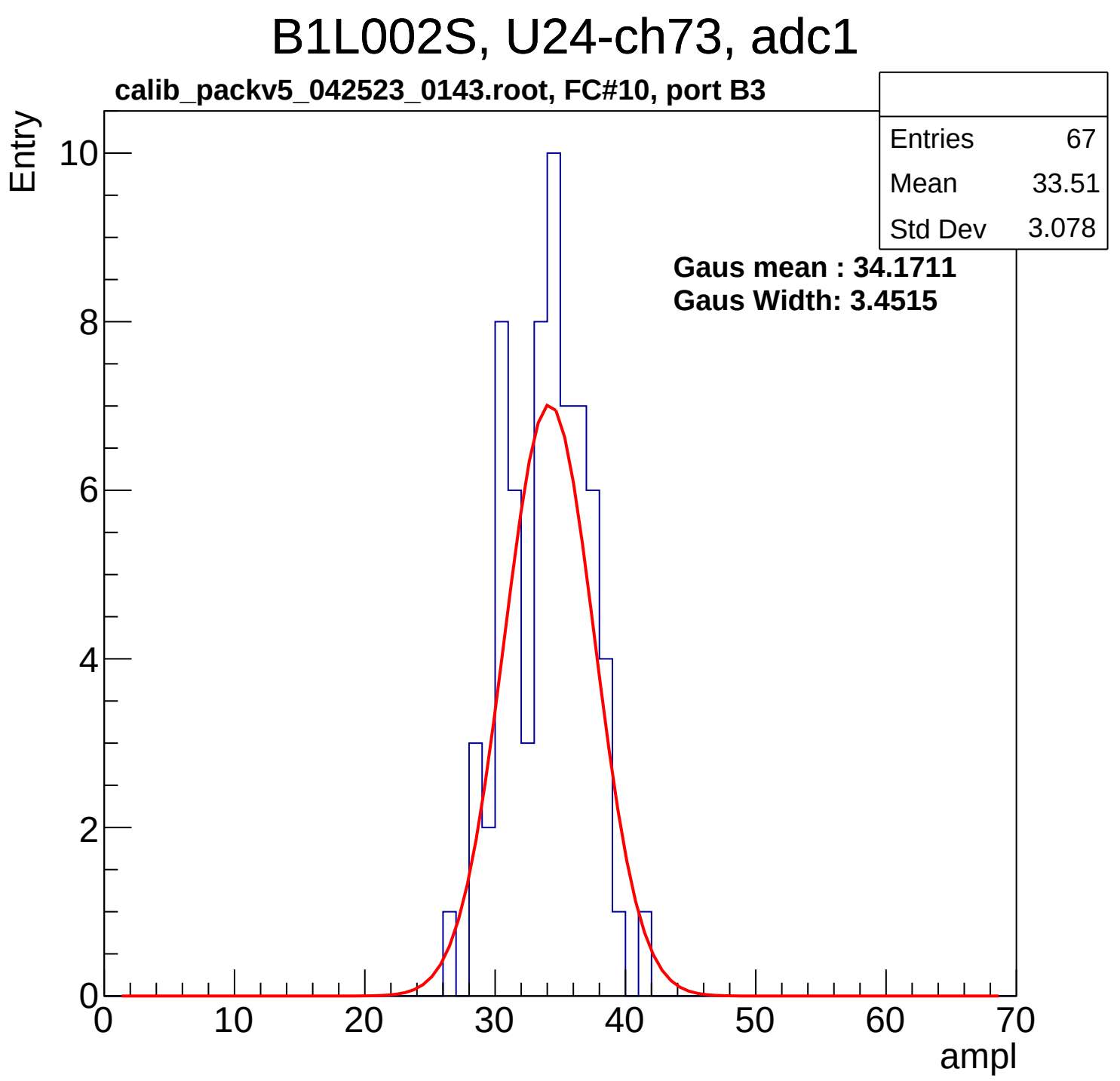
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch73, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.76
Std Dev 4.707
Gaus mean : 27.8212
Gaus Width: 3.8214
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch73, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.51
Std Dev 3.078
Gaus mean : 34.1711
Gaus Width: 3.4515
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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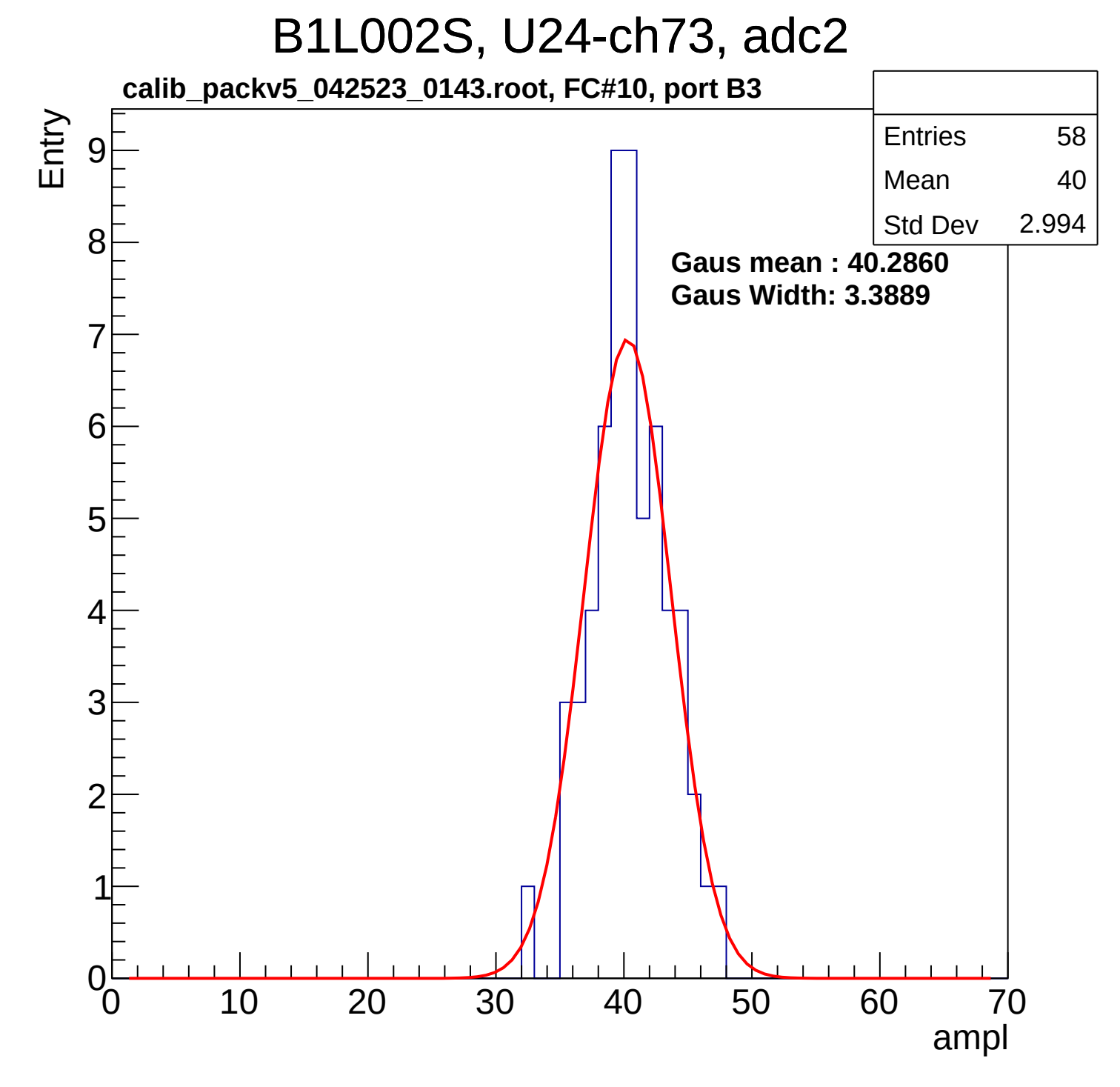
B1L002S, U24-ch73, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 40
Std Dev 2.994
Gaus mean : 40.2860
Gaus Width: 3.3889
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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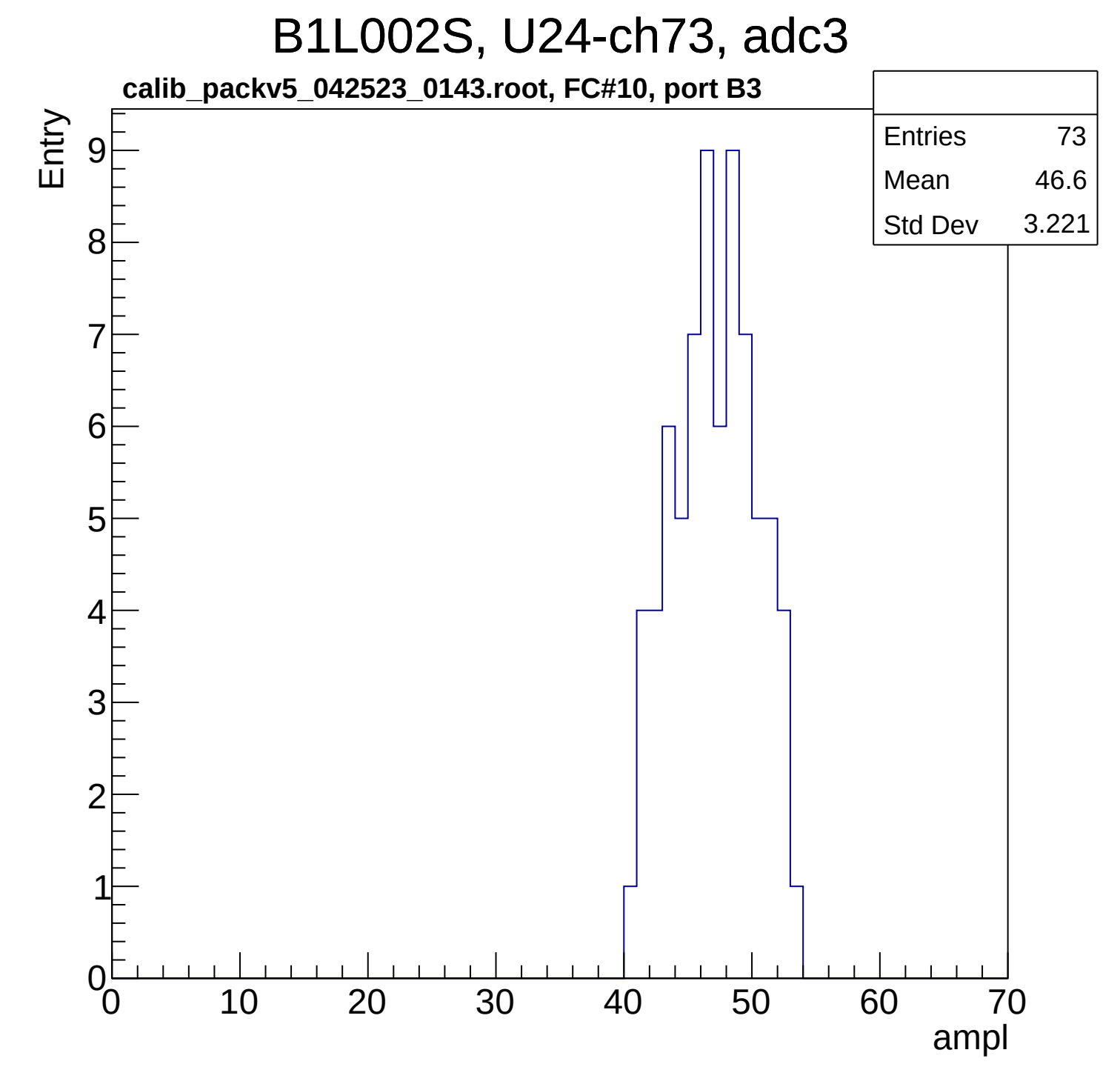
B1L002S, U24-ch73, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 46.6
Std Dev 3.221
Entry
ampl
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3
4
5
6
7
8
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0
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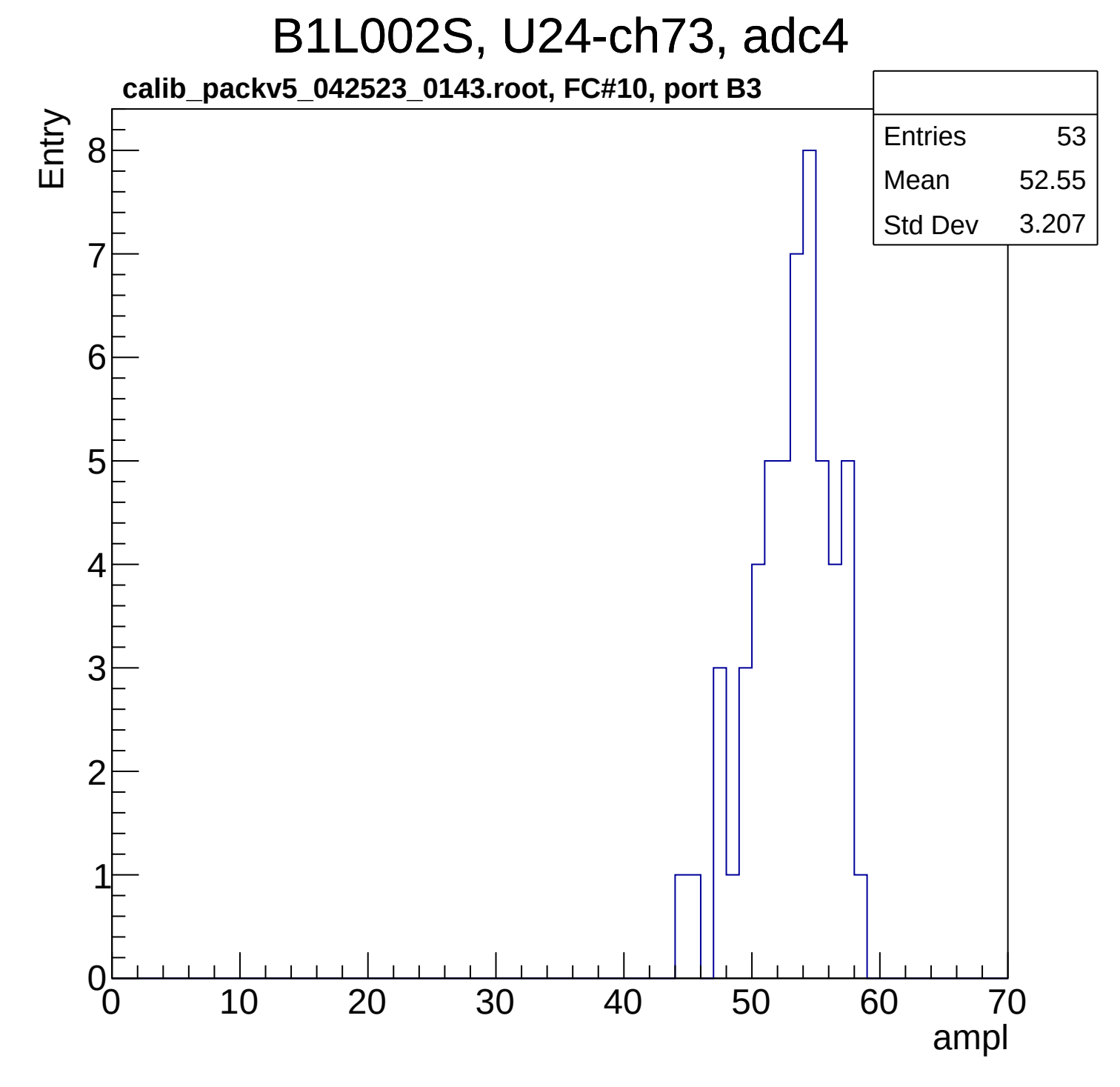
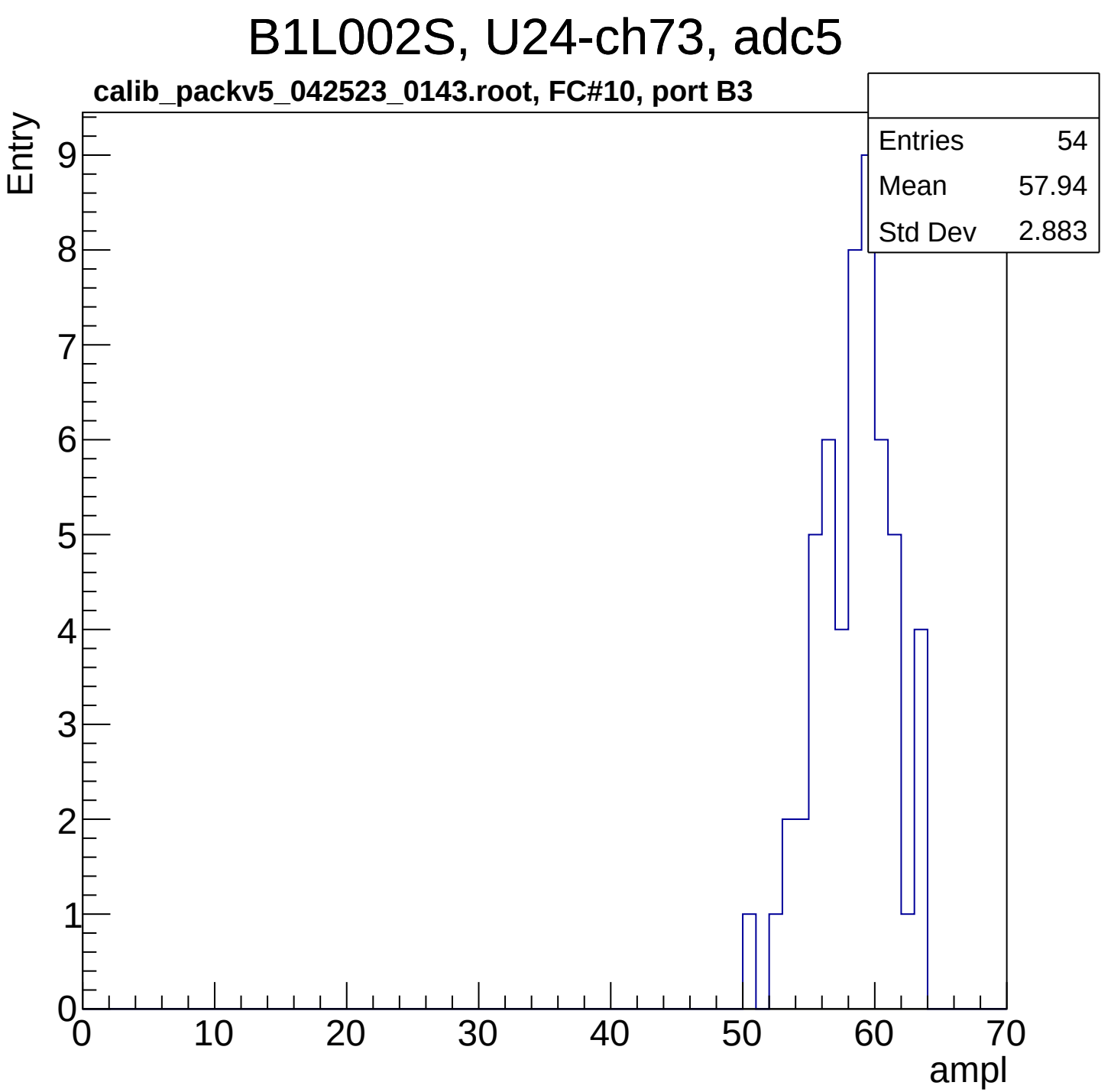

B1L002S, U24-ch73, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.55
Std Dev 3.207
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch73, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.94
Std Dev 2.883
Entry
ampl
0
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2
3
4
5
6
7
8
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0
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20
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40
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60
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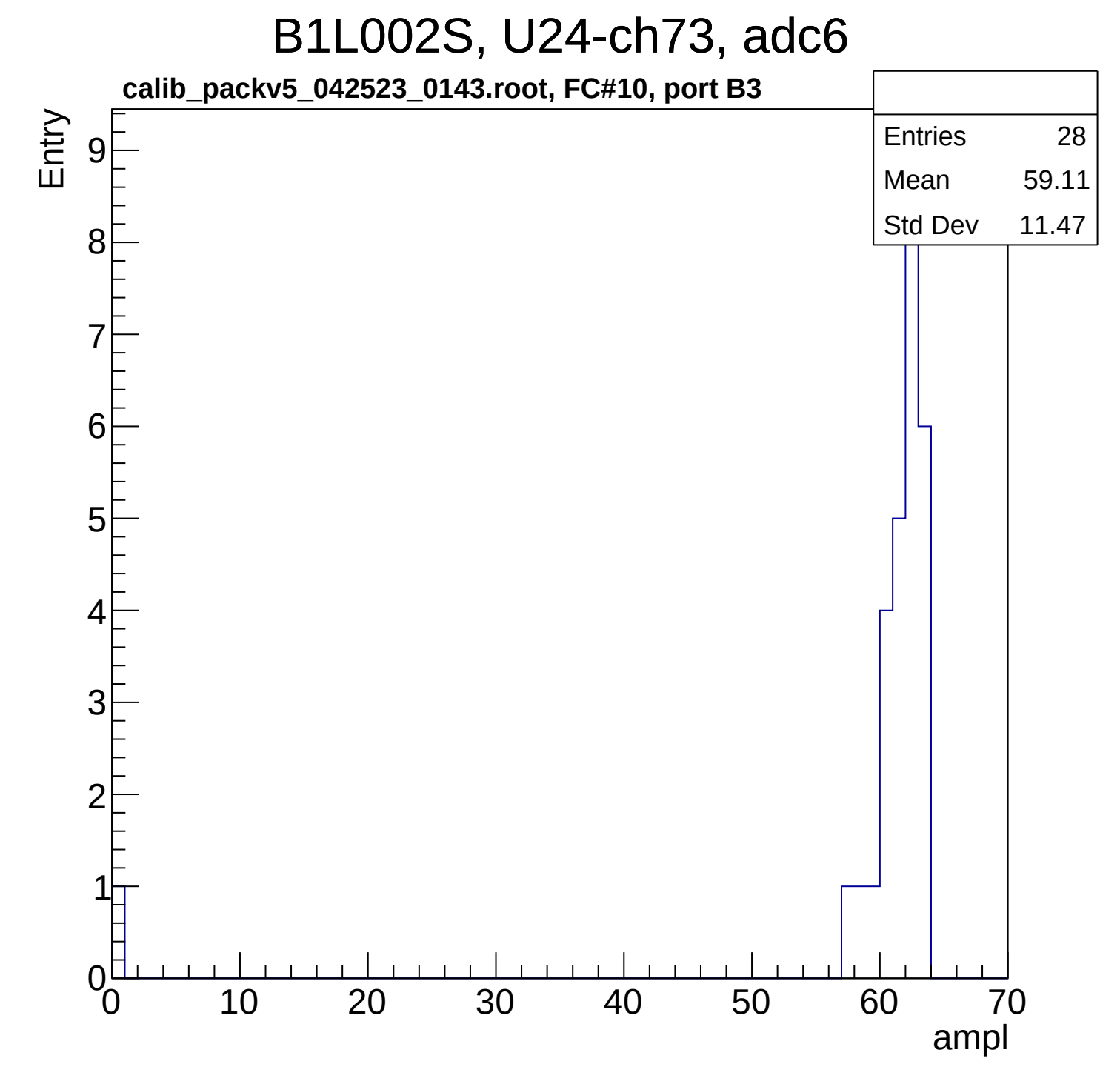
B1L002S, U24-ch73, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 59.11
Std Dev 11.47
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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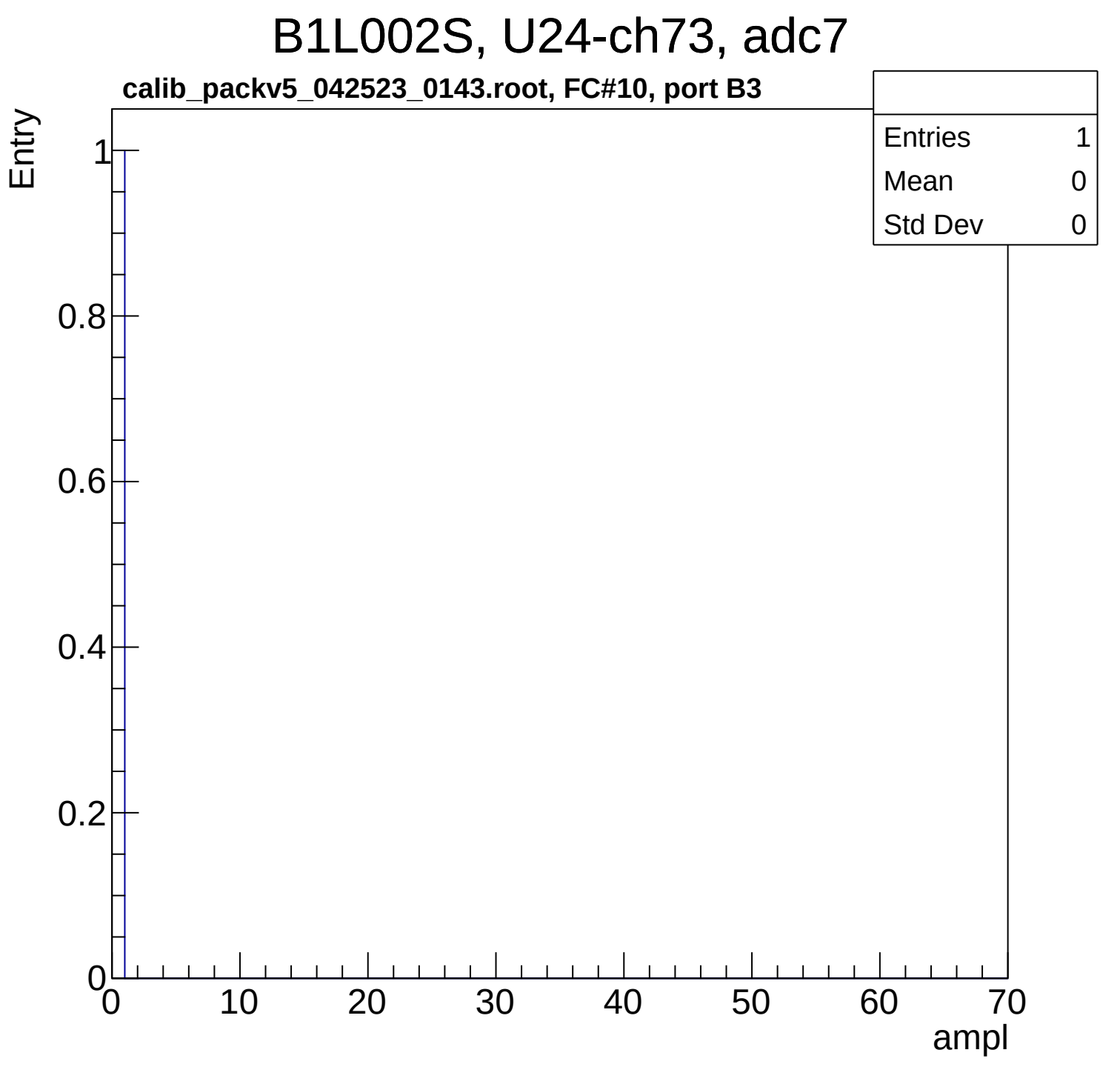
B1L002S, U24-ch73, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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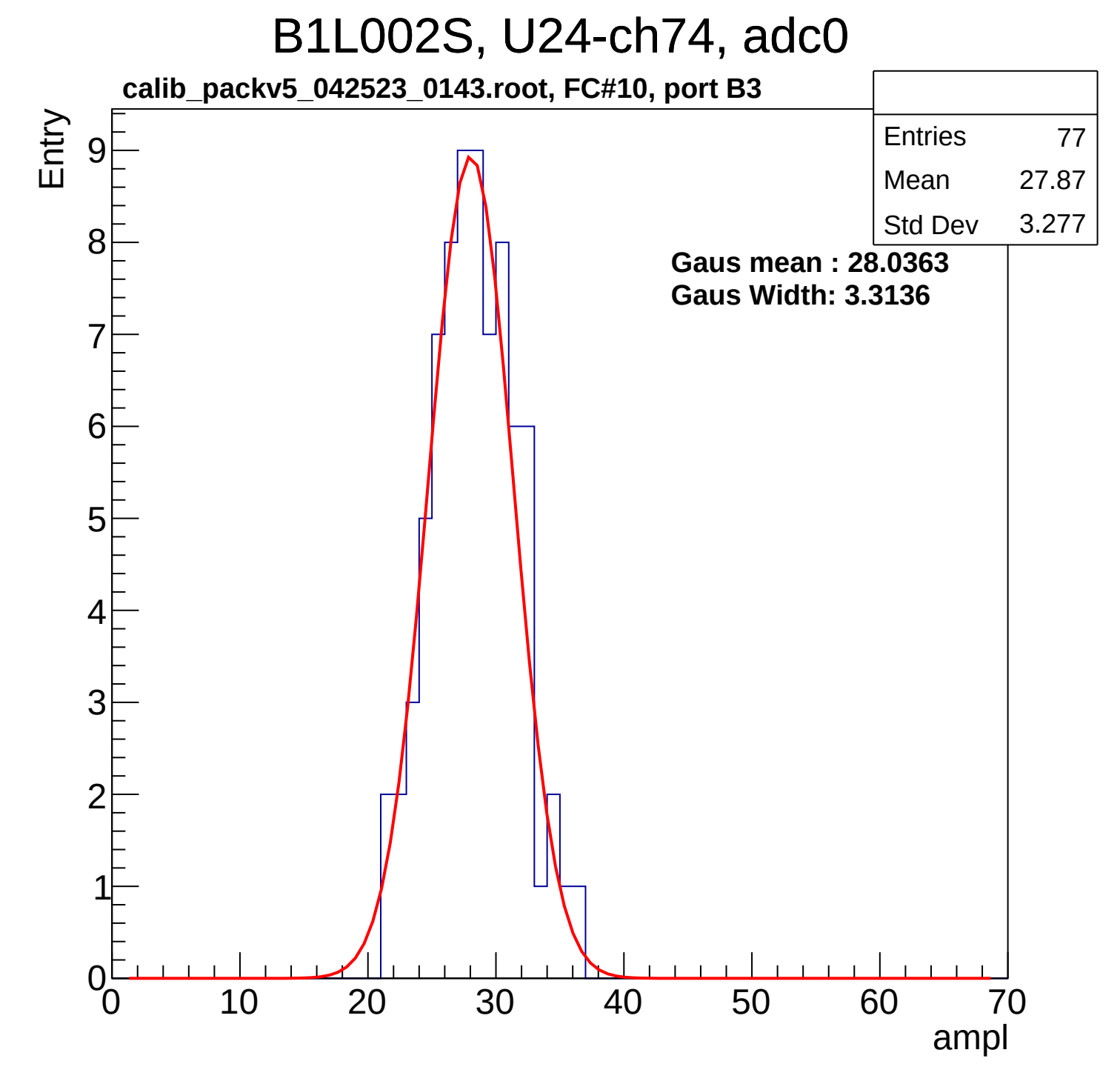
40
50
60
70
ampl


B1L002S, U24-ch74, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 27.87
Std Dev 3.277
Gaus mean : 28.0363
Gaus Width: 3.3136
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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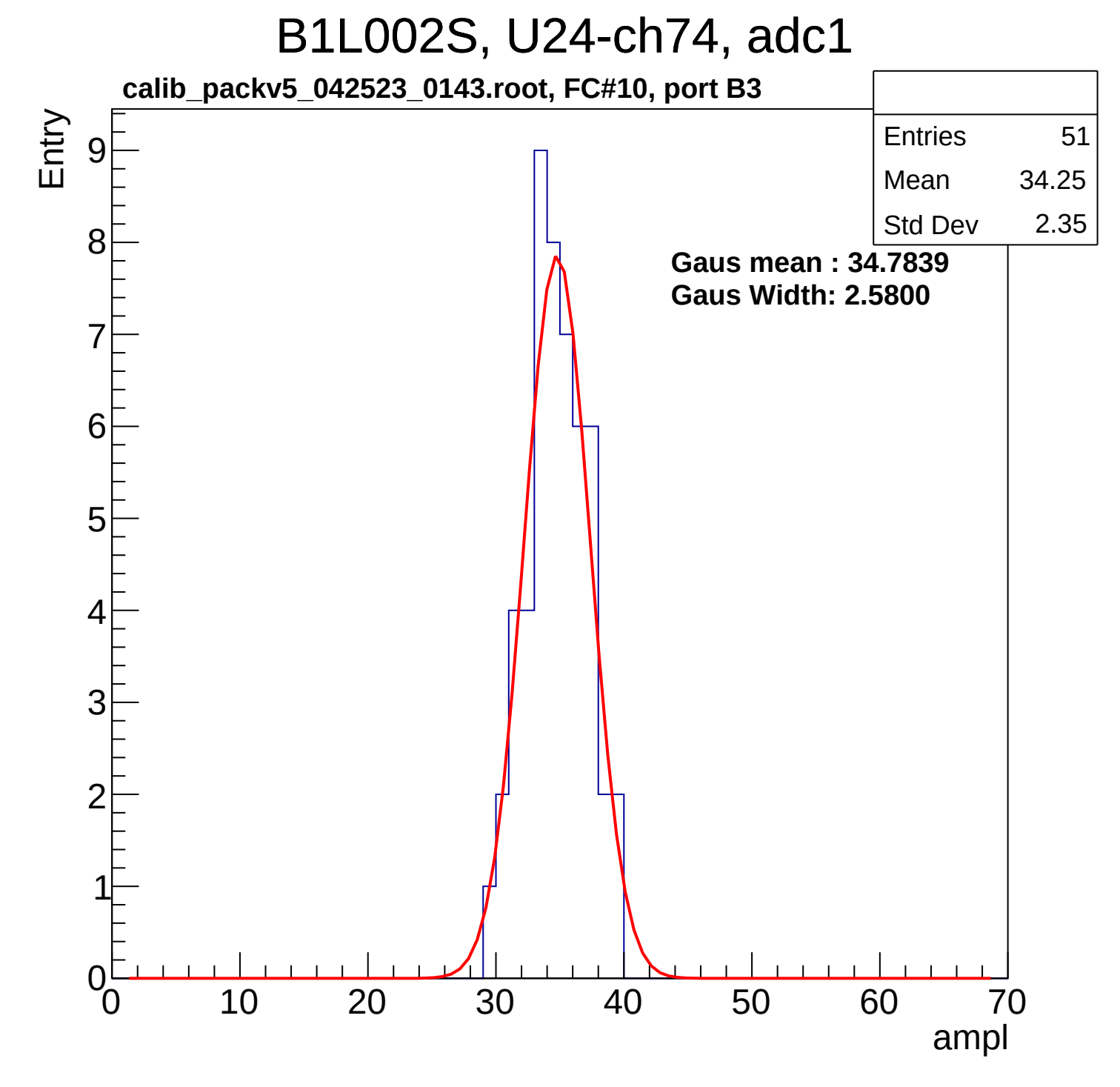
B1L002S, U24-ch74, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 34.25
Std Dev 2.35
Gaus mean : 34.7839
Gaus Width: 2.5800
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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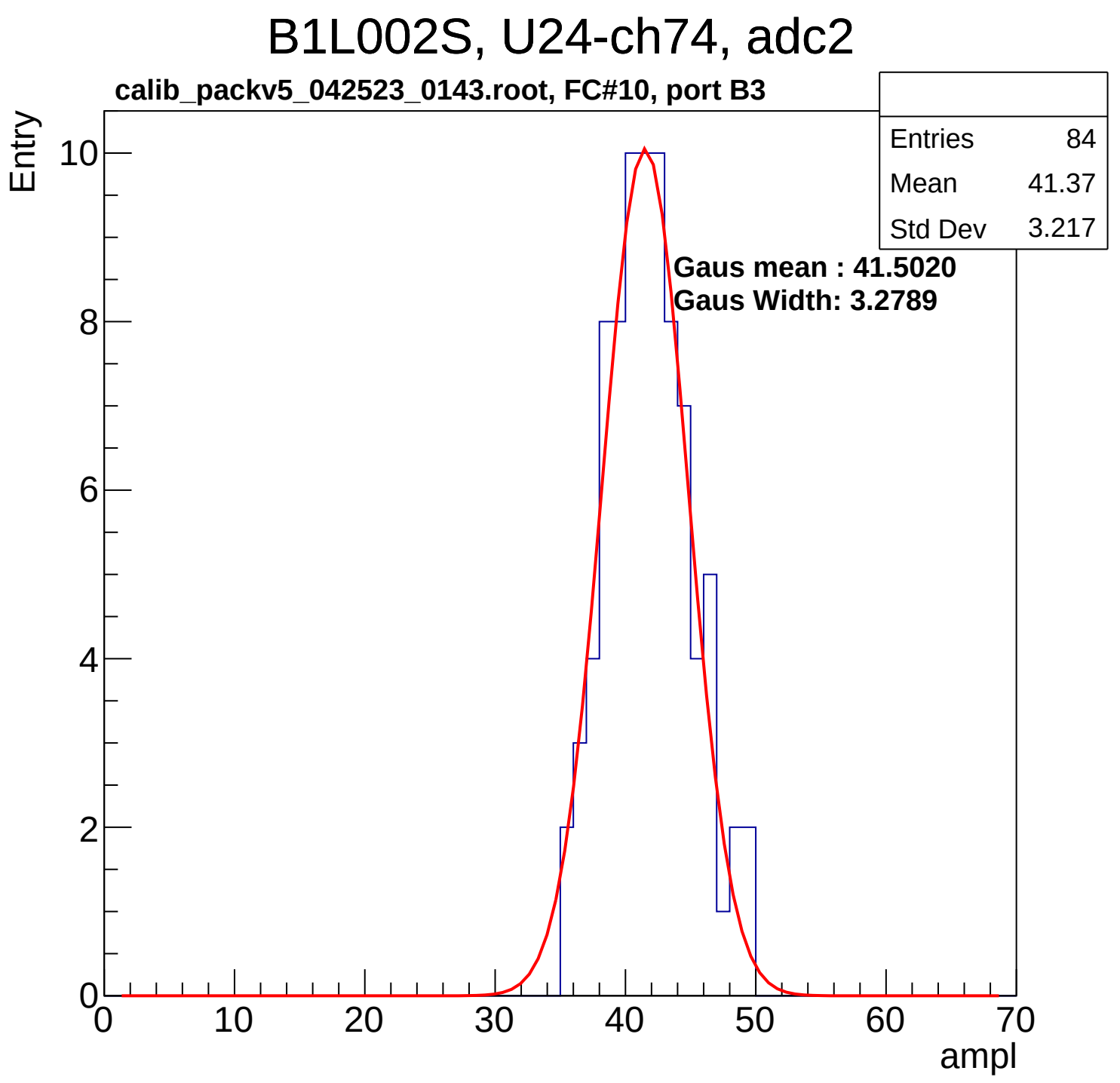
B1L002S, U24-ch74, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 84
Mean 41.37
Std Dev 3.217
Gaus mean : 41.5020
Gaus Width: 3.2789
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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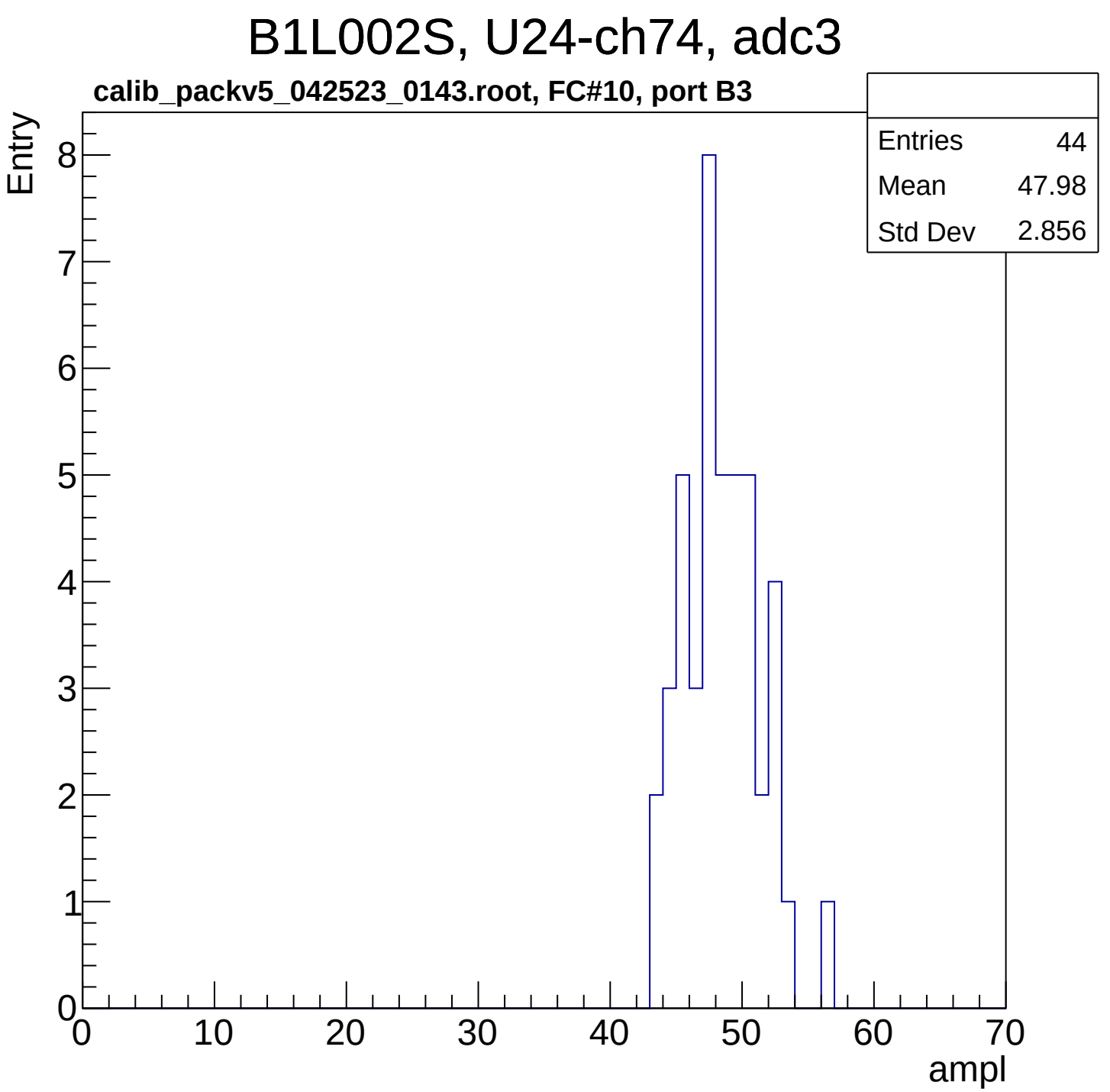
B1L002S, U24-ch74, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 47.98
Std Dev 2.856
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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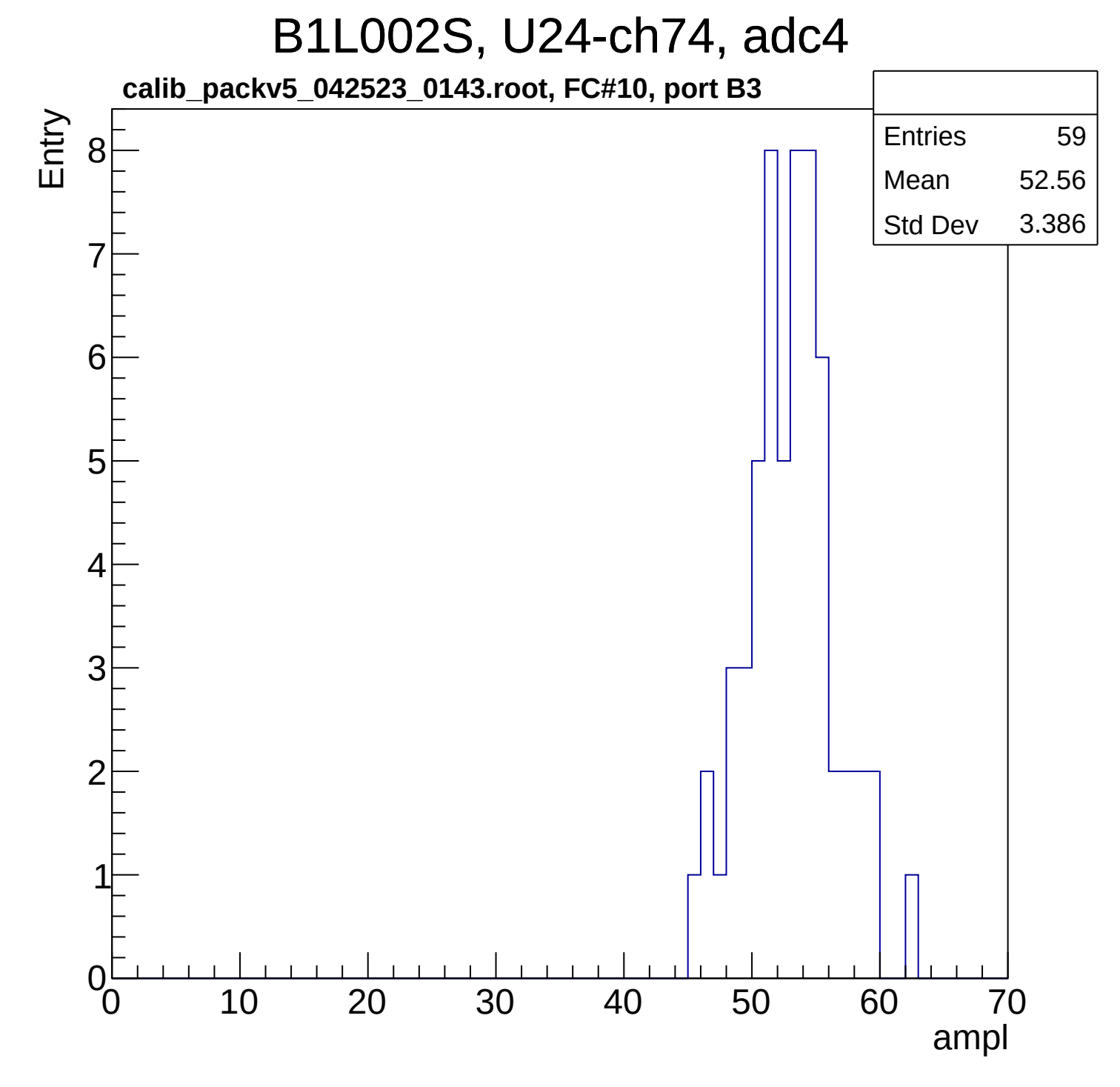
B1L002S, U24-ch74, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.56
Std Dev 3.386
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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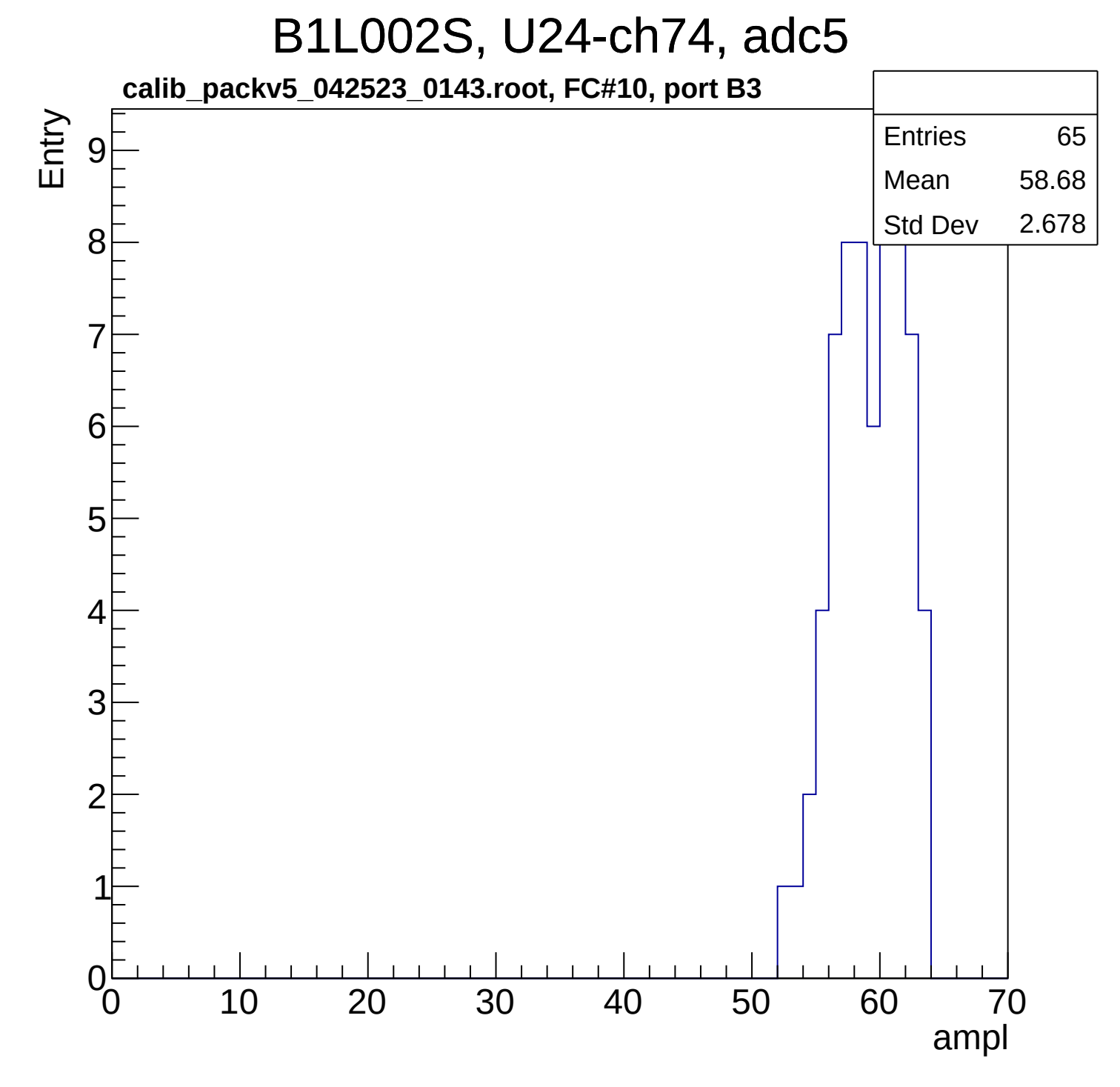
B1L002S, U24-ch74, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 58.68
Std Dev 2.678
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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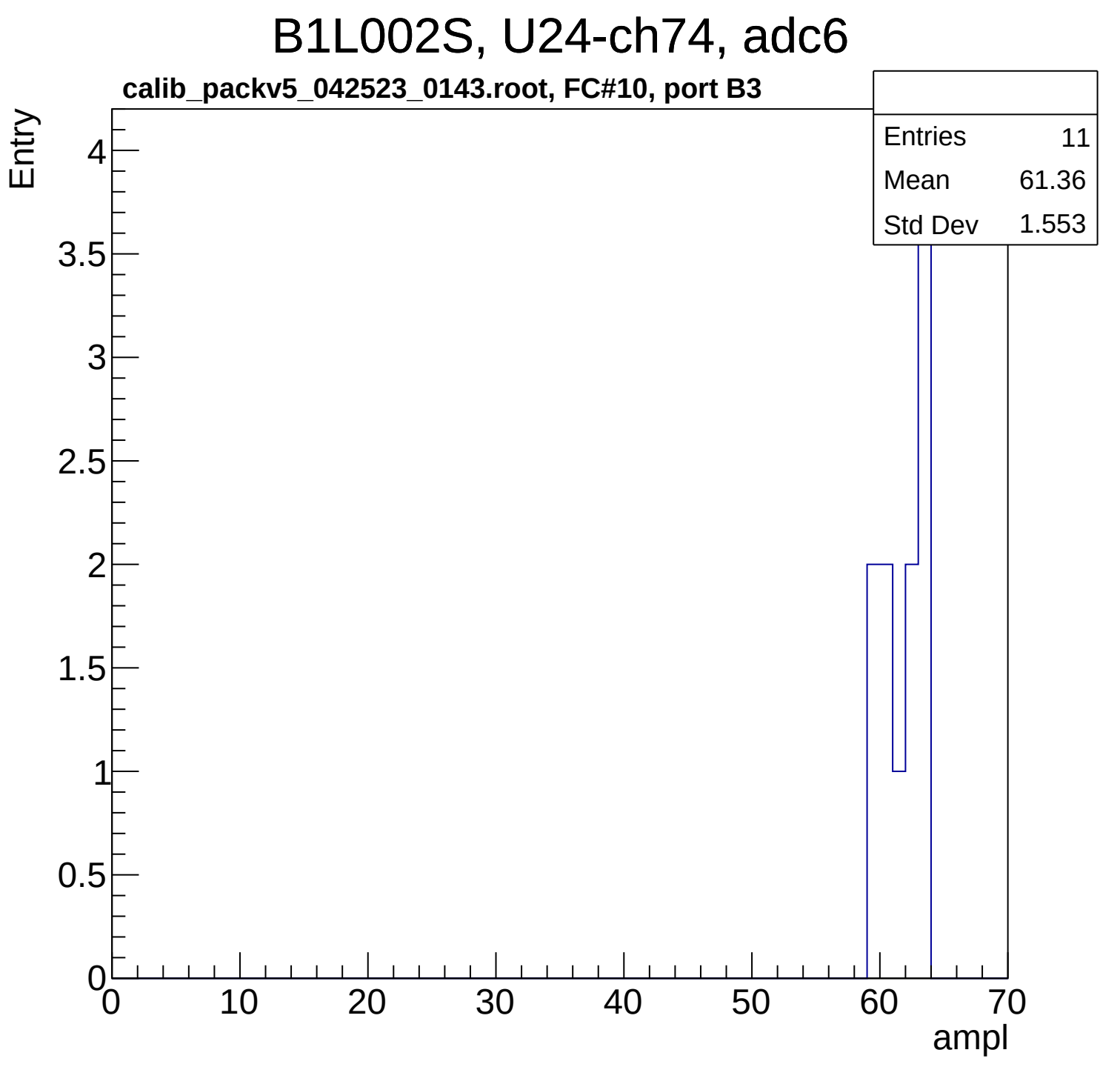
B1L002S, U24-ch74, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.36
Std Dev 1.553
Entry
ampl
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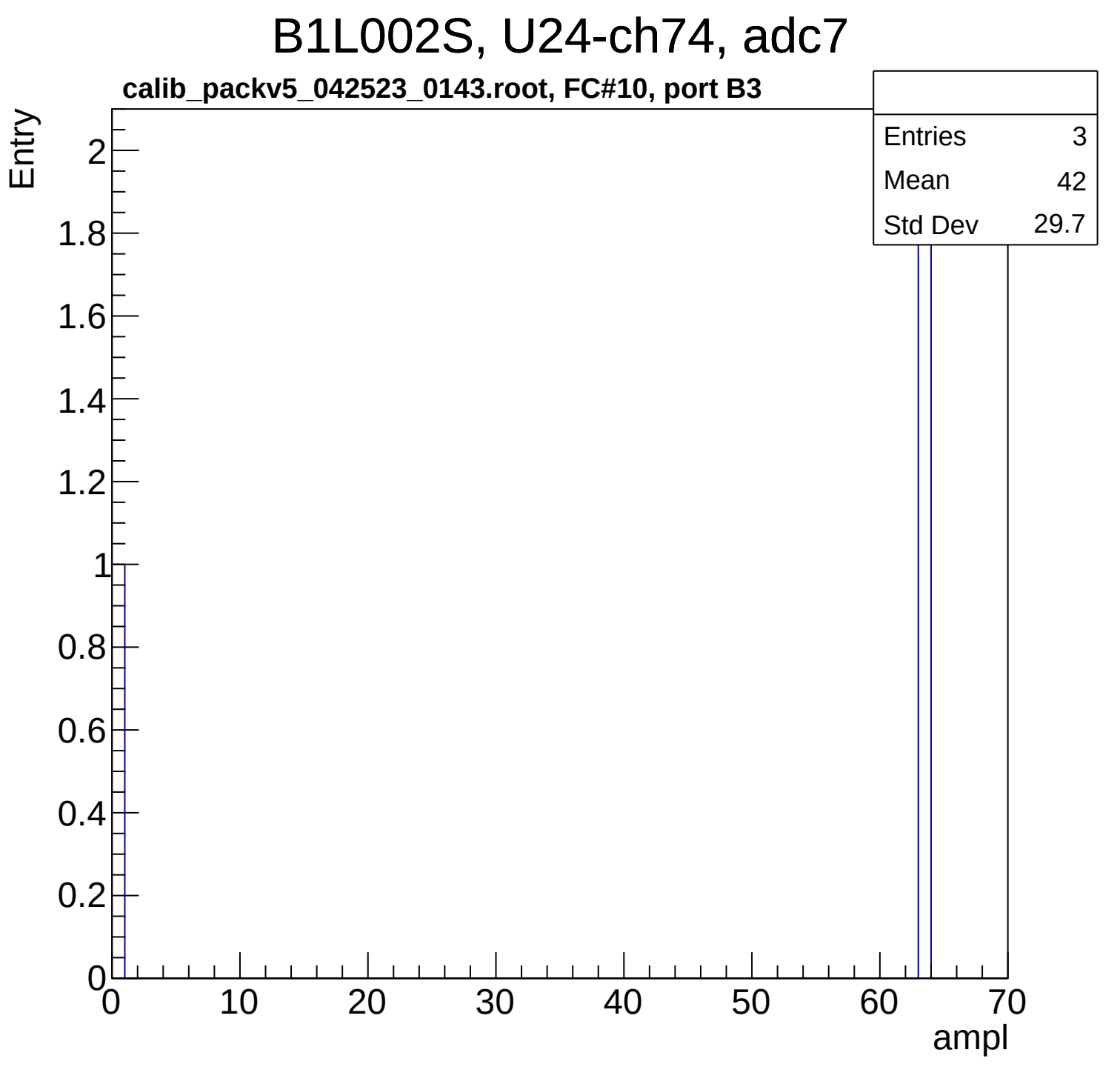
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U24-ch74, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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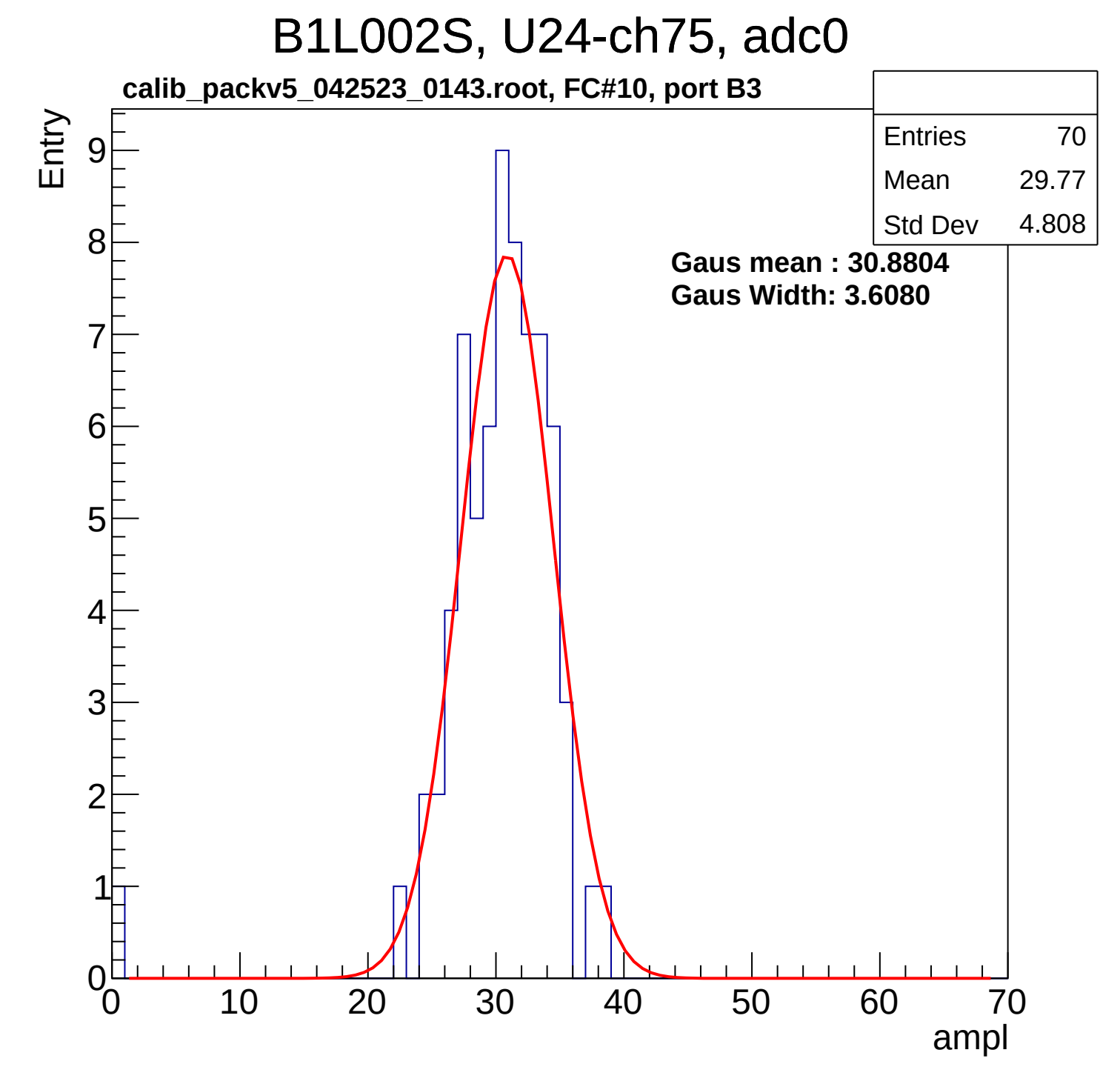
B1L002S, U24-ch75, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 29.77
Std Dev 4.808
Gaus mean : 30.8804
Gaus Width: 3.6080
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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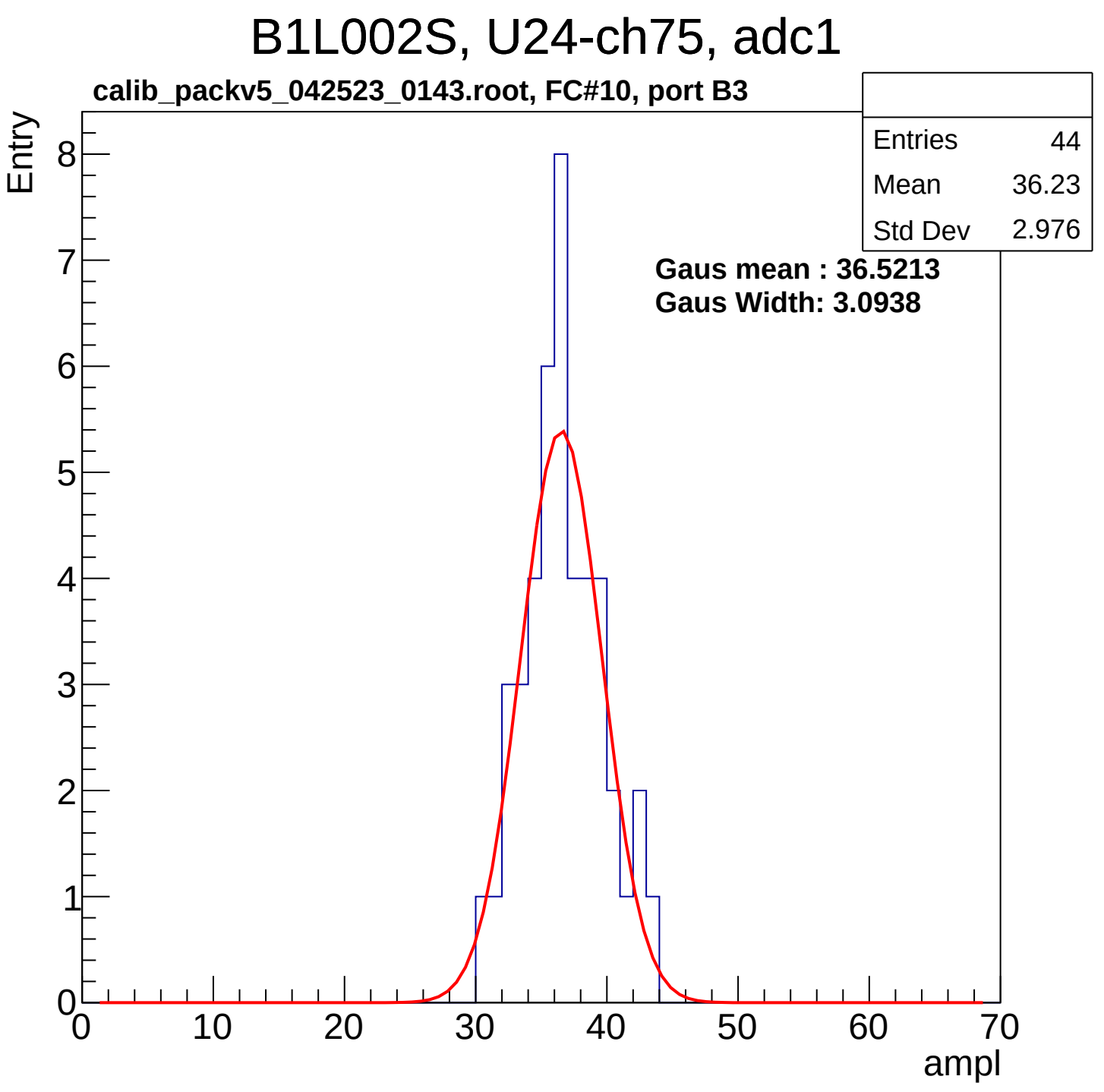
B1L002S, U24-ch75, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 36.23
Std Dev 2.976
Gaus mean : 36.5213
Gaus Width: 3.0938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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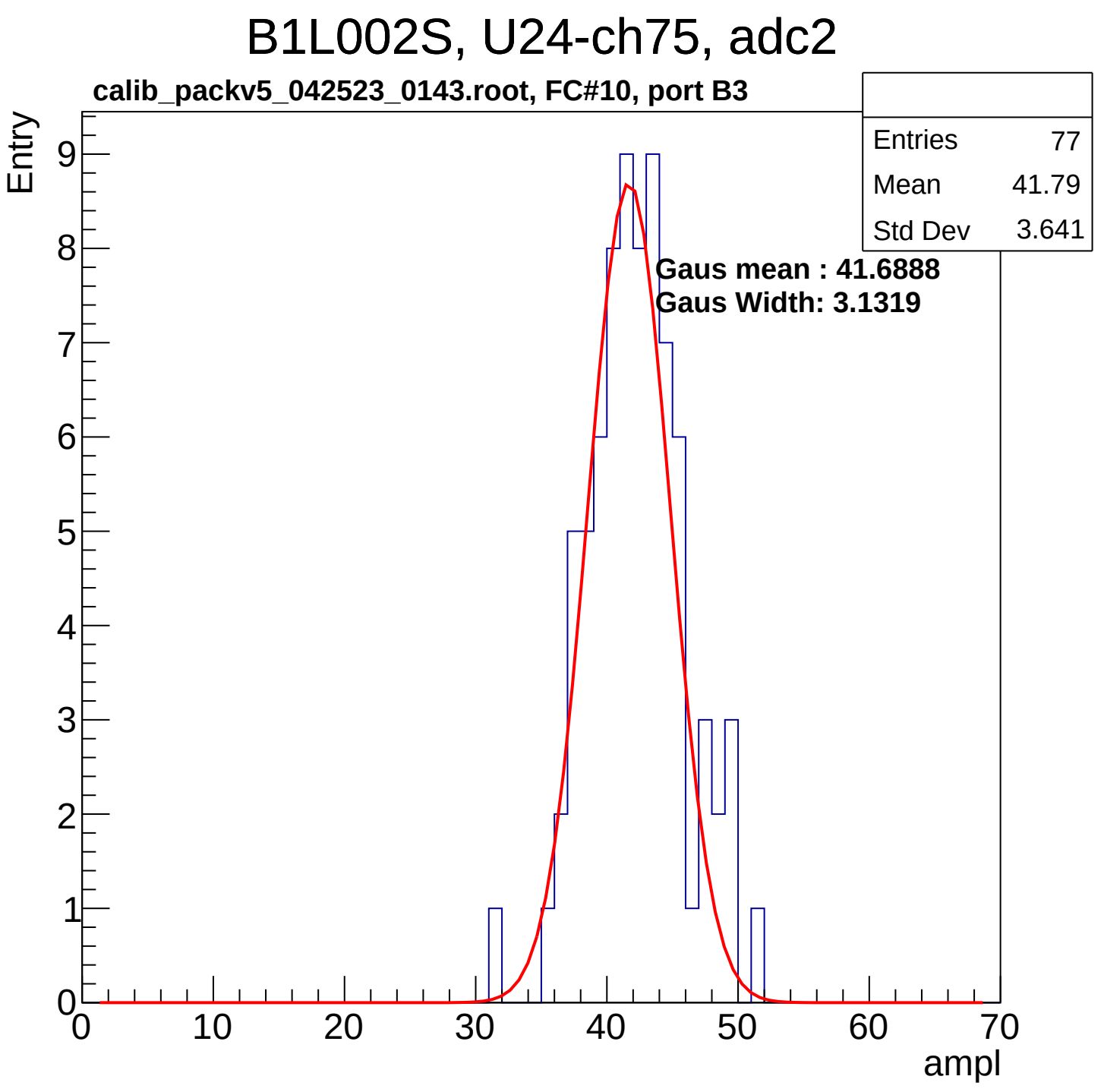
B1L002S, U24-ch75, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 41.79
Std Dev 3.641
Gaus mean : 41.6888
Gaus Width: 3.1319
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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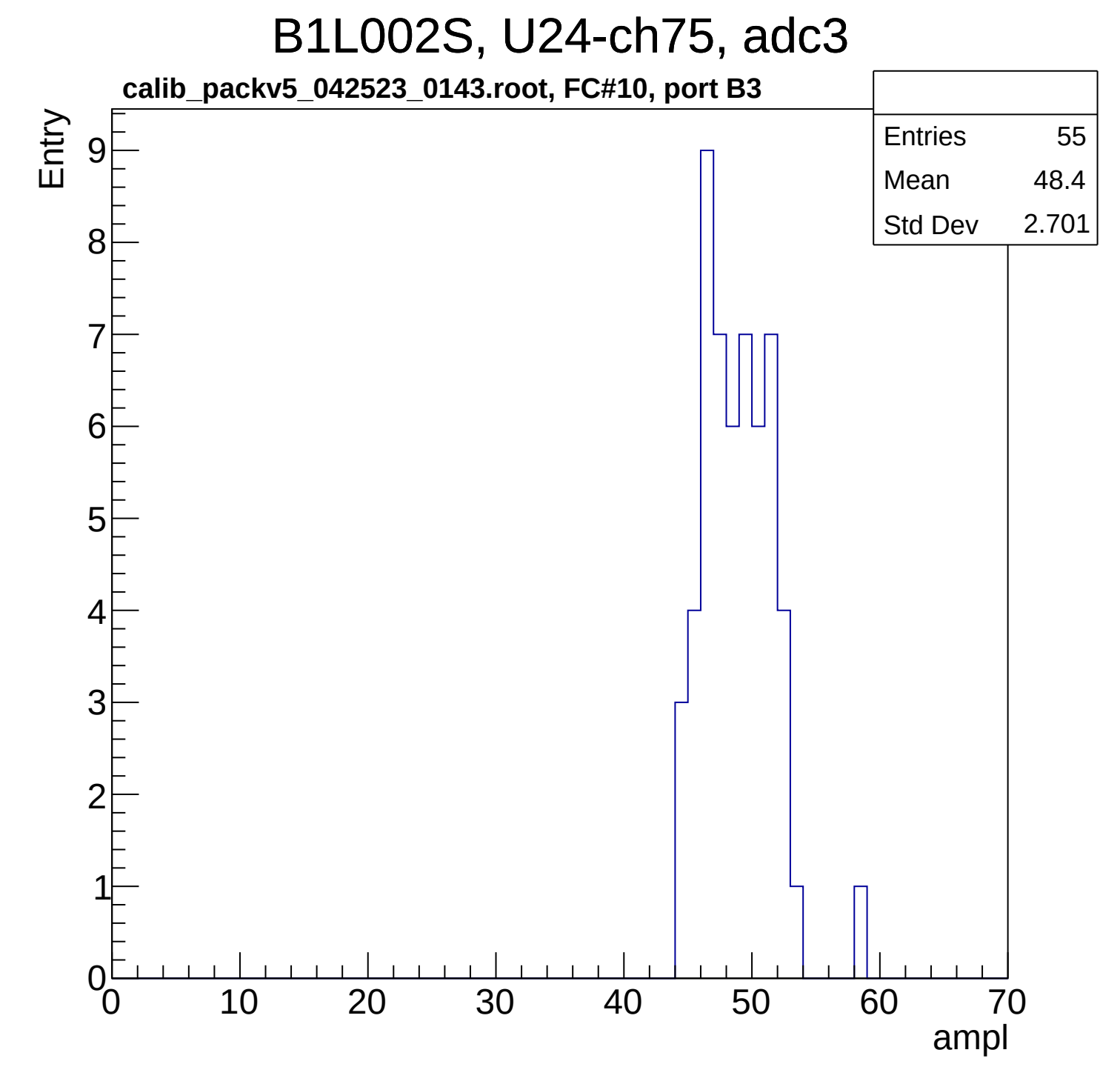
B1L002S, U24-ch75, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.4
Std Dev 2.701
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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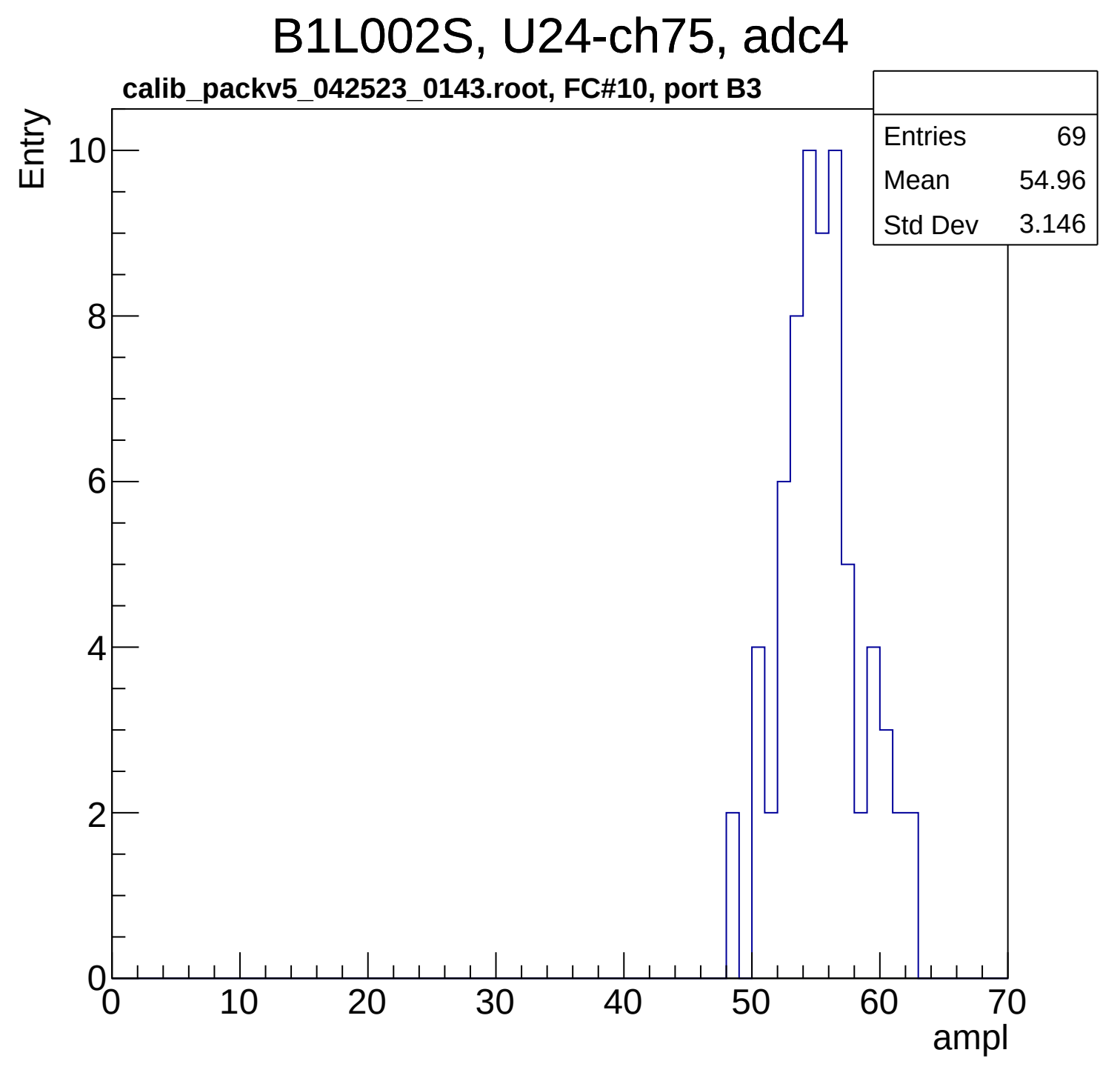
B1L002S, U24-ch75, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 54.96
Std Dev 3.146
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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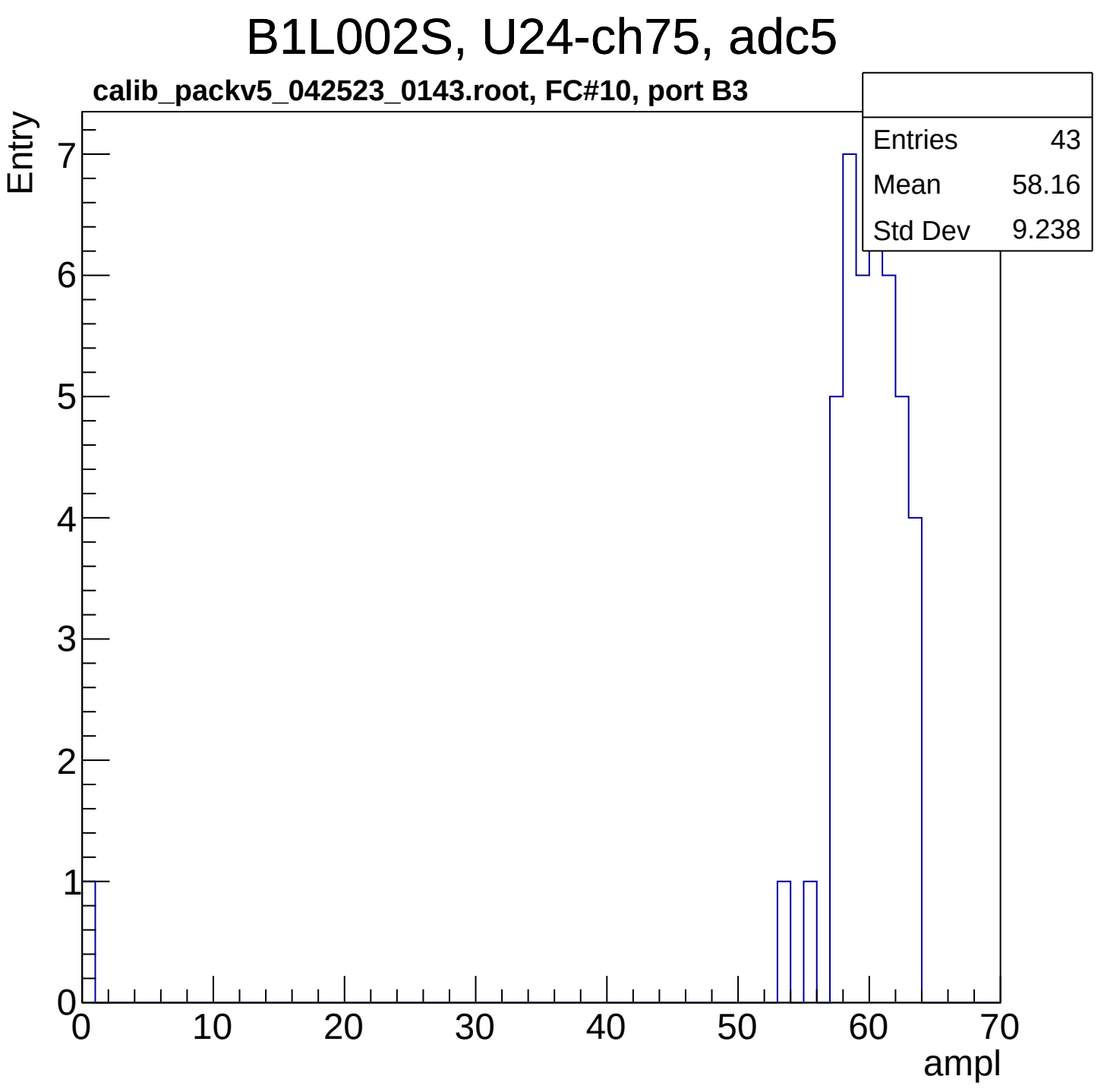
B1L002S, U24-ch75, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 58.16
Std Dev 9.238
Entry
ampl
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2
3
4
5
6
7
0
10
20
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40
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60
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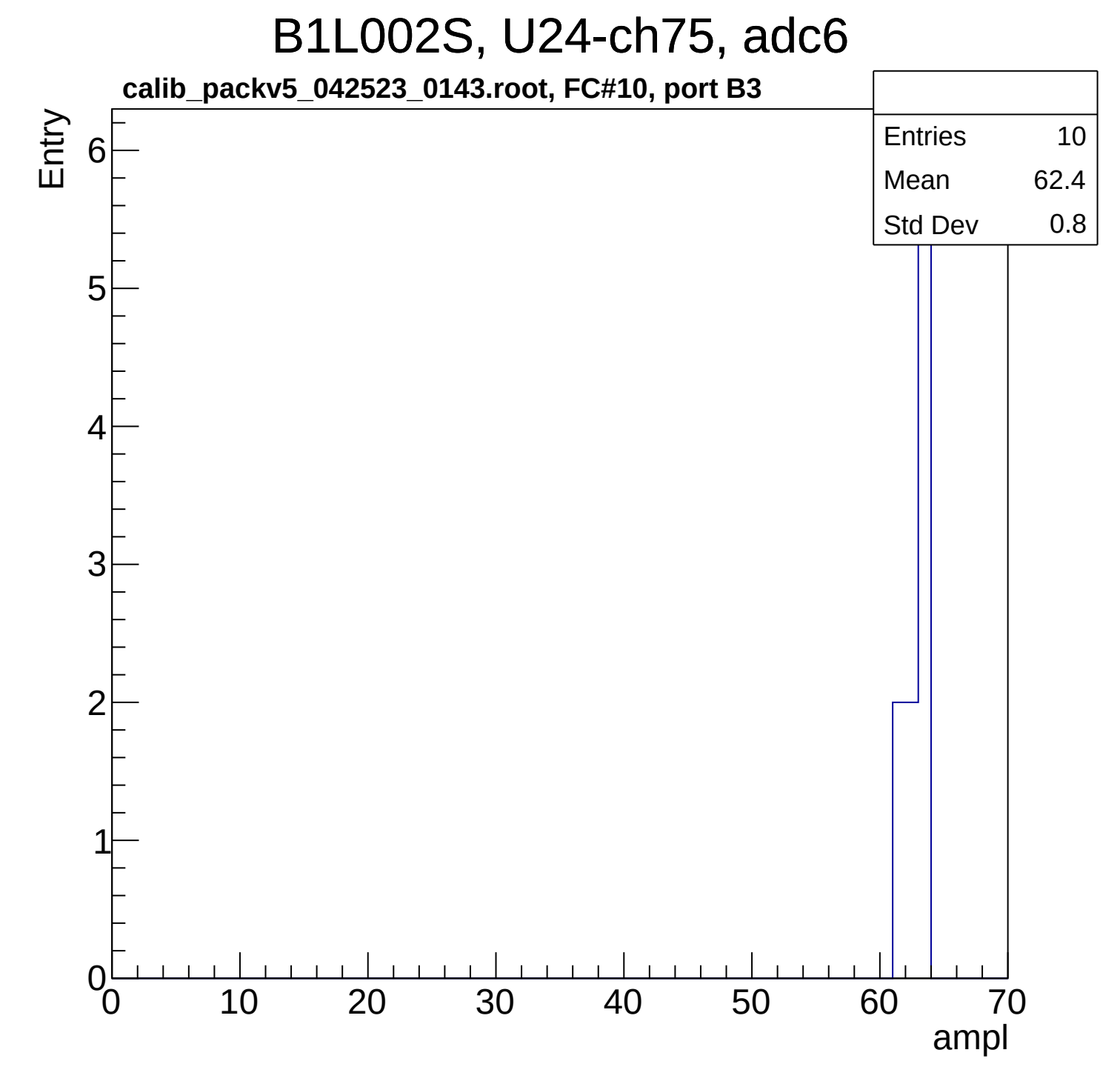
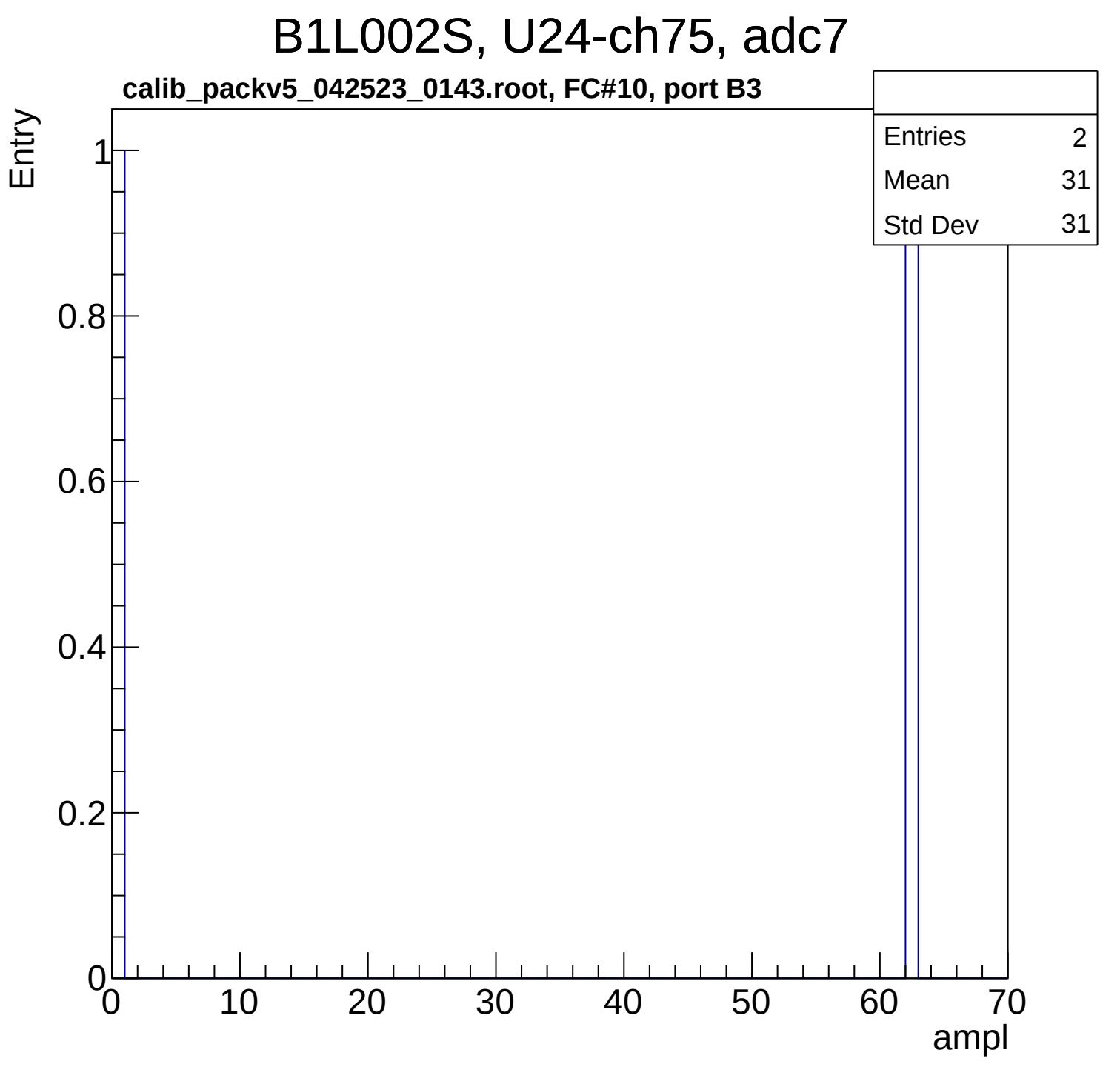

B1L002S, U24-ch75, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 62.4
Std Dev 0.8
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch75, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31
Std Dev 31
Entry
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0.8
0.6
0.4
0.2
0
0
10
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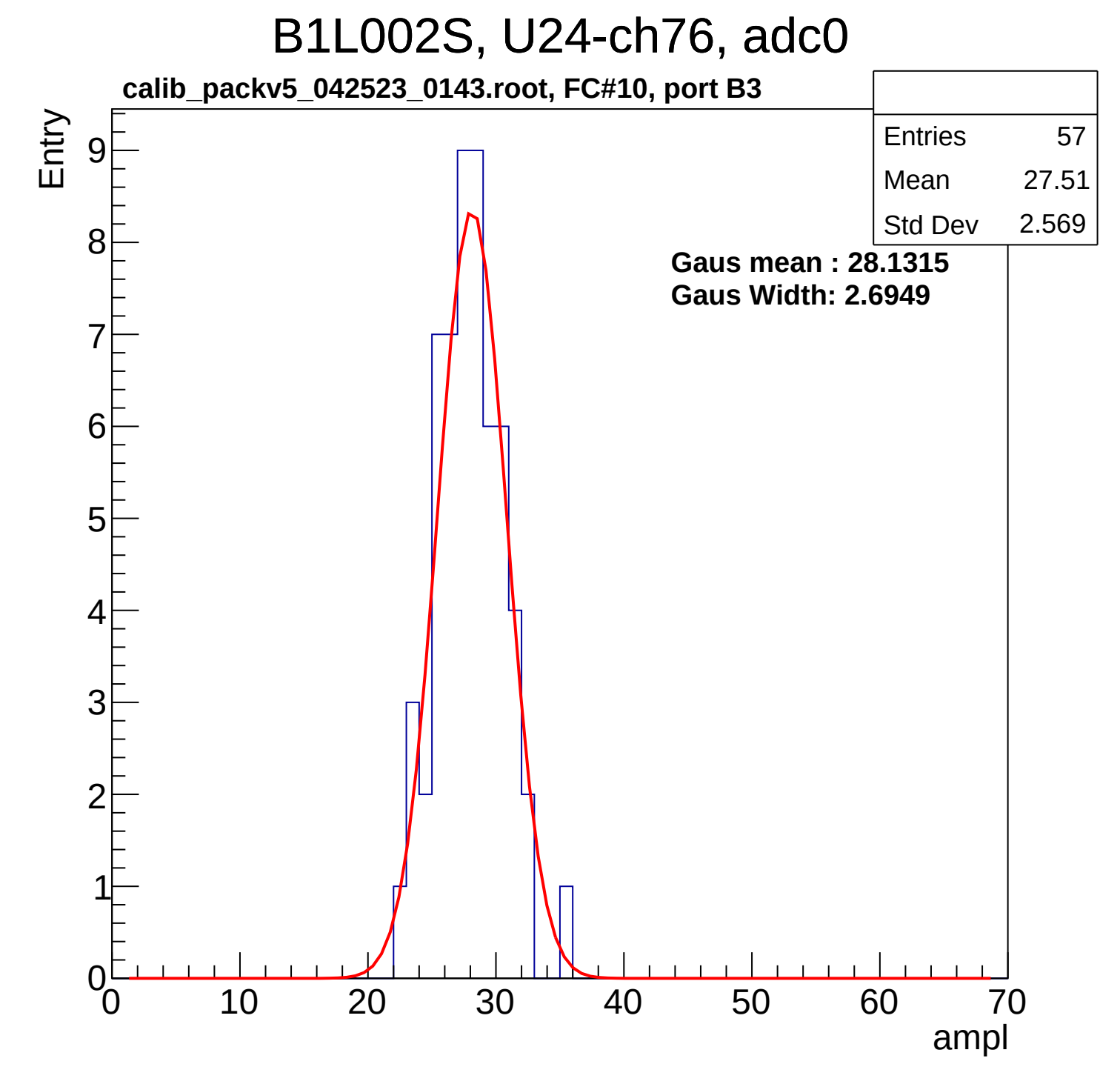
50
60
70
ampl


B1L002S, U24-ch76, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.51
Std Dev 2.569
Gaus mean : 28.1315
Gaus Width: 2.6949
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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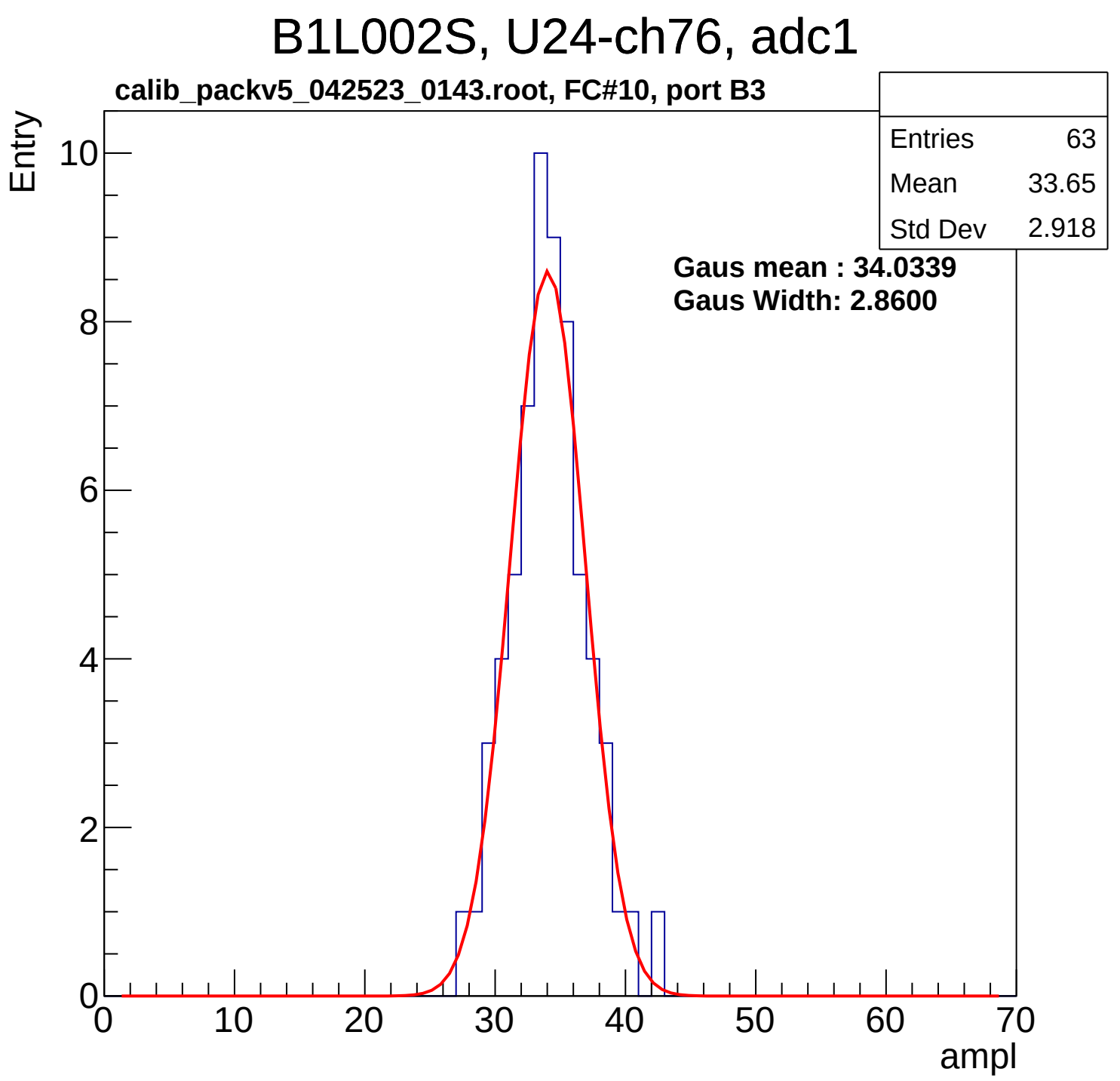
B1L002S, U24-ch76, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.65
Std Dev 2.918
Gaus mean : 34.0339
Gaus Width: 2.8600
Entry
ampl
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2
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6
8
10
0
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20
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40
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60
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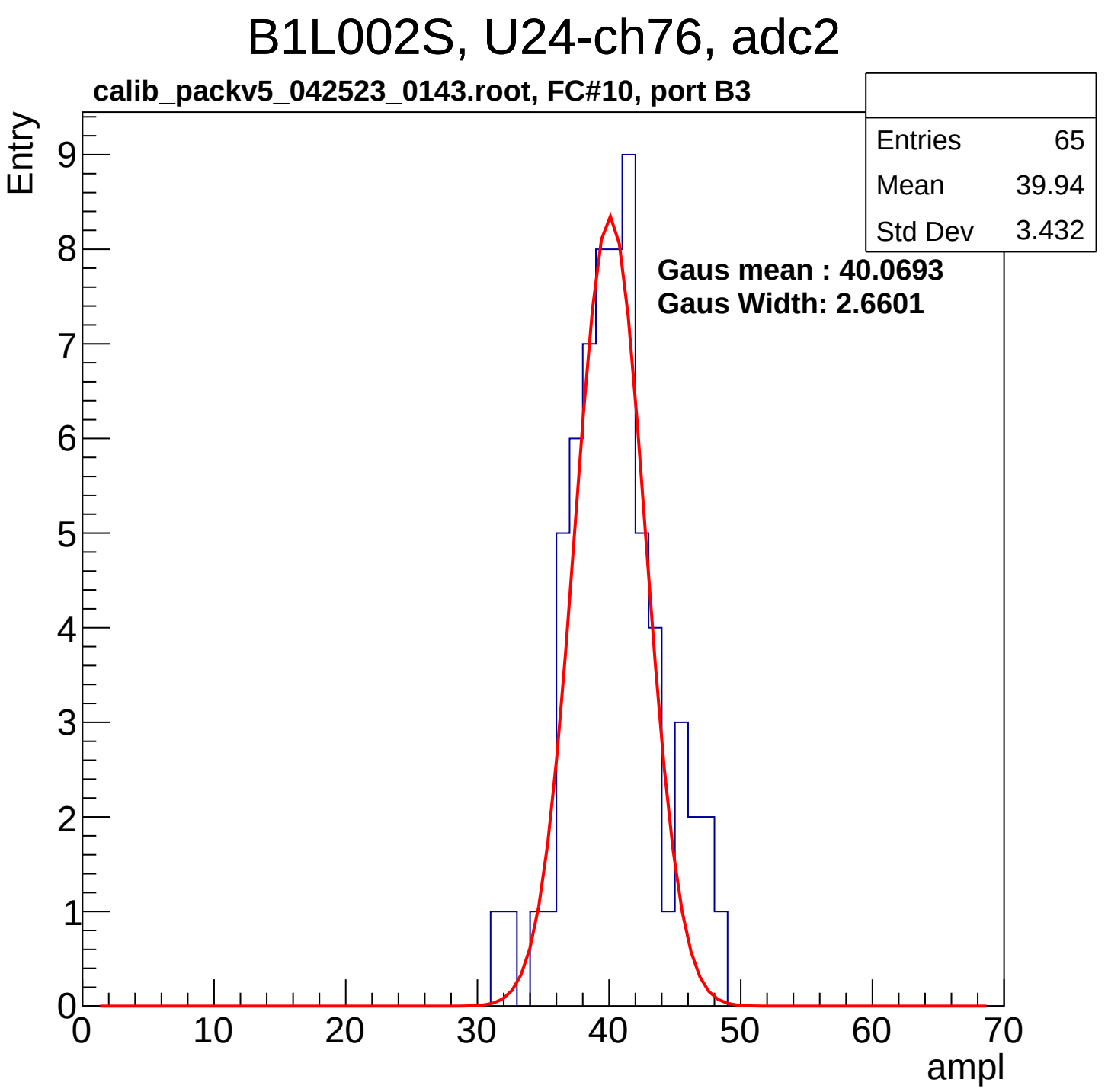
B1L002S, U24-ch76, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 39.94
Std Dev 3.432
Gaus mean : 40.0693
Gaus Width: 2.6601
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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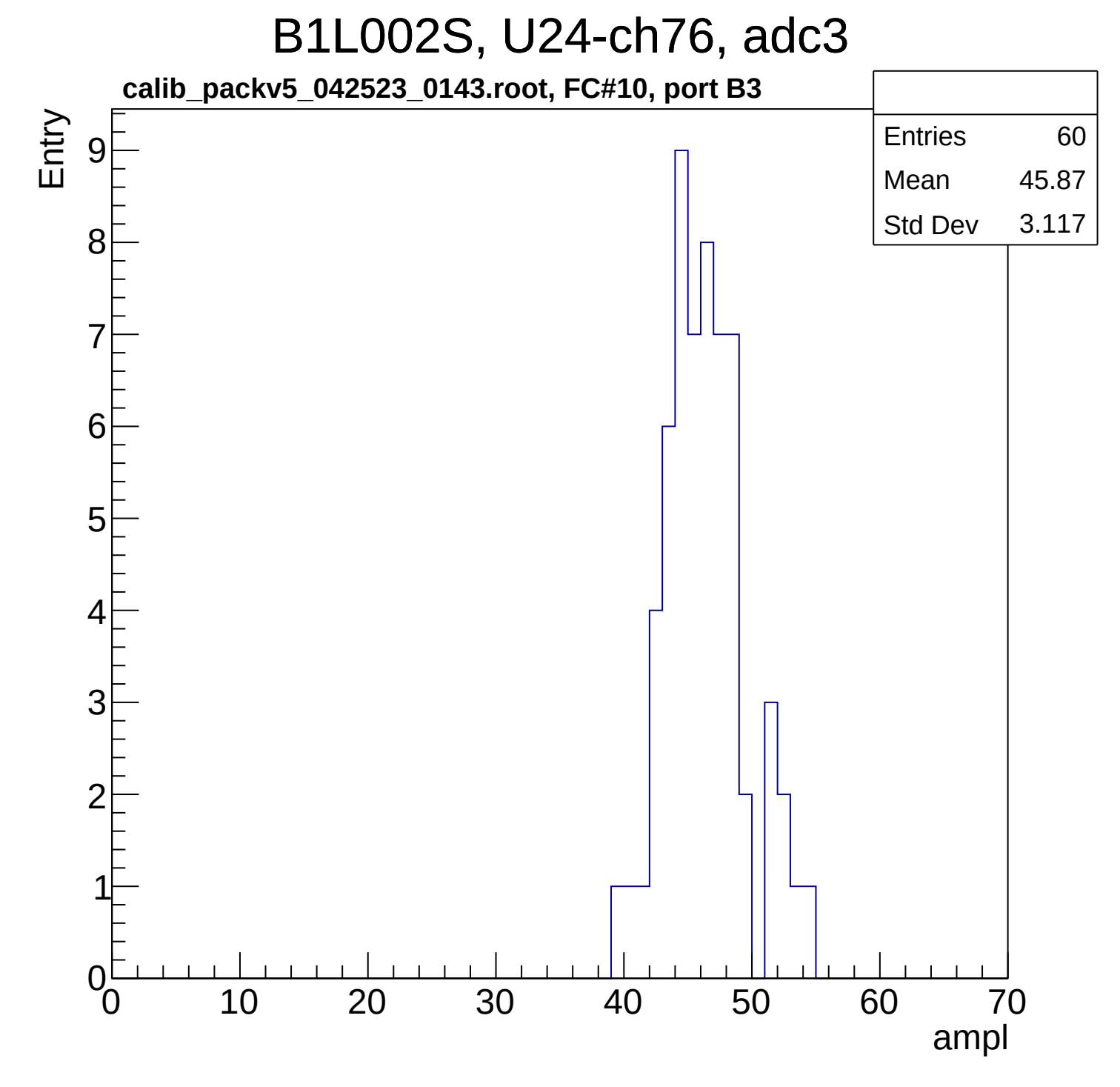
B1L002S, U24-ch76, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 45.87
Std Dev 3.117
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
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60
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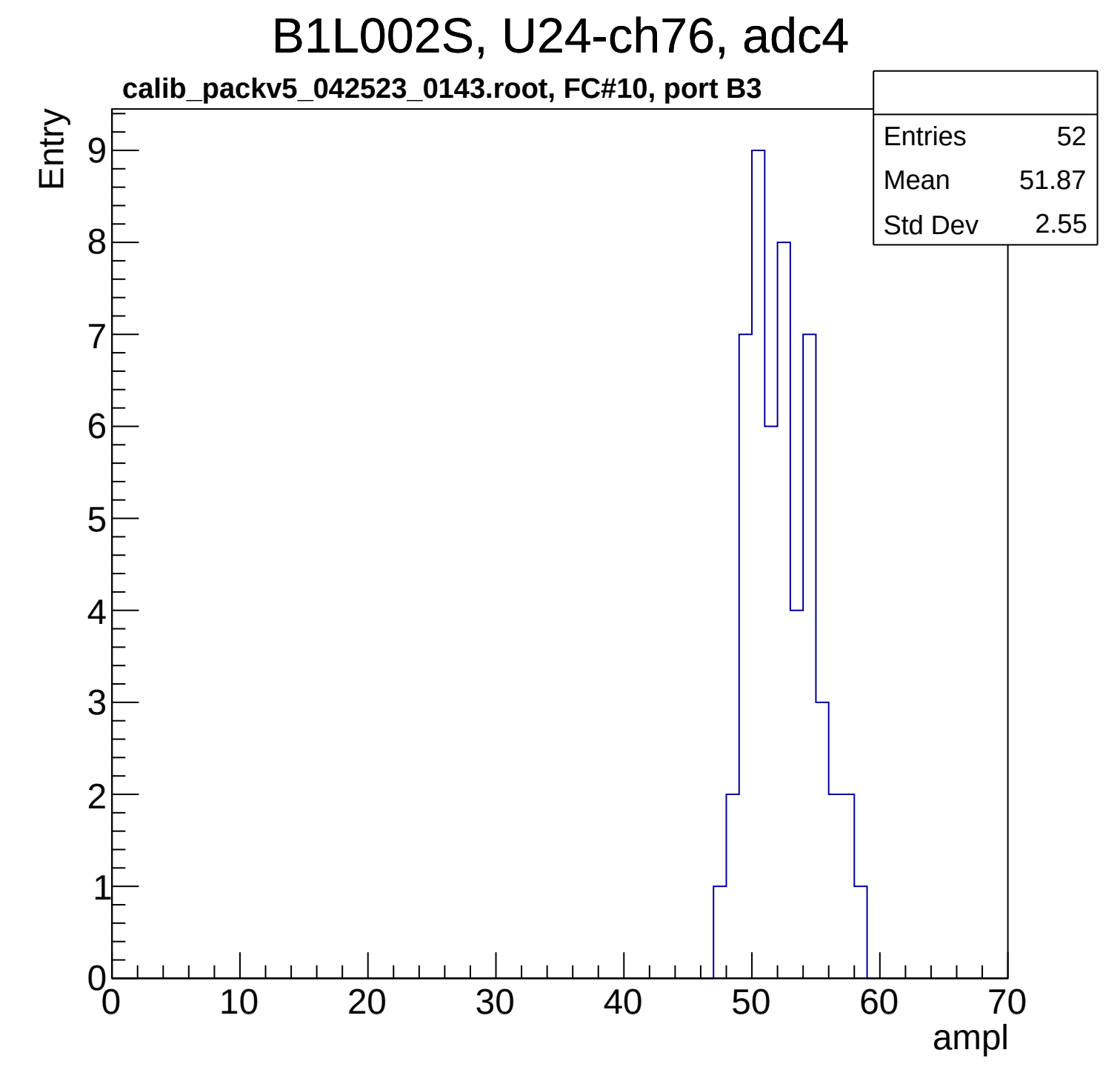
B1L002S, U24-ch76, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 51.87
Std Dev 2.55
Entry
ampl
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3
4
5
6
7
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0
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60
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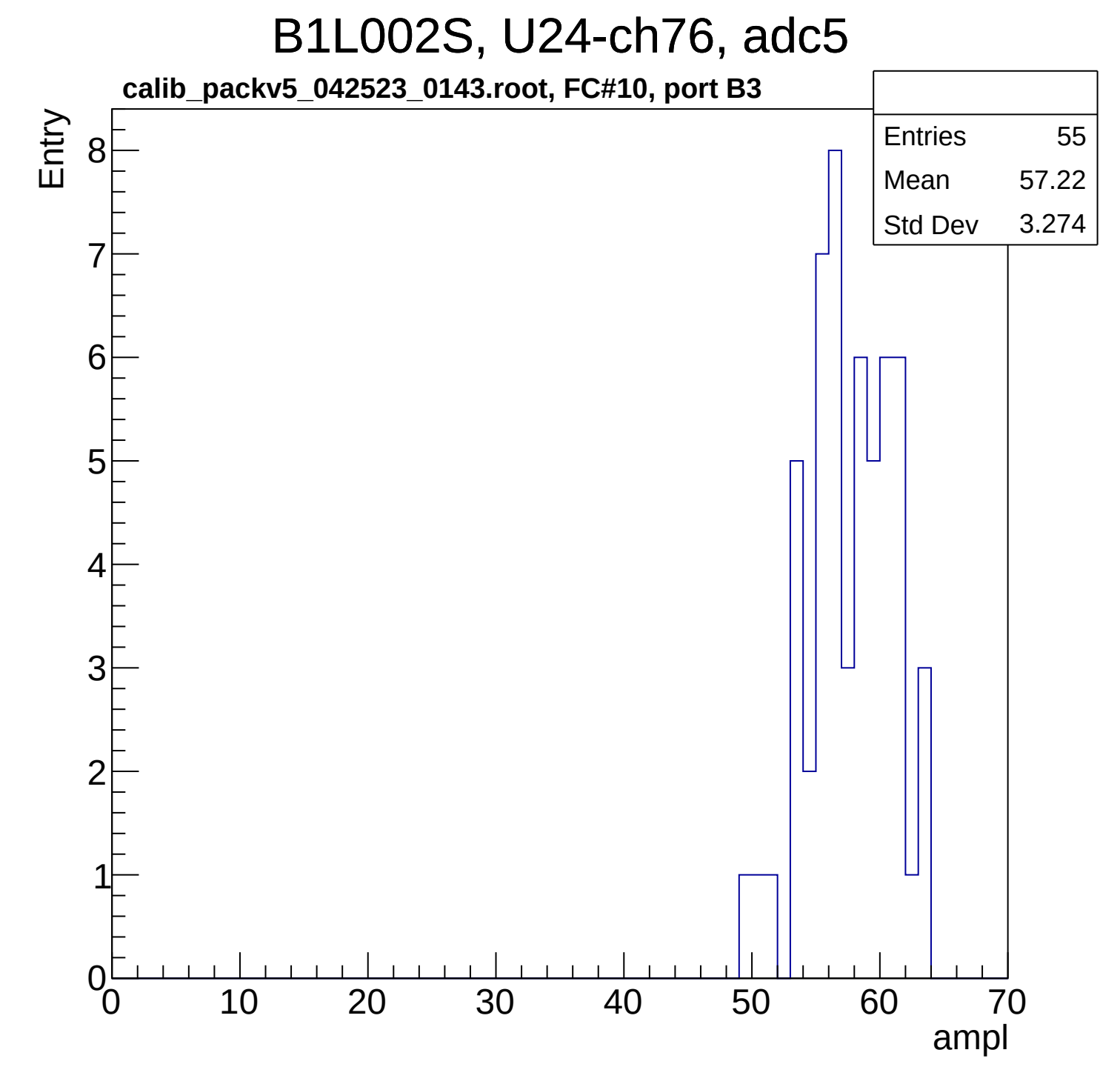
B1L002S, U24-ch76, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 57.22
Std Dev 3.274
Entry
ampl
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0
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60
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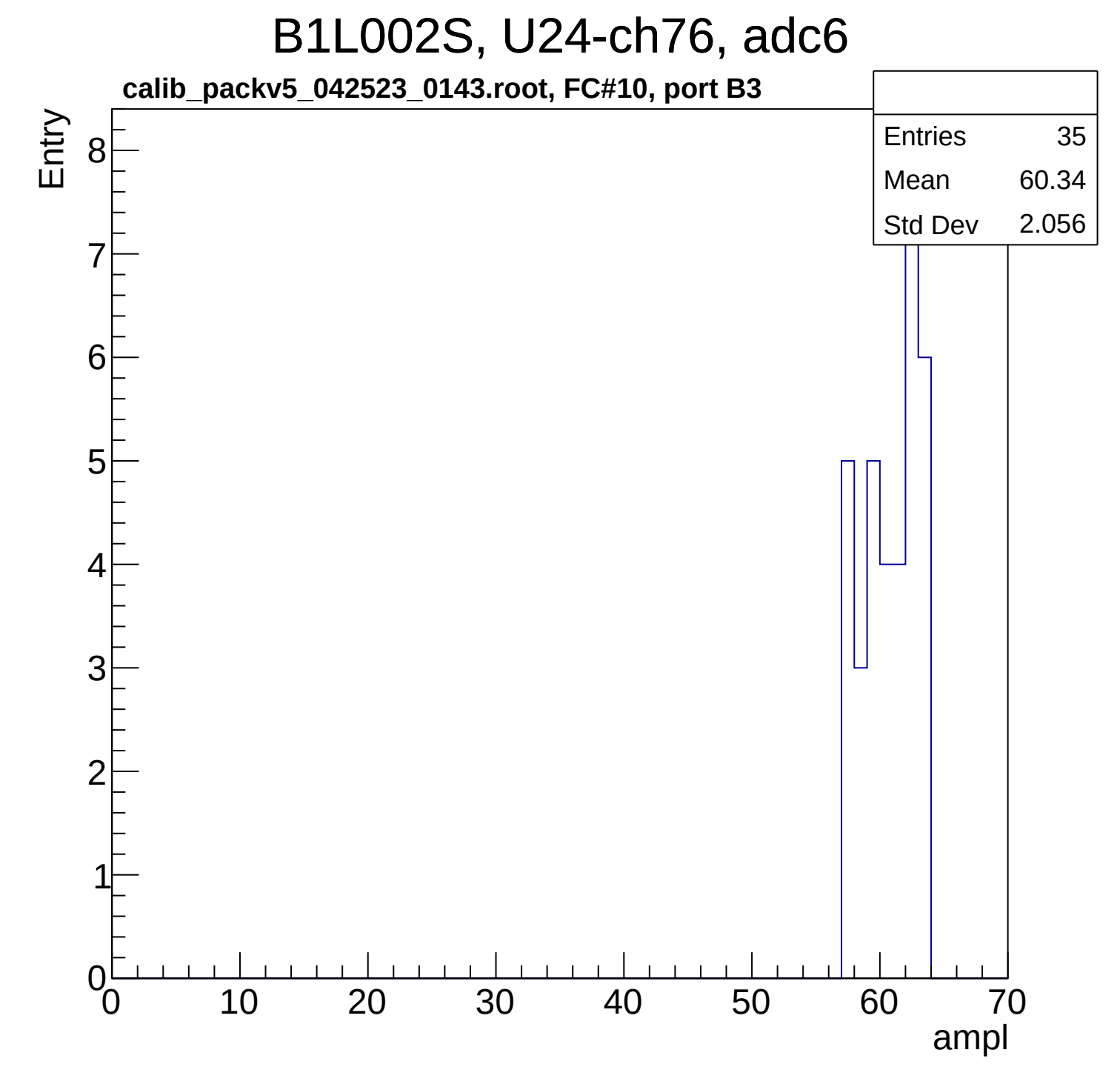
B1L002S, U24-ch76, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 35
Mean 60.34
Std Dev 2.056
Entry
ampl
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3
4
5
6
7
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0
10
20
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40
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60
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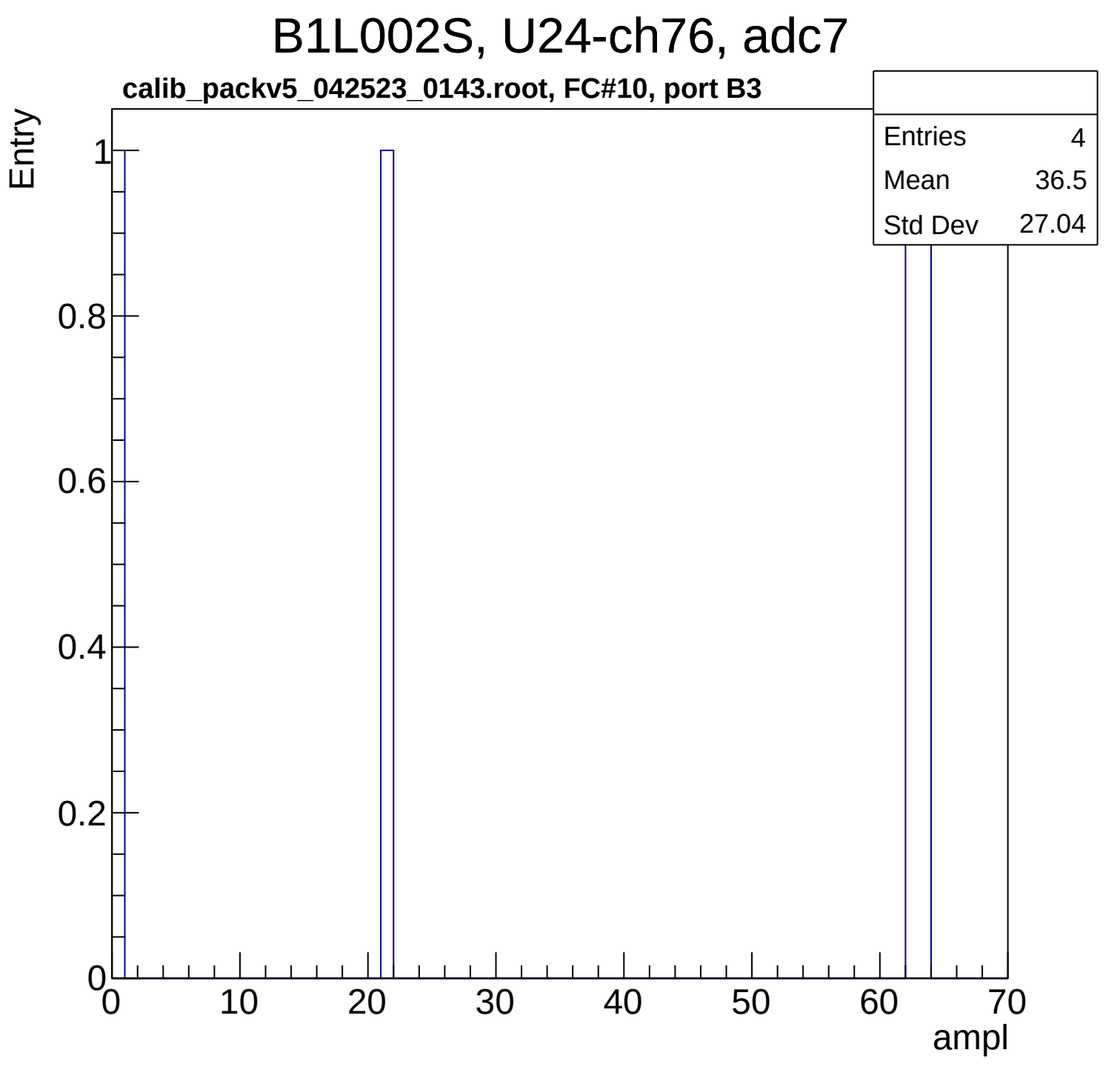
B1L002S, U24-ch76, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 36.5
Std Dev 27.04
Entry
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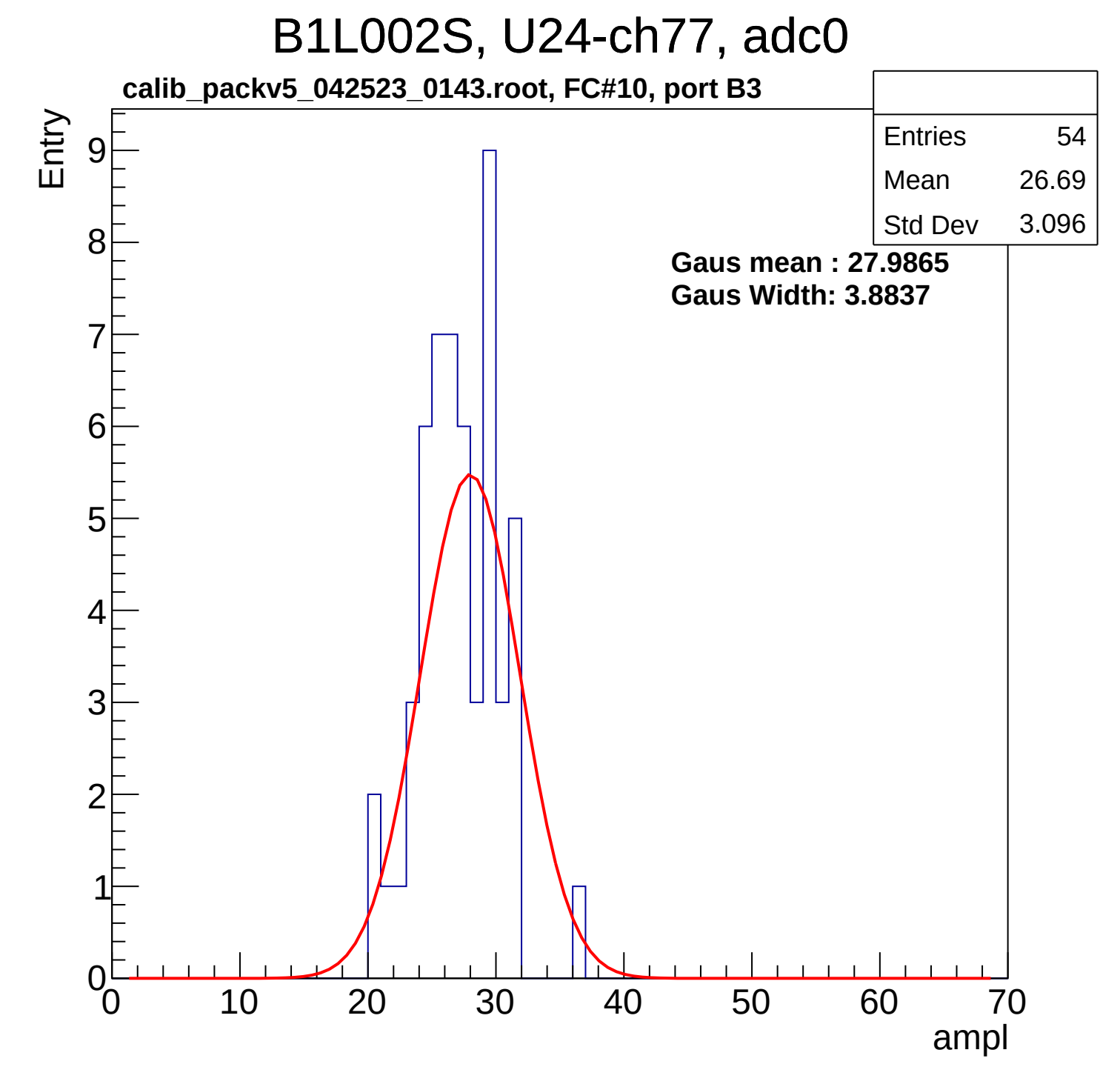
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch77, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 26.69
Std Dev 3.096
Gaus mean : 27.9865
Gaus Width: 3.8837
Entry
ampl
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3
4
5
6
7
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0
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60
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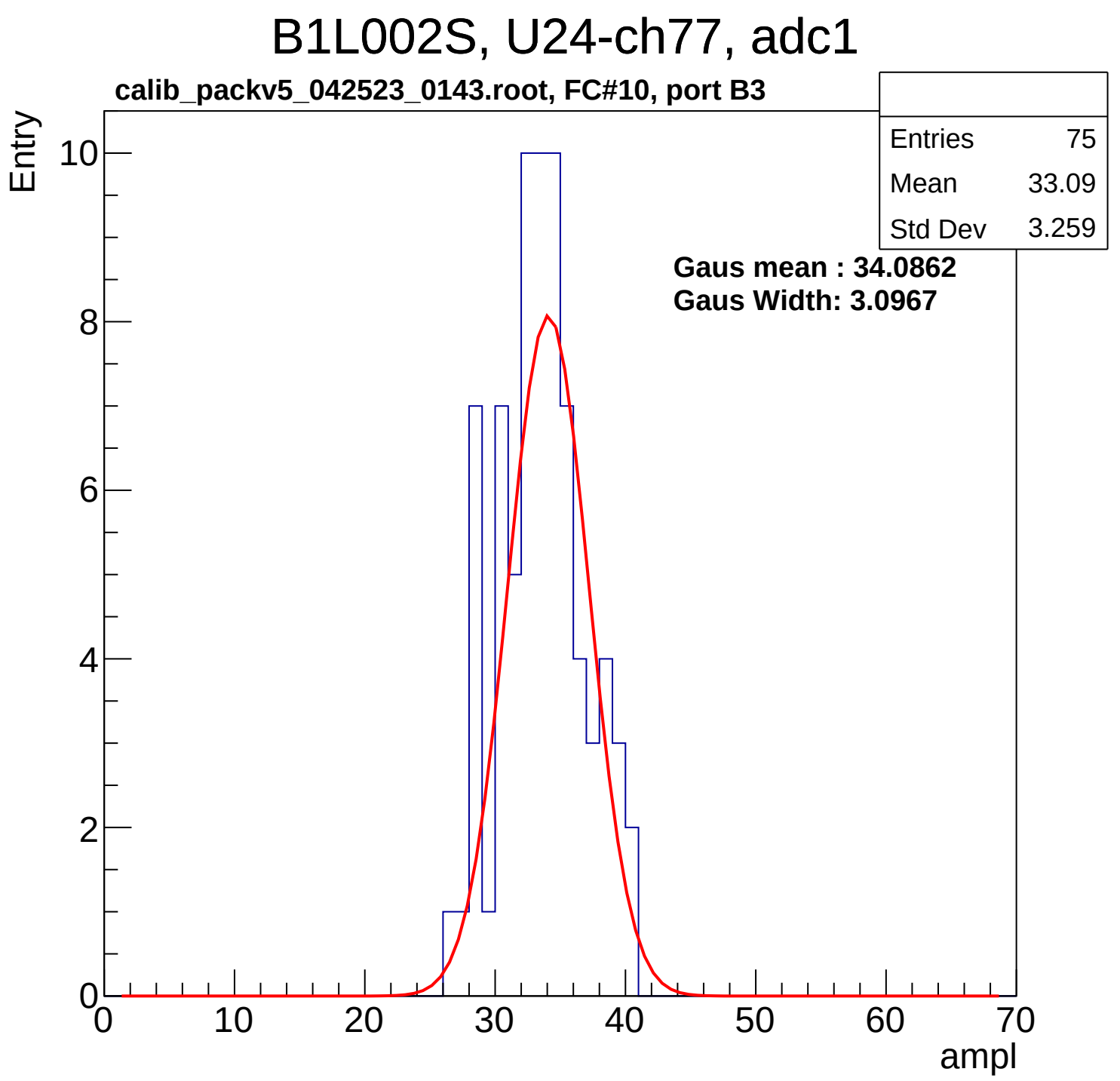
B1L002S, U24-ch77, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 33.09
Std Dev 3.259
Gaus mean : 34.0862
Gaus Width: 3.0967
Entry
ampl
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4
6
8
10
0
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20
30
40
50
60
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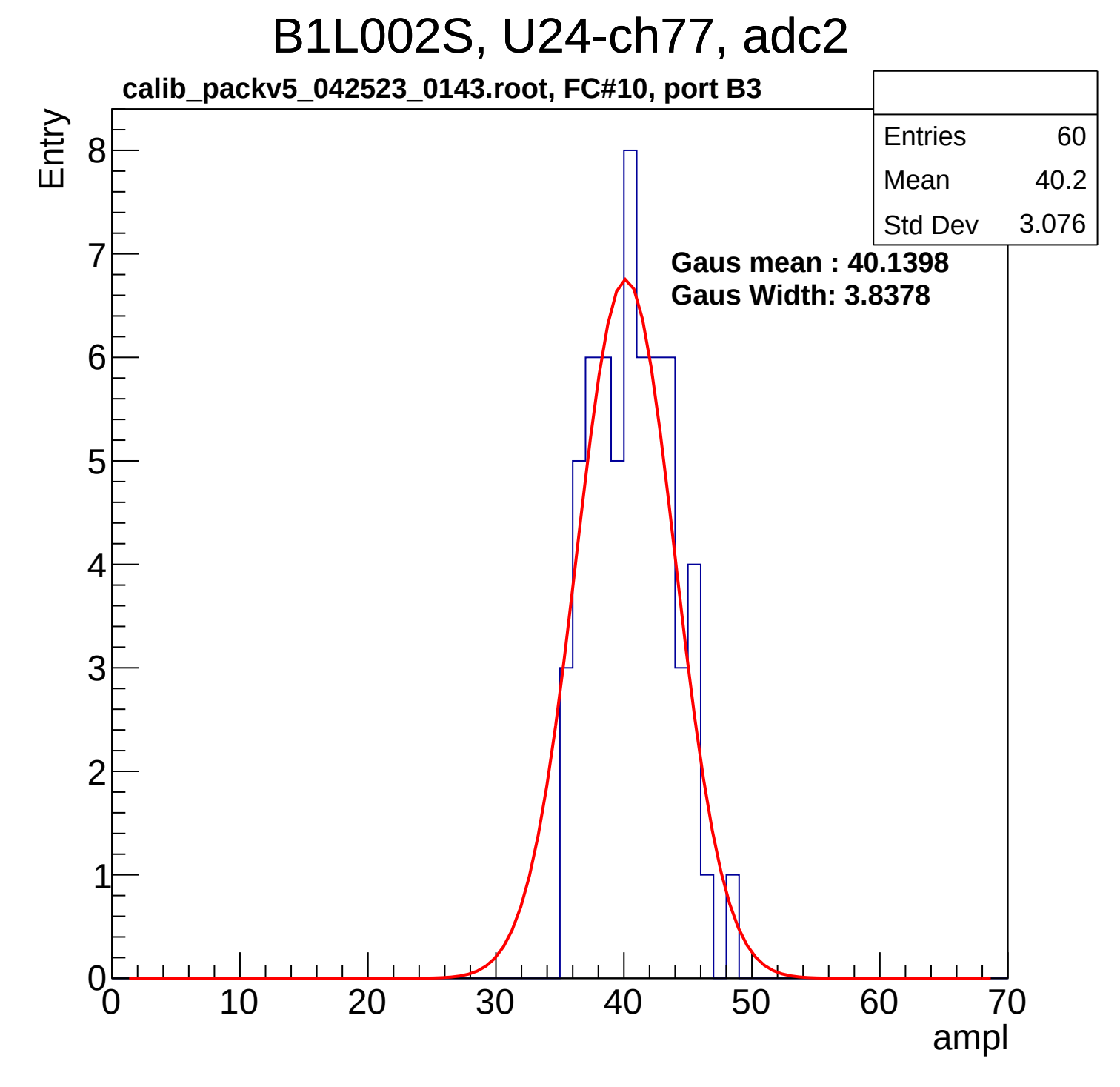
B1L002S, U24-ch77, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.2
Std Dev 3.076
Gaus mean : 40.1398
Gaus Width: 3.8378
Entry
ampl
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4
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0
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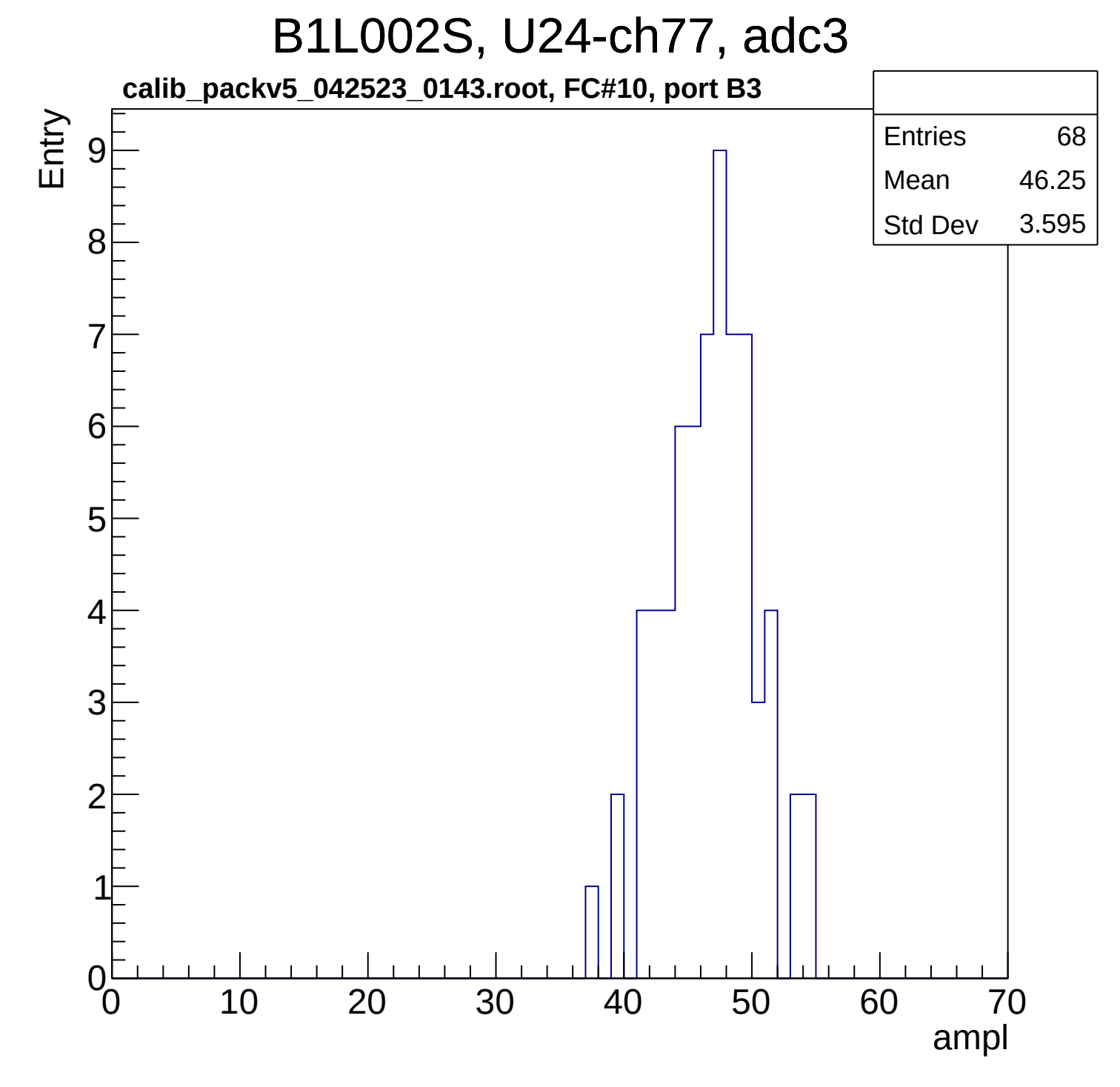
B1L002S, U24-ch77, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.25
Std Dev 3.595
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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60
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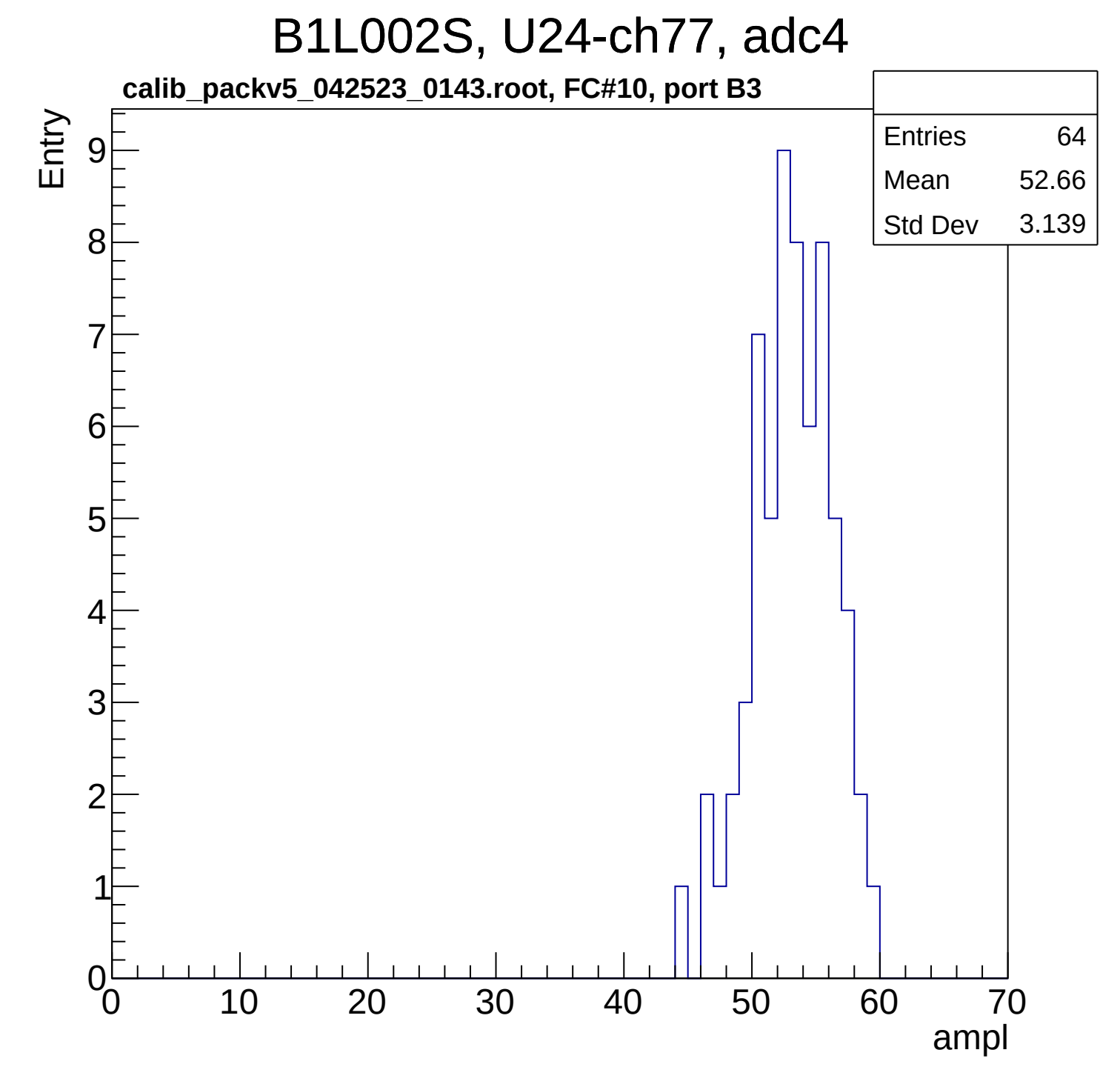
B1L002S, U24-ch77, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.66
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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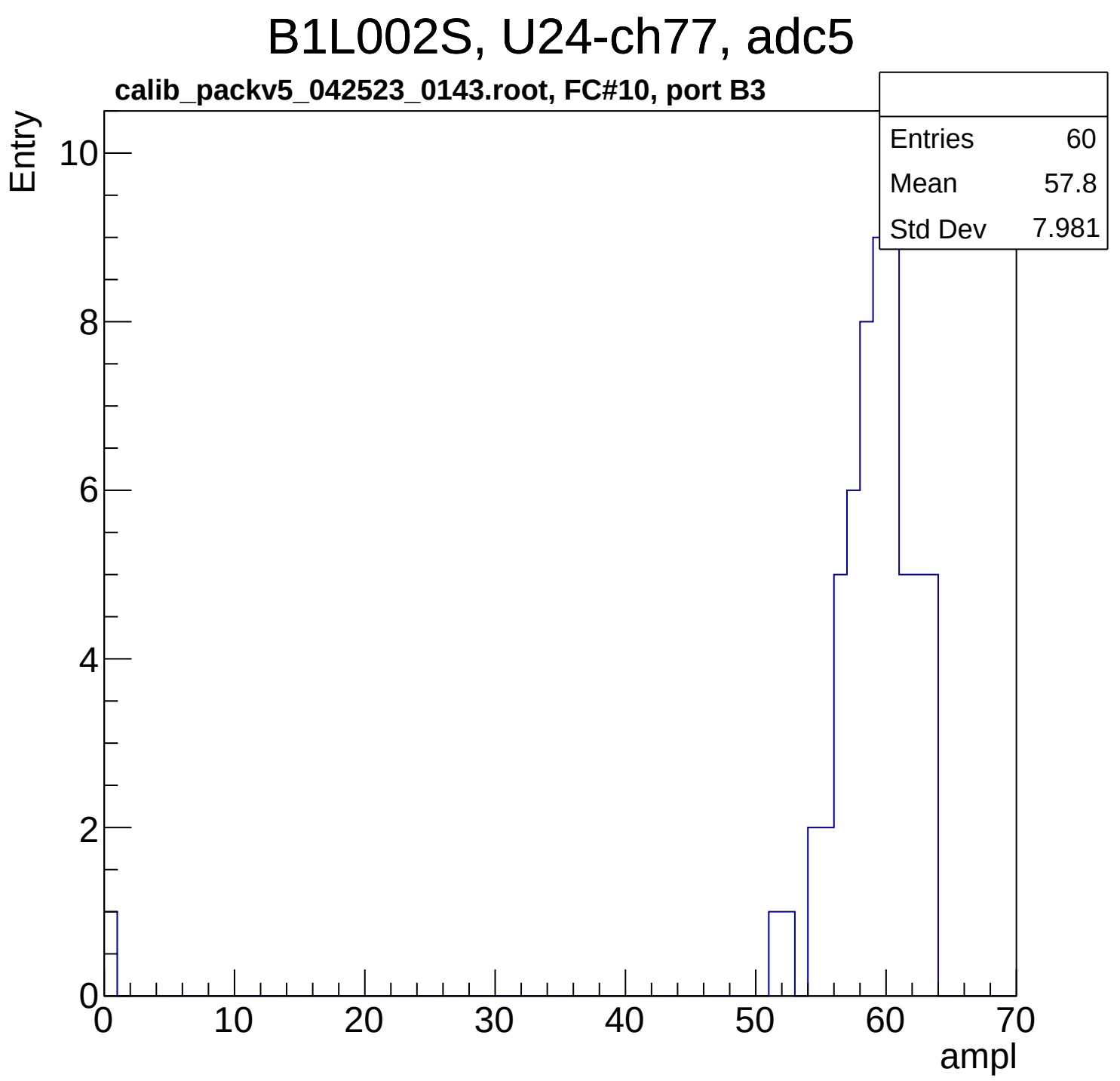
B1L002S, U24-ch77, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.8
Std Dev 7.981
Entry
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2
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6
8
10
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10
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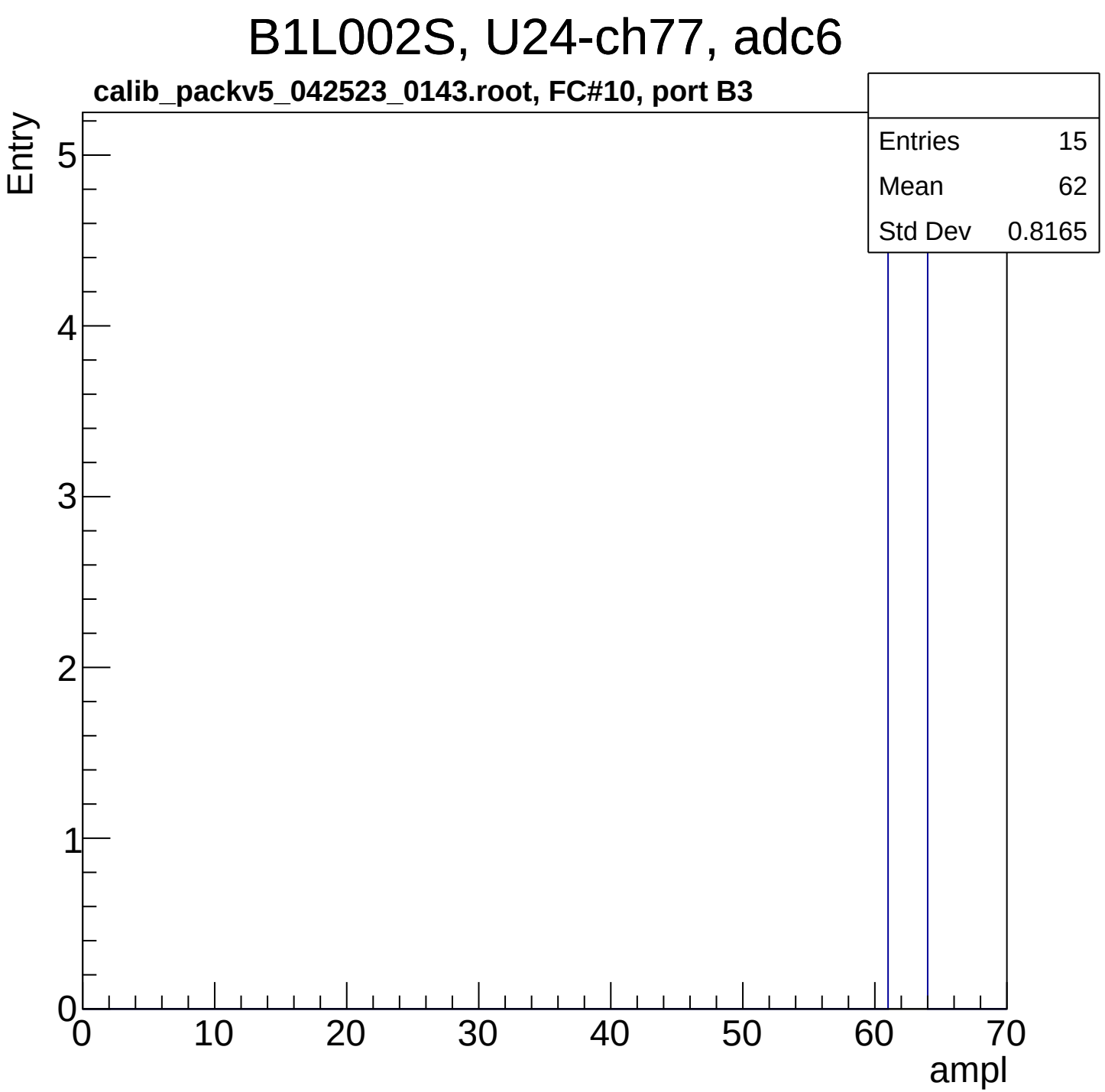
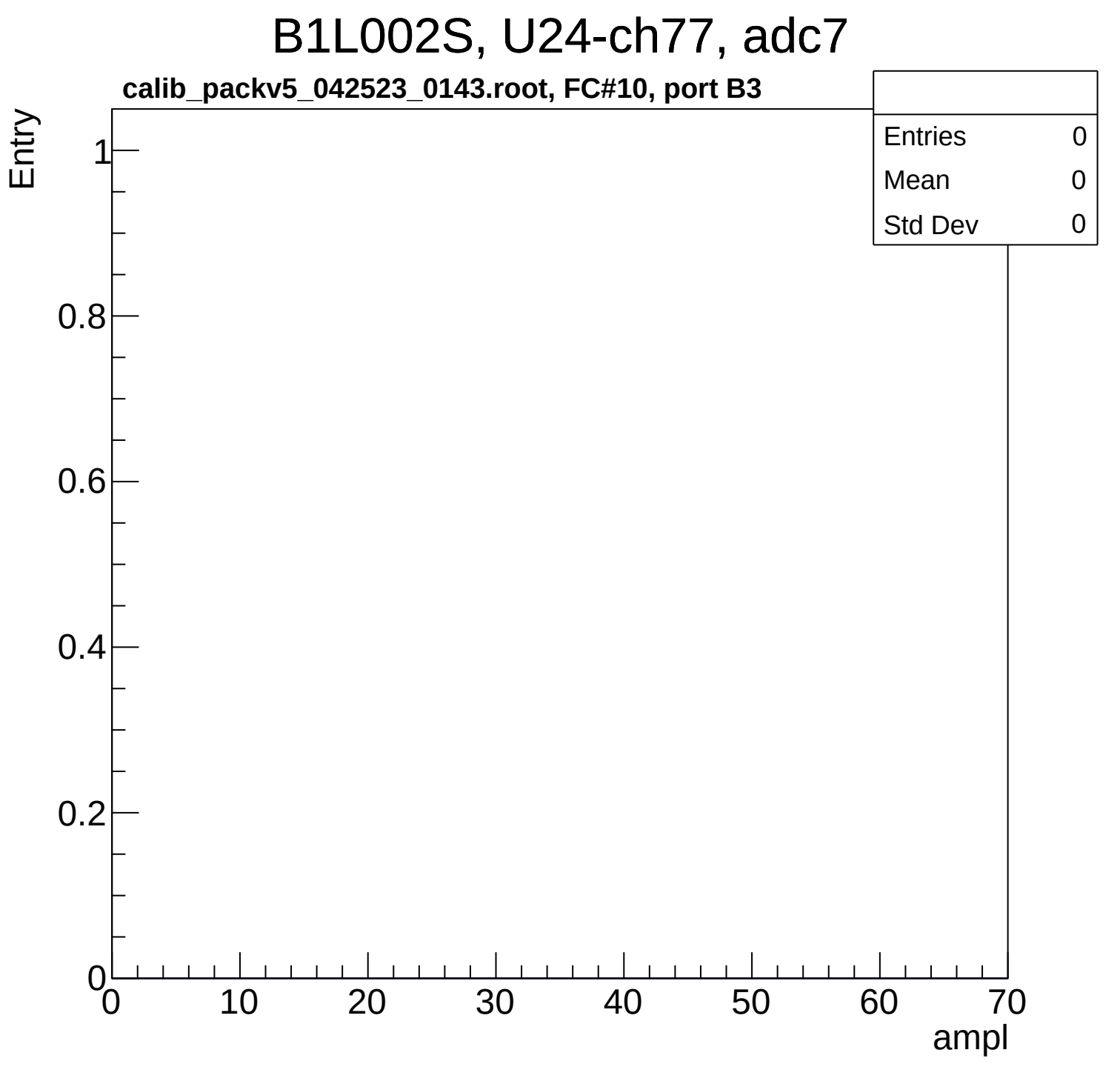
50
60
70
ampl


B1L002S, U24-ch77, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 62
Std Dev 0.8165
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch77, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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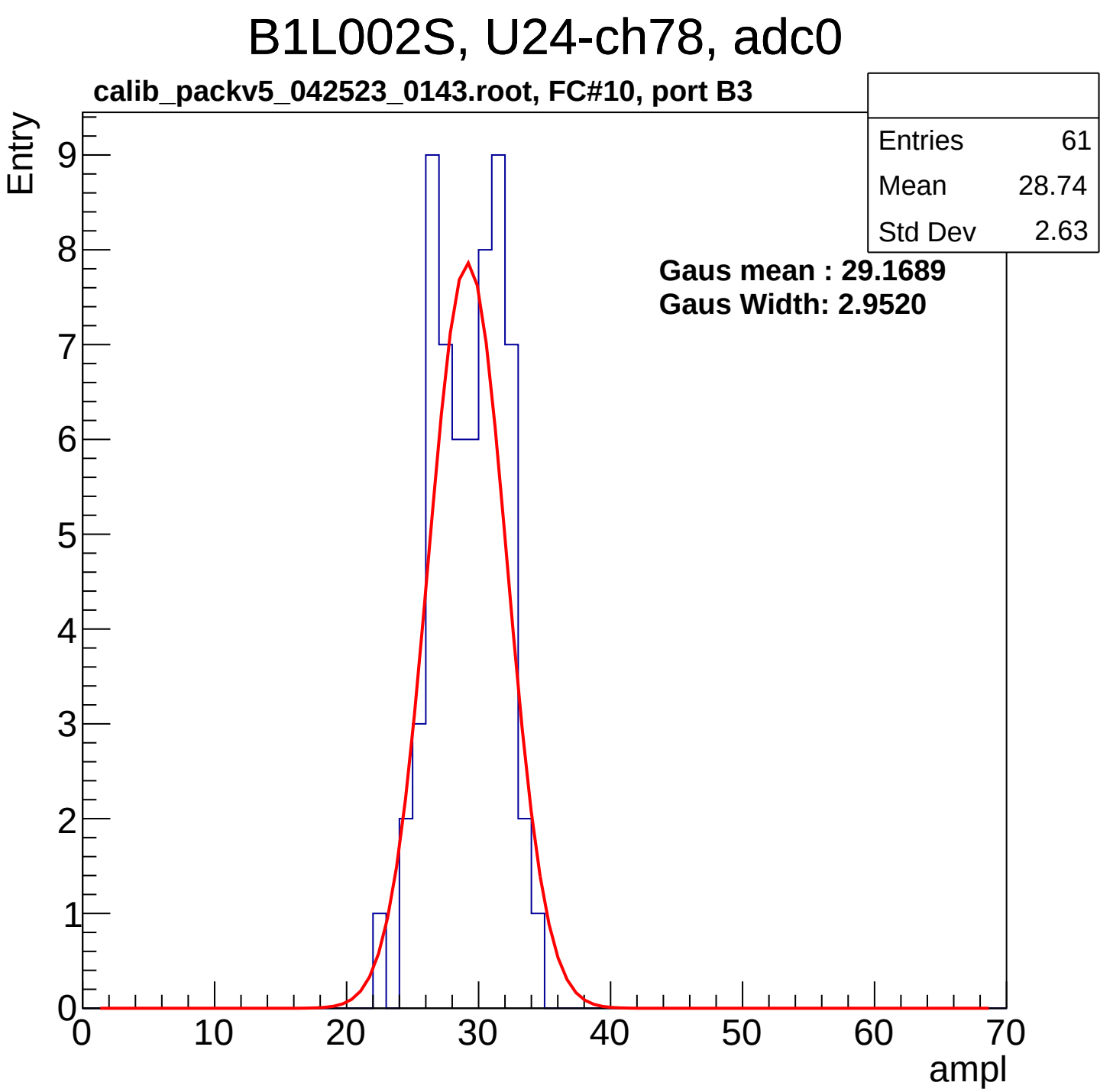
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch78, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 28.74
Std Dev 2.63
Gaus mean : 29.1689
Gaus Width: 2.9520
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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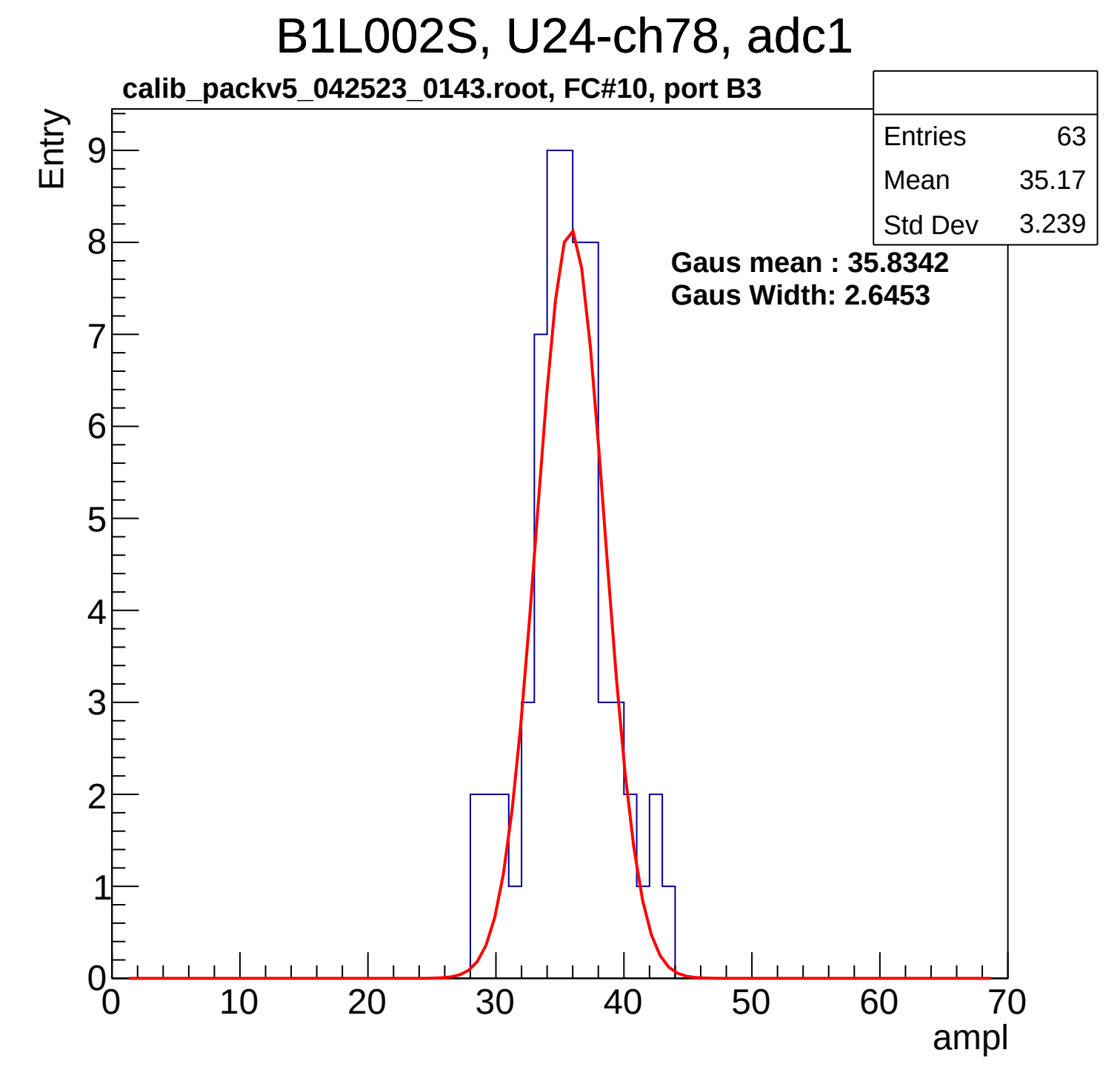
B1L002S, U24-ch78, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 35.17
Std Dev 3.239
Gaus mean : 35.8342
Gaus Width: 2.6453
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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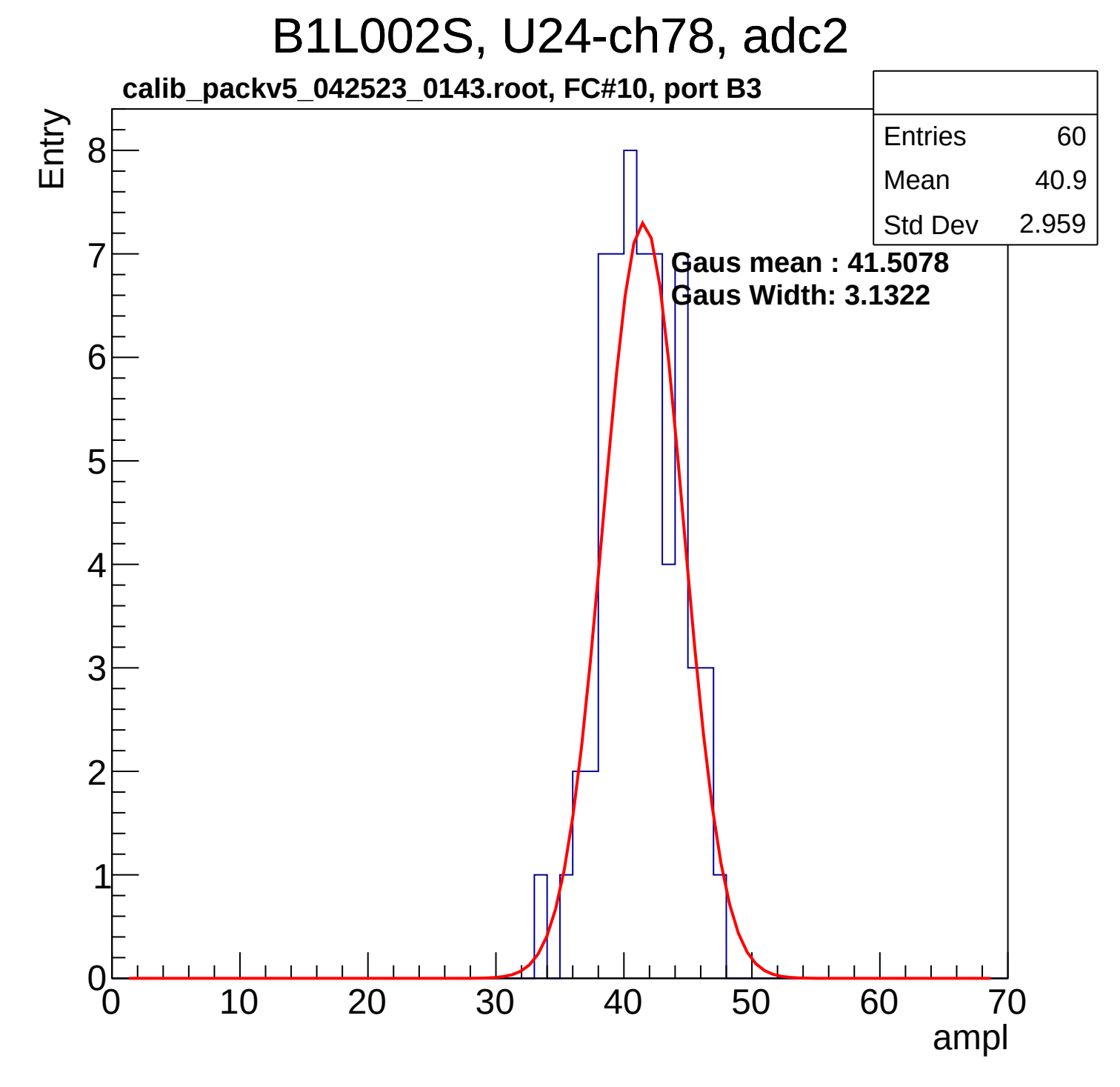
B1L002S, U24-ch78, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 40.9
Std Dev 2.959
Gaus mean : 41.5078
Gaus Width: 3.1322
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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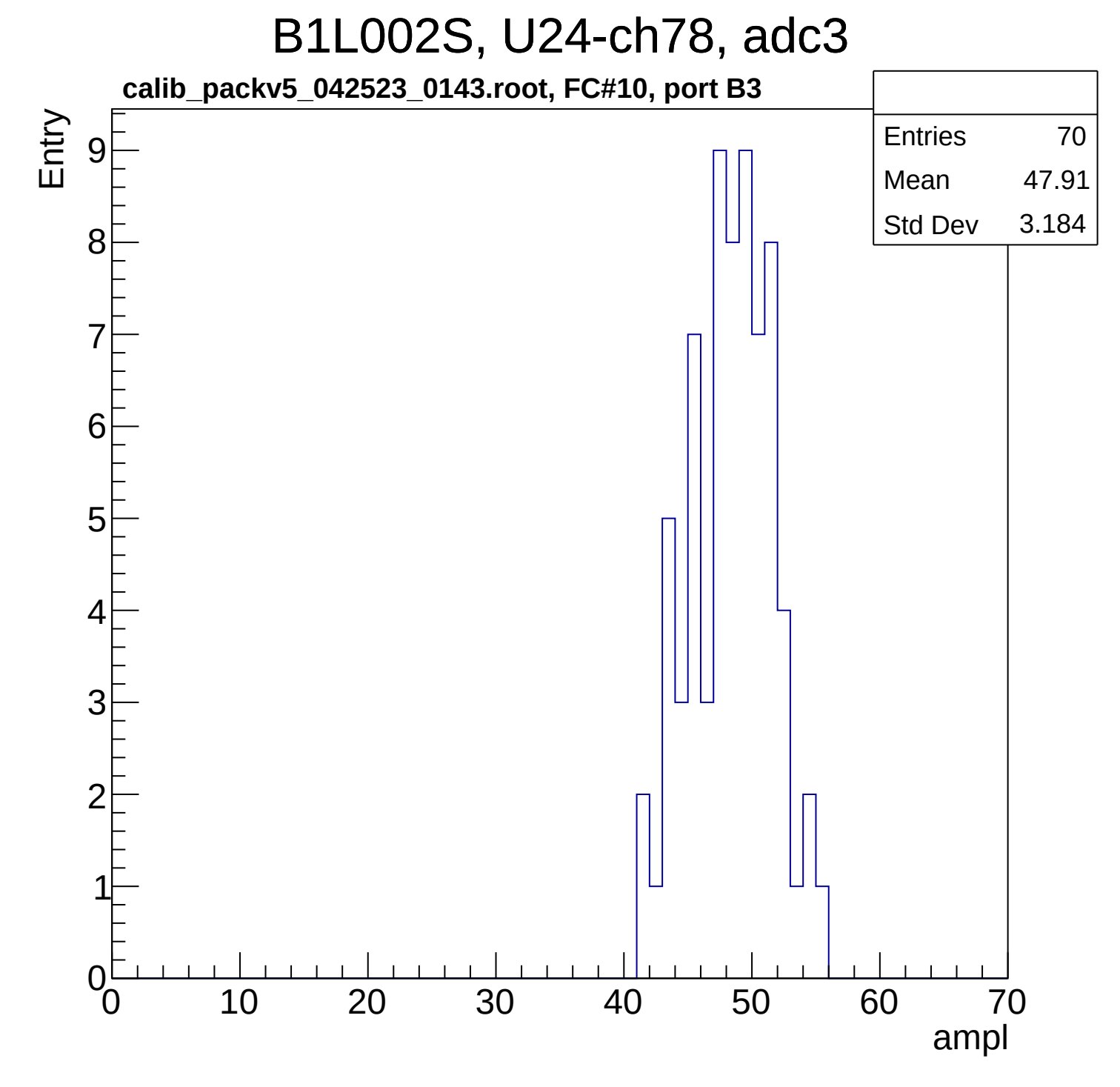
B1L002S, U24-ch78, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.91
Std Dev 3.184
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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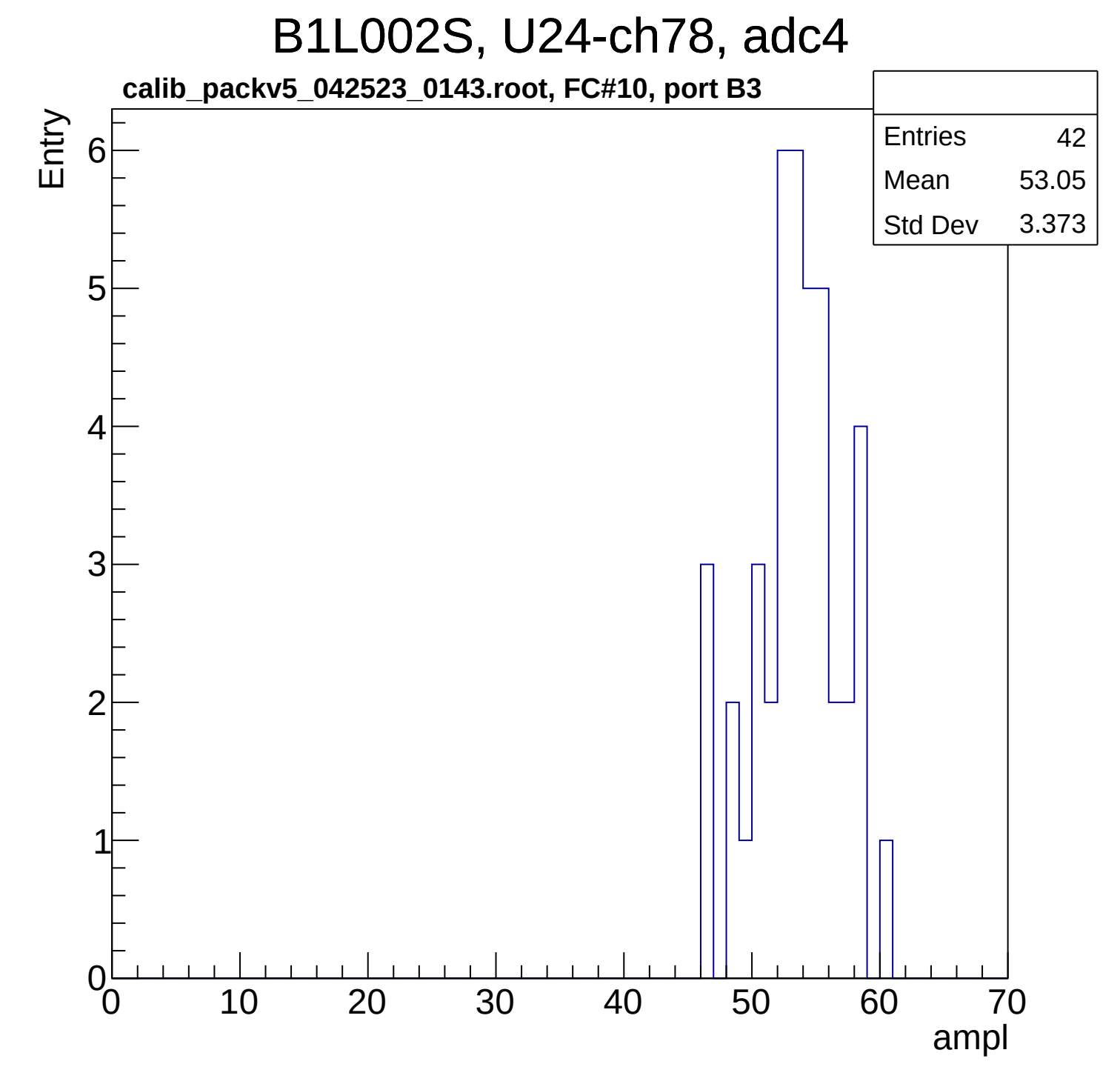
B1L002S, U24-ch78, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 53.05
Std Dev 3.373
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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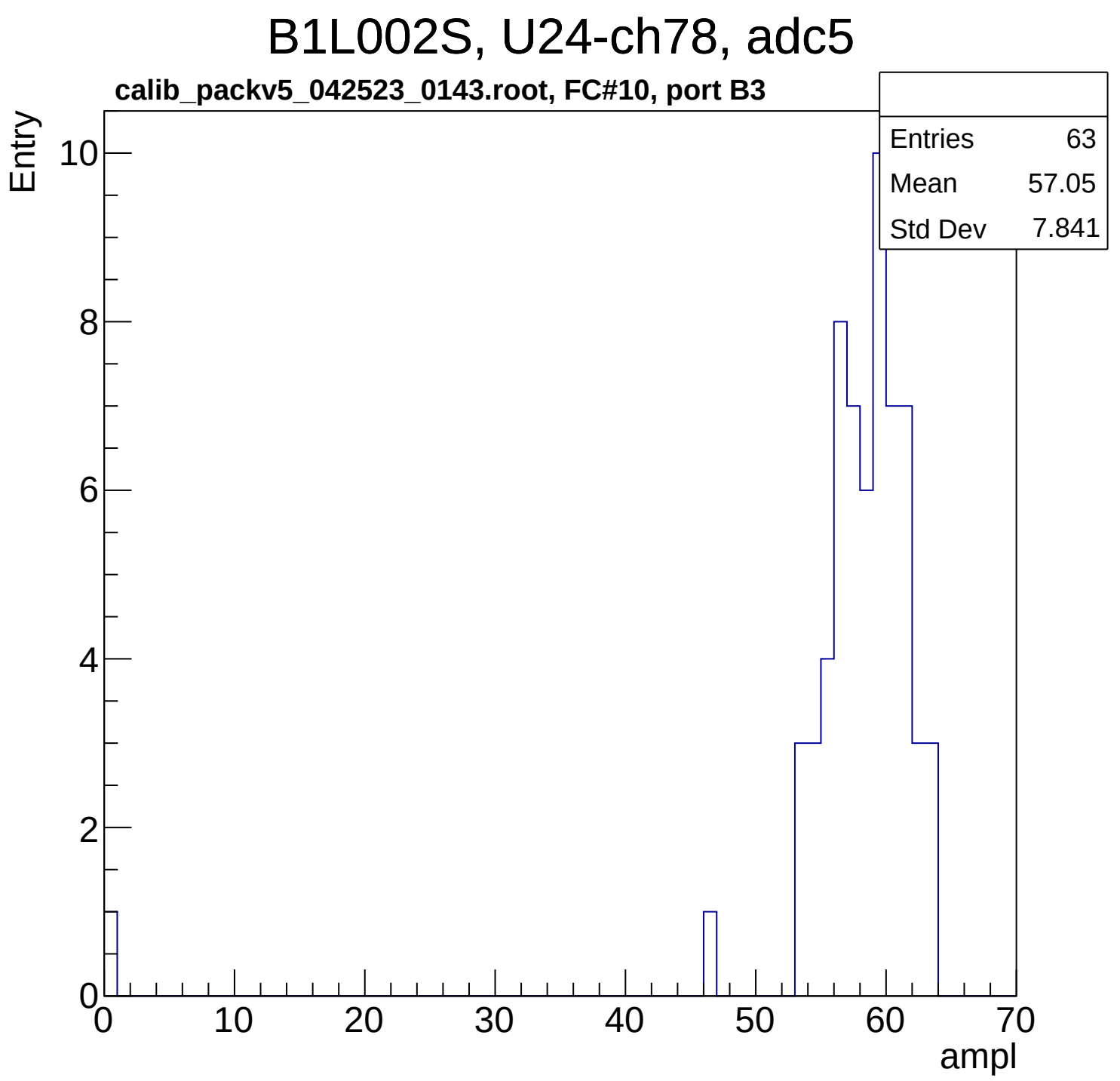
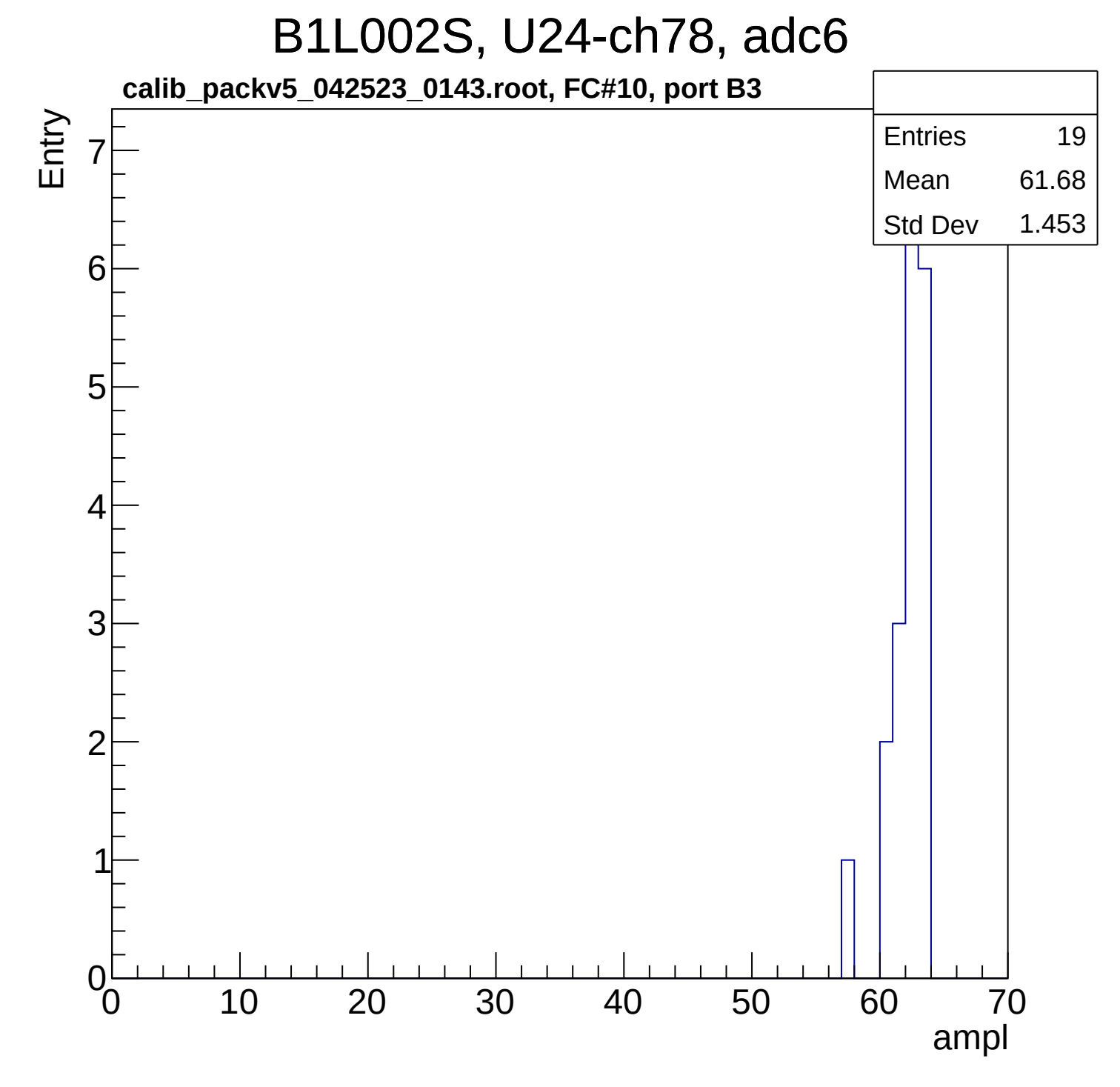
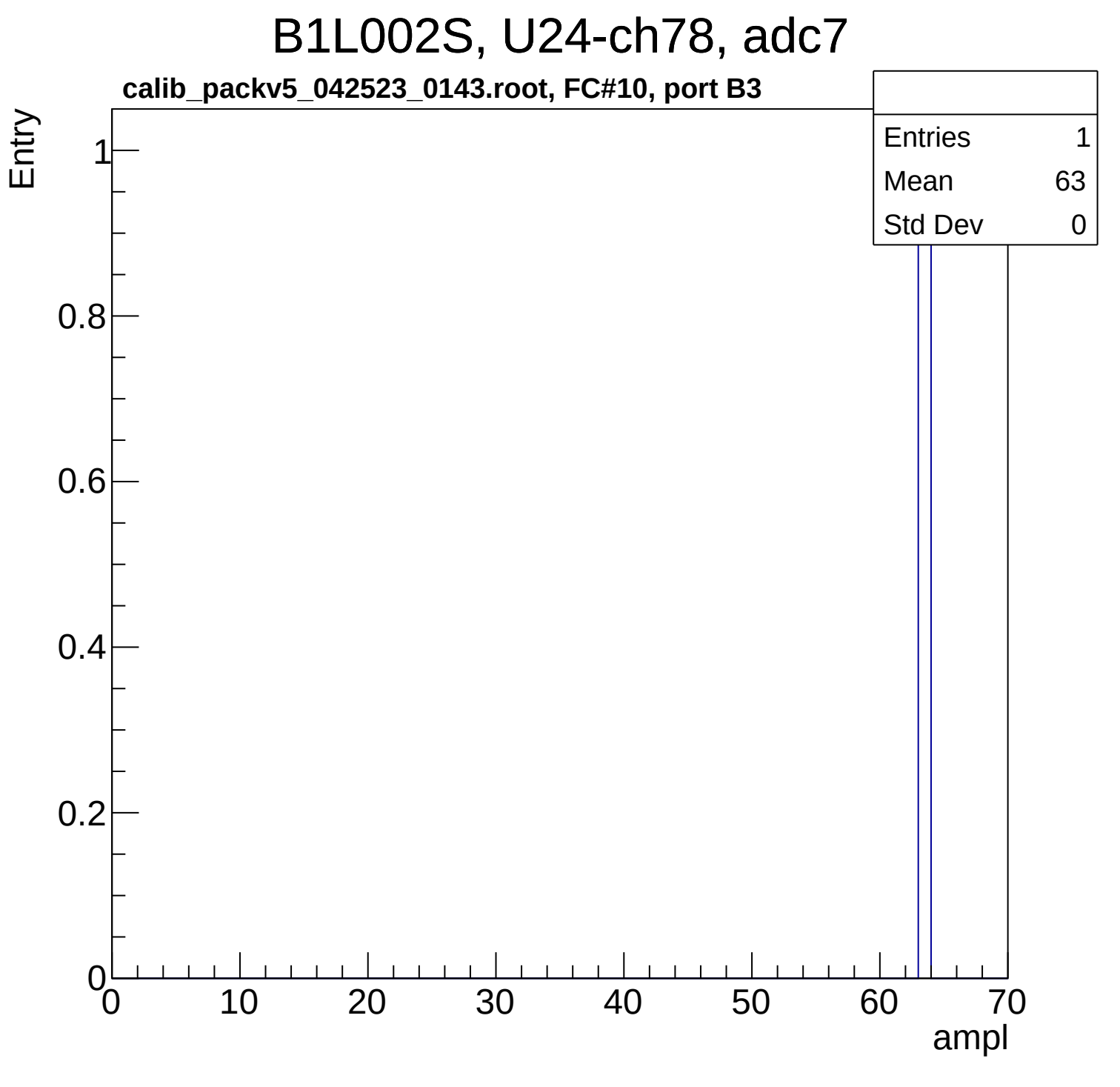
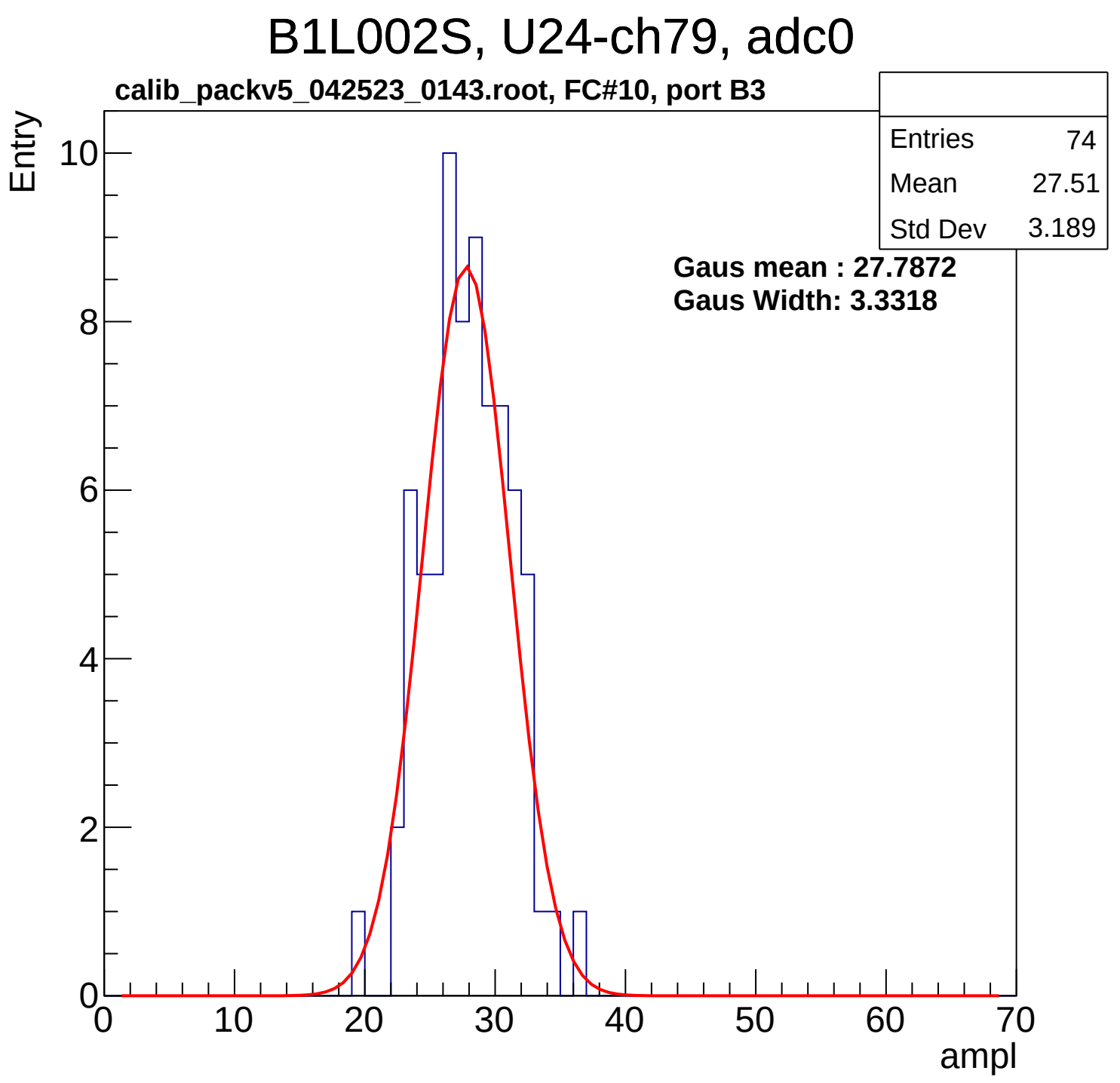
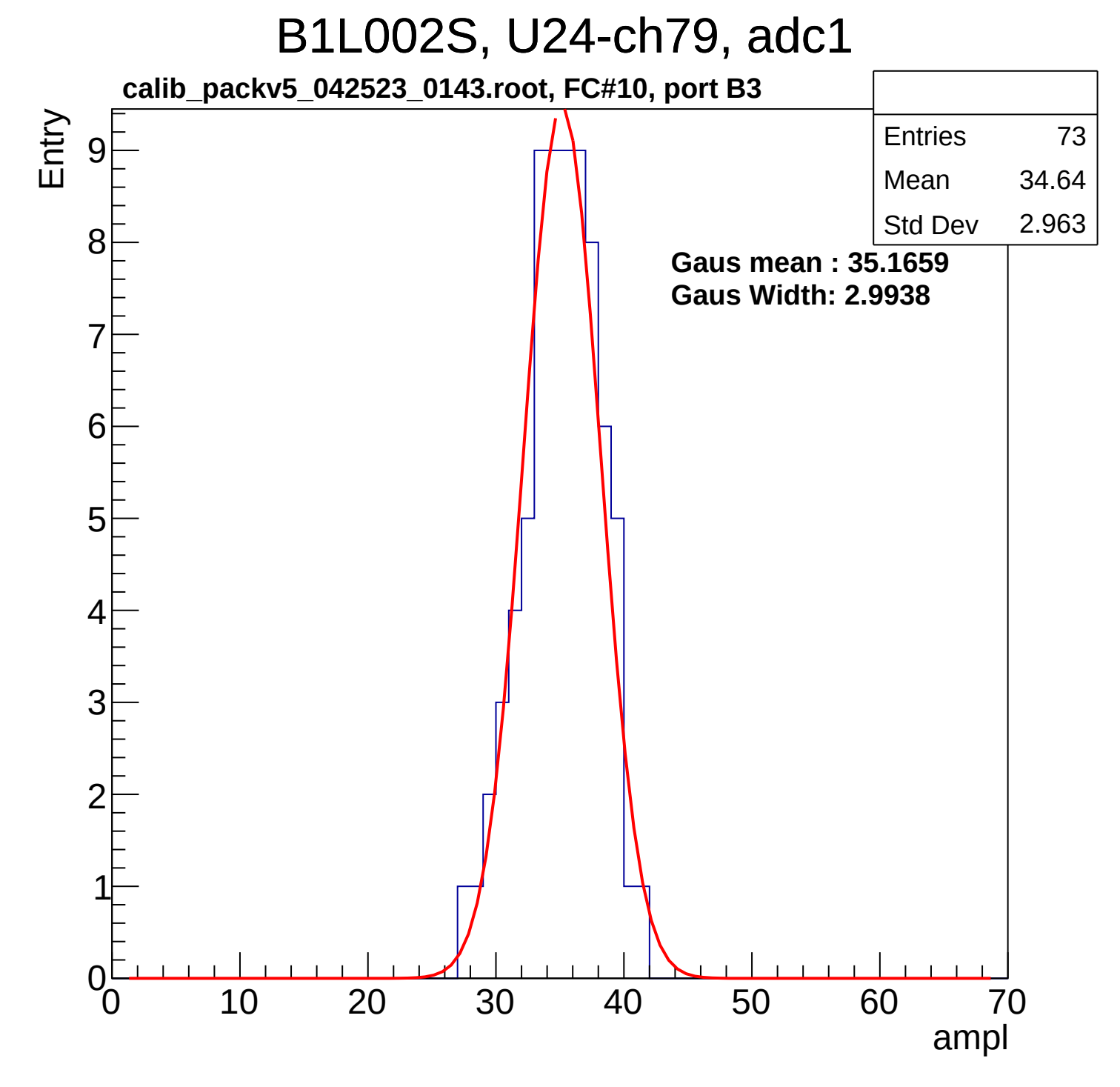
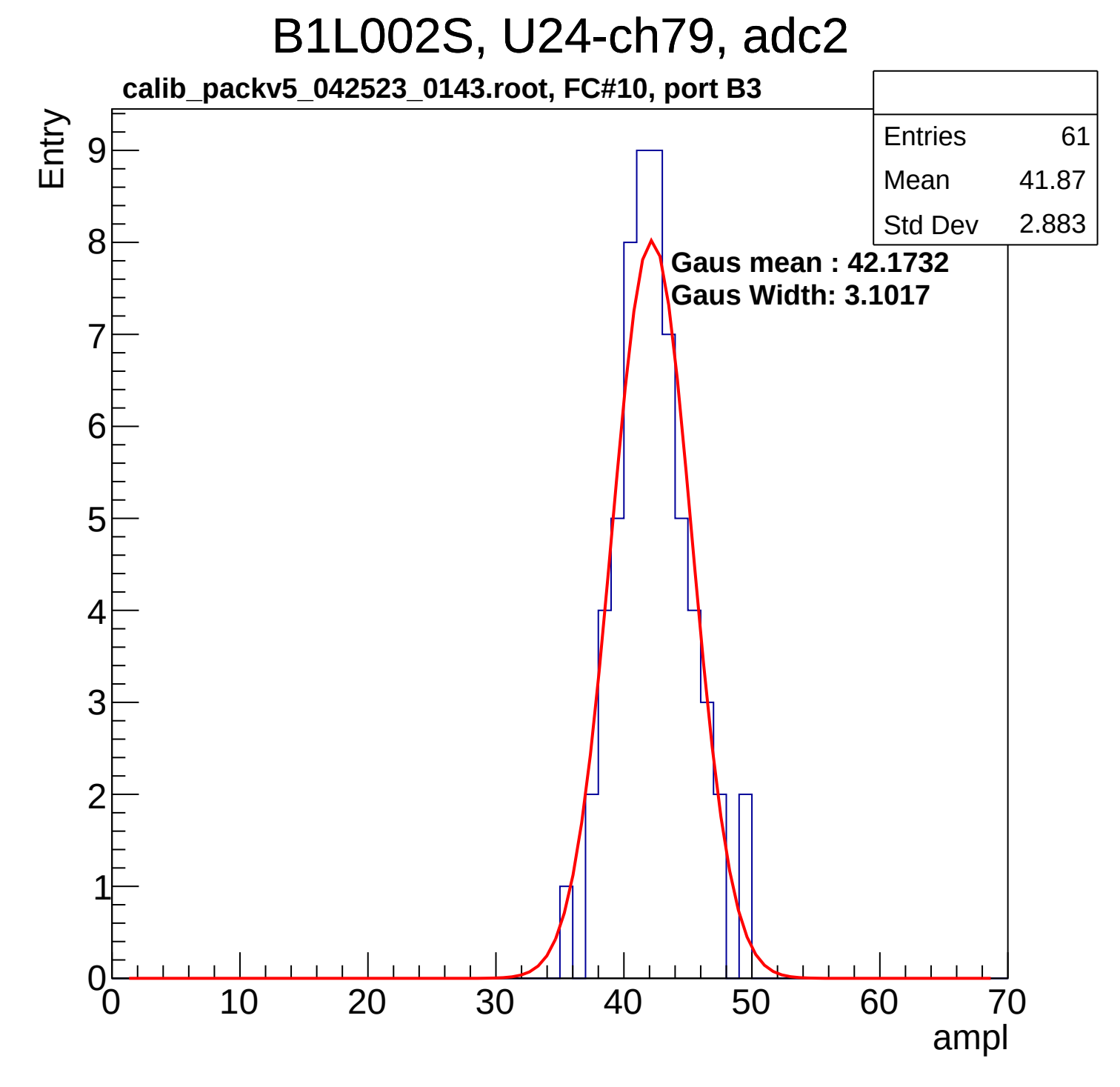

B1L002S, U24-ch78, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 57.05
Std Dev 7.841
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch78, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.68
Std Dev 1.453
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch78, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch79, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 27.51
Std Dev 3.189
Gaus mean : 27.7872
Gaus Width: 3.3318
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch79, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 34.64
Std Dev 2.963
Gaus mean : 35.1659
Gaus Width: 2.9938
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch79, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 41.87
Std Dev 2.883
Gaus mean : 42.1732
Gaus Width: 3.1017
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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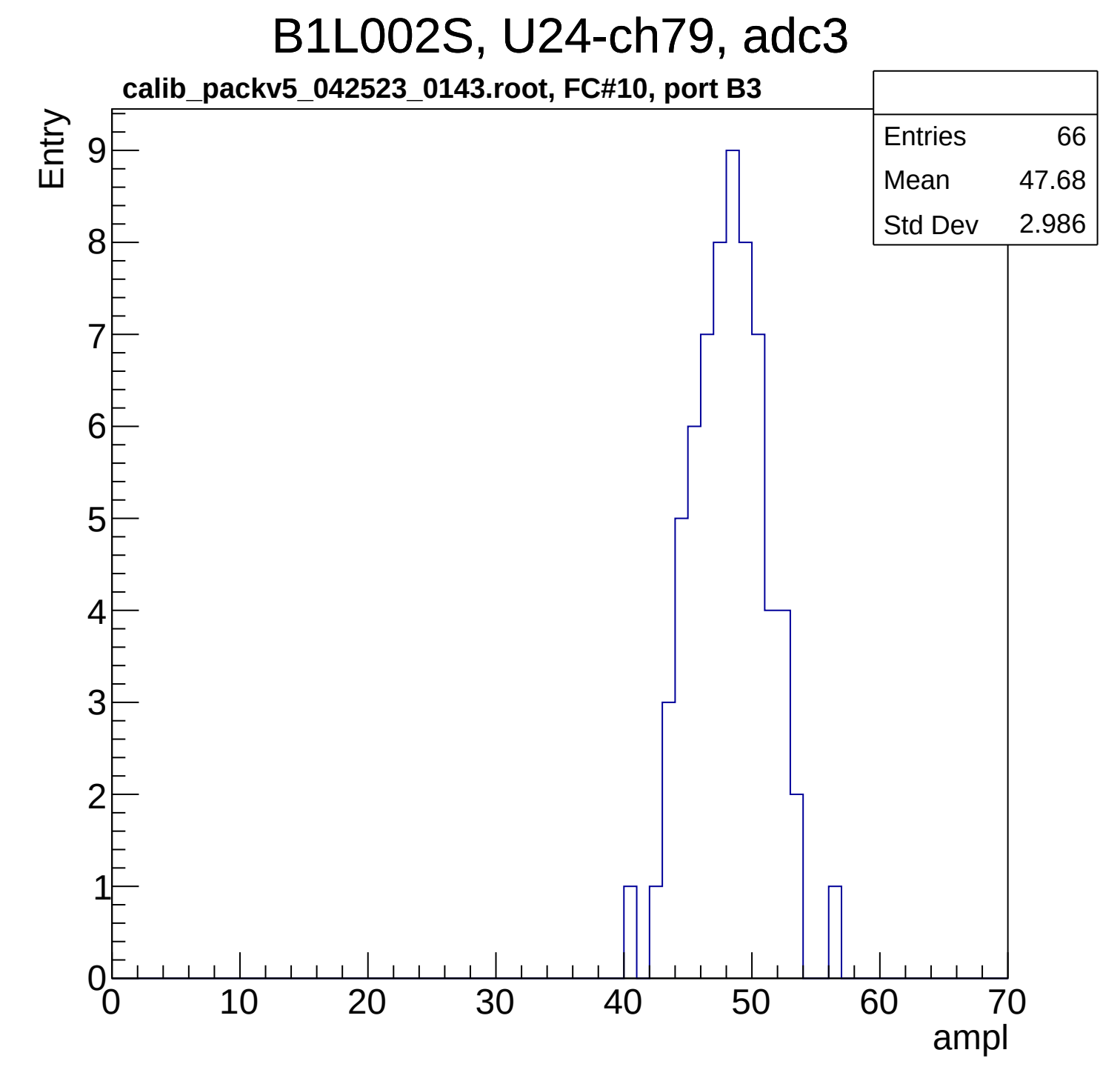
B1L002S, U24-ch79, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.68
Std Dev 2.986
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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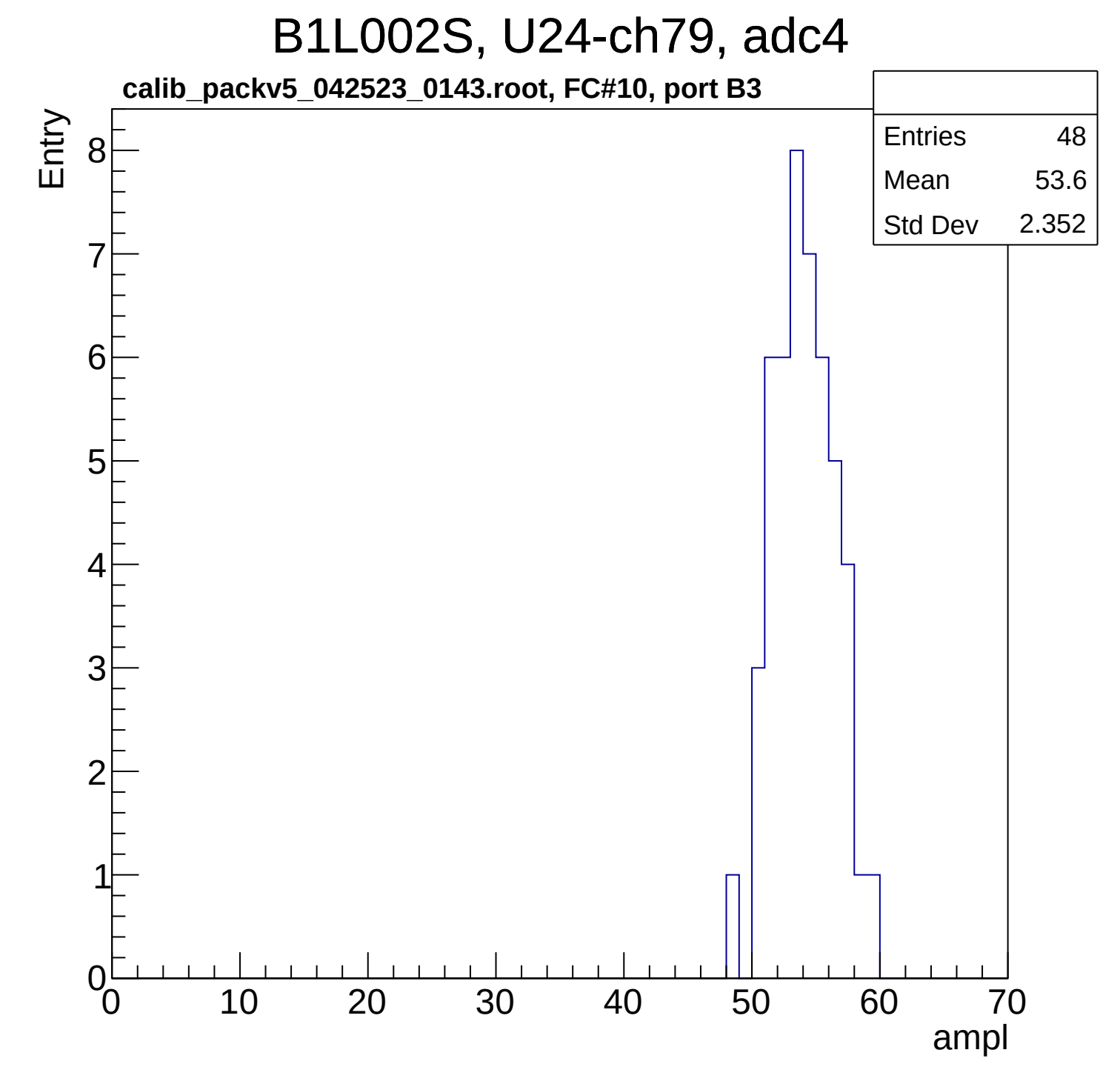
B1L002S, U24-ch79, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 53.6
Std Dev 2.352
Entry
ampl
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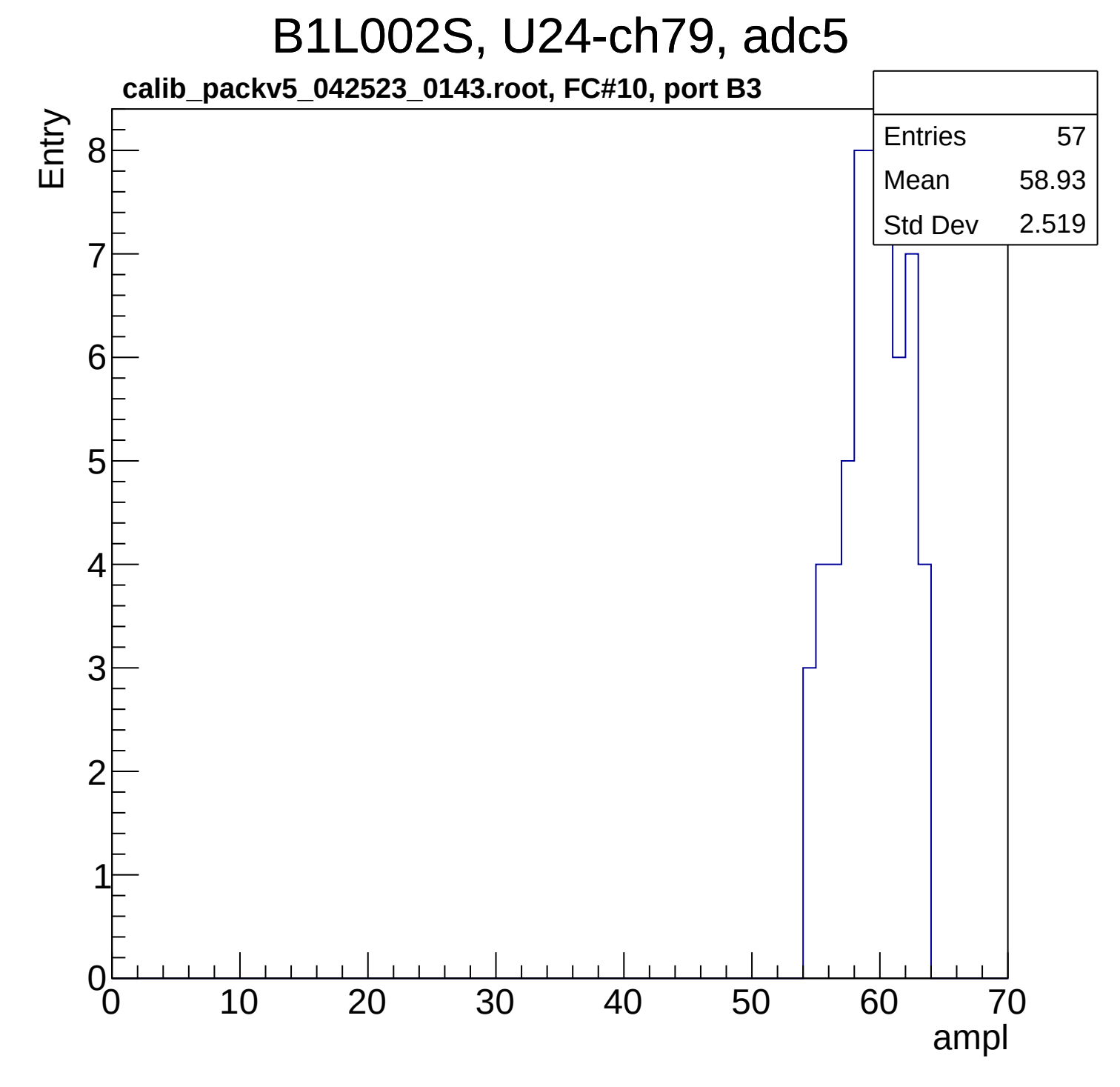
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch79, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.93
Std Dev 2.519
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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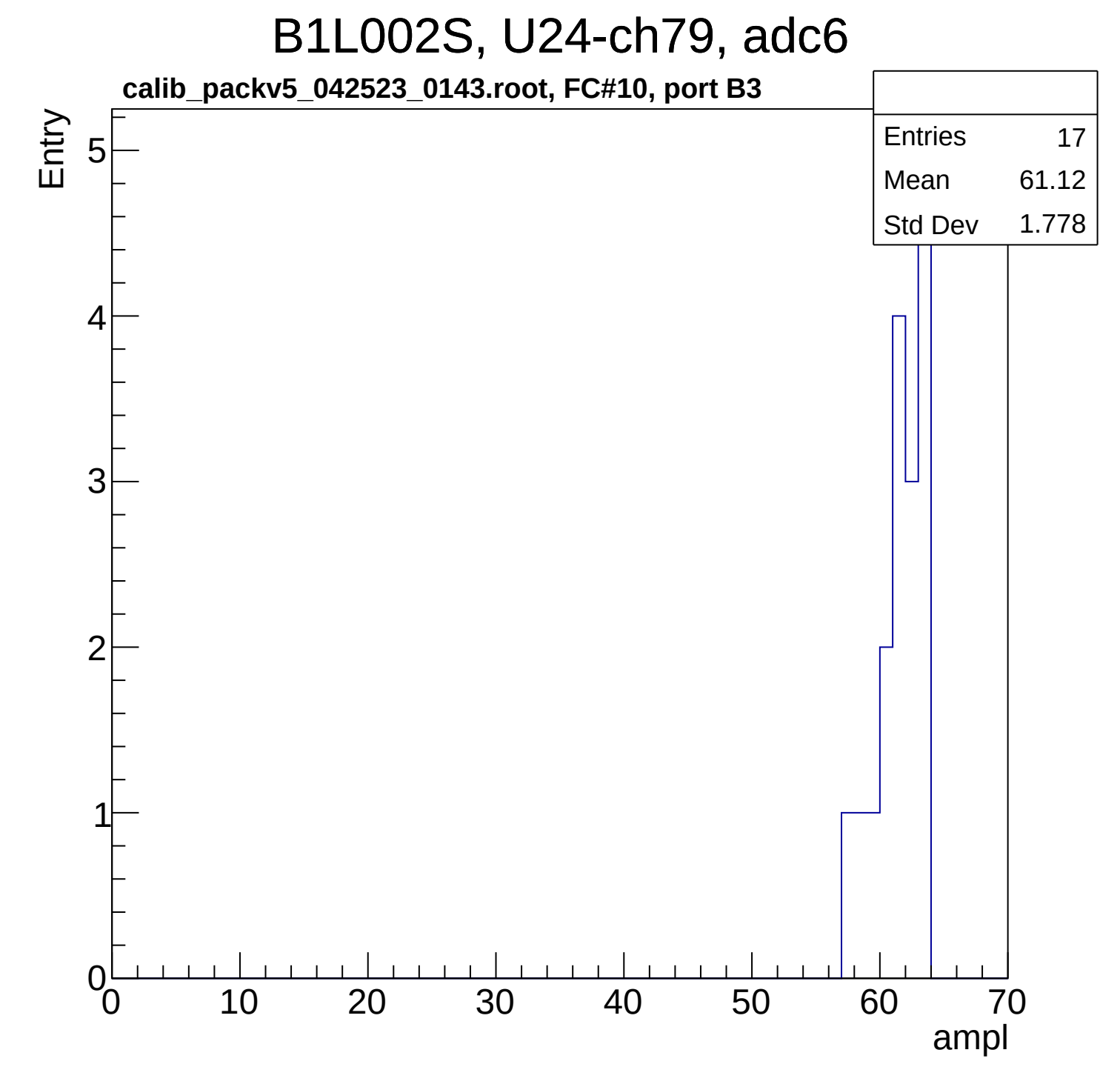
B1L002S, U24-ch79, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.12
Std Dev 1.778
Entry
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2
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4
5
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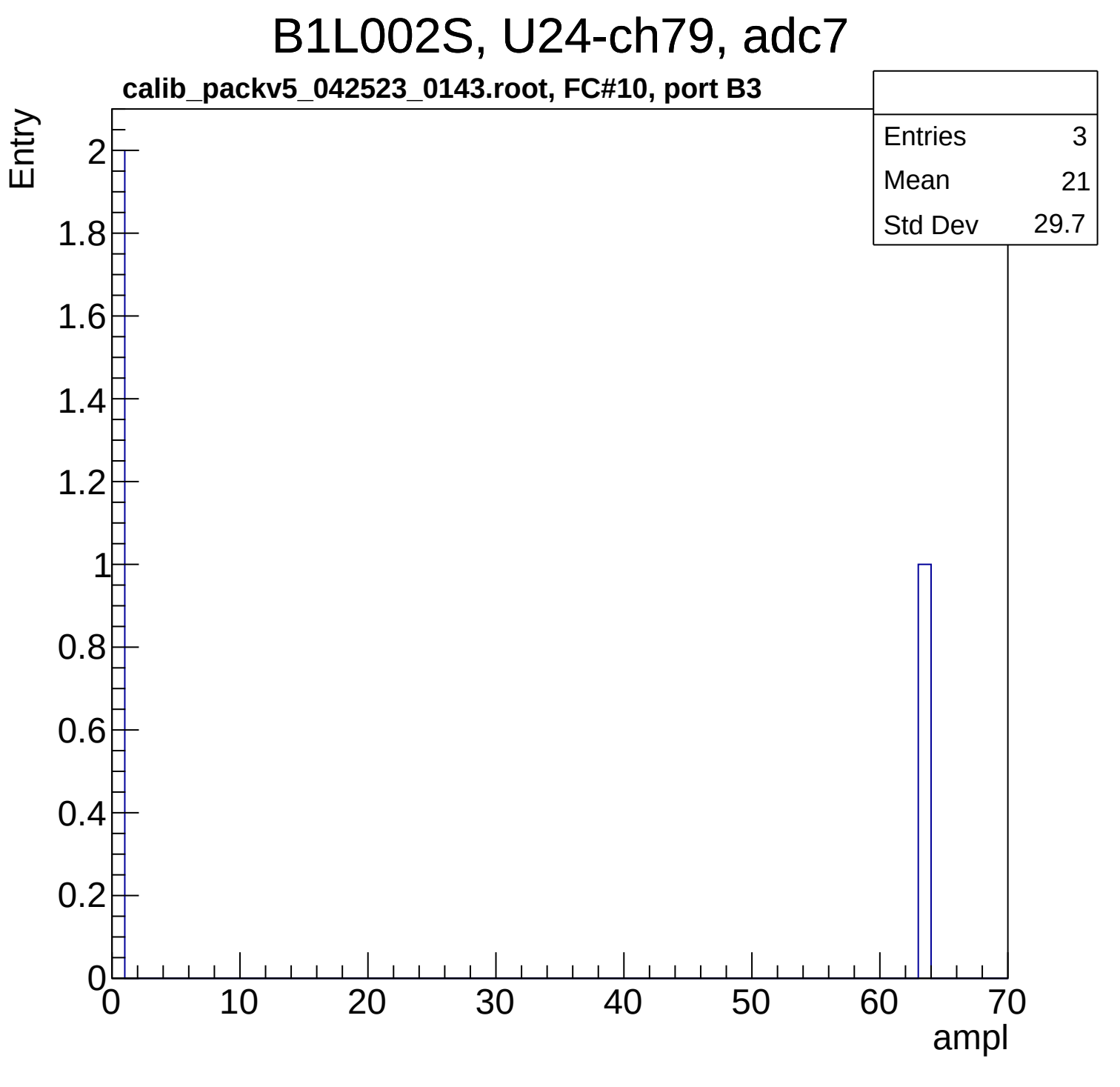
40
50
60
70
ampl


B1L002S, U24-ch79, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 21
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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60
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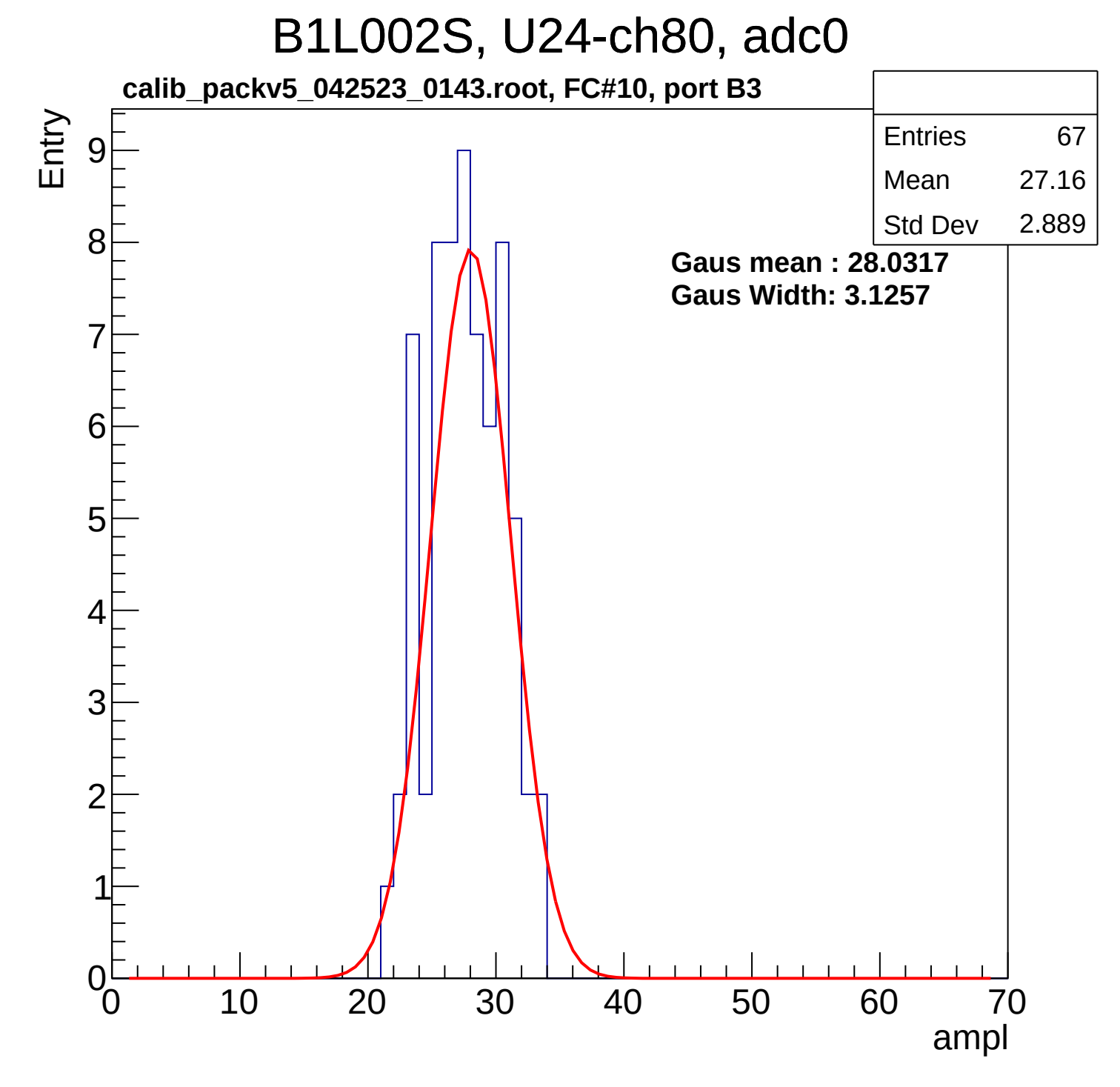
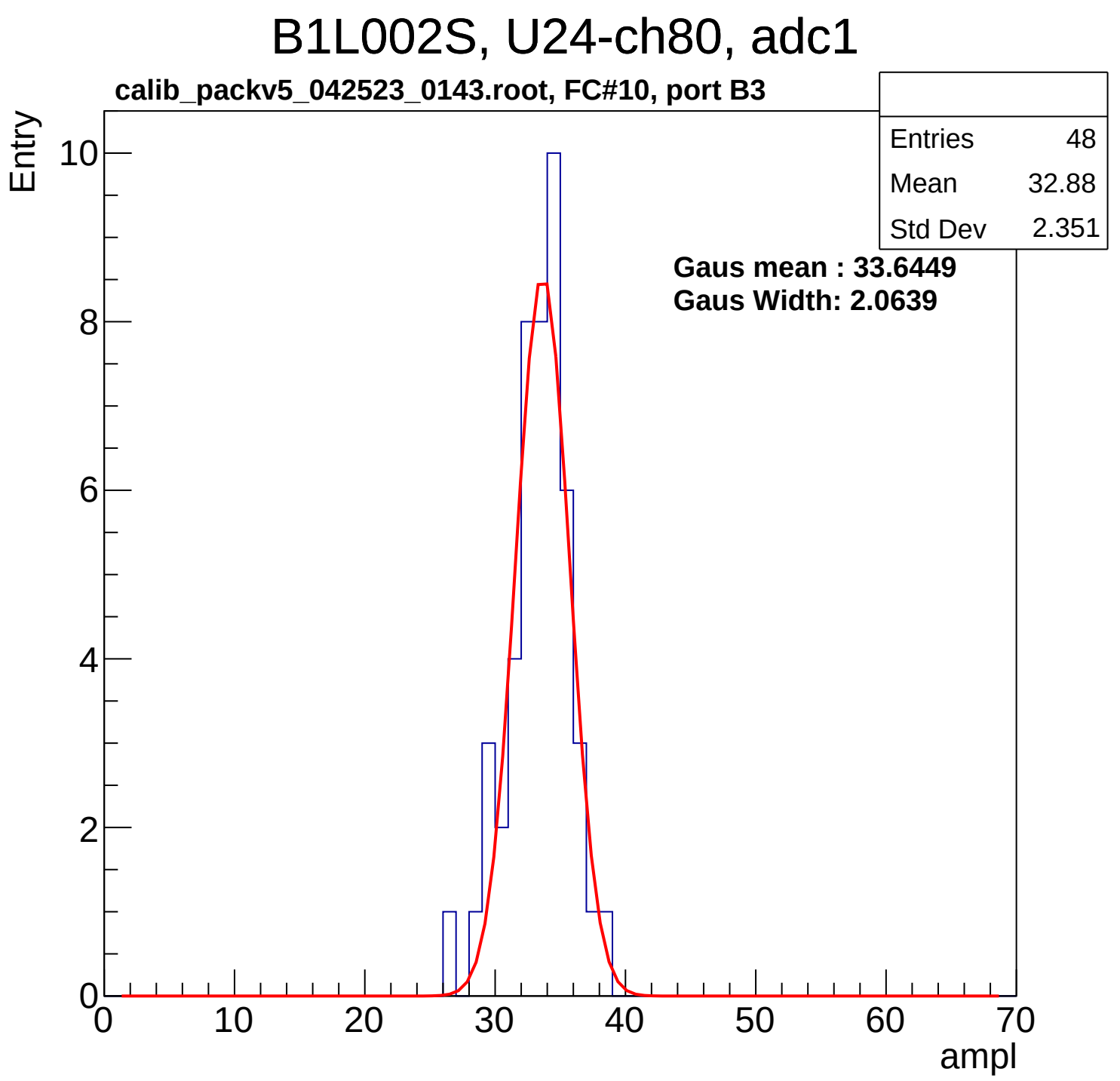

B1L002S, U24-ch80, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 27.16
Std Dev 2.889
Gaus mean : 28.0317
Gaus Width: 3.1257
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch80, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 32.88
Std Dev 2.351
Gaus mean : 33.6449
Gaus Width: 2.0639
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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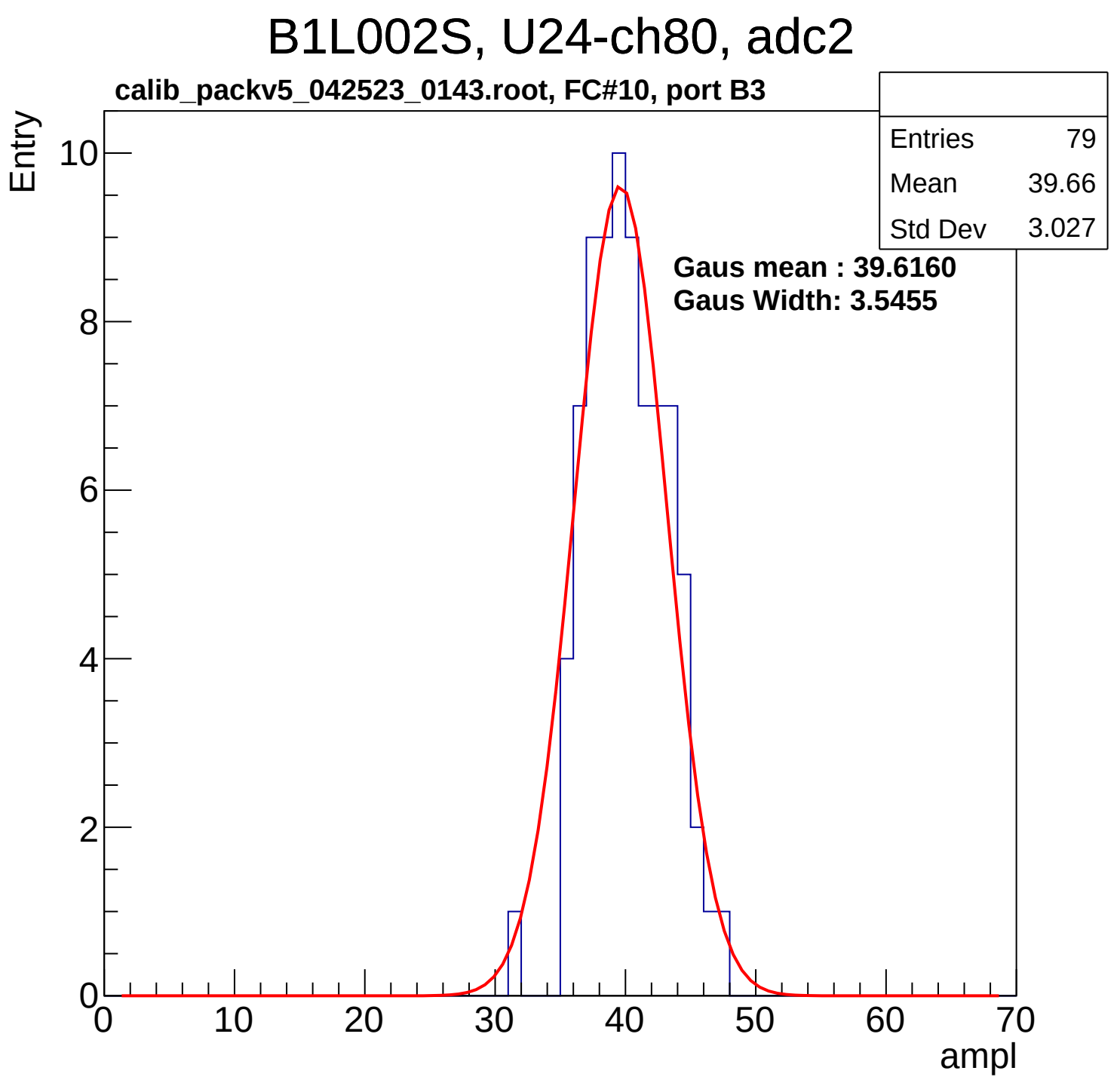
B1L002S, U24-ch80, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 39.66
Std Dev 3.027
Gaus mean : 39.6160
Gaus Width: 3.5455
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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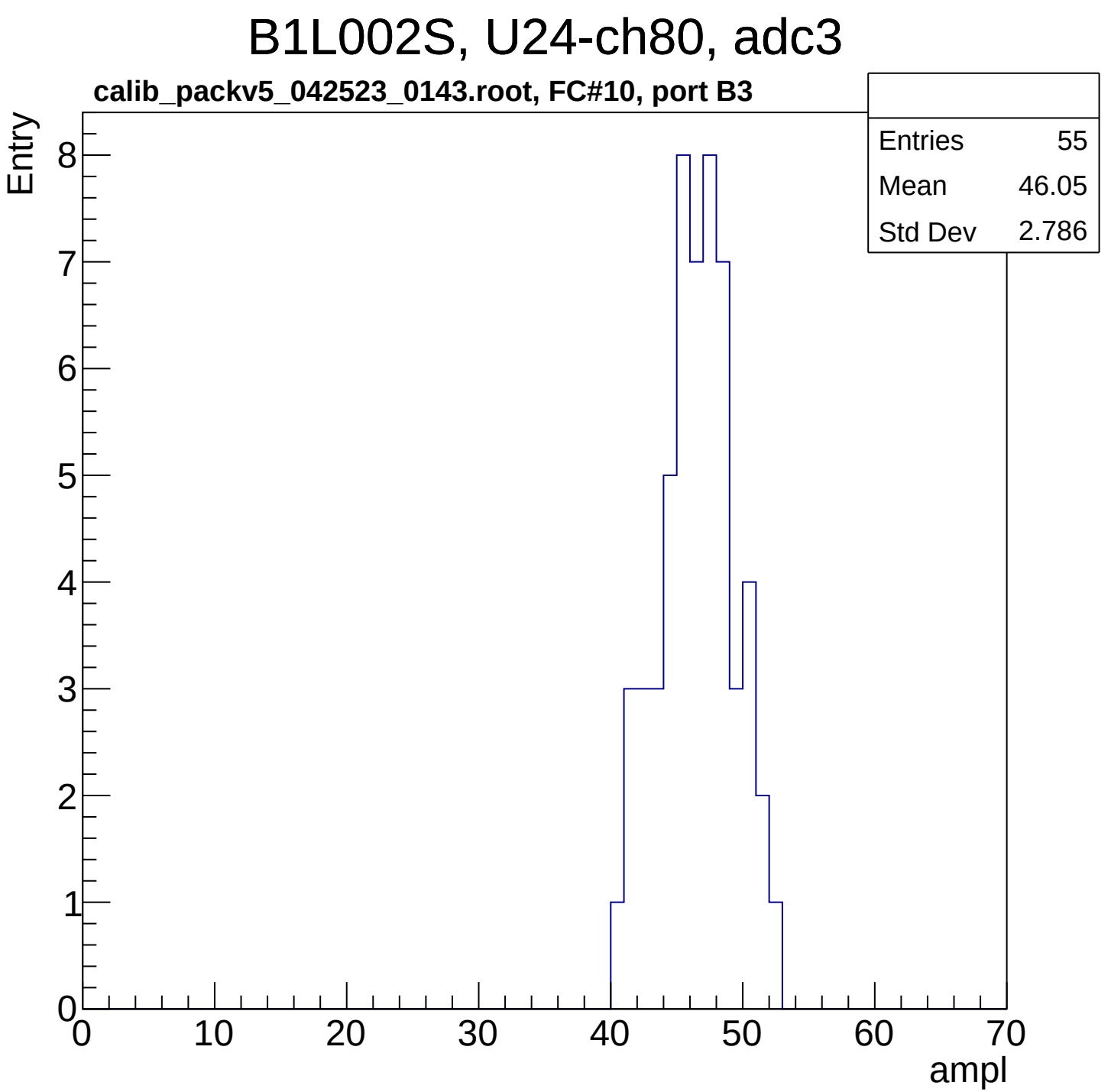
B1L002S, U24-ch80, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 46.05
Std Dev 2.786
Entry
ampl
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4
5
6
7
8
0
10
20
30
40
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60
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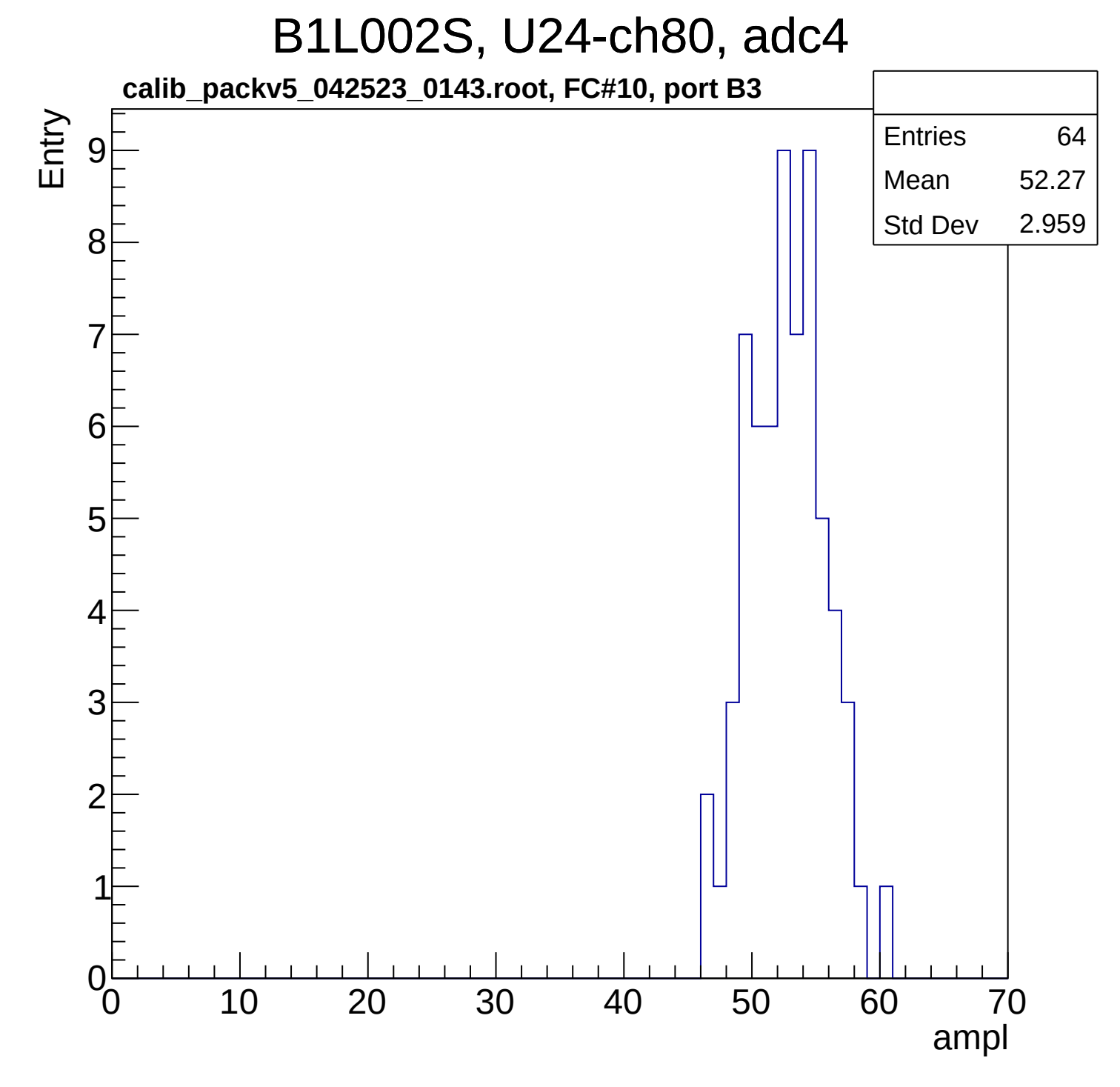
B1L002S, U24-ch80, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 52.27
Std Dev 2.959
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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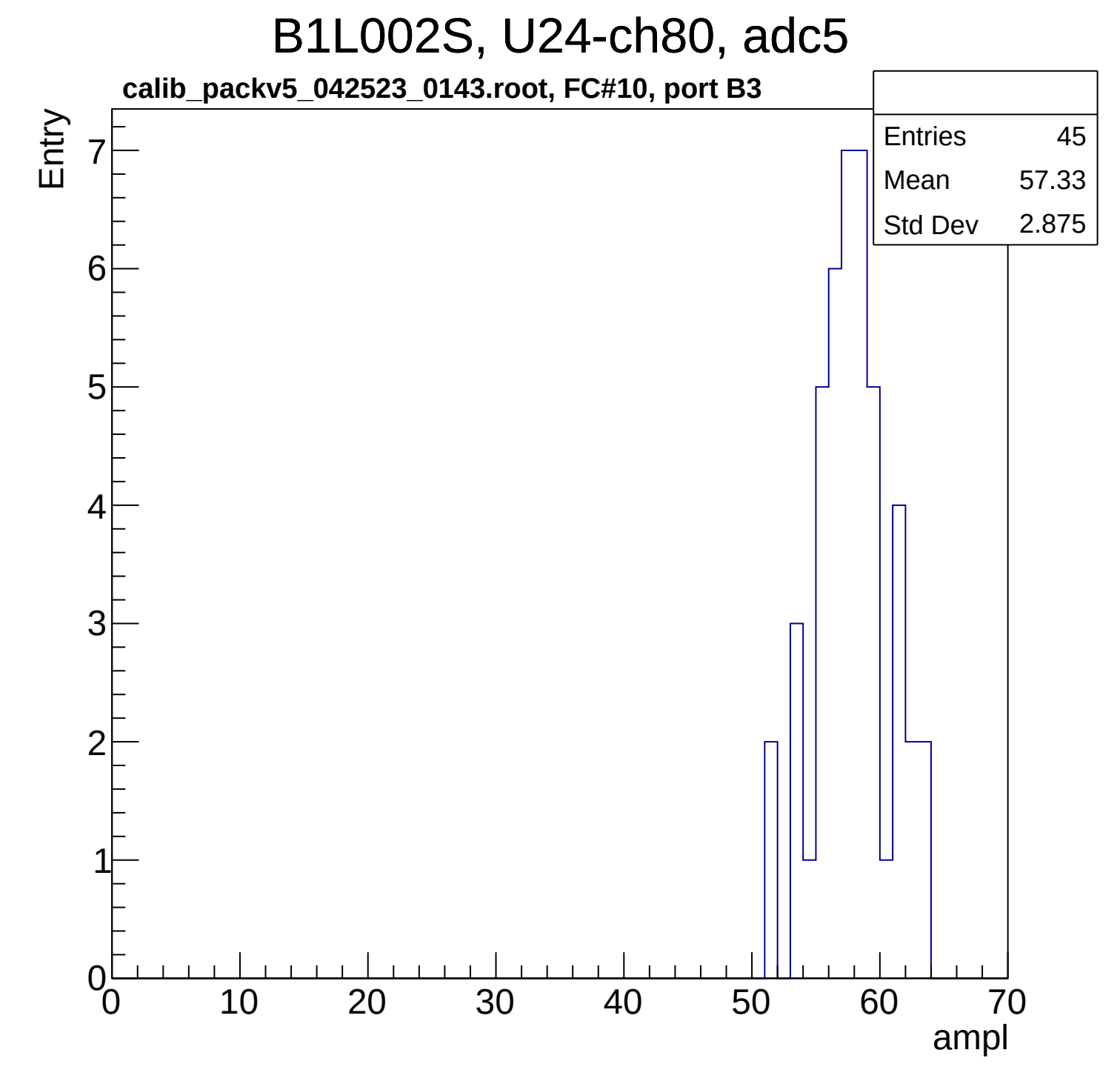
B1L002S, U24-ch80, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 57.33
Std Dev 2.875
Entry
ampl
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2
3
4
5
6
7
0
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20
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40
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60
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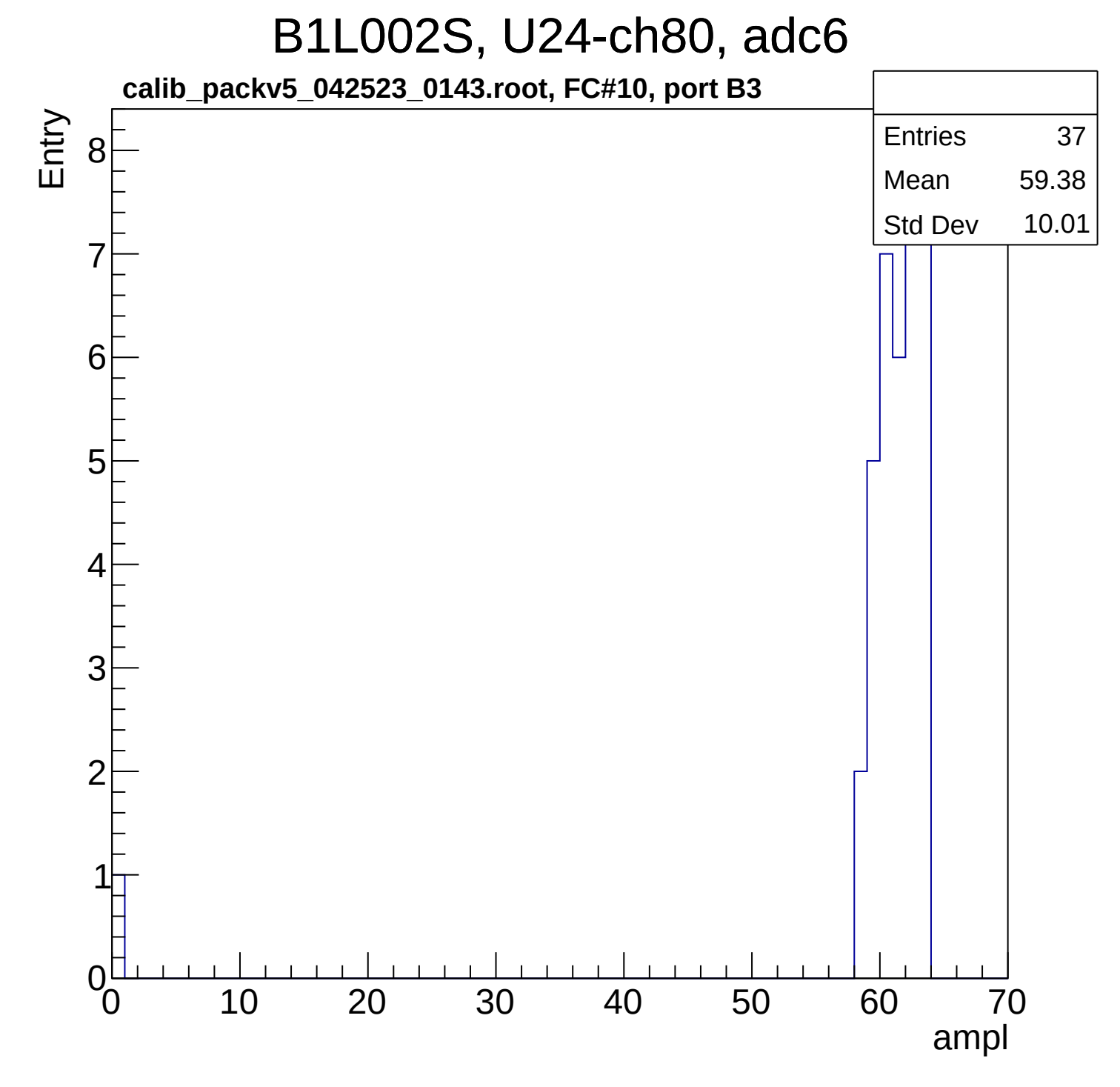
B1L002S, U24-ch80, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 59.38
Std Dev 10.01
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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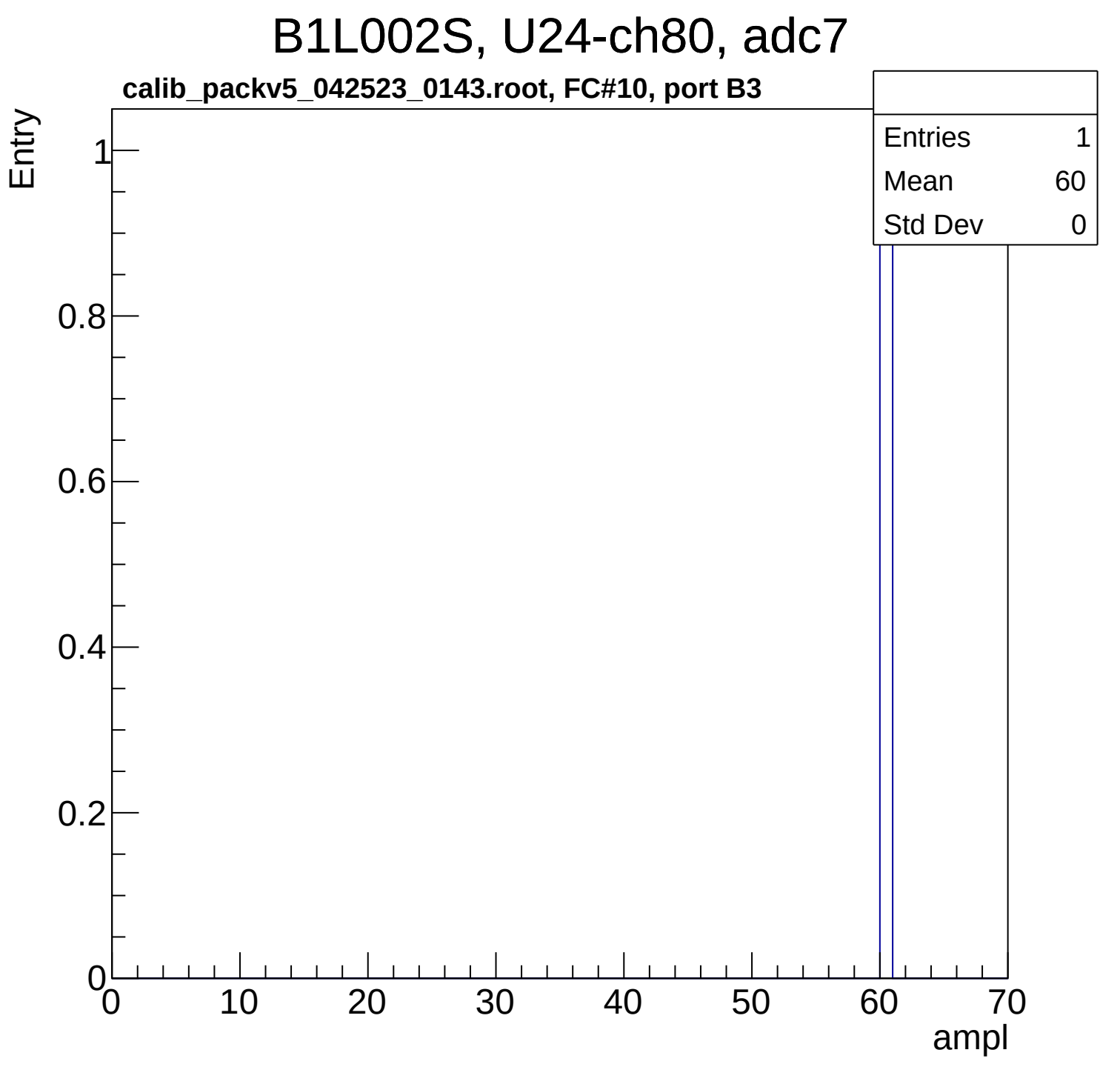
B1L002S, U24-ch80, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 60
Std Dev 0
Entry
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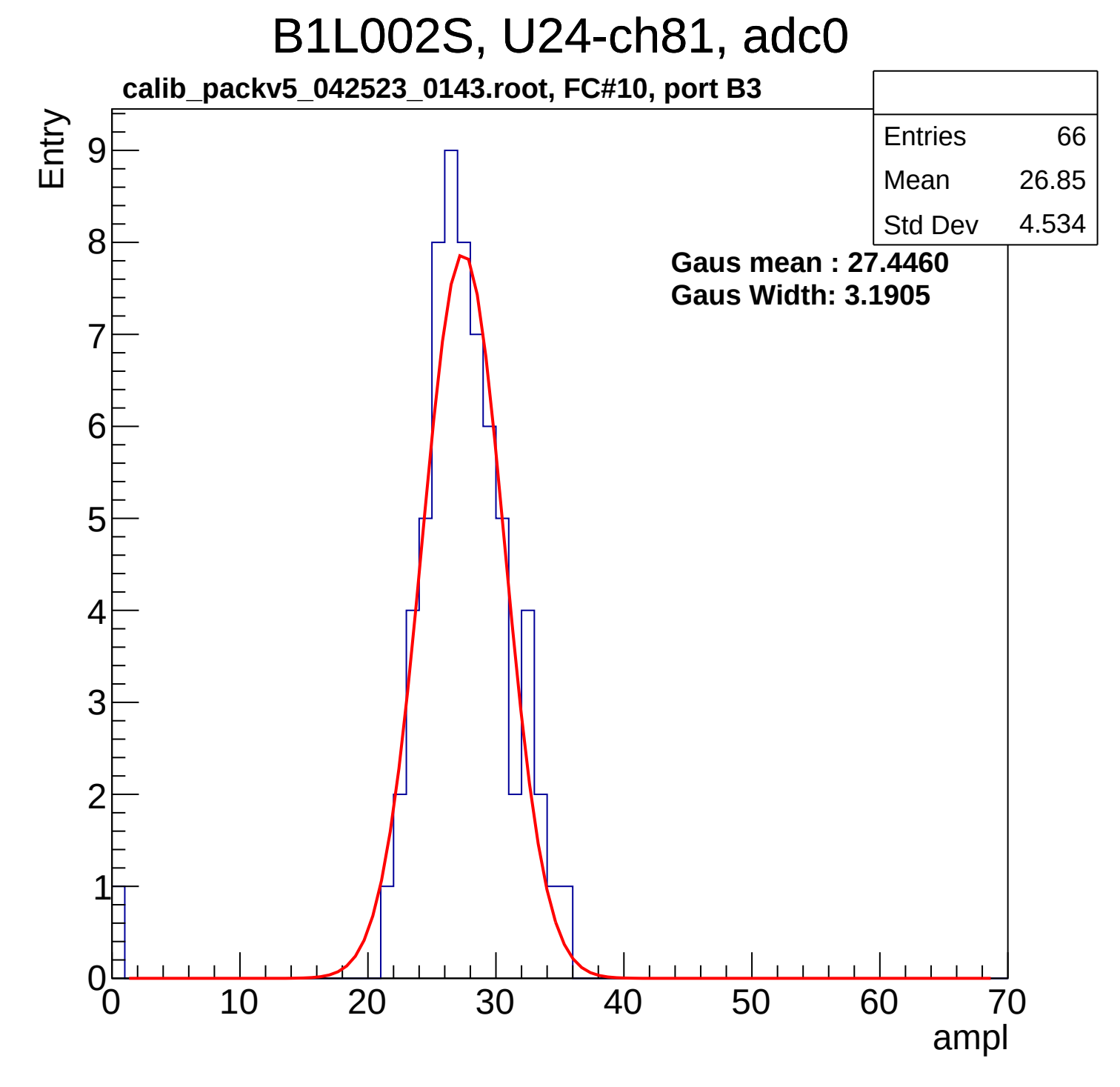
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch81, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 26.85
Std Dev 4.534
Gaus mean : 27.4460
Gaus Width: 3.1905
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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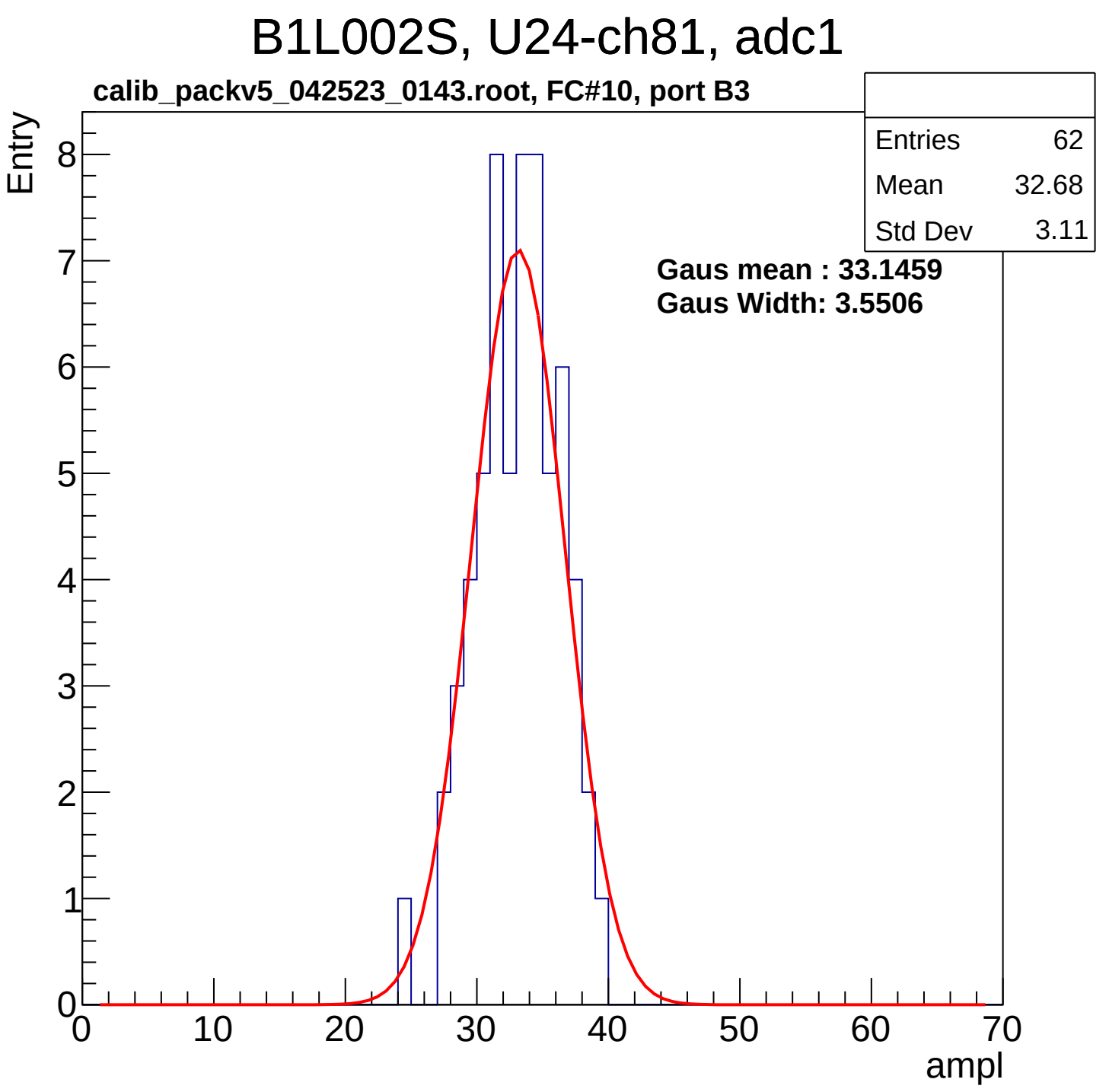
B1L002S, U24-ch81, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 32.68
Std Dev 3.11
Gaus mean : 33.1459
Gaus Width: 3.5506
Entry
ampl
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2
3
4
5
6
7
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0
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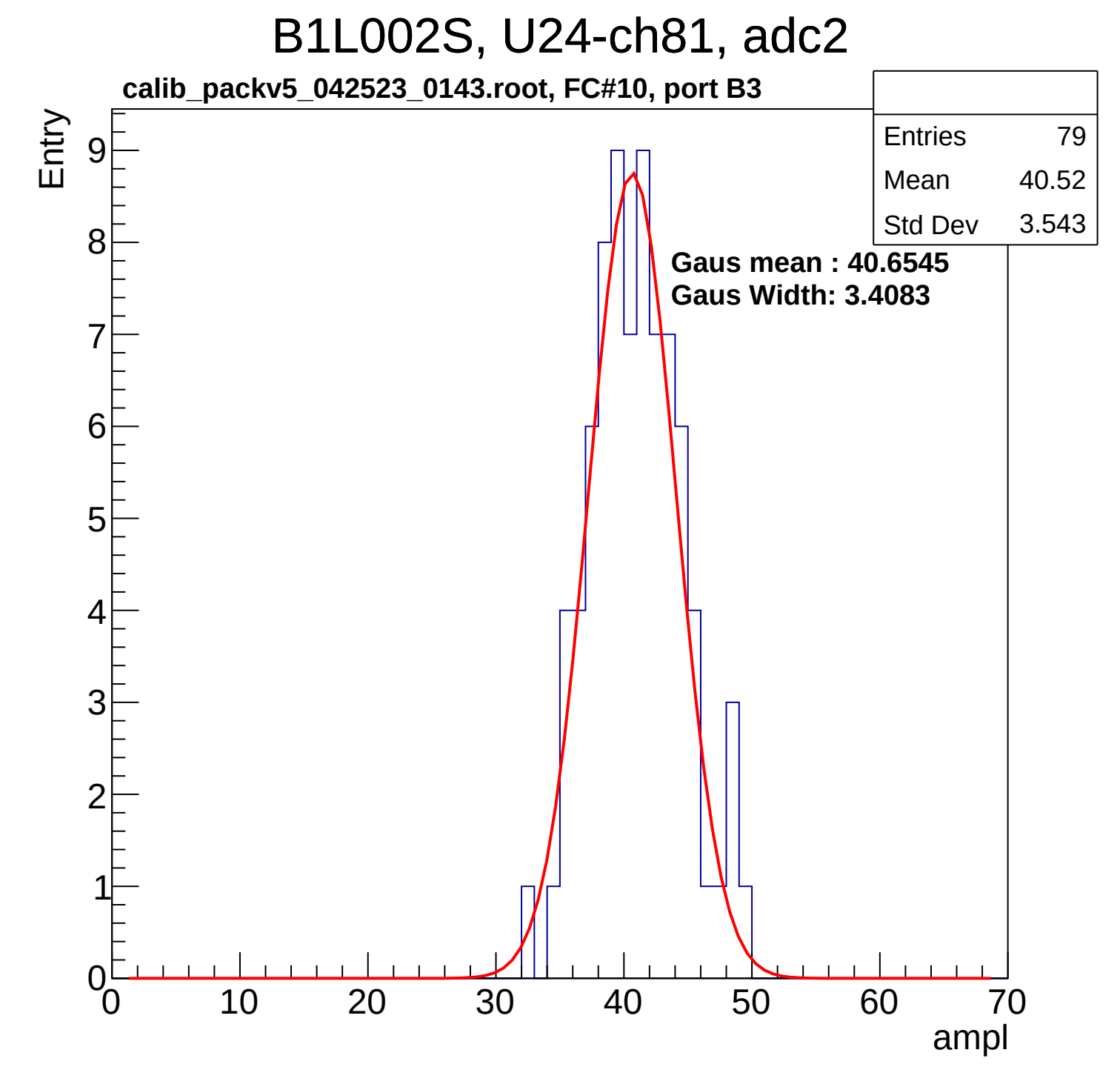
B1L002S, U24-ch81, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 40.52
Std Dev 3.543
Gaus mean : 40.6545
Gaus Width: 3.4083
Entry
ampl
0
1
2
3
4
5
6
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8
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0
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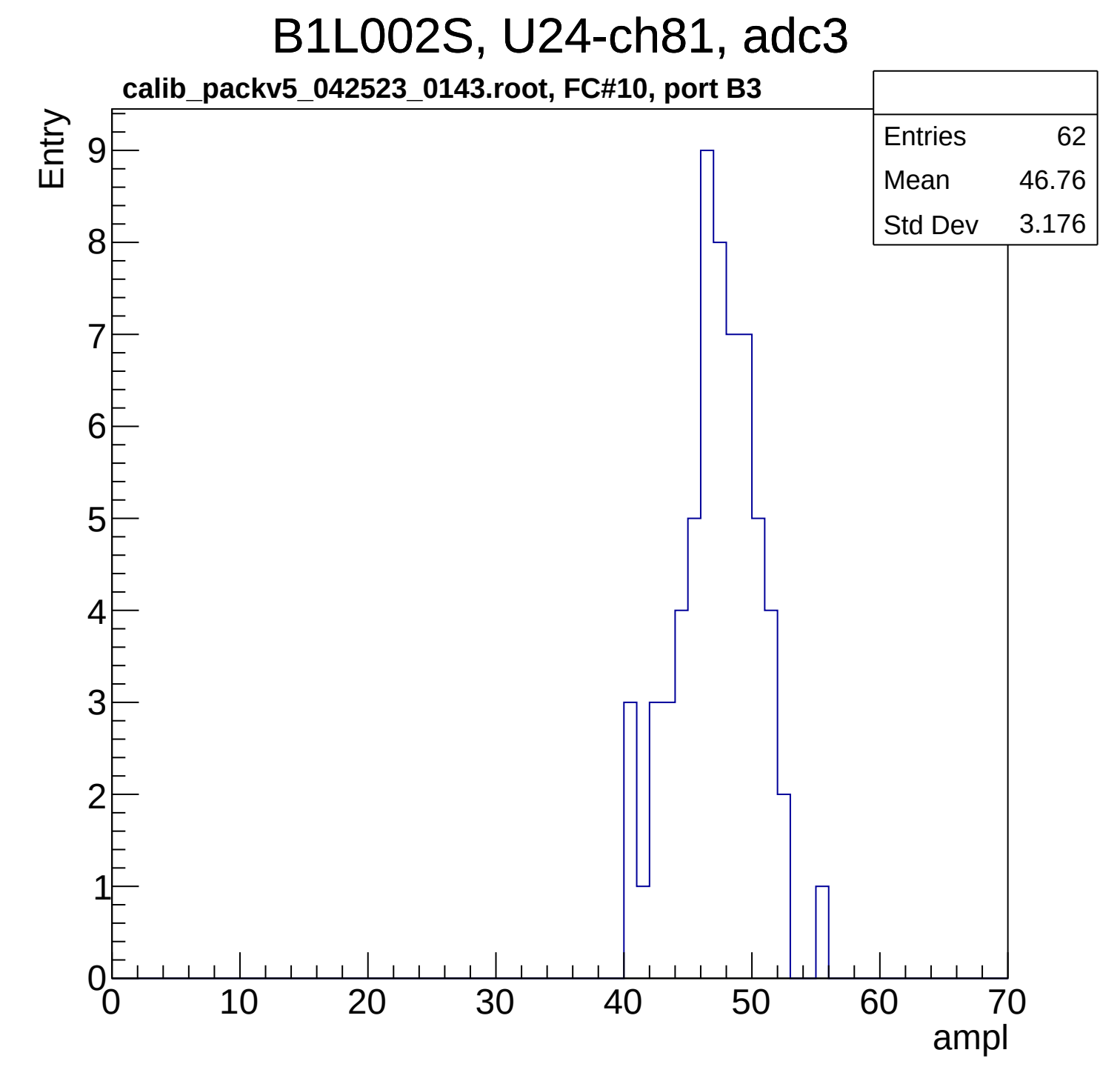
B1L002S, U24-ch81, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 46.76
Std Dev 3.176
Entry
ampl
0
1
2
3
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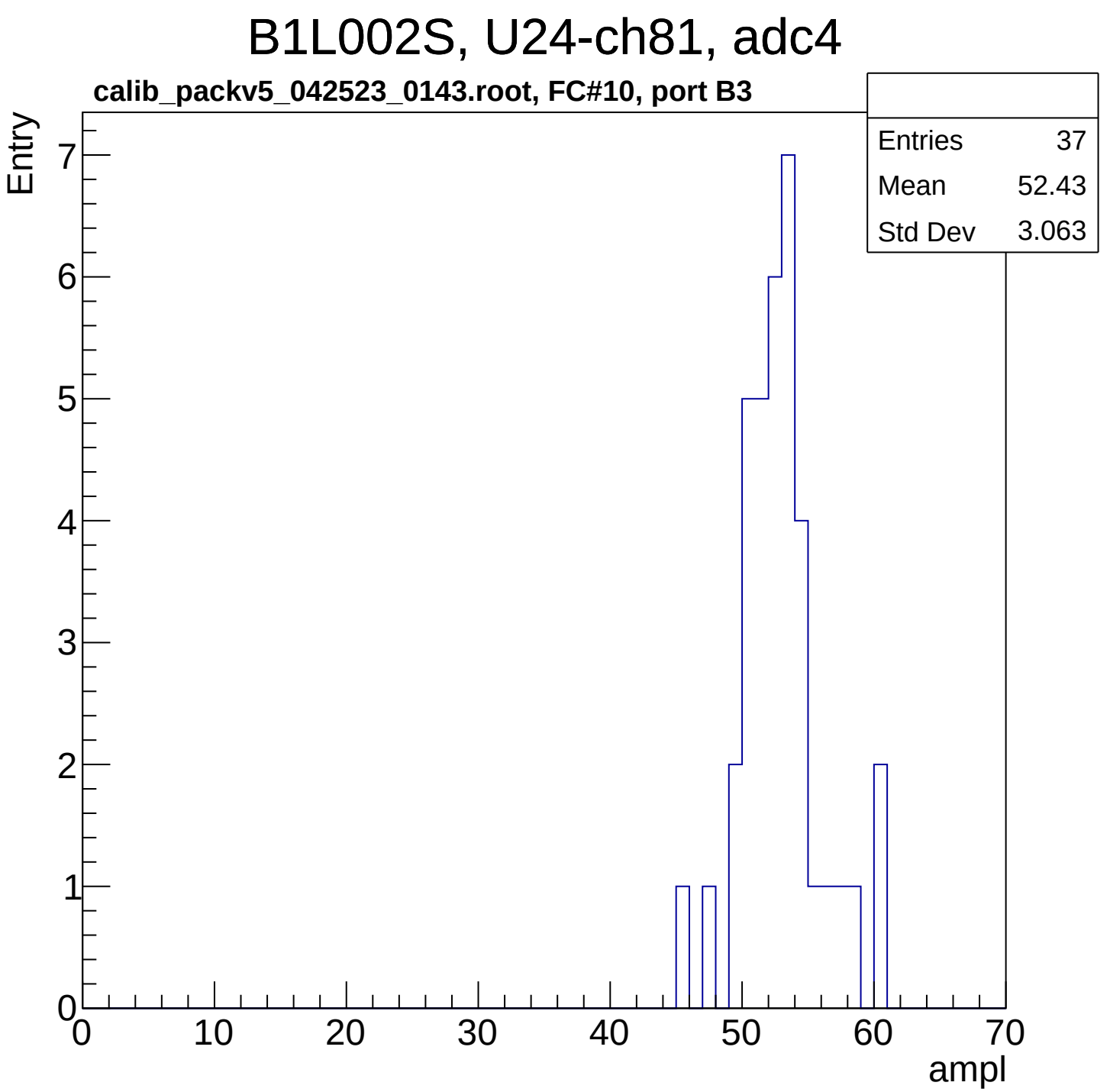
B1L002S, U24-ch81, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 52.43
Std Dev 3.063
Entry
ampl
0
1
2
3
4
5
6
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0
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40
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60
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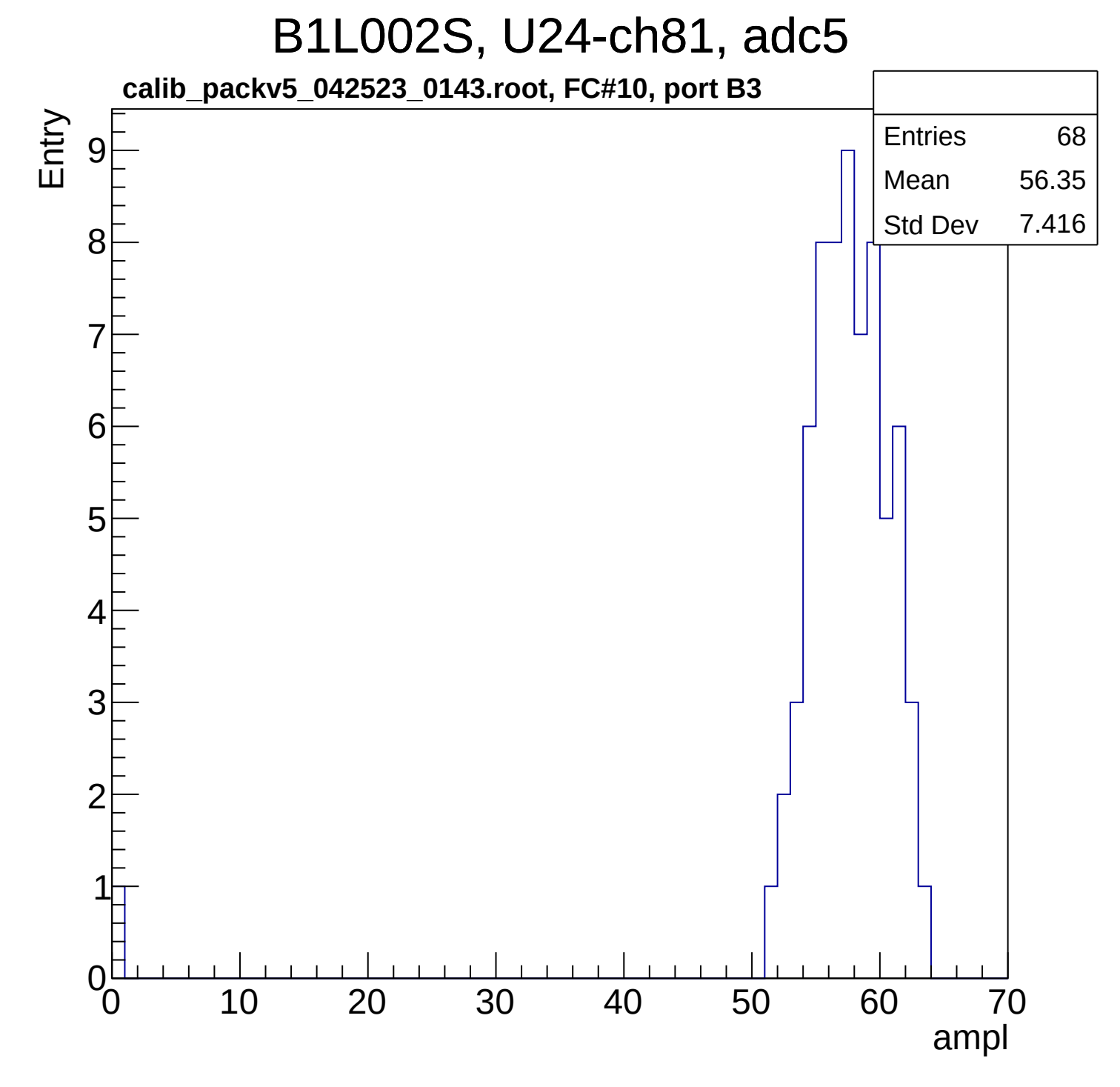
B1L002S, U24-ch81, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 56.35
Std Dev 7.416
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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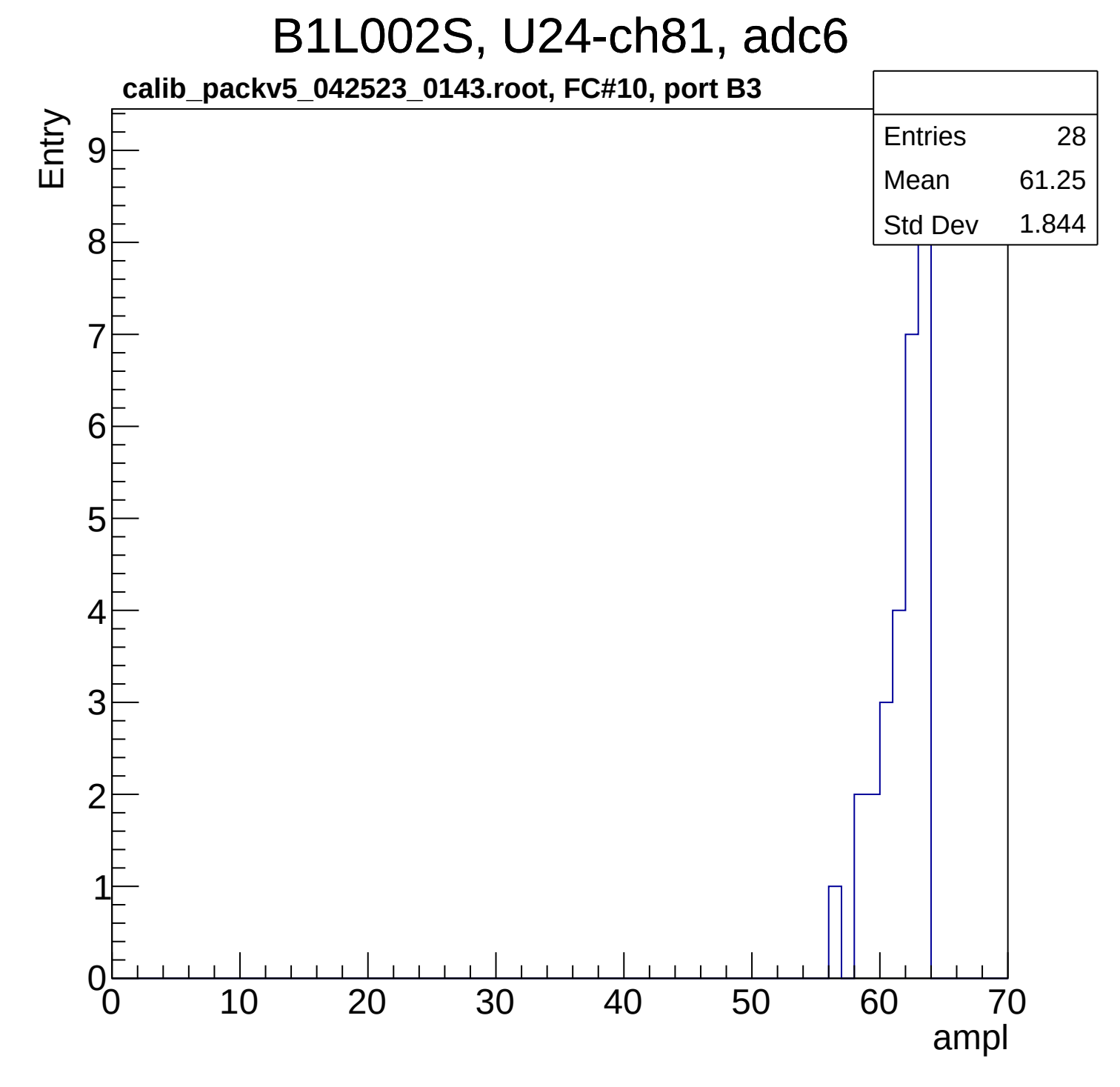
B1L002S, U24-ch81, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 61.25
Std Dev 1.844
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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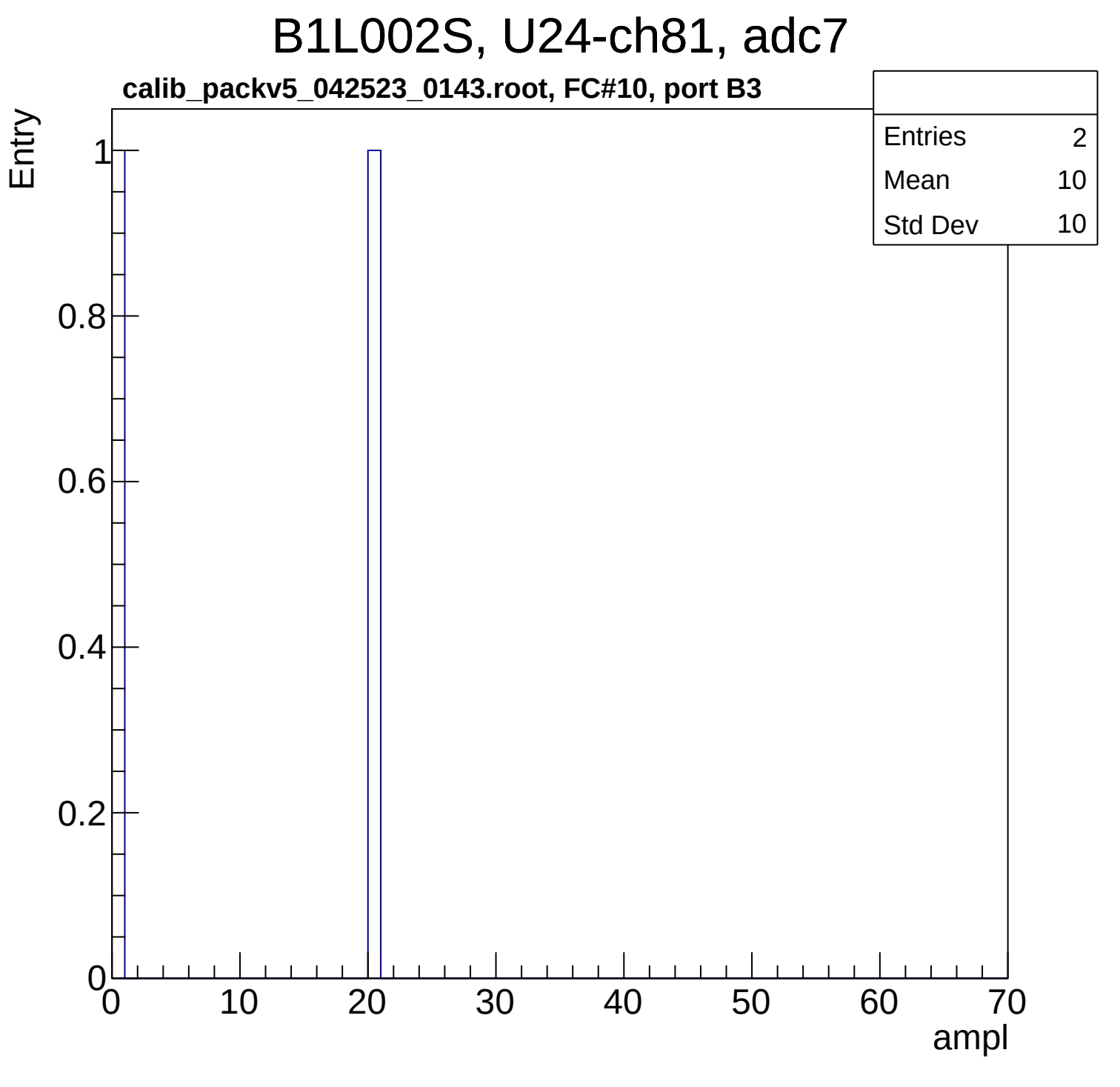
B1L002S, U24-ch81, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10
Std Dev 10
Entry
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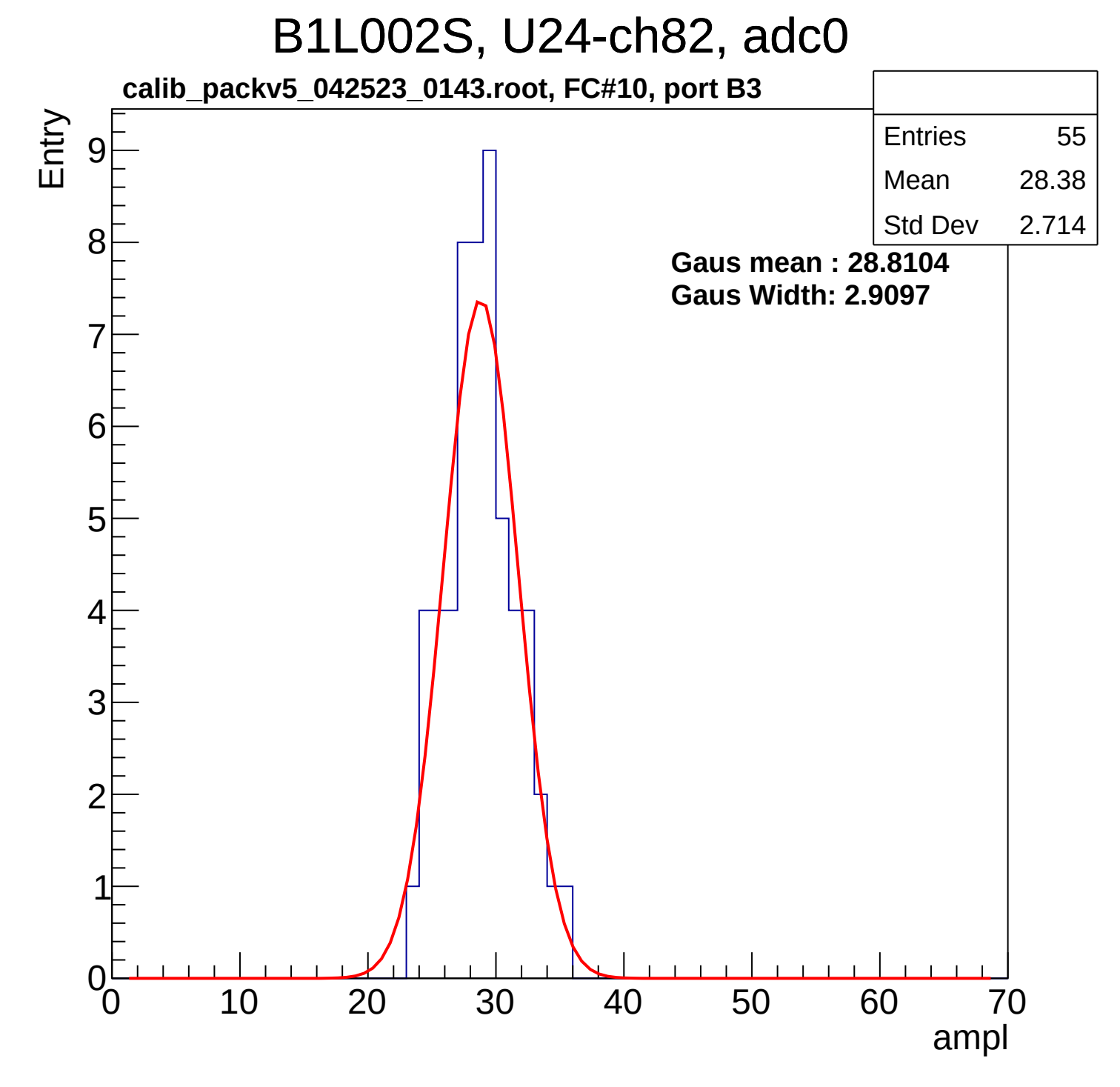
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch82, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 28.38
Std Dev 2.714
Gaus mean : 28.8104
Gaus Width: 2.9097
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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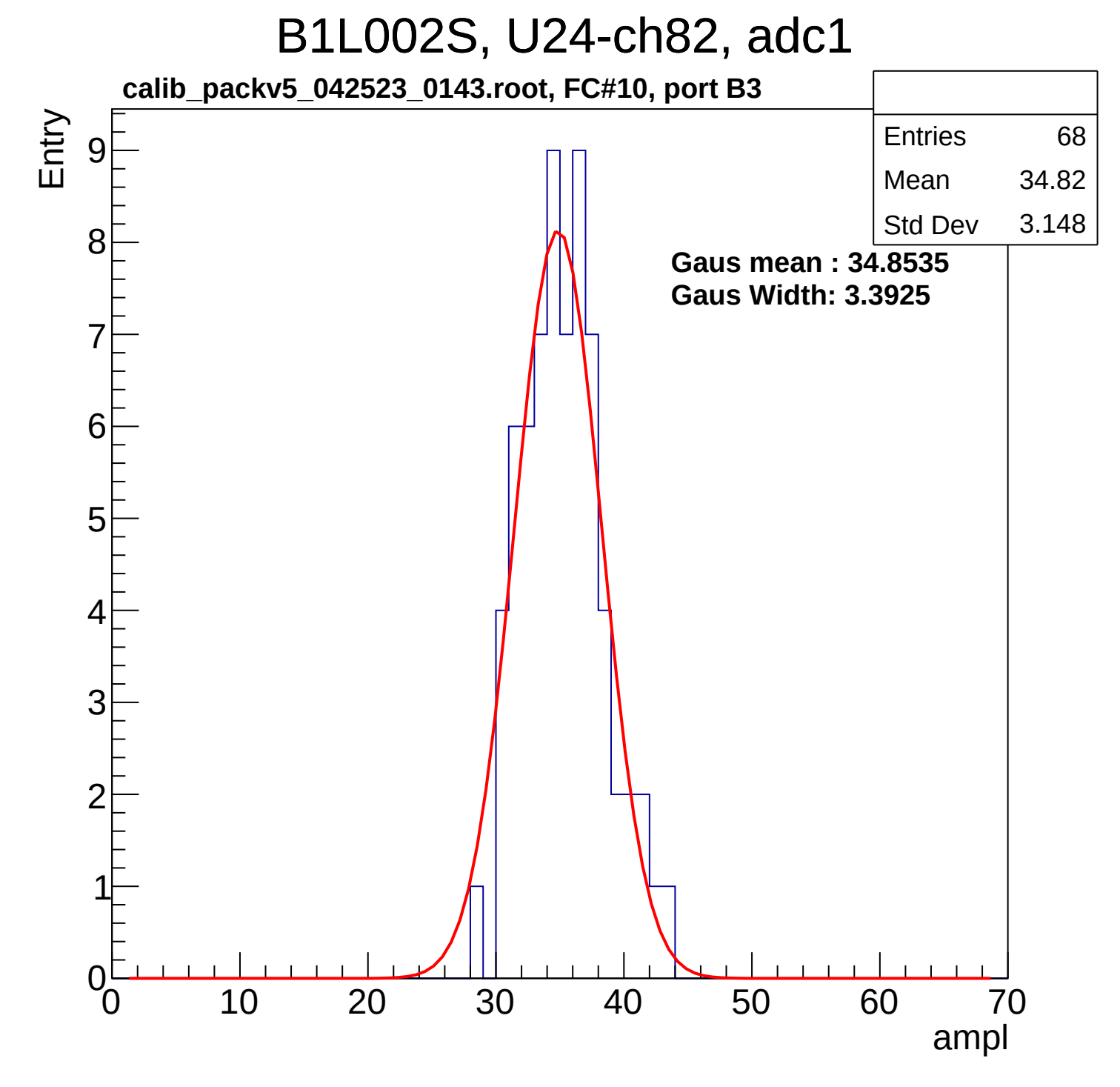
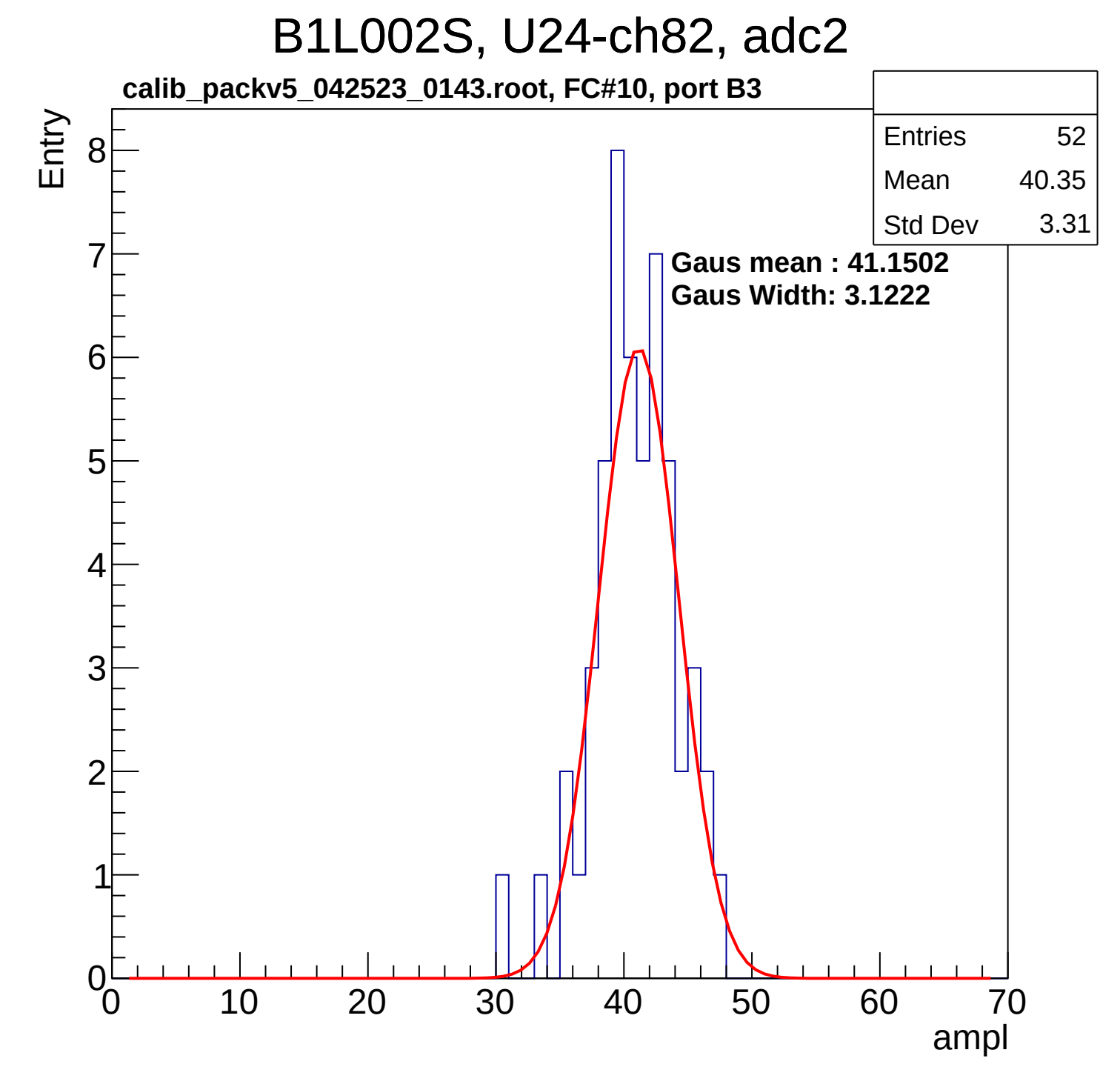

B1L002S, U24-ch82, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.82
Std Dev 3.148
Gaus mean : 34.8535
Gaus Width: 3.3925
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch82, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 40.35
Std Dev 3.31
Gaus mean : 41.1502
Gaus Width: 3.1222
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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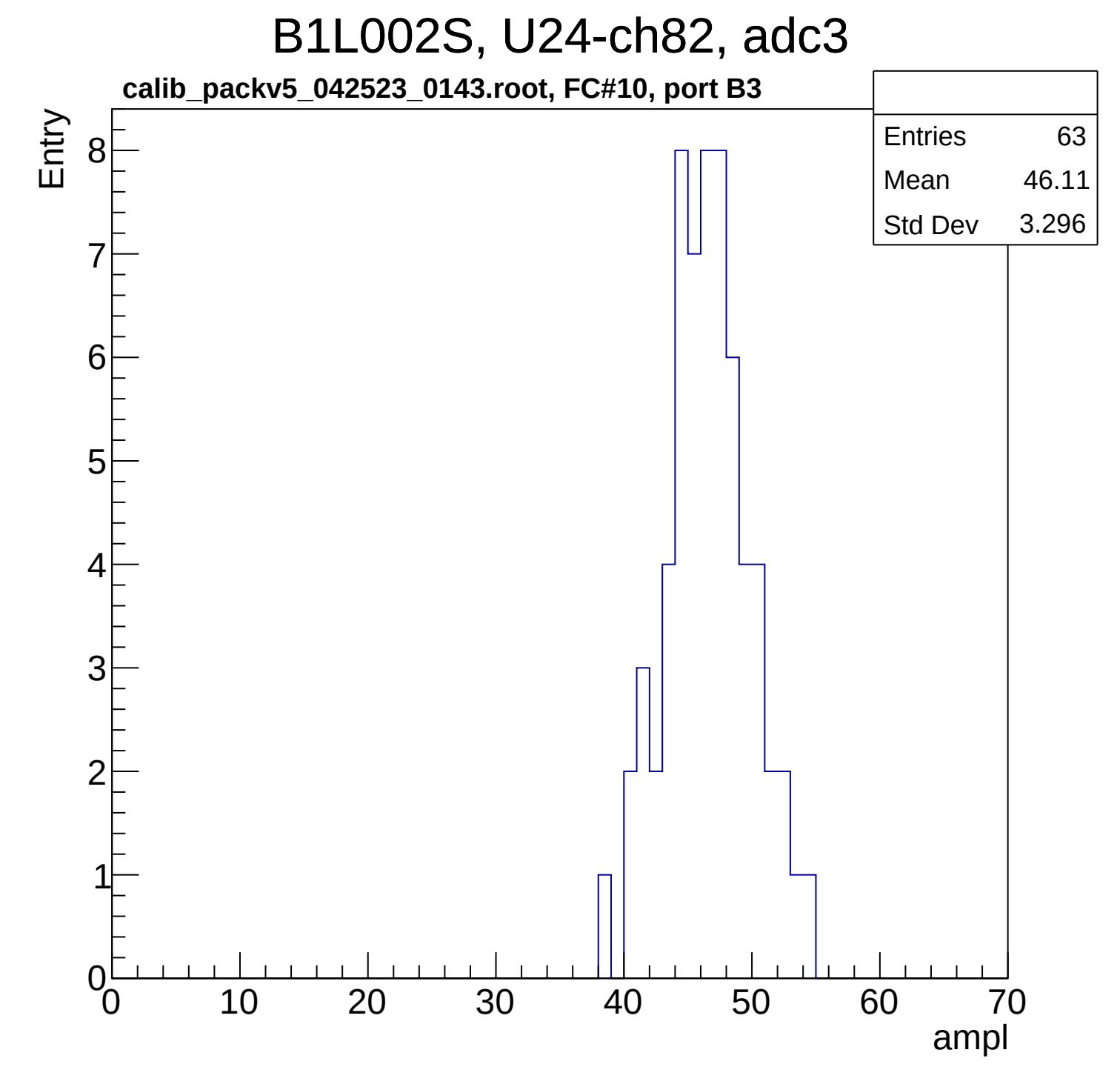
B1L002S, U24-ch82, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 46.11
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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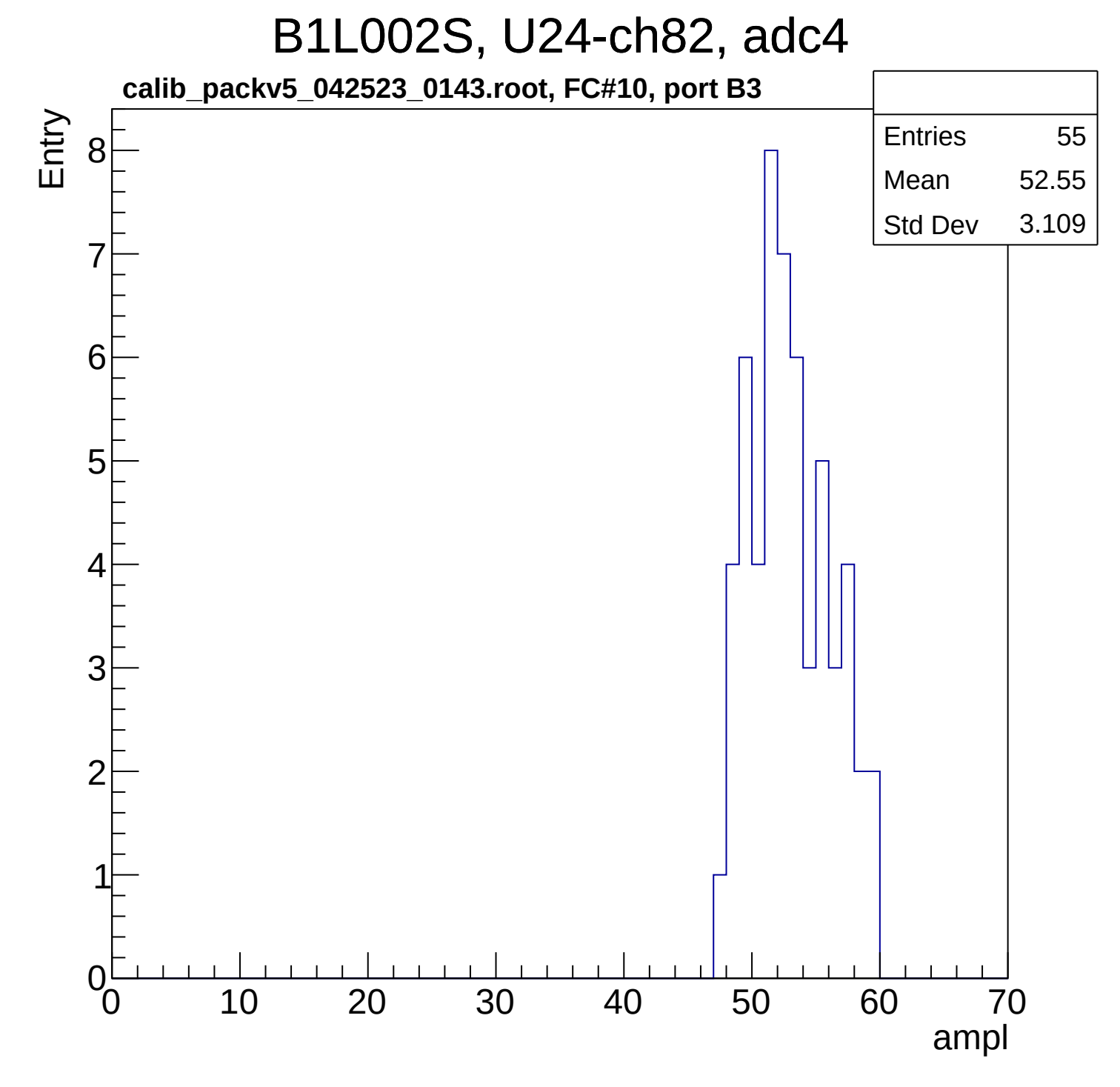
40
50
60
70


B1L002S, U24-ch82, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 52.55
Std Dev 3.109
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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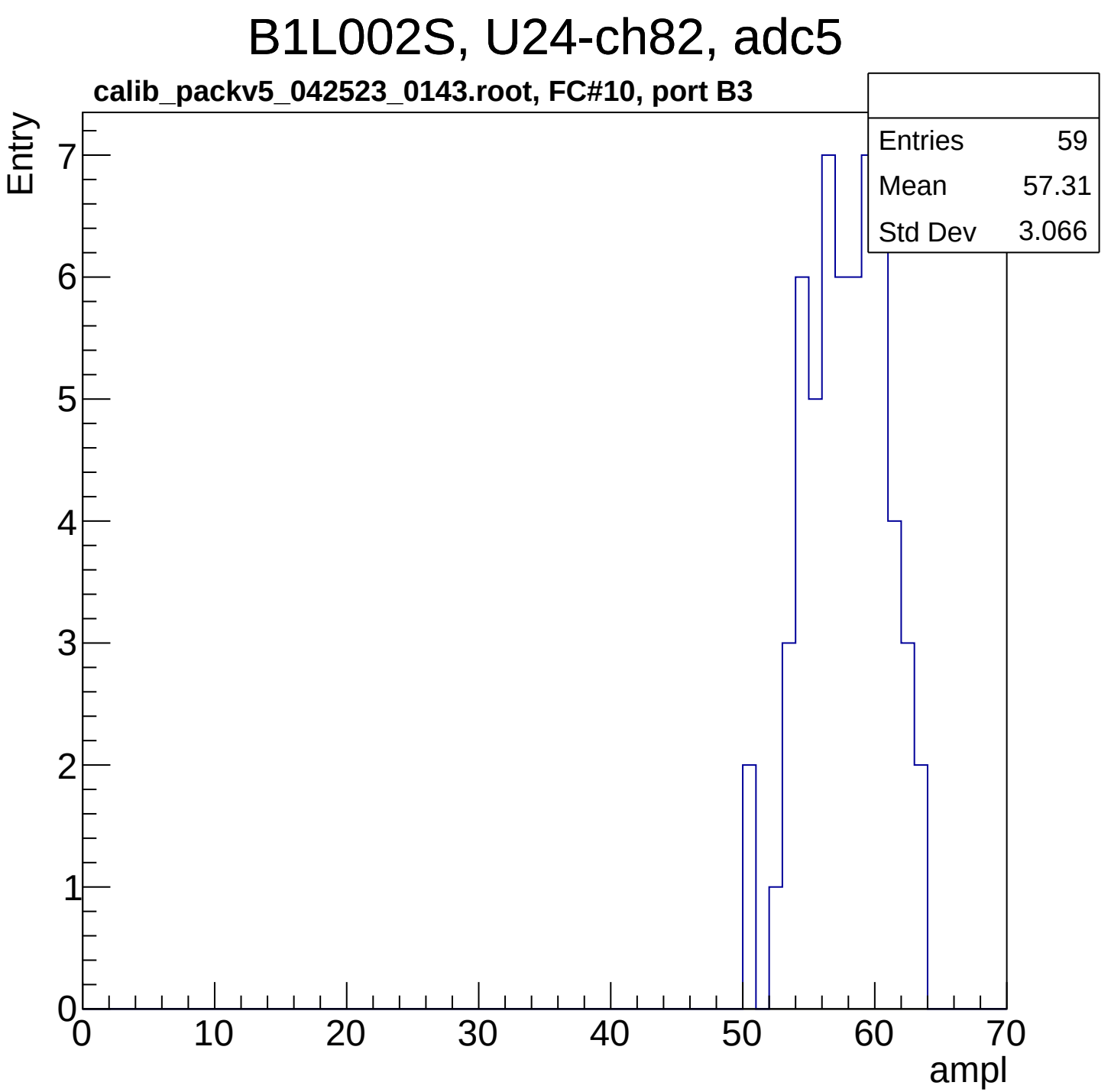
B1L002S, U24-ch82, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 57.31
Std Dev 3.066
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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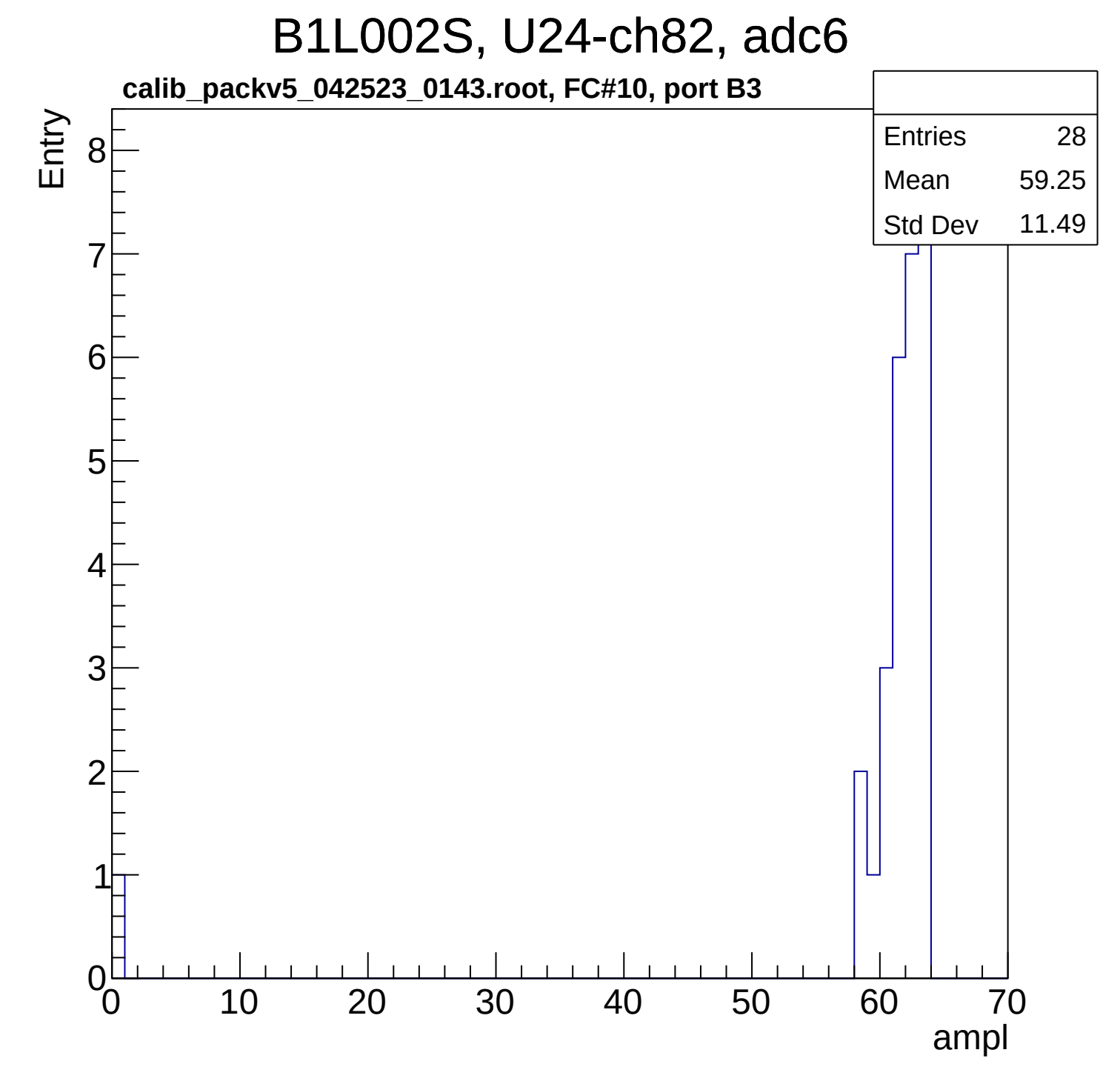
B1L002S, U24-ch82, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 28
Mean 59.25
Std Dev 11.49
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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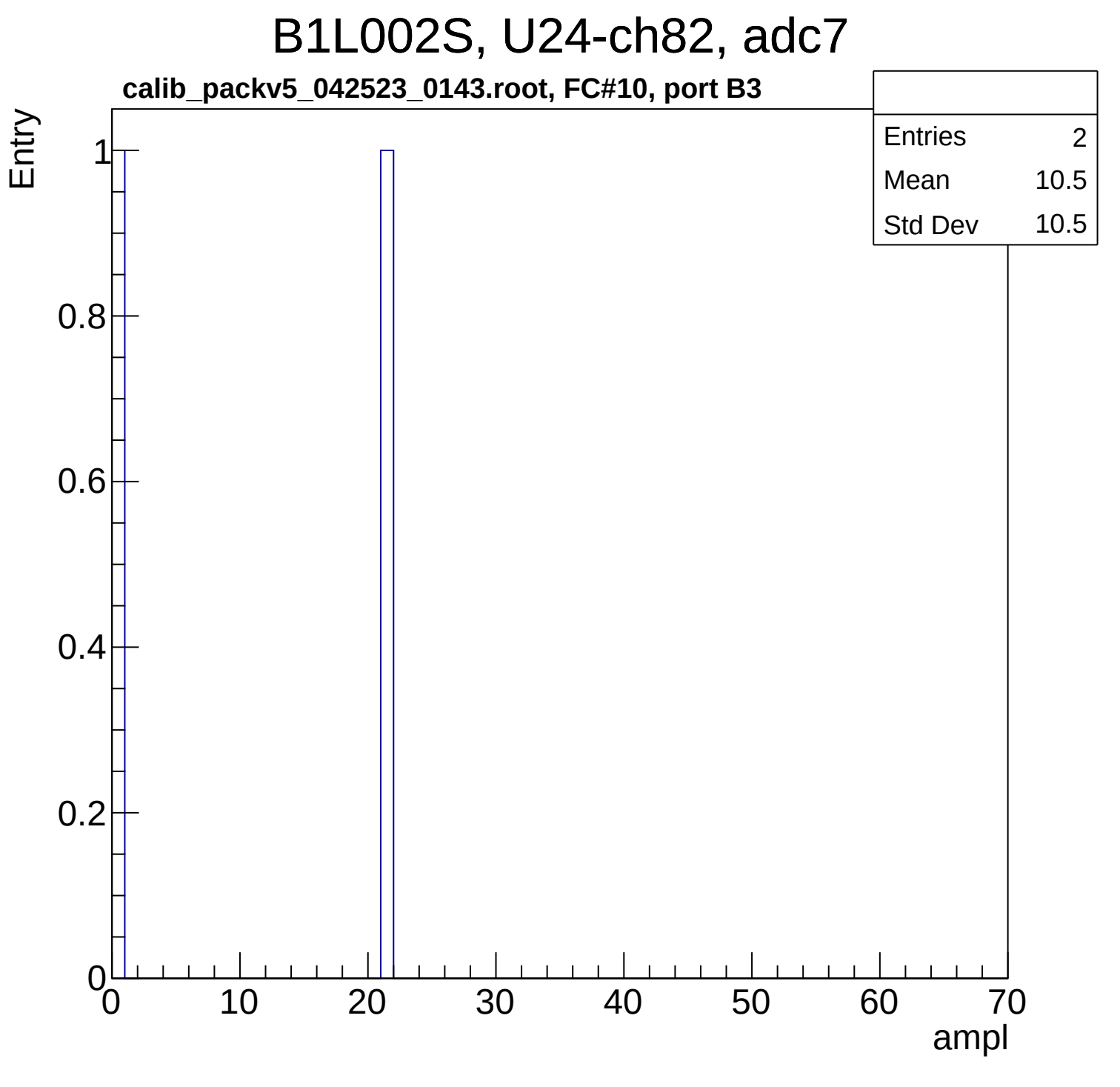
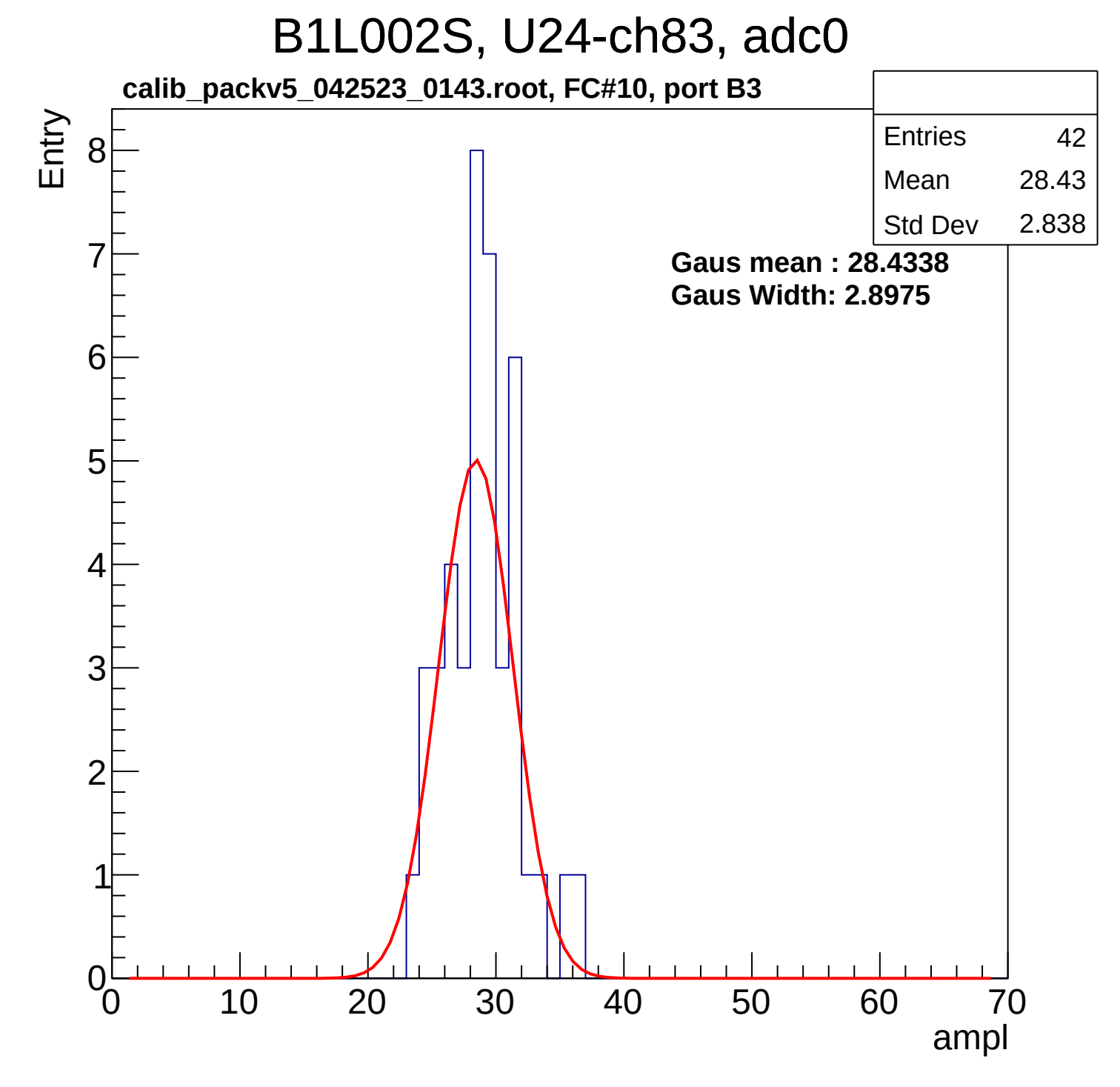
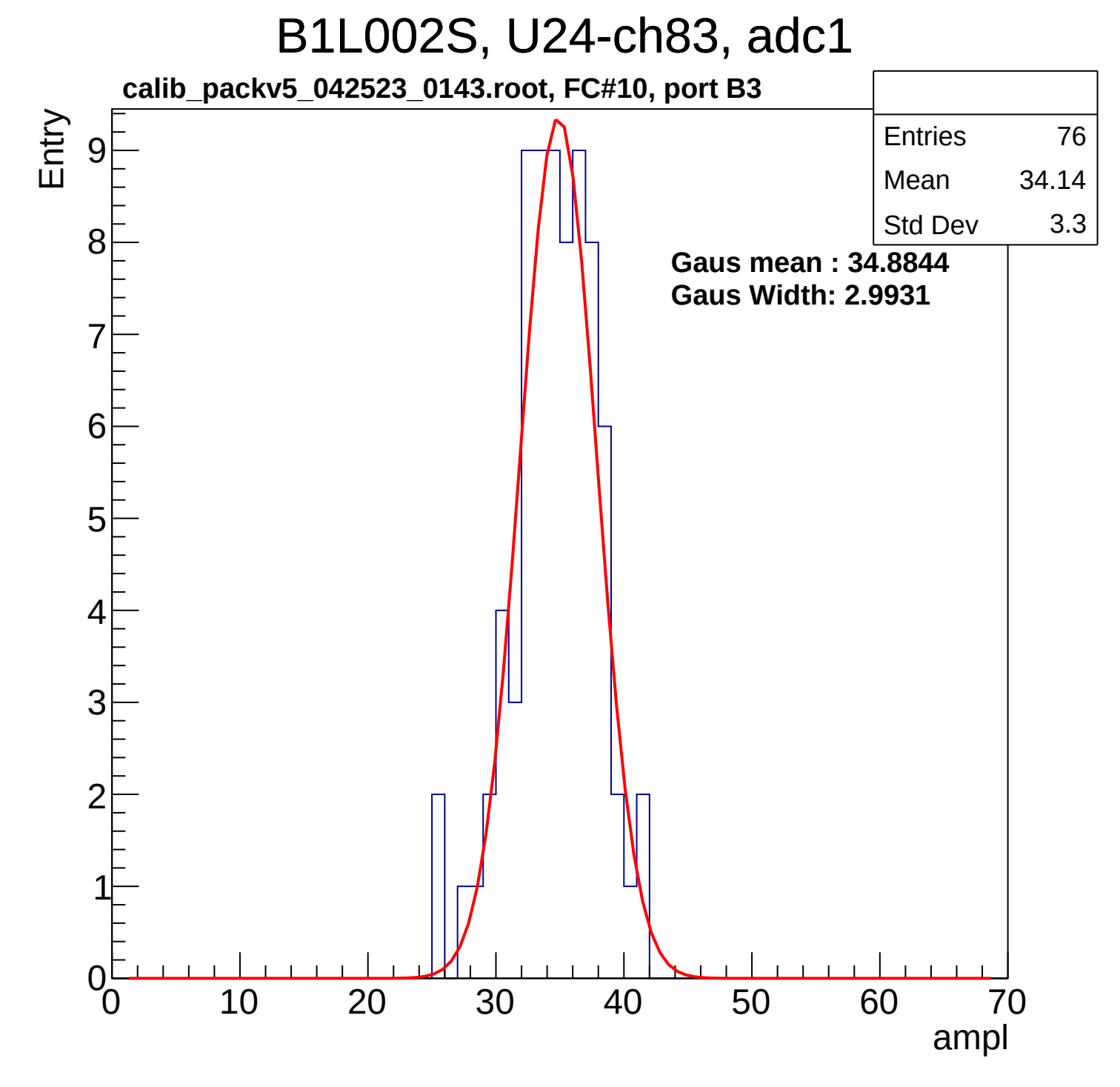

B1L002S, U24-ch82, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 10.5
Std Dev 10.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch83, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 28.43
Std Dev 2.838
Gaus mean : 28.4338
Gaus Width: 2.8975
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch83, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.14
Std Dev 3.3
Gaus mean : 34.8844
Gaus Width: 2.9931
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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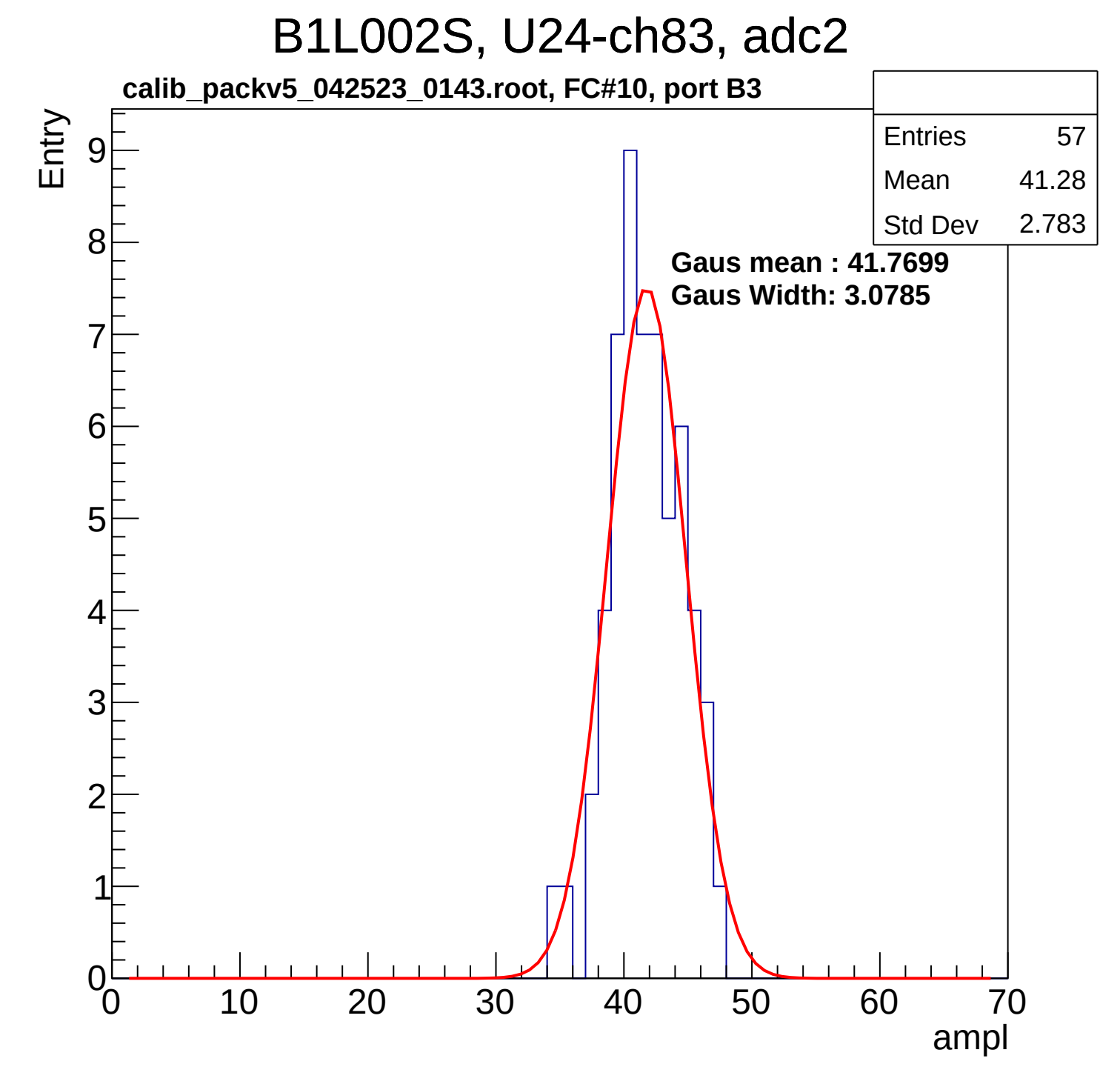
B1L002S, U24-ch83, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 41.28
Std Dev 2.783
Gaus mean : 41.7699
Gaus Width: 3.0785
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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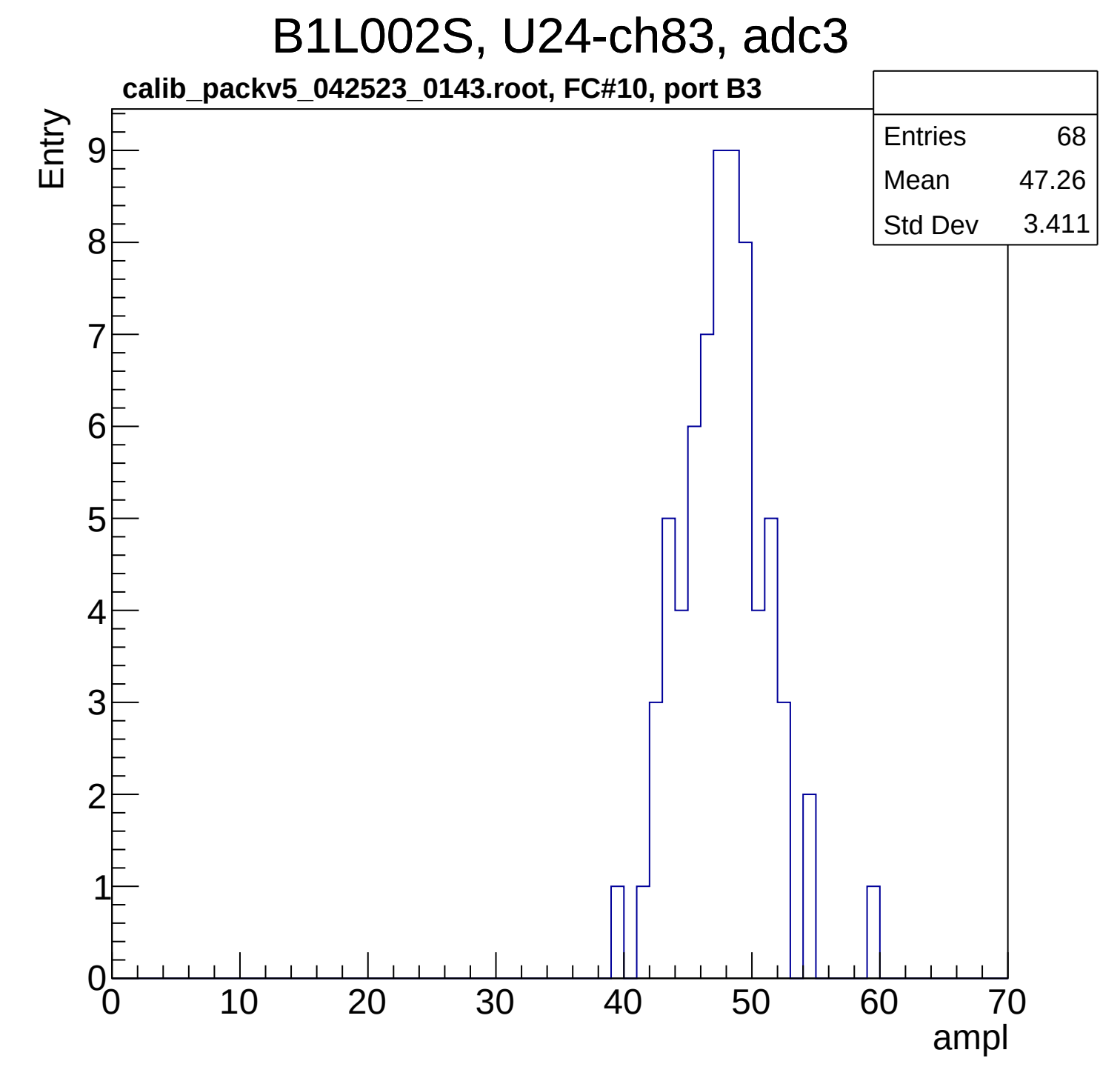
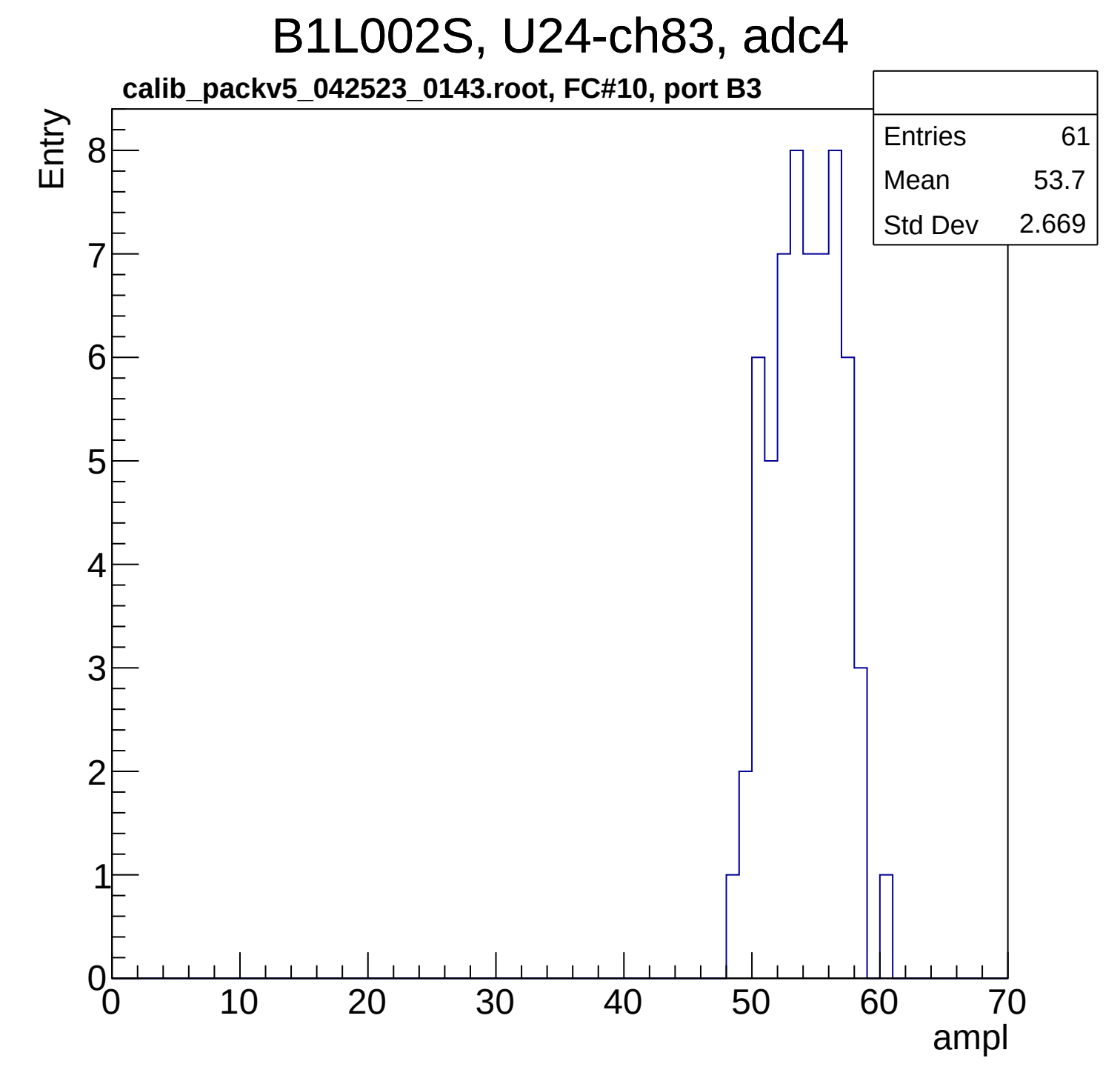

B1L002S, U24-ch83, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 47.26
Std Dev 3.411
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch83, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 53.7
Std Dev 2.669
Entry
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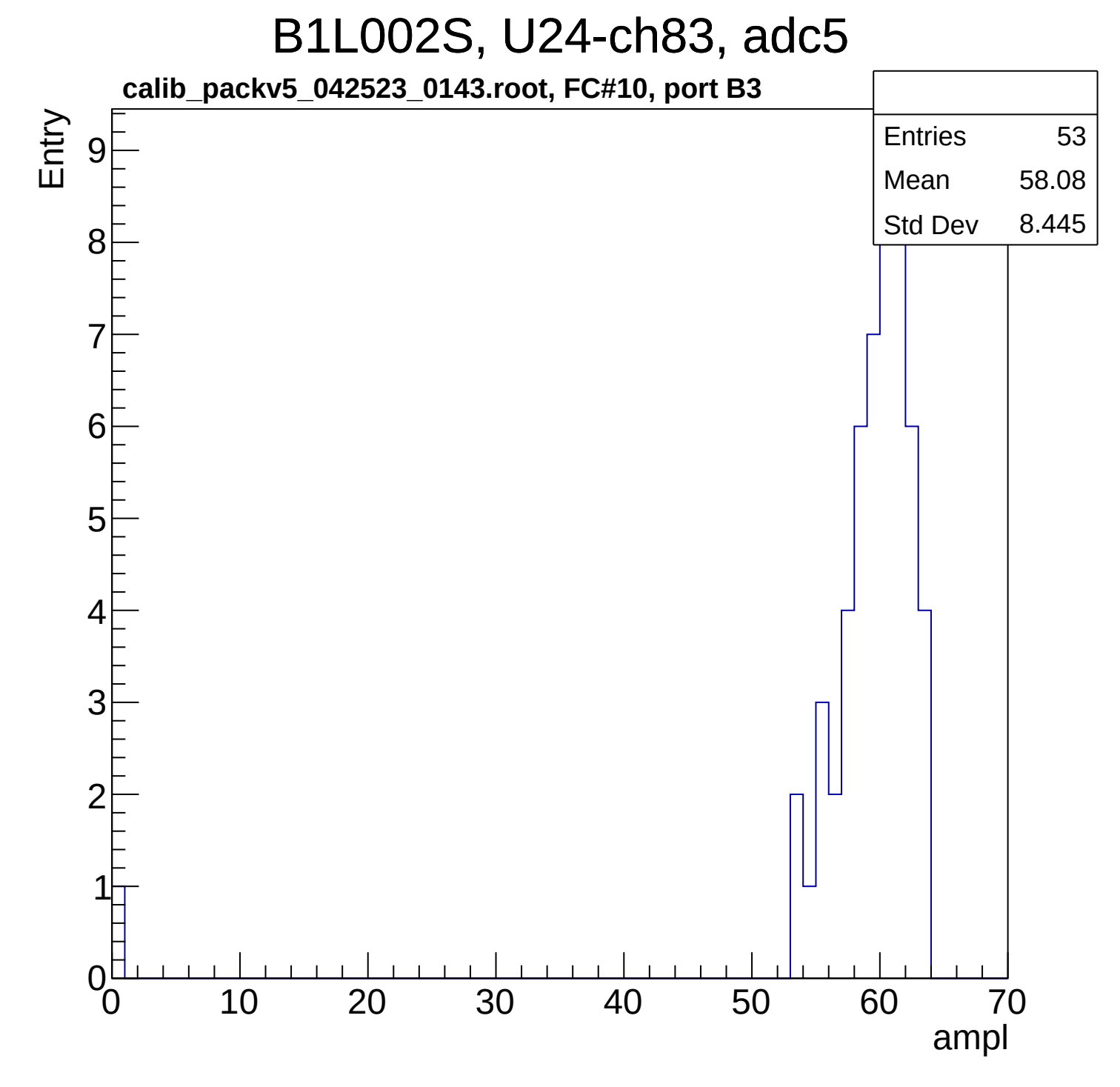
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch83, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 58.08
Std Dev 8.445
Entry
ampl
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2
3
4
5
6
7
8
9
0
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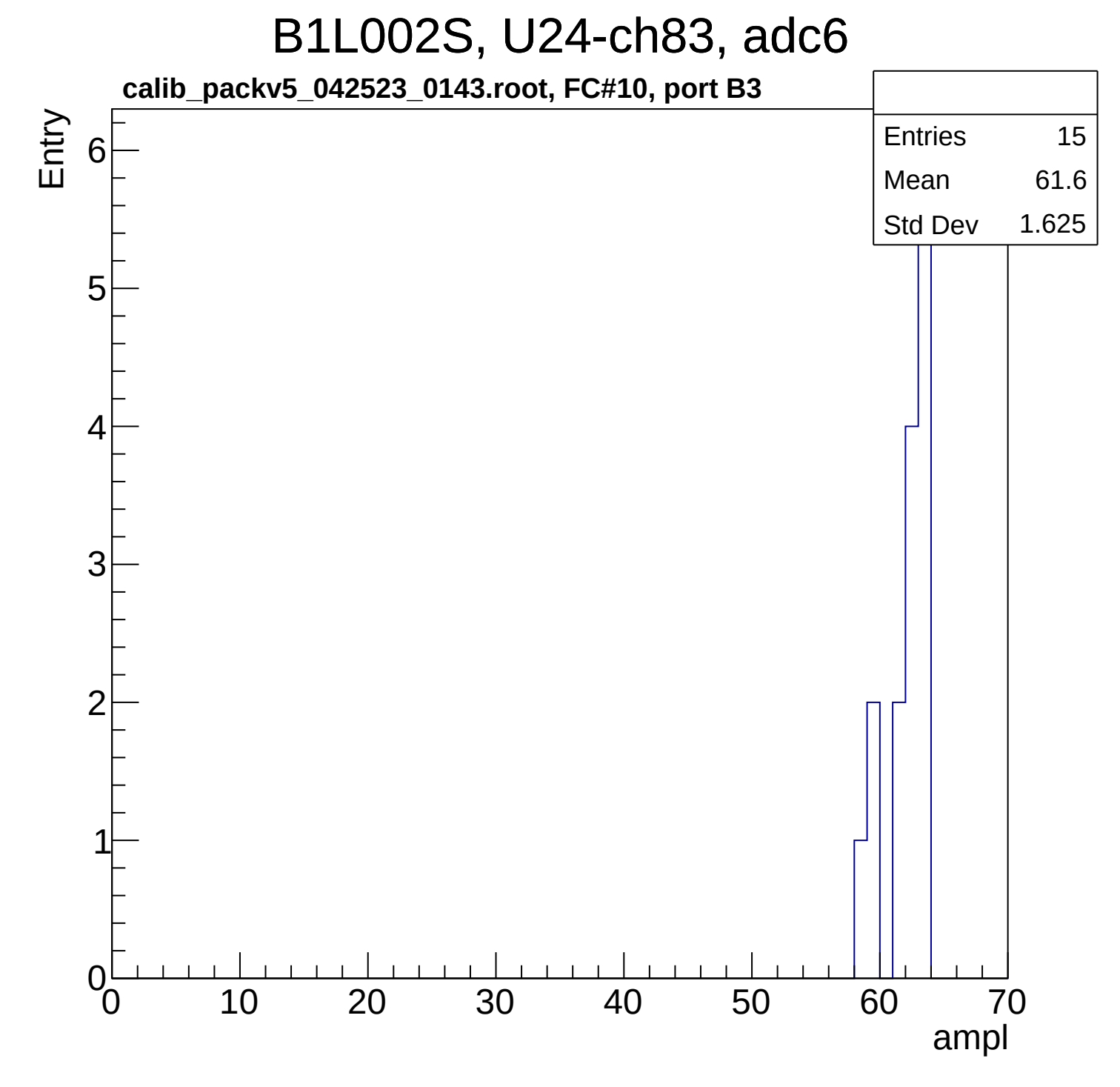
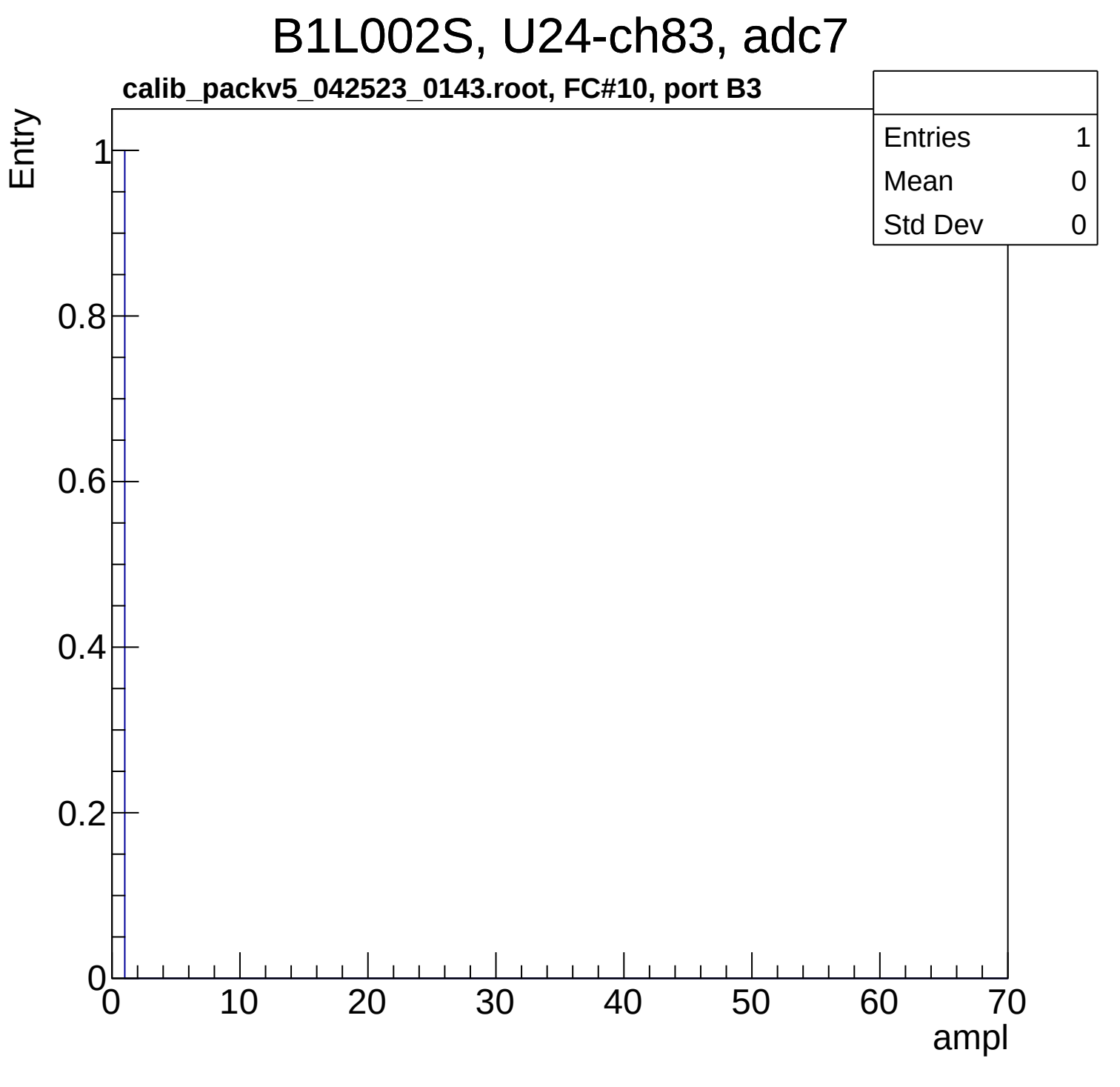
20
30
40
50
60
70


B1L002S, U24-ch83, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.6
Std Dev 1.625
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch83, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
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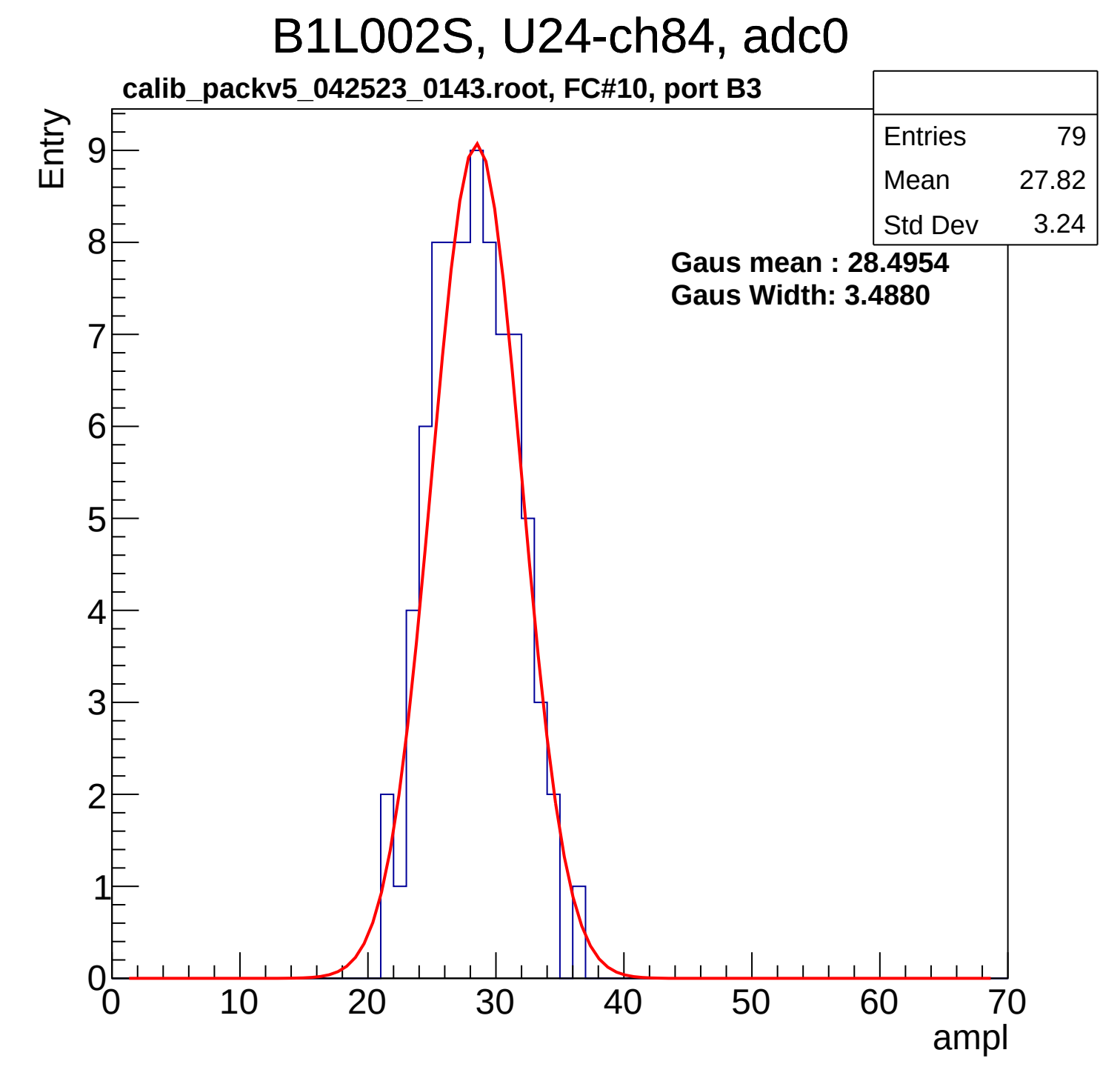
40
50
60
70
ampl


B1L002S, U24-ch84, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 27.82
Std Dev 3.24
Gaus mean : 28.4954
Gaus Width: 3.4880
Entry
ampl
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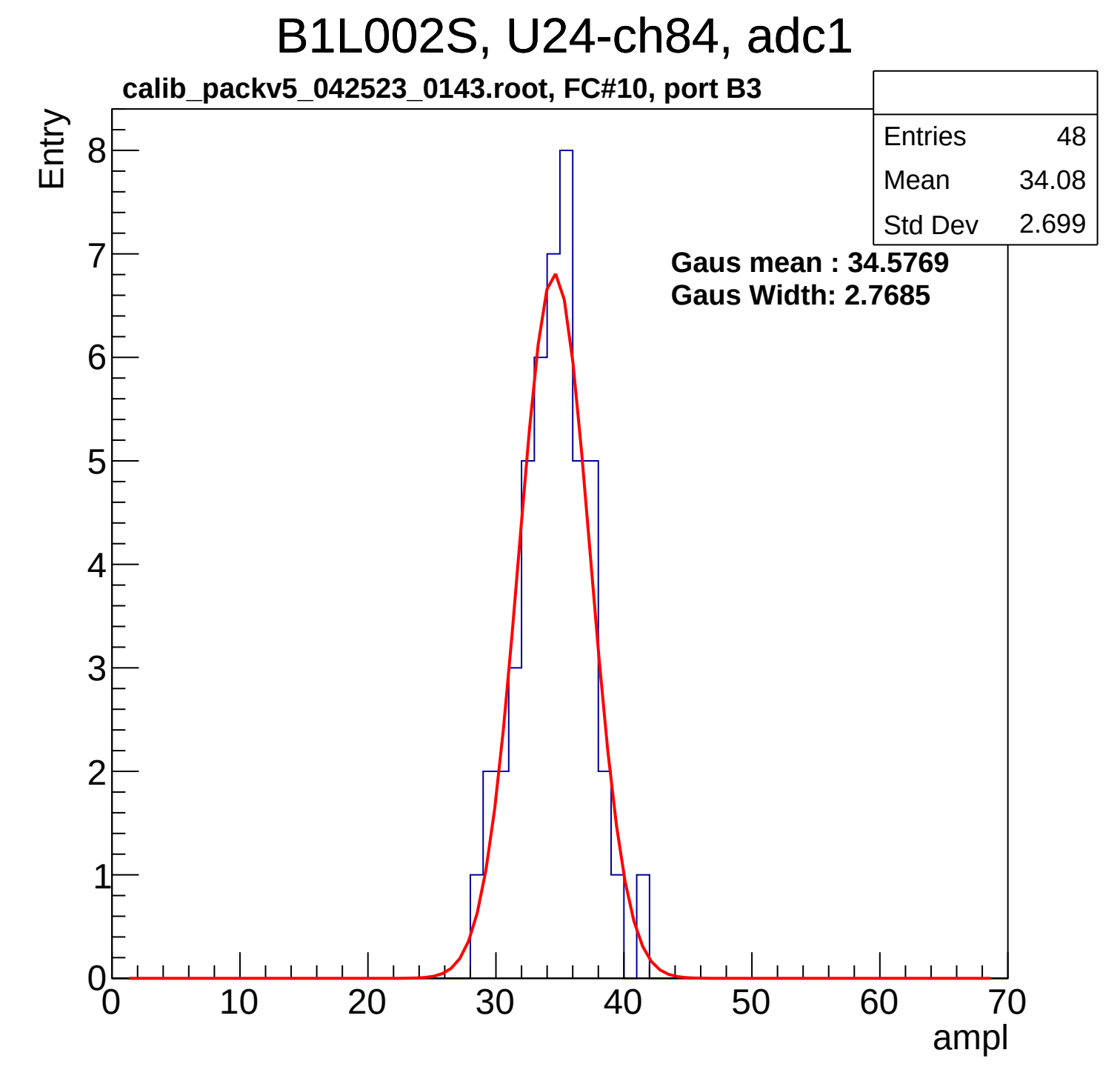
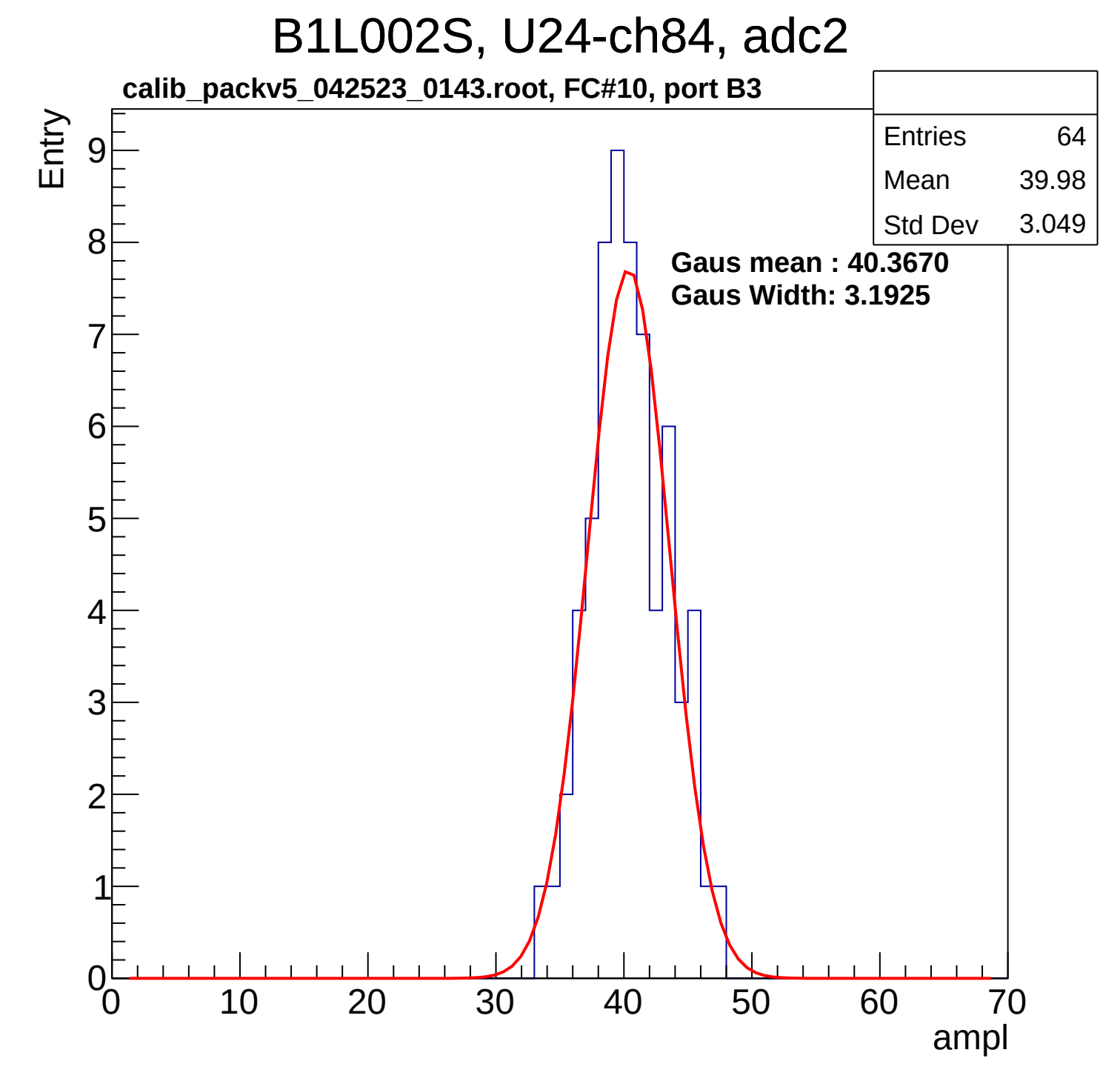
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch84, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 34.08
Std Dev 2.699
Gaus mean : 34.5769
Gaus Width: 2.7685
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch84, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 39.98
Std Dev 3.049
Gaus mean : 40.3670
Gaus Width: 3.1925
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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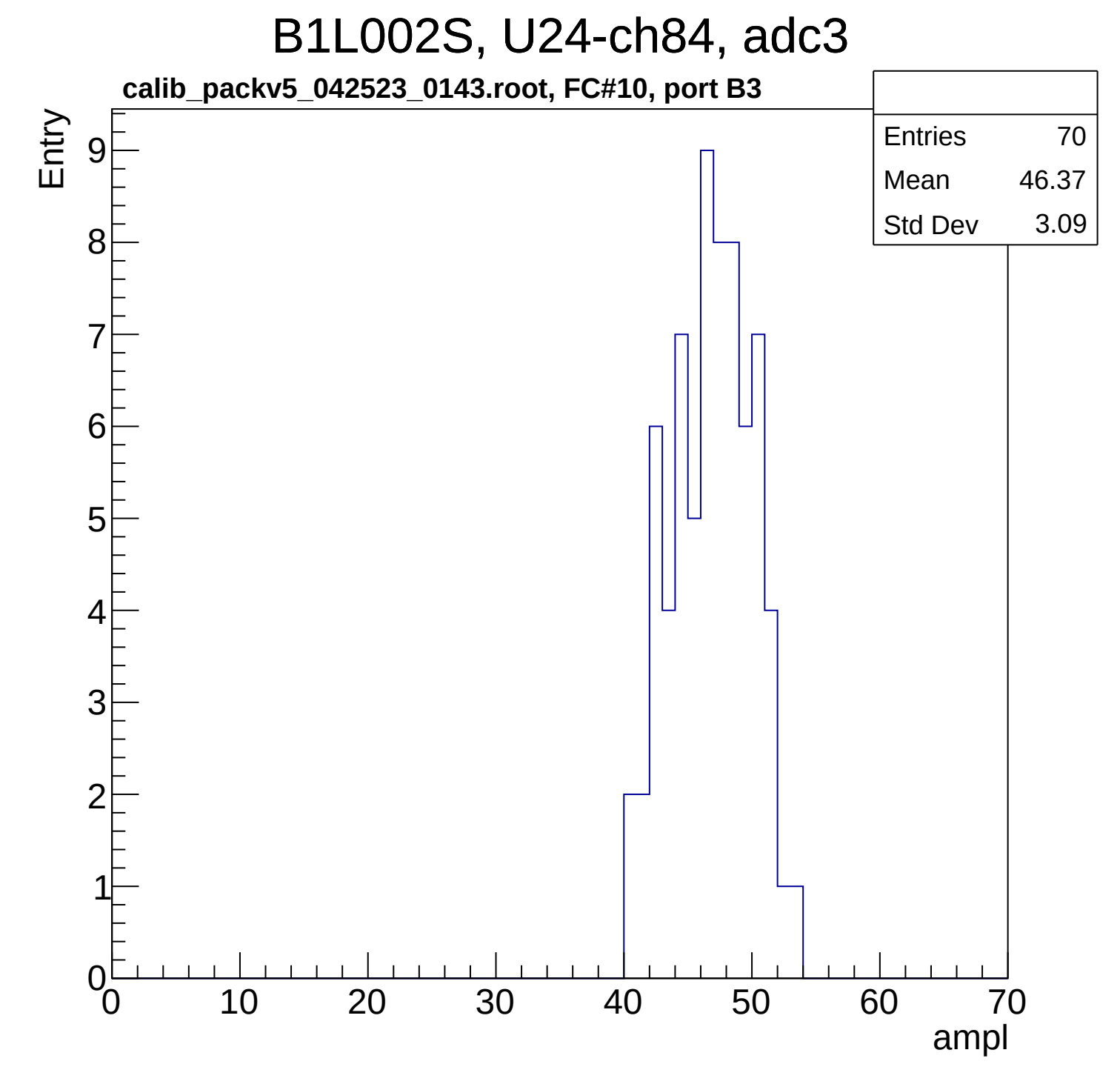
B1L002S, U24-ch84, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.37
Std Dev 3.09
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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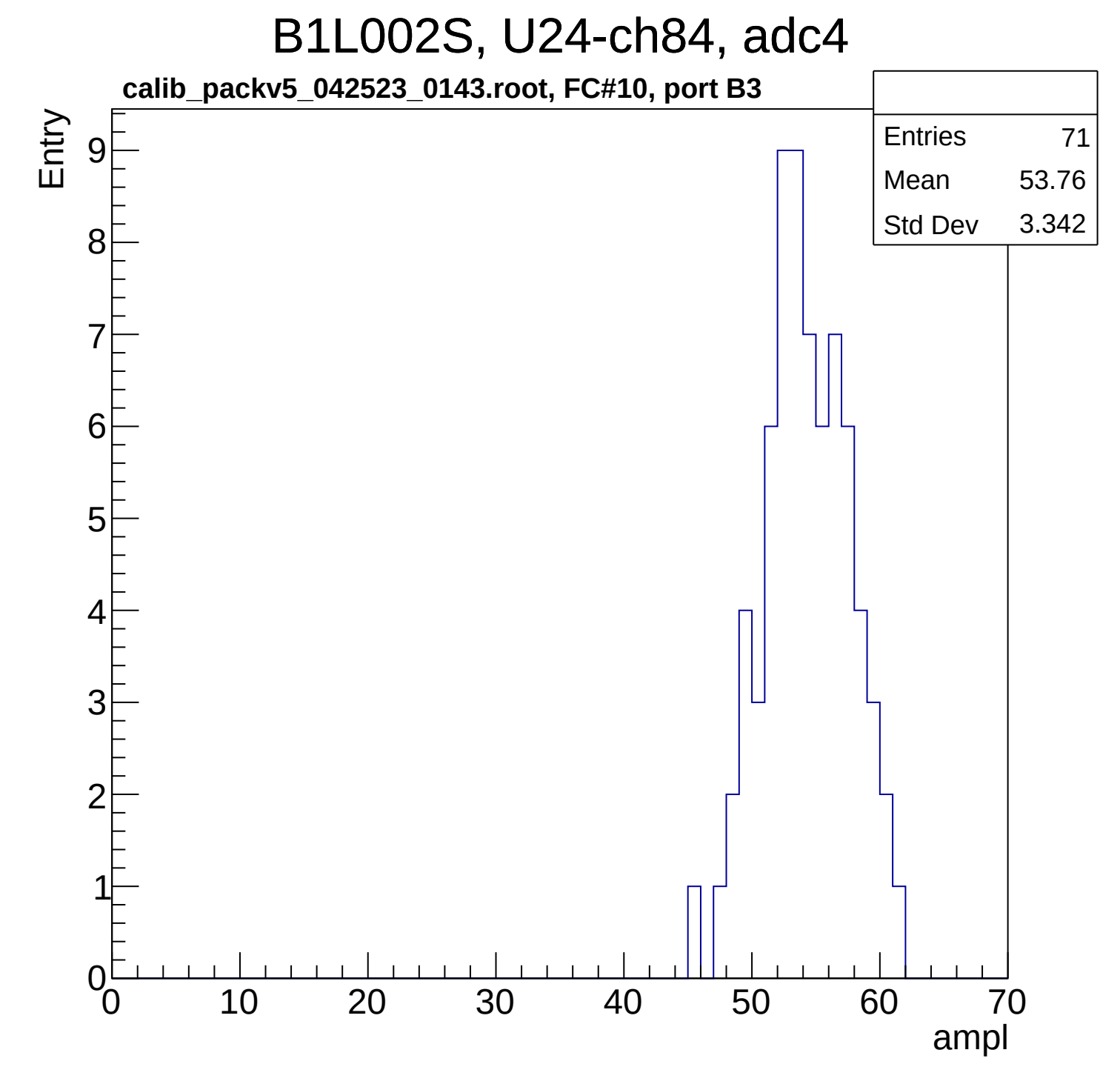
B1L002S, U24-ch84, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 53.76
Std Dev 3.342
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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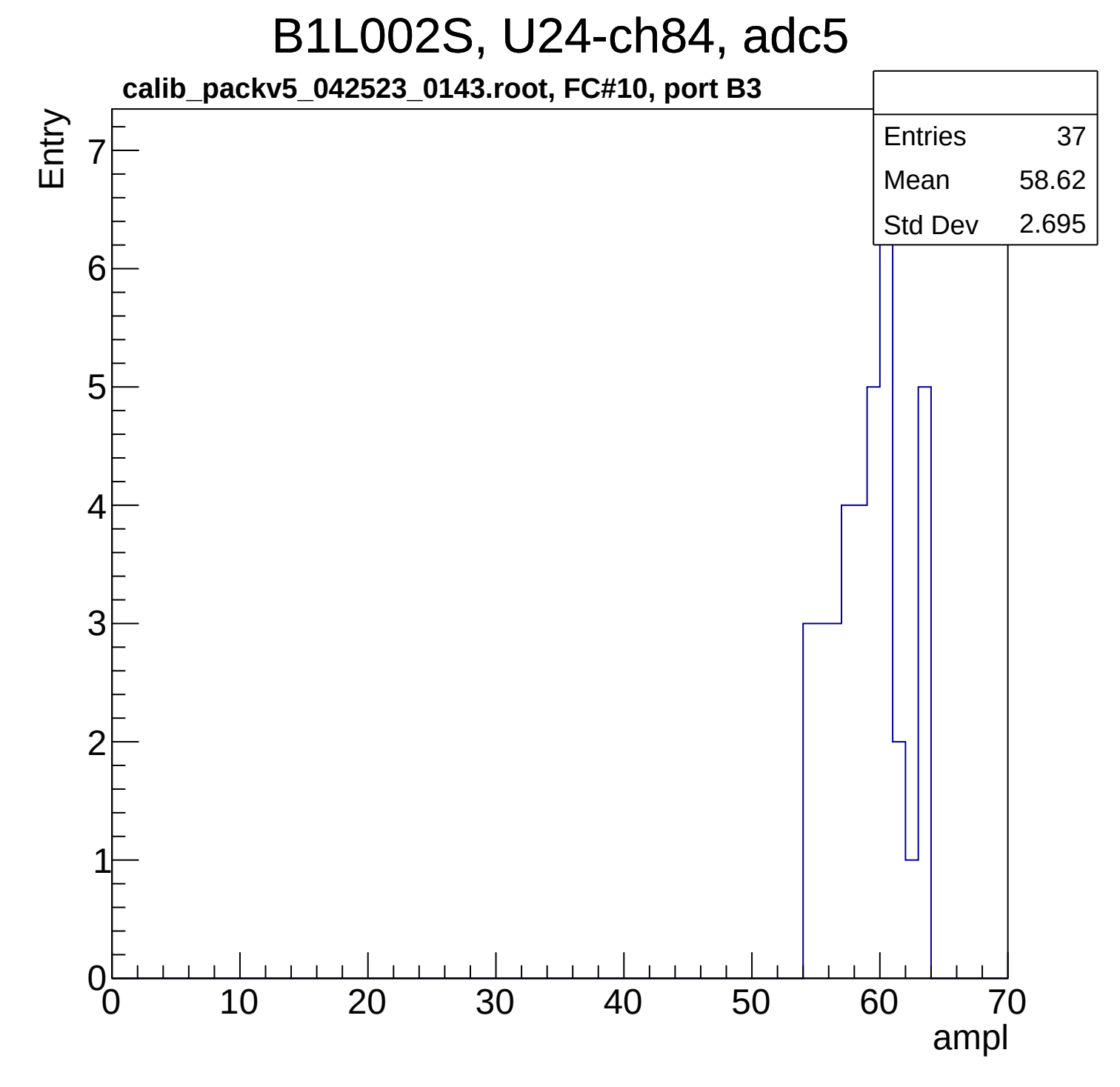
B1L002S, U24-ch84, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.62
Std Dev 2.695
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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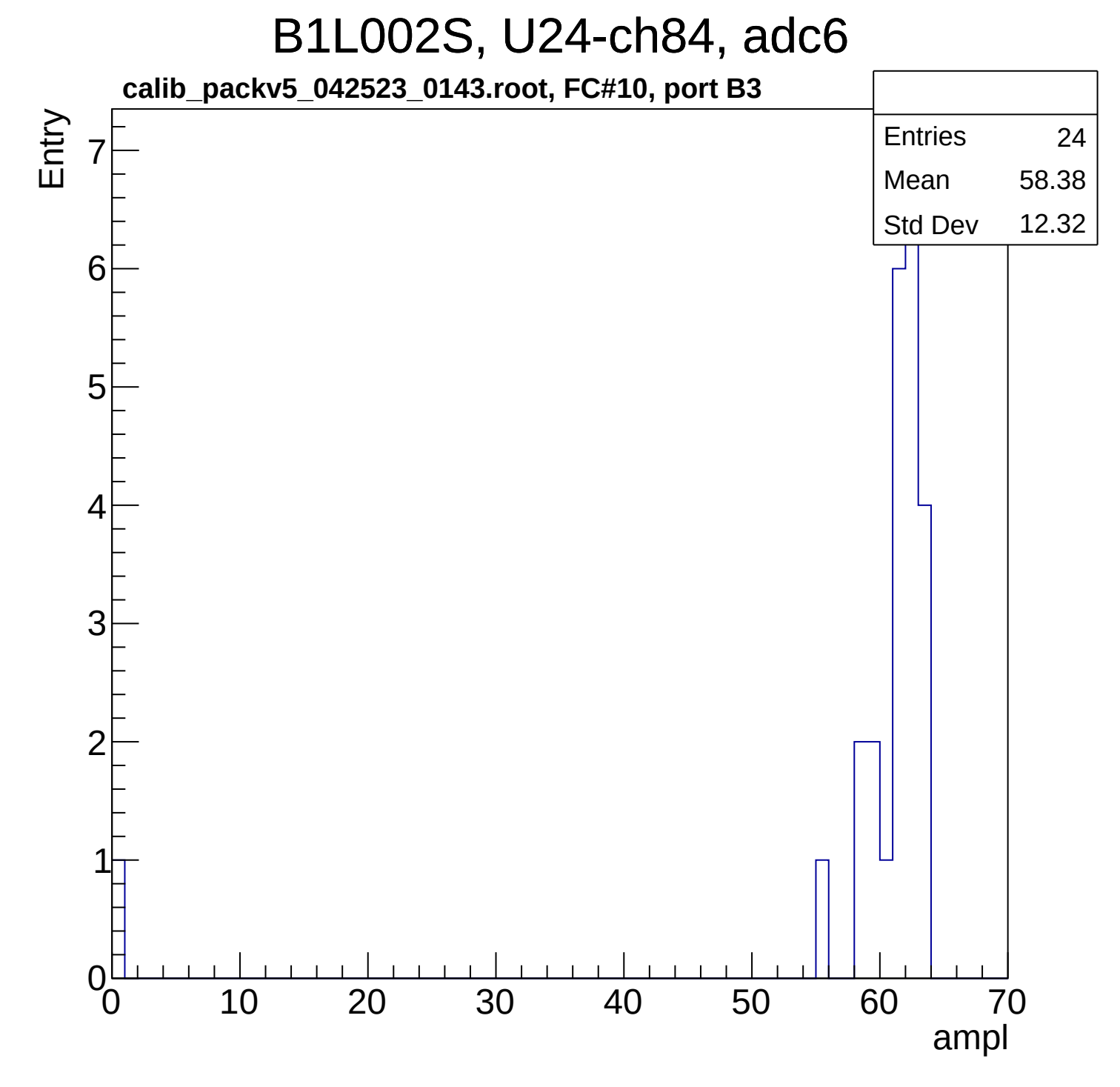
B1L002S, U24-ch84, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.38
Std Dev 12.32
Entry
ampl
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2
3
4
5
6
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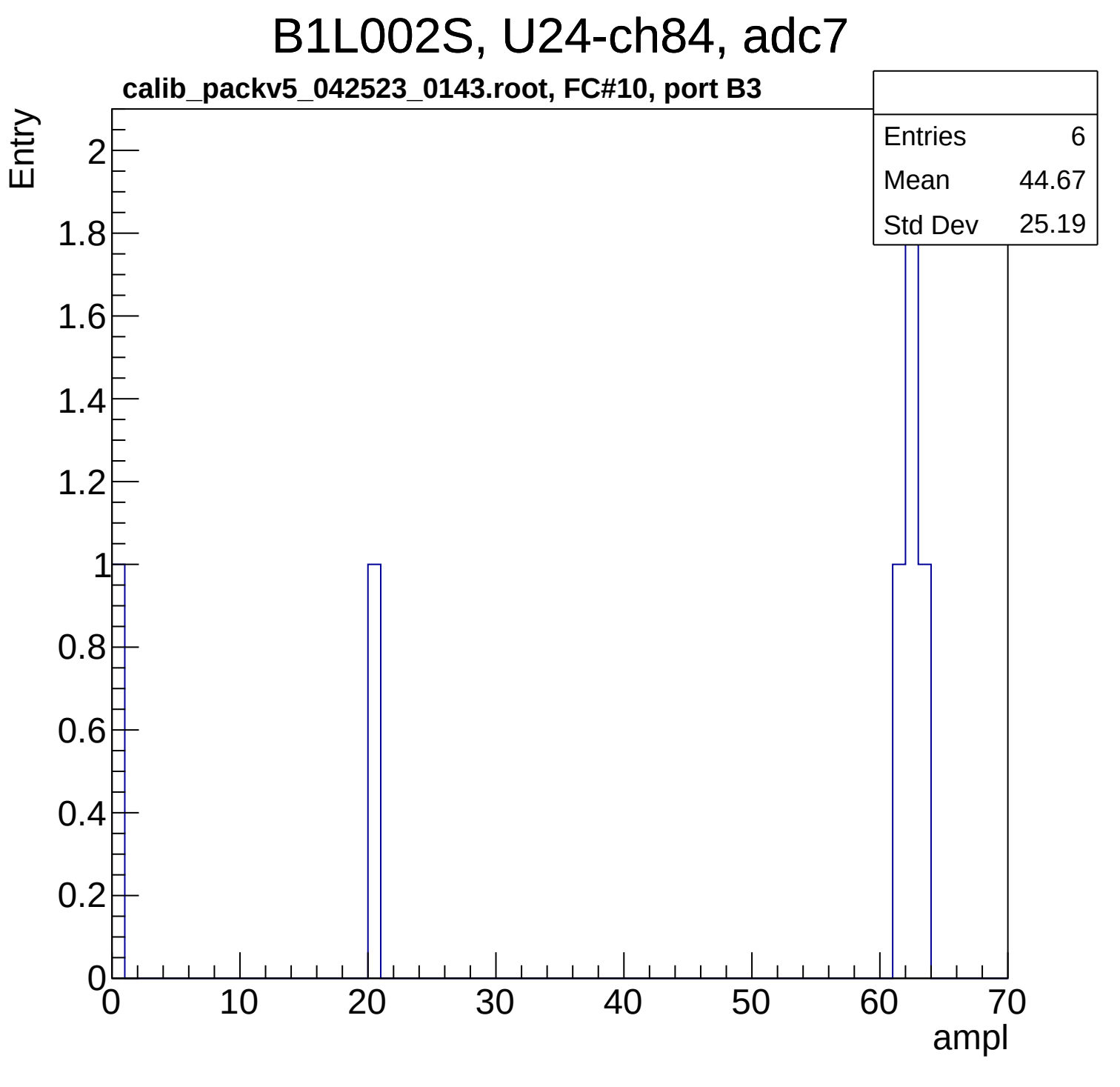
B1L002S, U24-ch84, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 44.67
Std Dev 25.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
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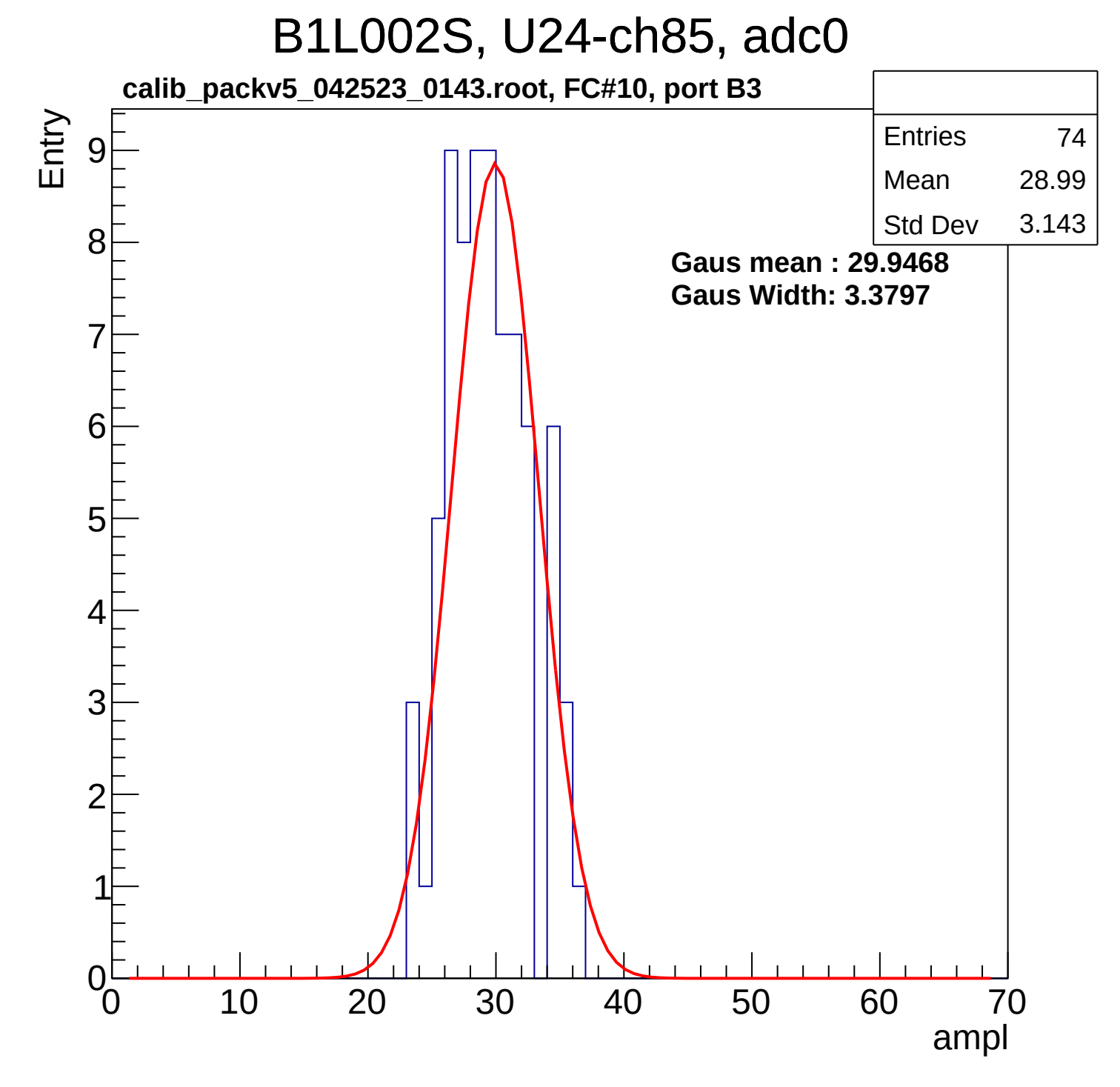
60
70


B1L002S, U24-ch85, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 28.99
Std Dev 3.143
Gaus mean : 29.9468
Gaus Width: 3.3797
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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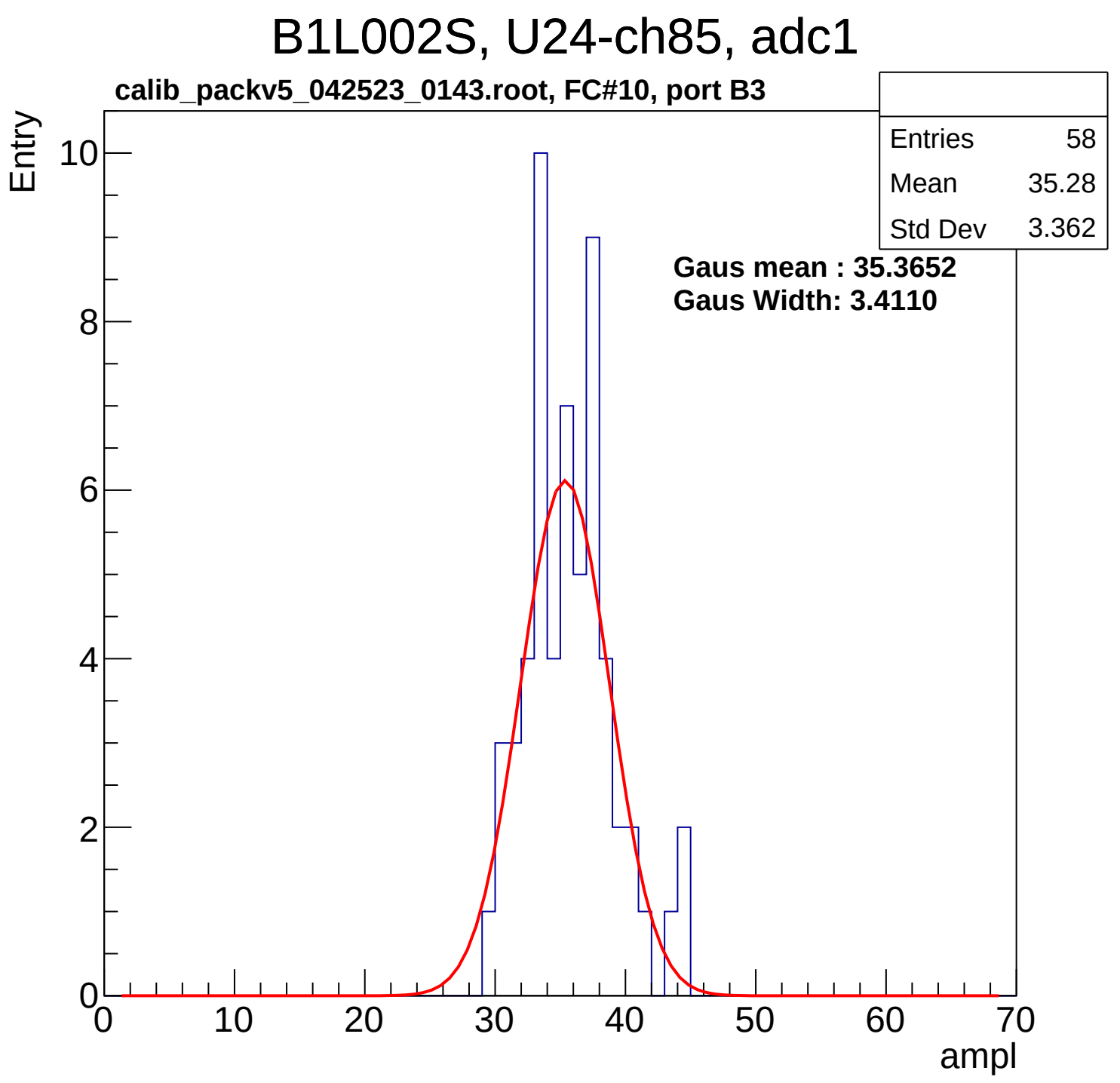
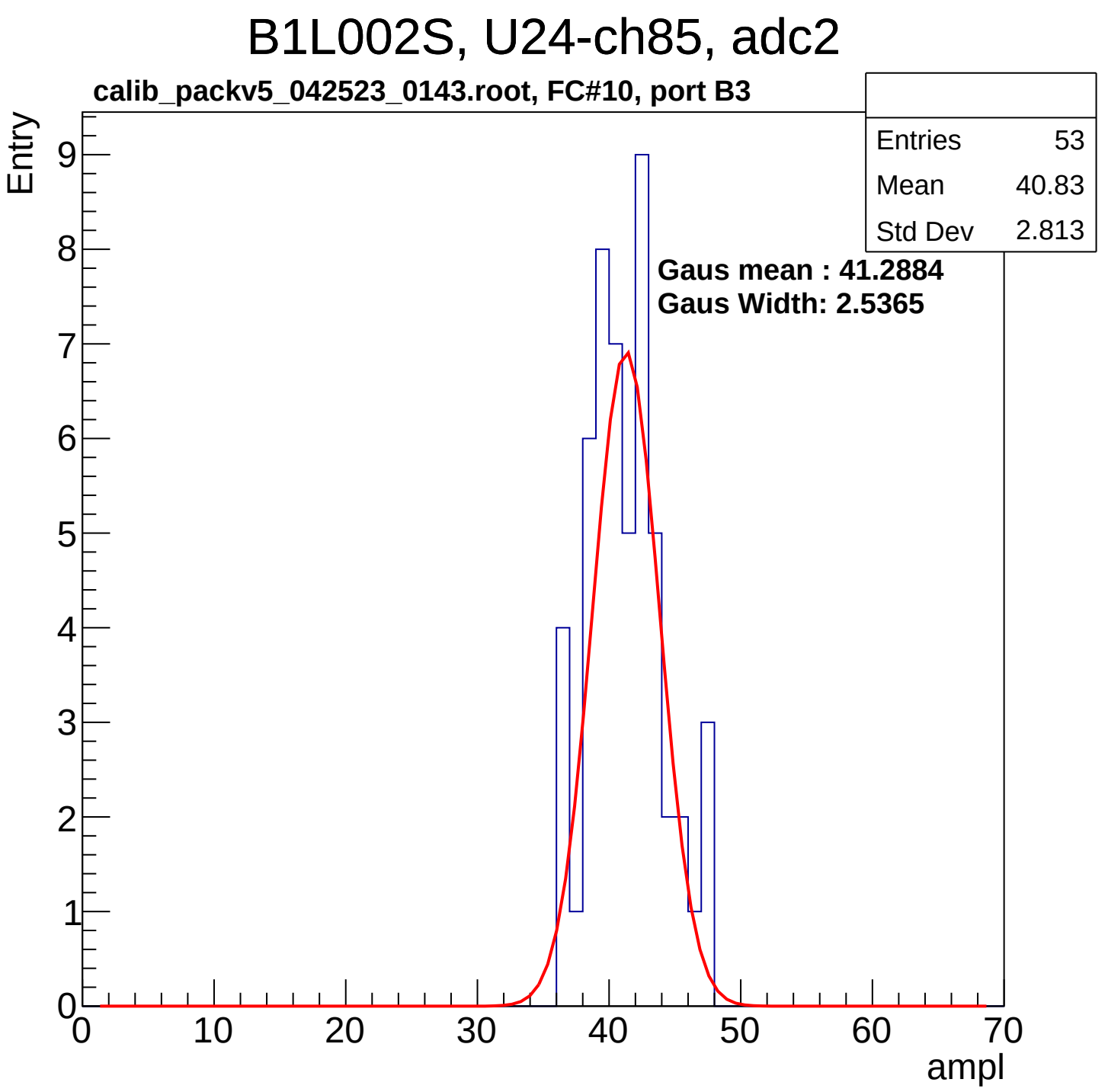

B1L002S, U24-ch85, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 35.28
Std Dev 3.362
Gaus mean : 35.3652
Gaus Width: 3.4110
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch85, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 40.83
Std Dev 2.813
Gaus mean : 41.2884
Gaus Width: 2.5365
Entry
ampl
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2
3
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5
6
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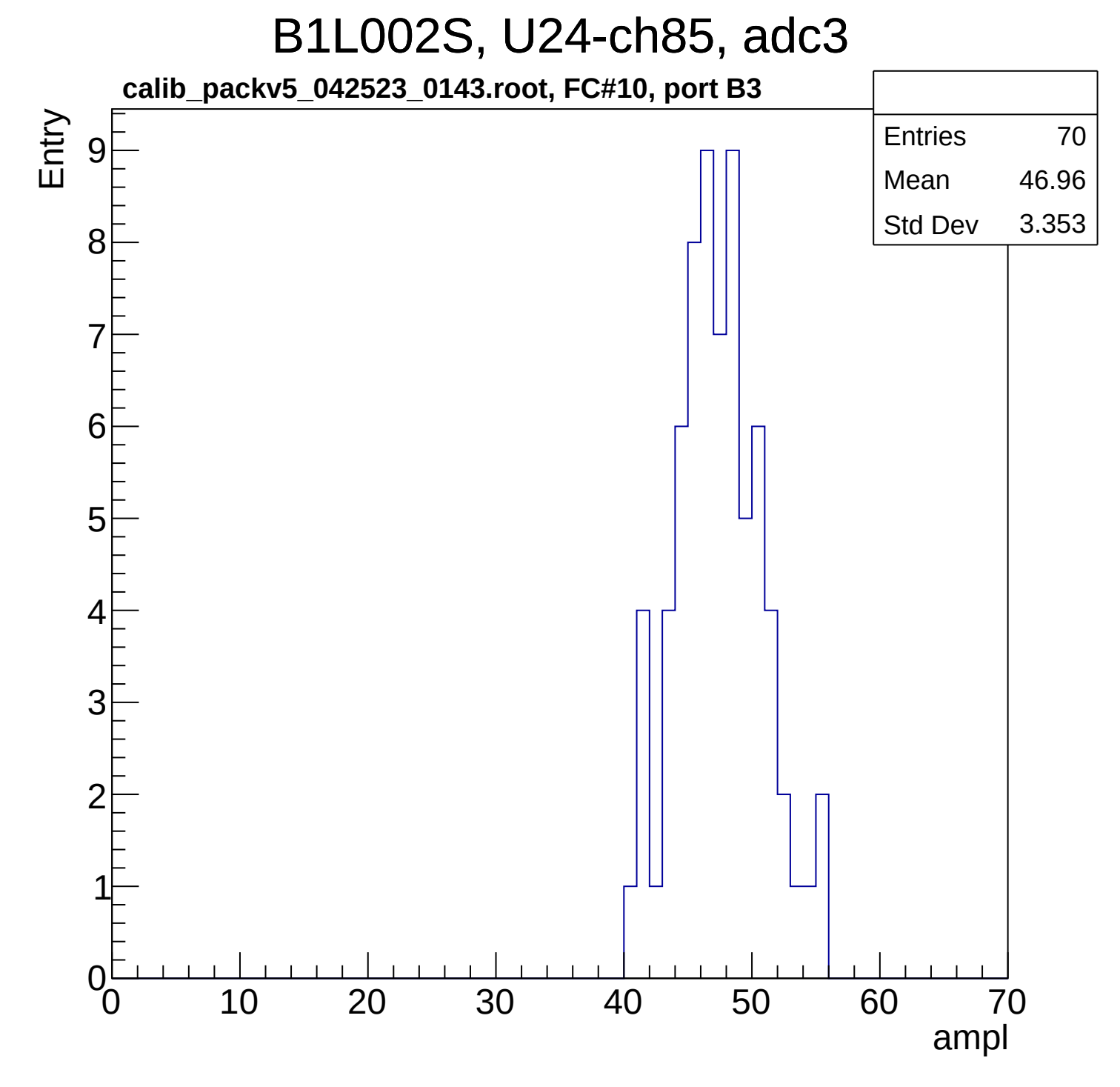
B1L002S, U24-ch85, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 46.96
Std Dev 3.353
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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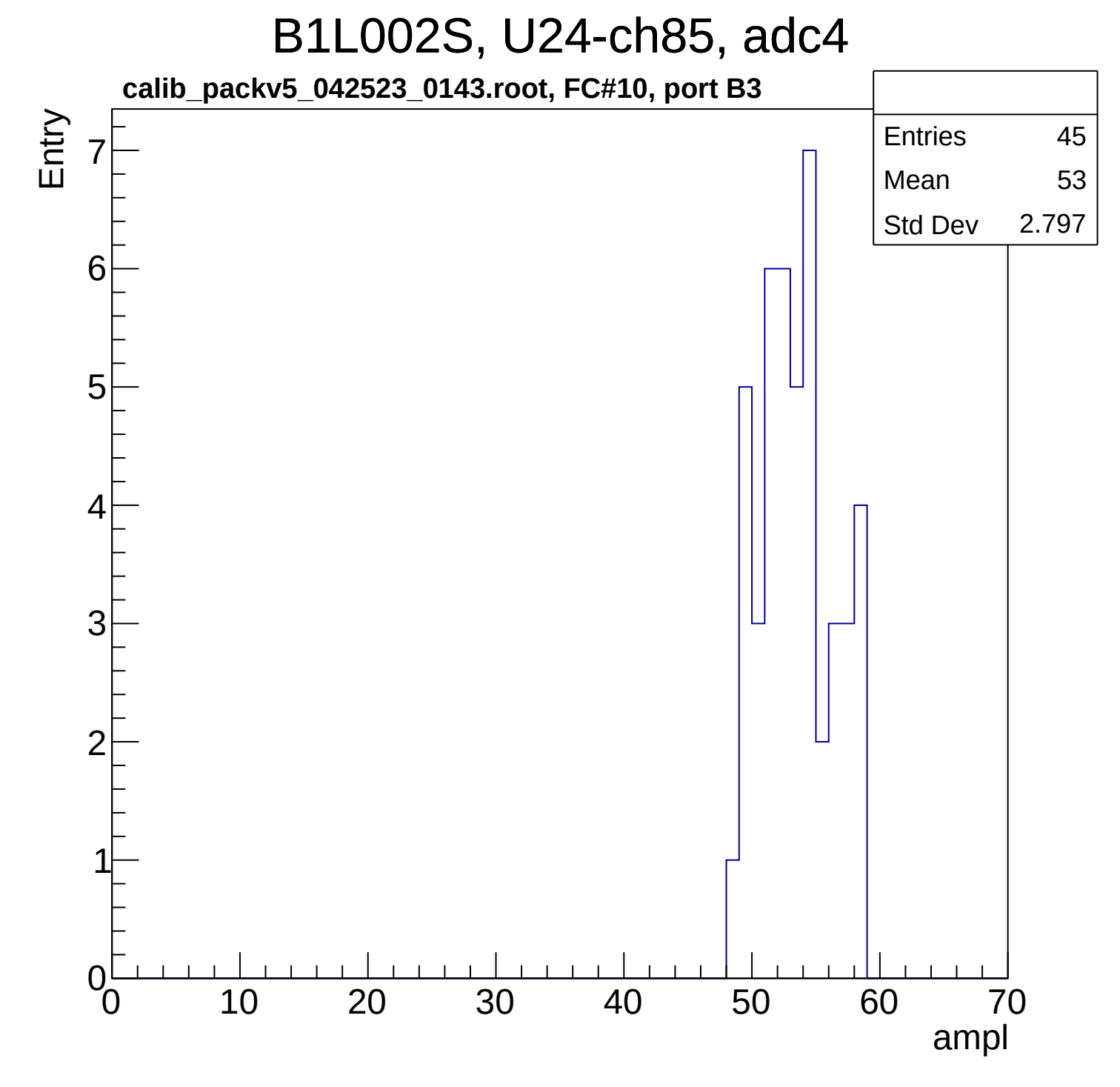
B1L002S, U24-ch85, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 53
Std Dev 2.797
Entry
ampl
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1
2
3
4
5
6
7
0
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60
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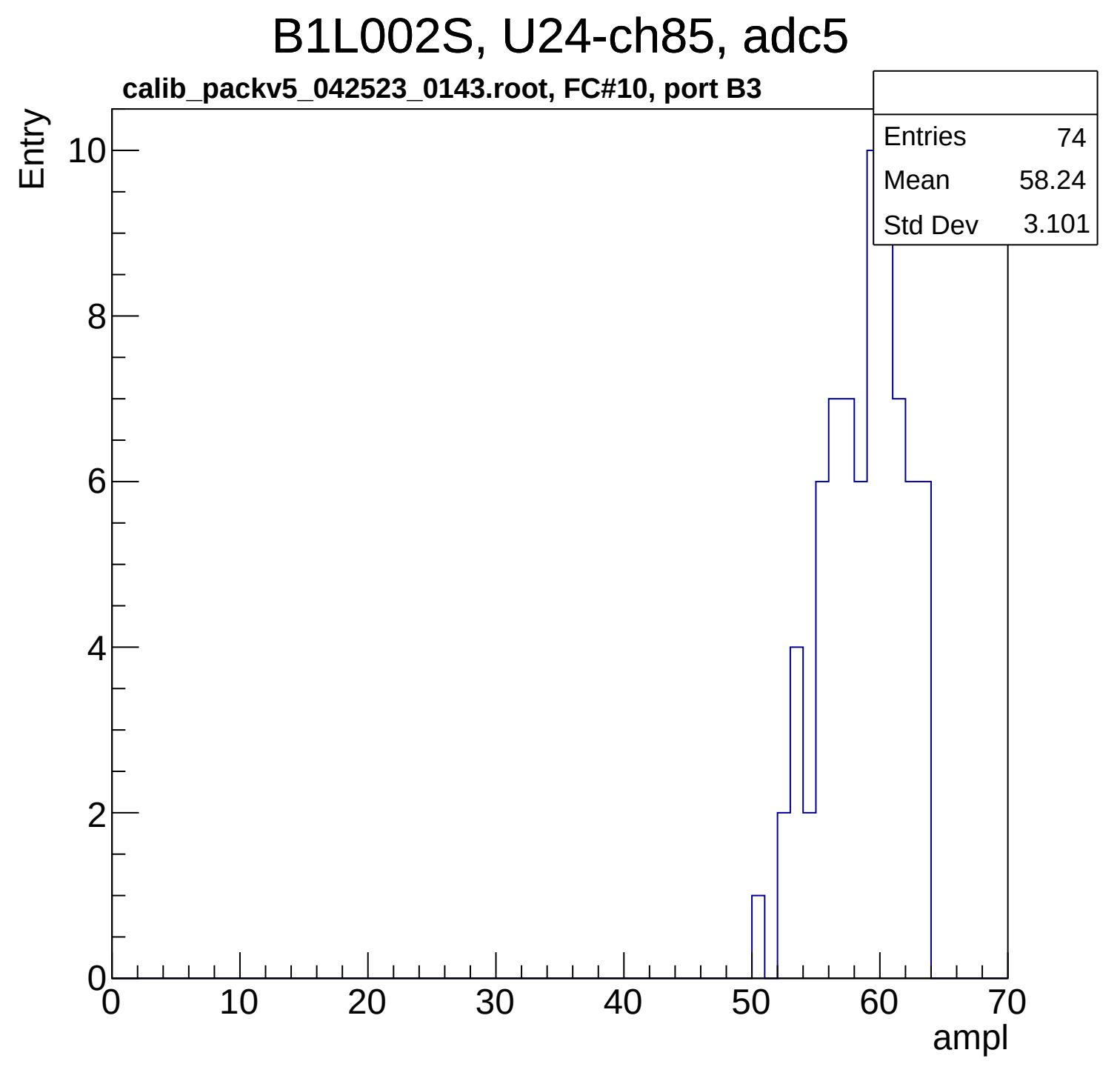
B1L002S, U24-ch85, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 58.24
Std Dev 3.101
Entry
ampl
0
2
4
6
8
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0
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60
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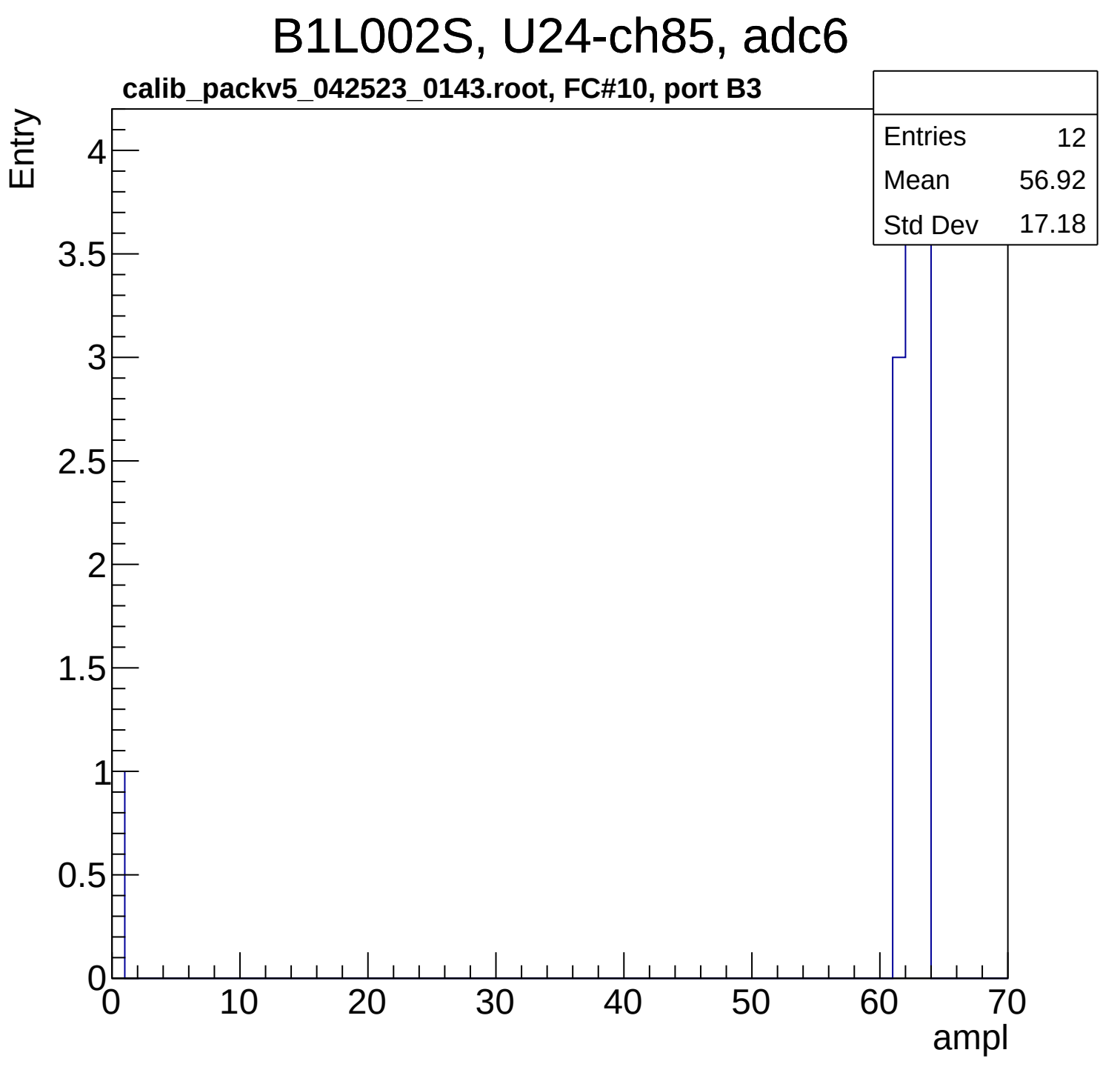
B1L002S, U24-ch85, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.92
Std Dev 17.18
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
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40
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60
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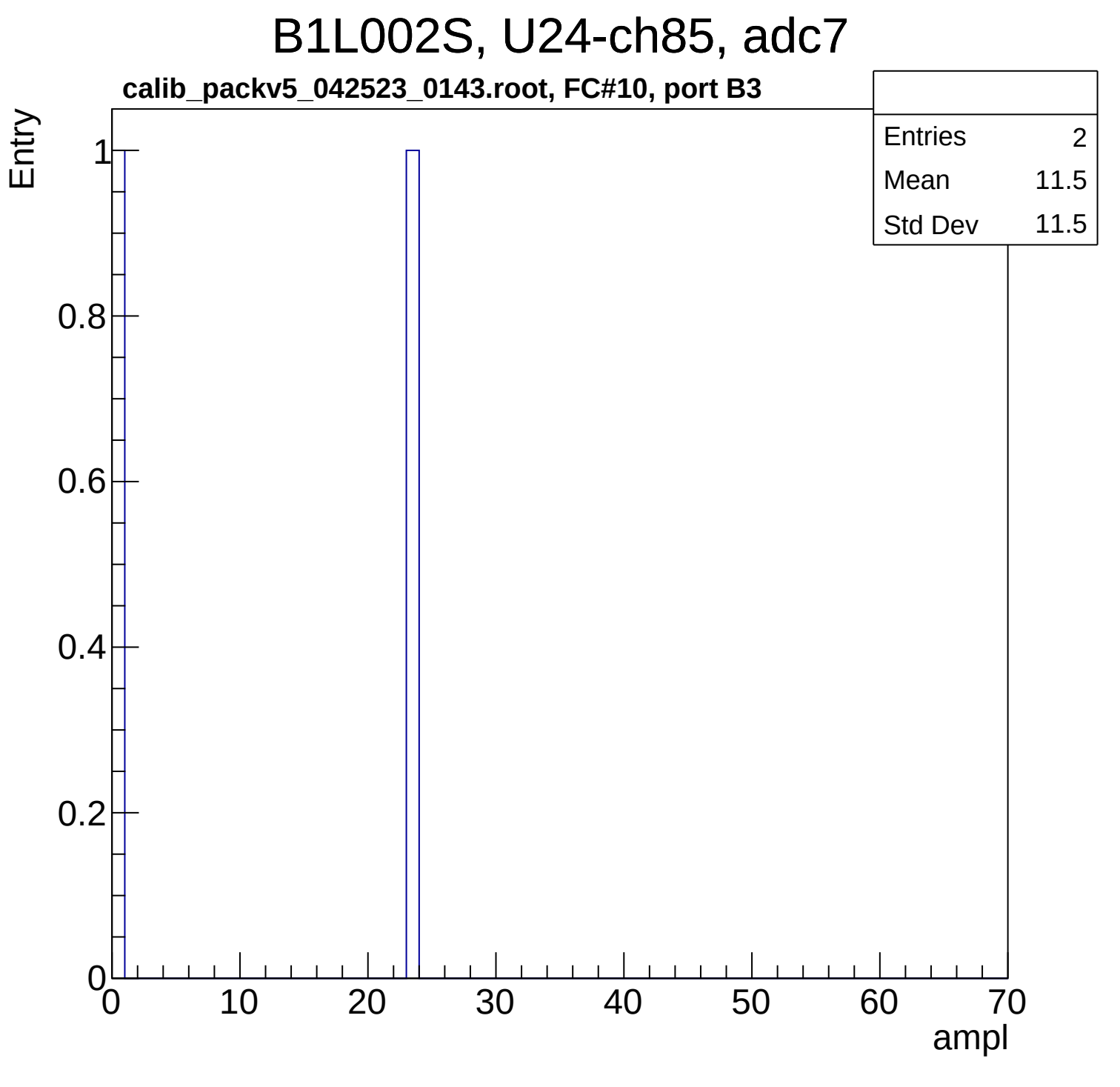
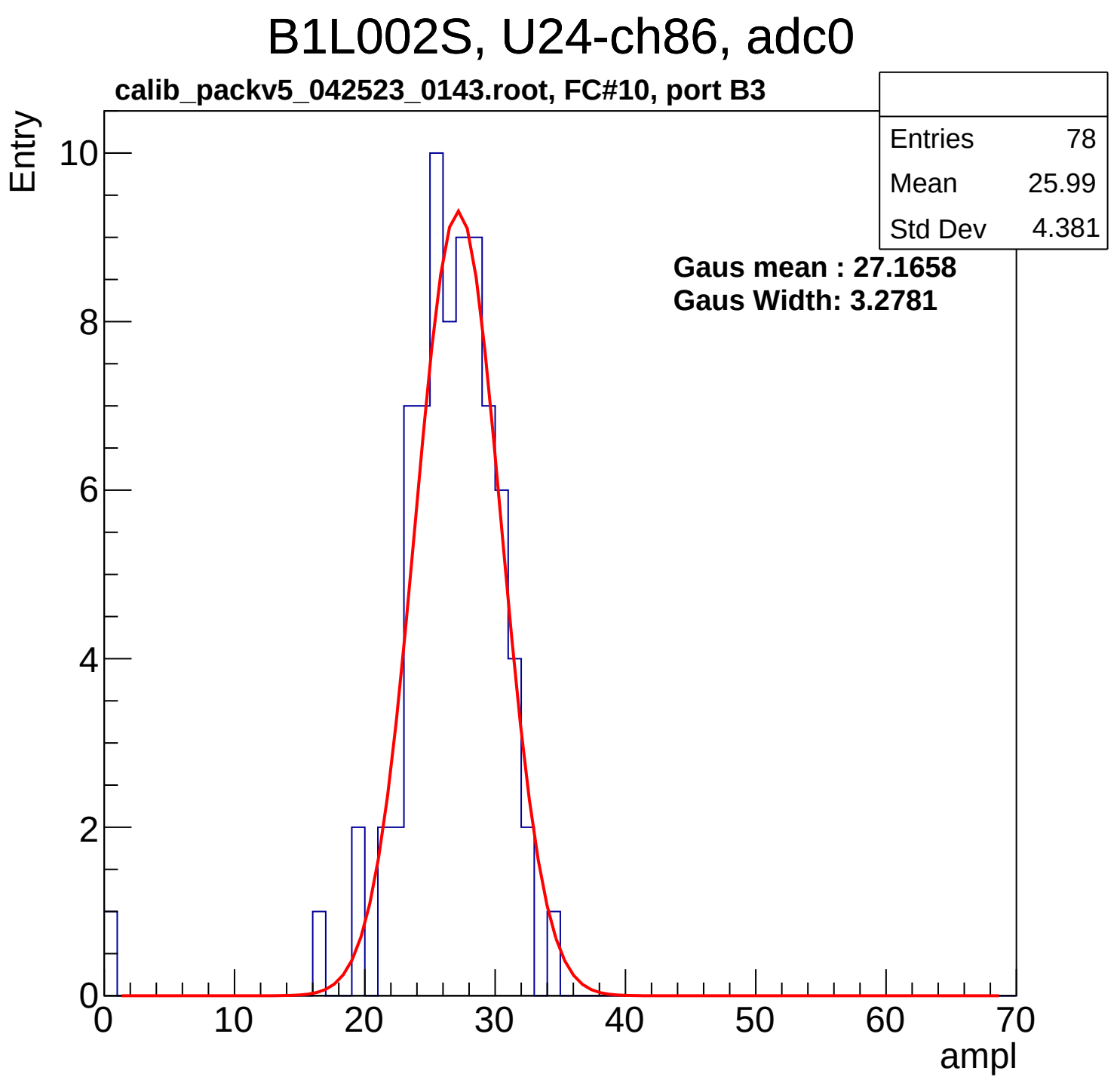
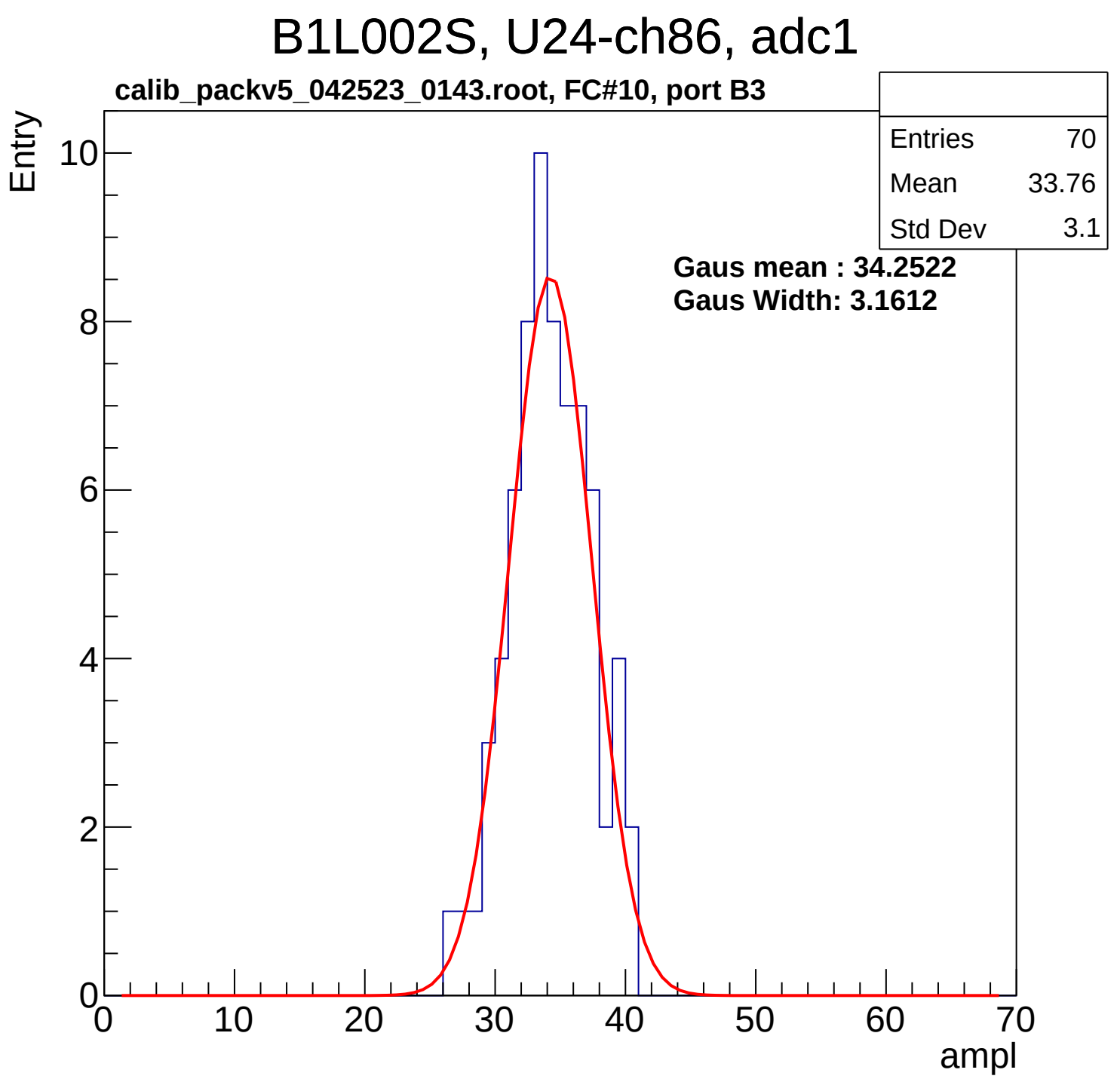
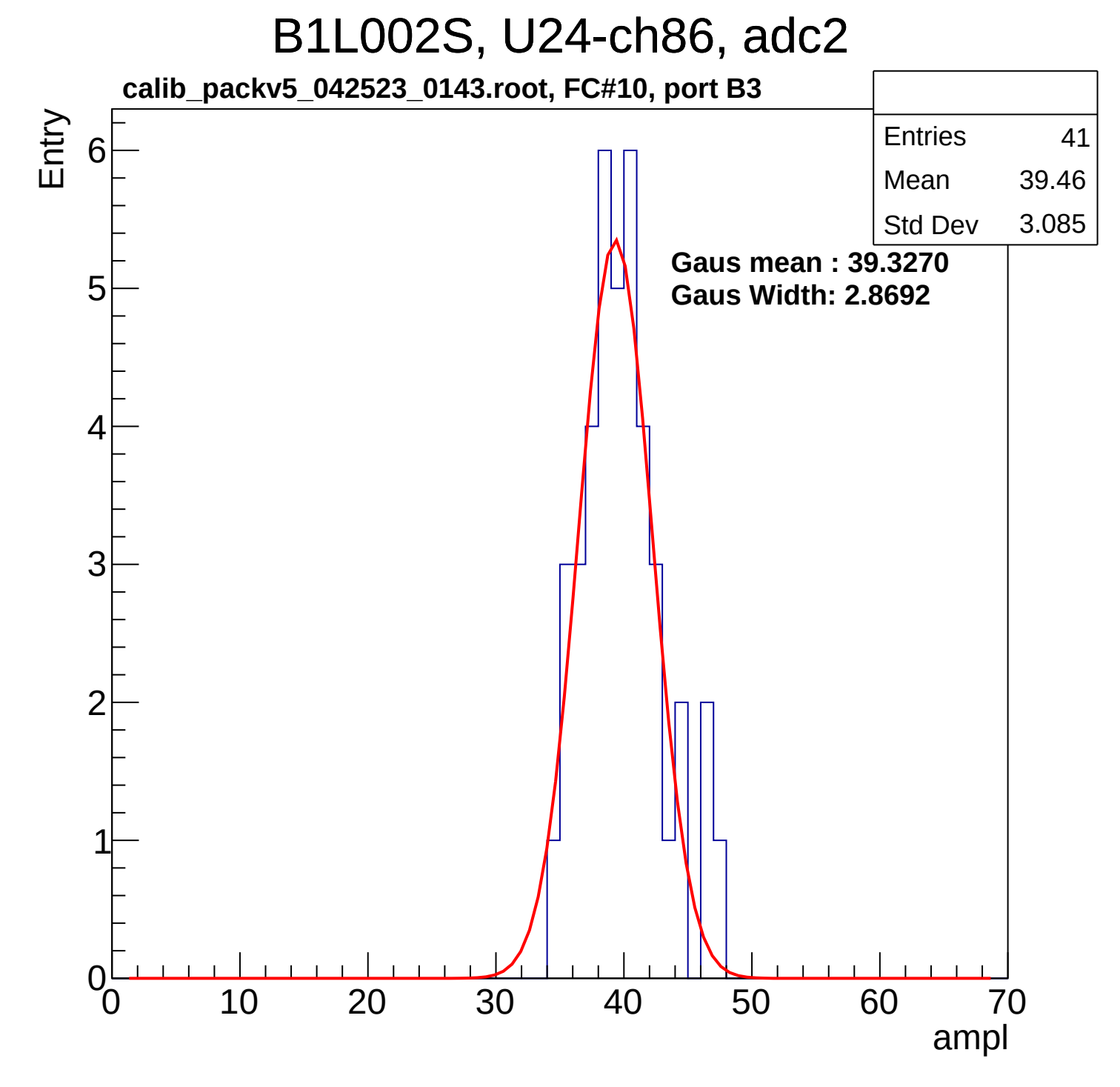
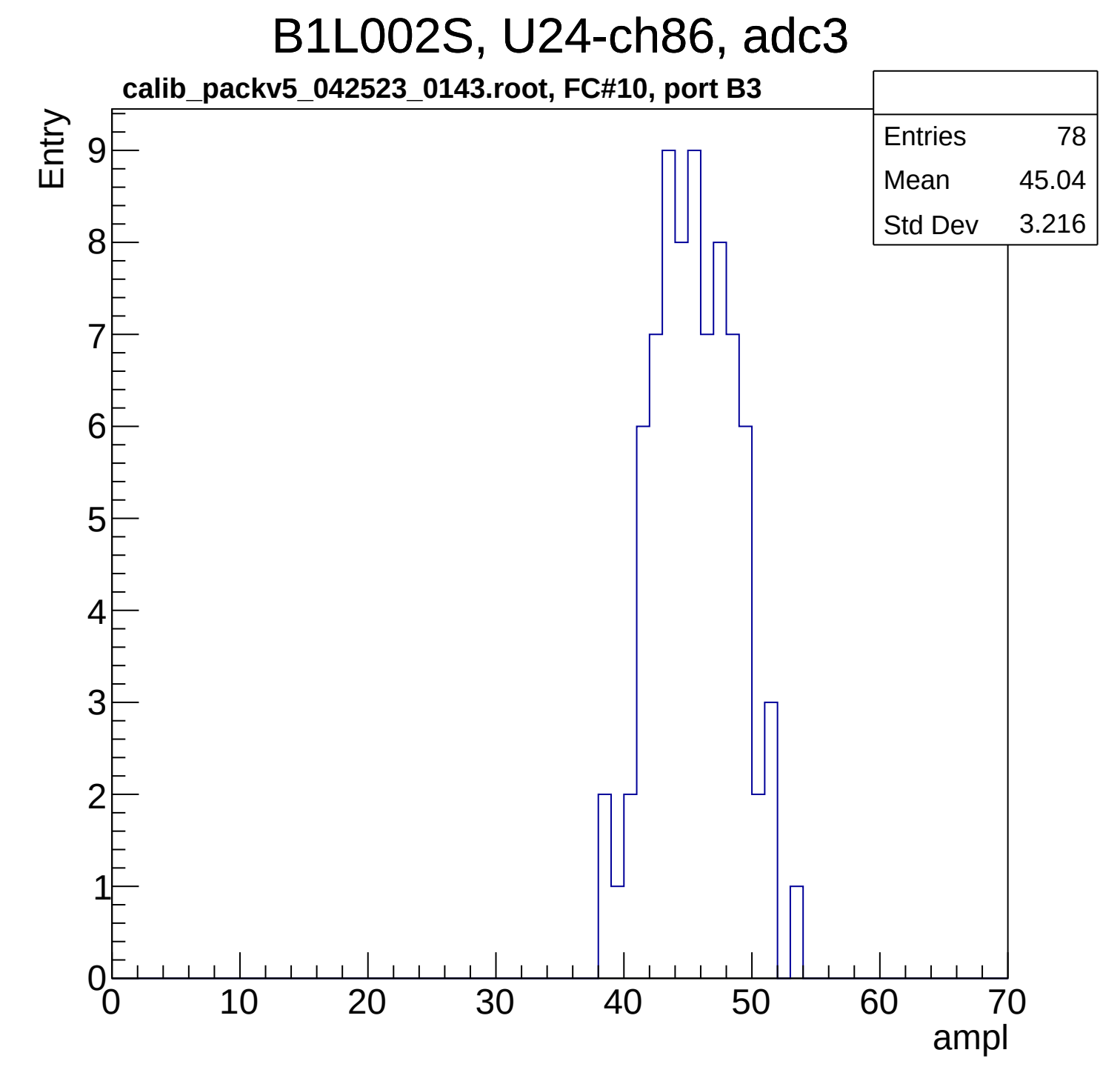

B1L002S, U24-ch85, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch86, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 25.99
Std Dev 4.381
Gaus mean : 27.1658
Gaus Width: 3.2781
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch86, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 33.76
Std Dev 3.1
Gaus mean : 34.2522
Gaus Width: 3.1612
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch86, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 39.46
Std Dev 3.085
Gaus mean : 39.3270
Gaus Width: 2.8692
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch86, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 45.04
Std Dev 3.216
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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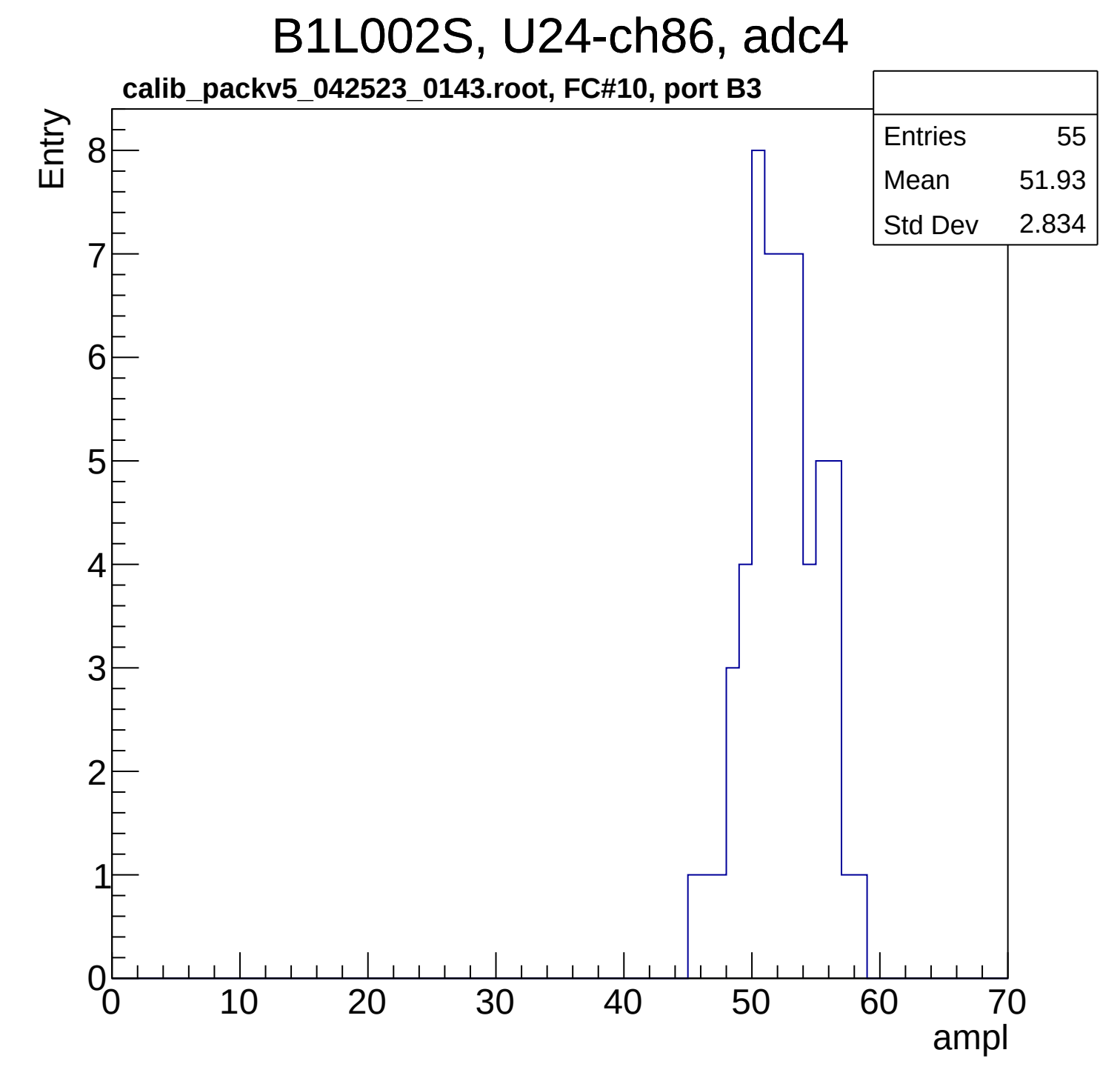
B1L002S, U24-ch86, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 51.93
Std Dev 2.834
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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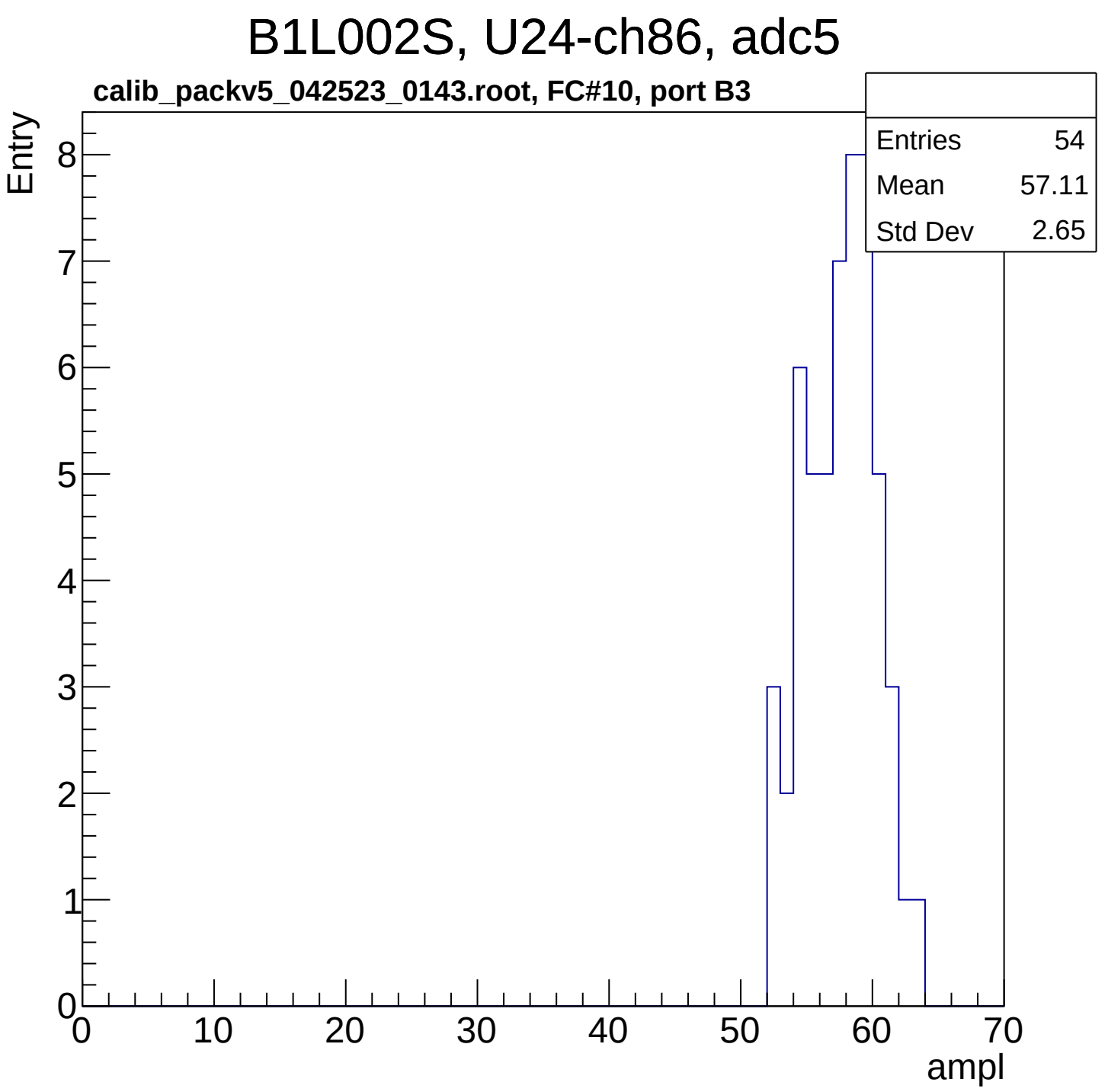
B1L002S, U24-ch86, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.11
Std Dev 2.65
Entry
ampl
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2
3
4
5
6
7
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60
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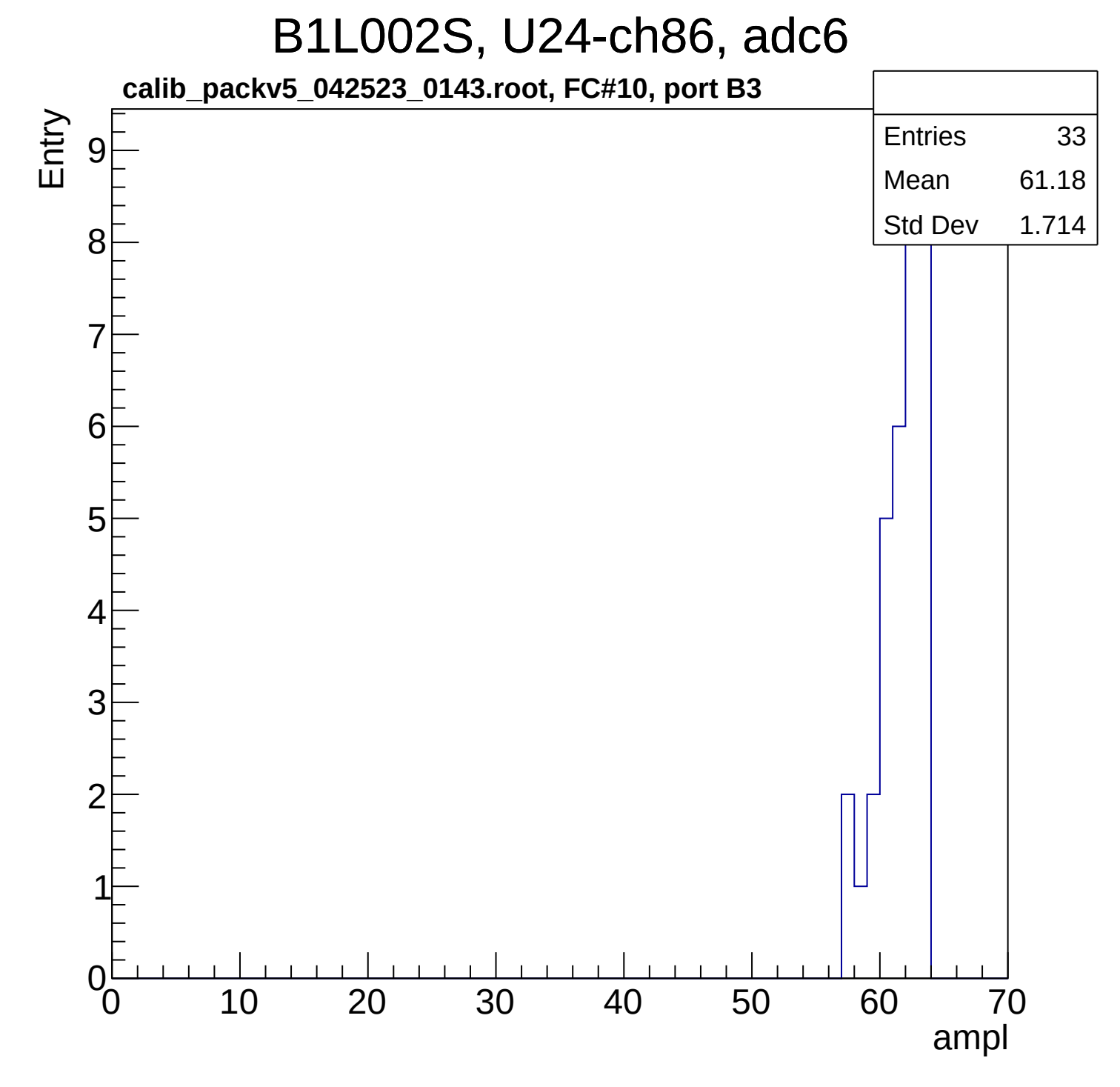
B1L002S, U24-ch86, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 61.18
Std Dev 1.714
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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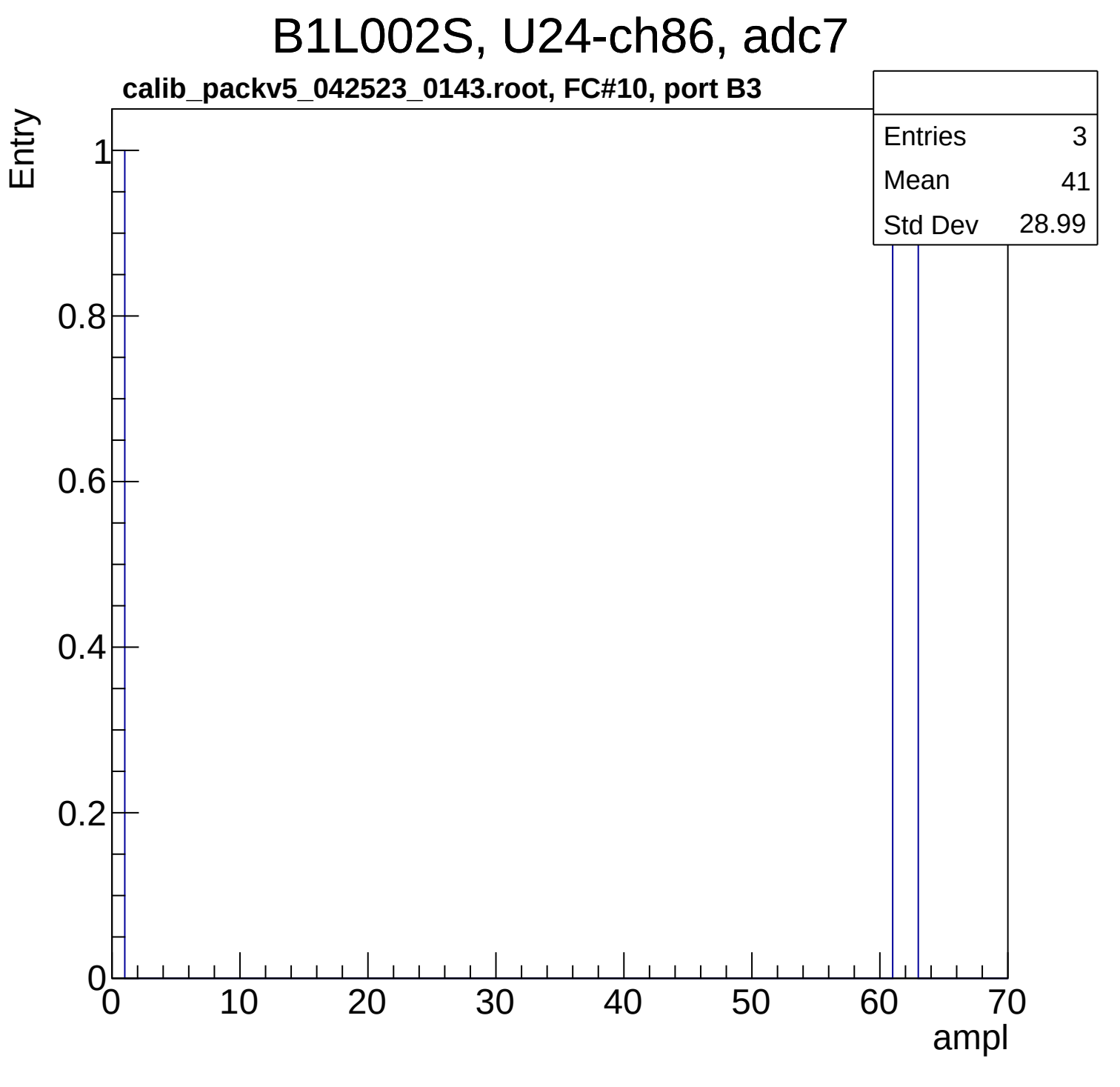
B1L002S, U24-ch86, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 41
Std Dev 28.99
Entry
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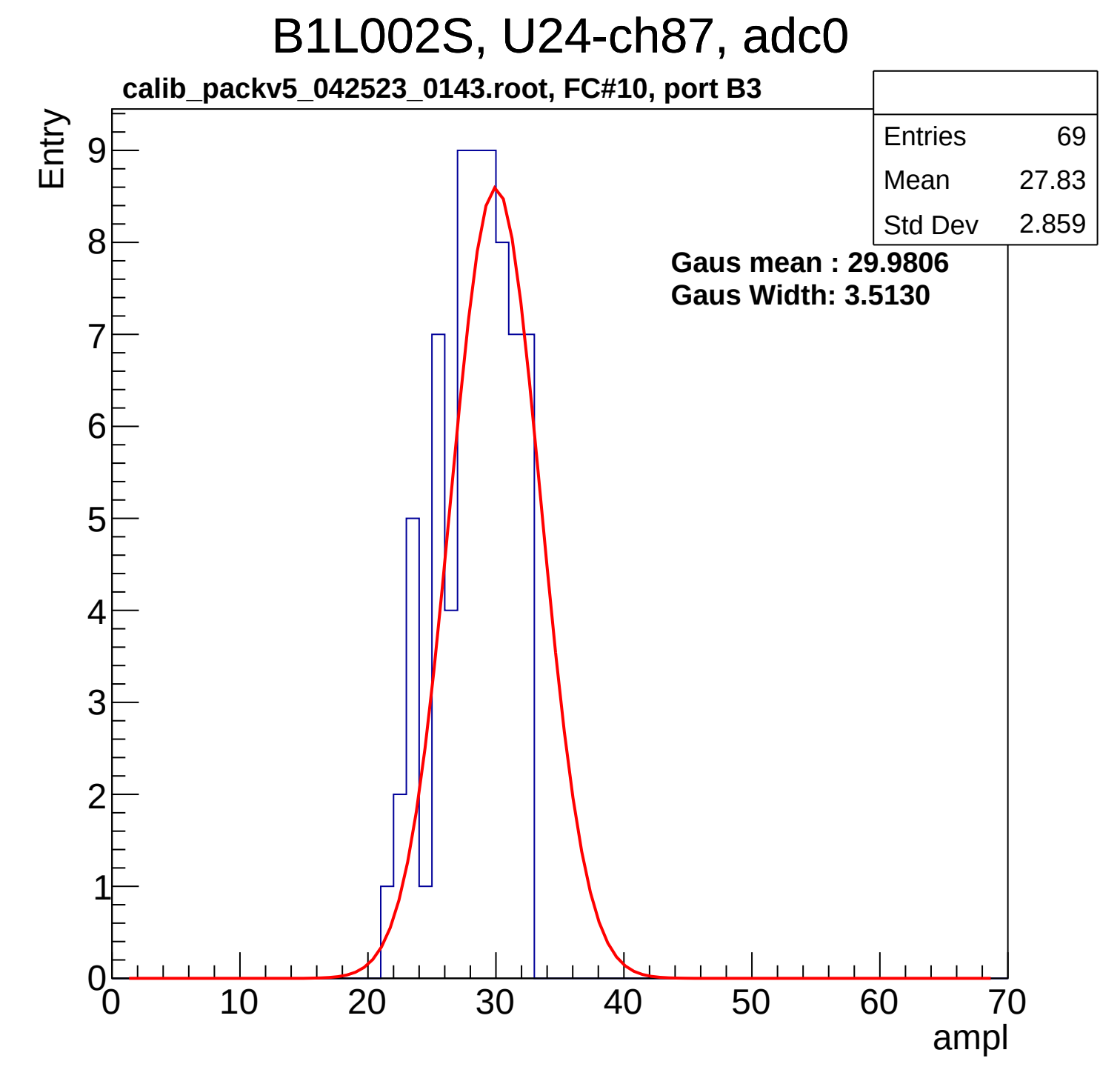
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch87, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 27.83
Std Dev 2.859
Gaus mean : 29.9806
Gaus Width: 3.5130
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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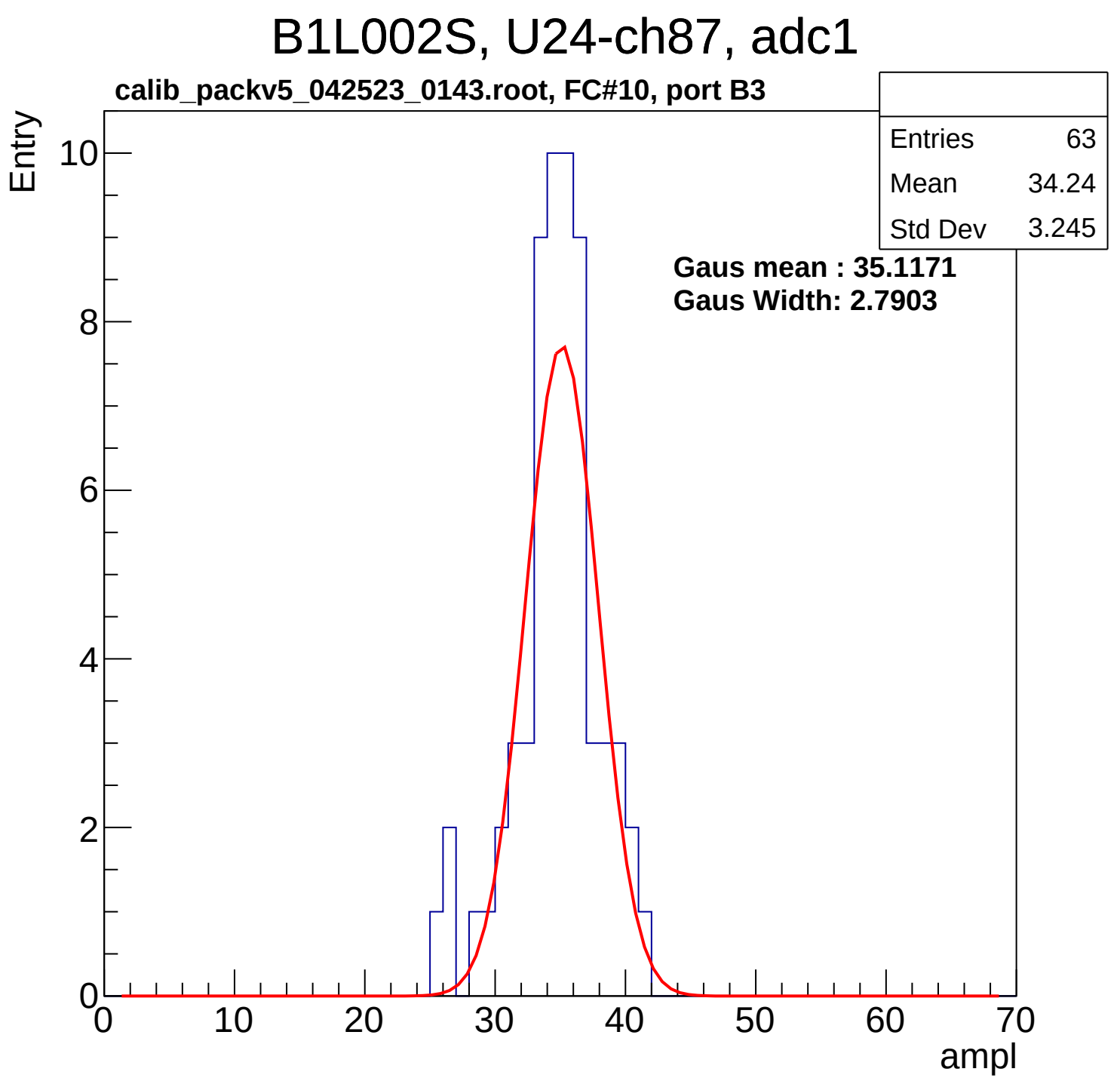
B1L002S, U24-ch87, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 34.24
Std Dev 3.245
Gaus mean : 35.1171
Gaus Width: 2.7903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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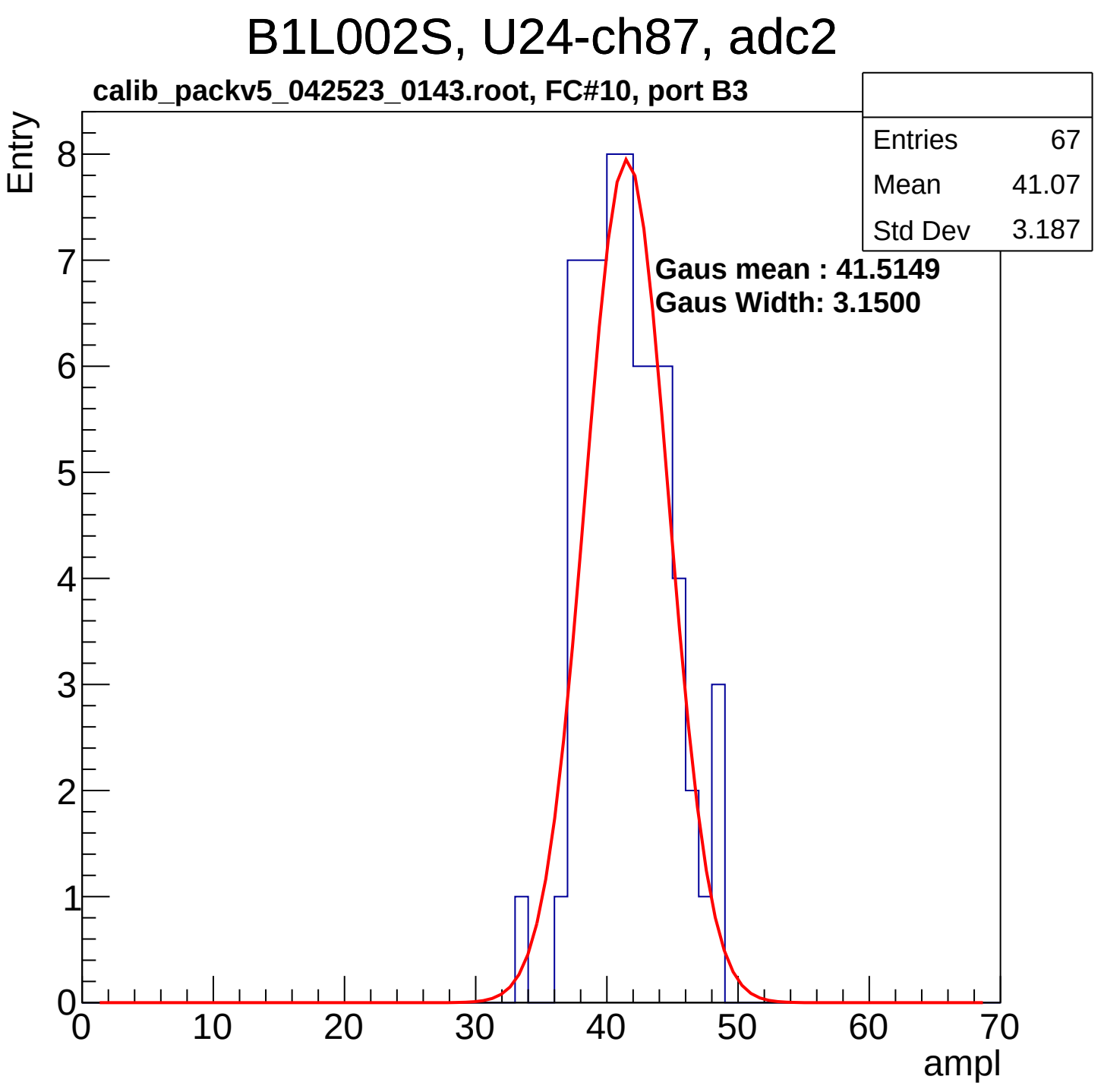
B1L002S, U24-ch87, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 41.07
Std Dev 3.187
Gaus mean : 41.5149
Gaus Width: 3.1500
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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60
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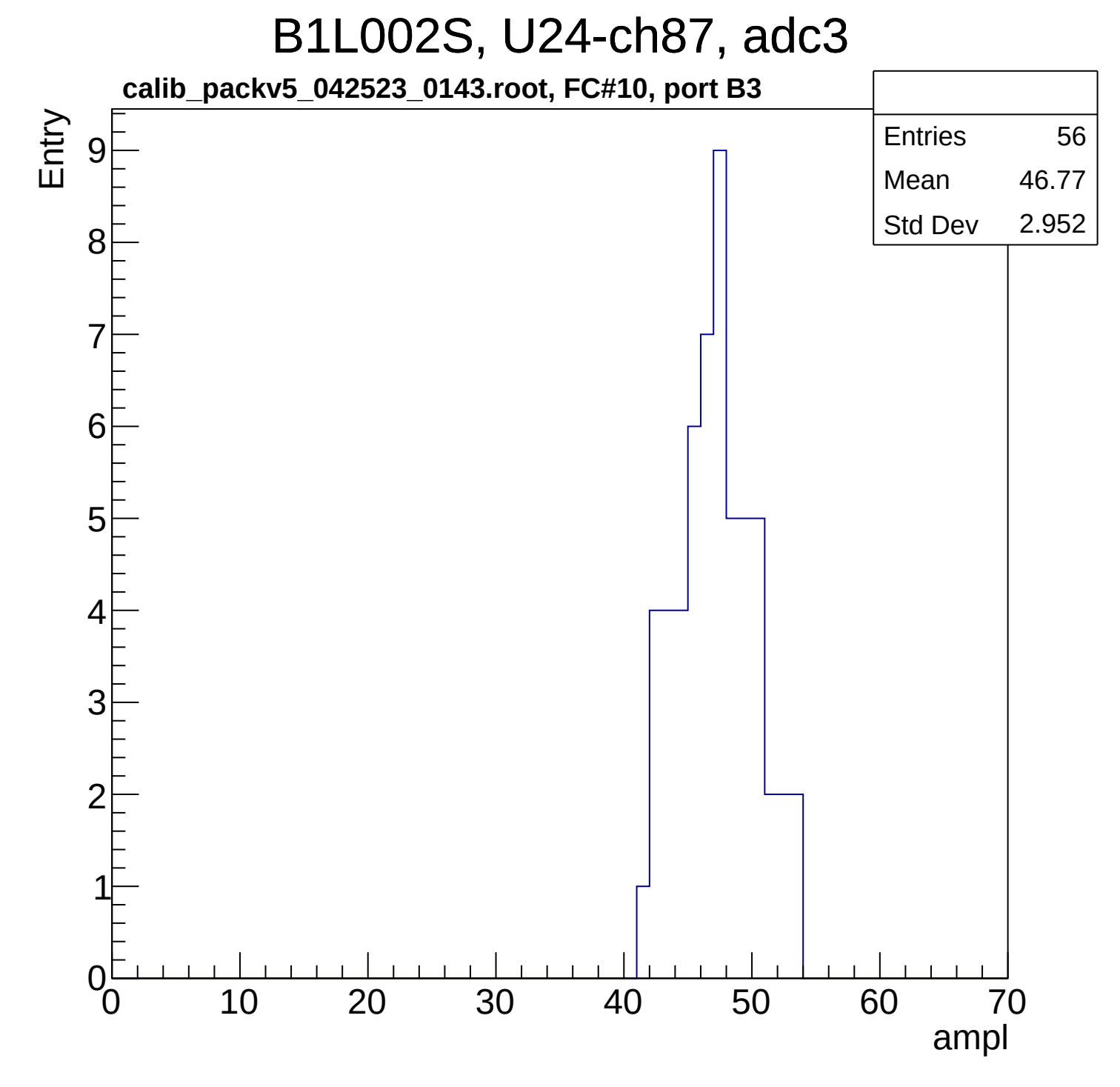
B1L002S, U24-ch87, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 46.77
Std Dev 2.952
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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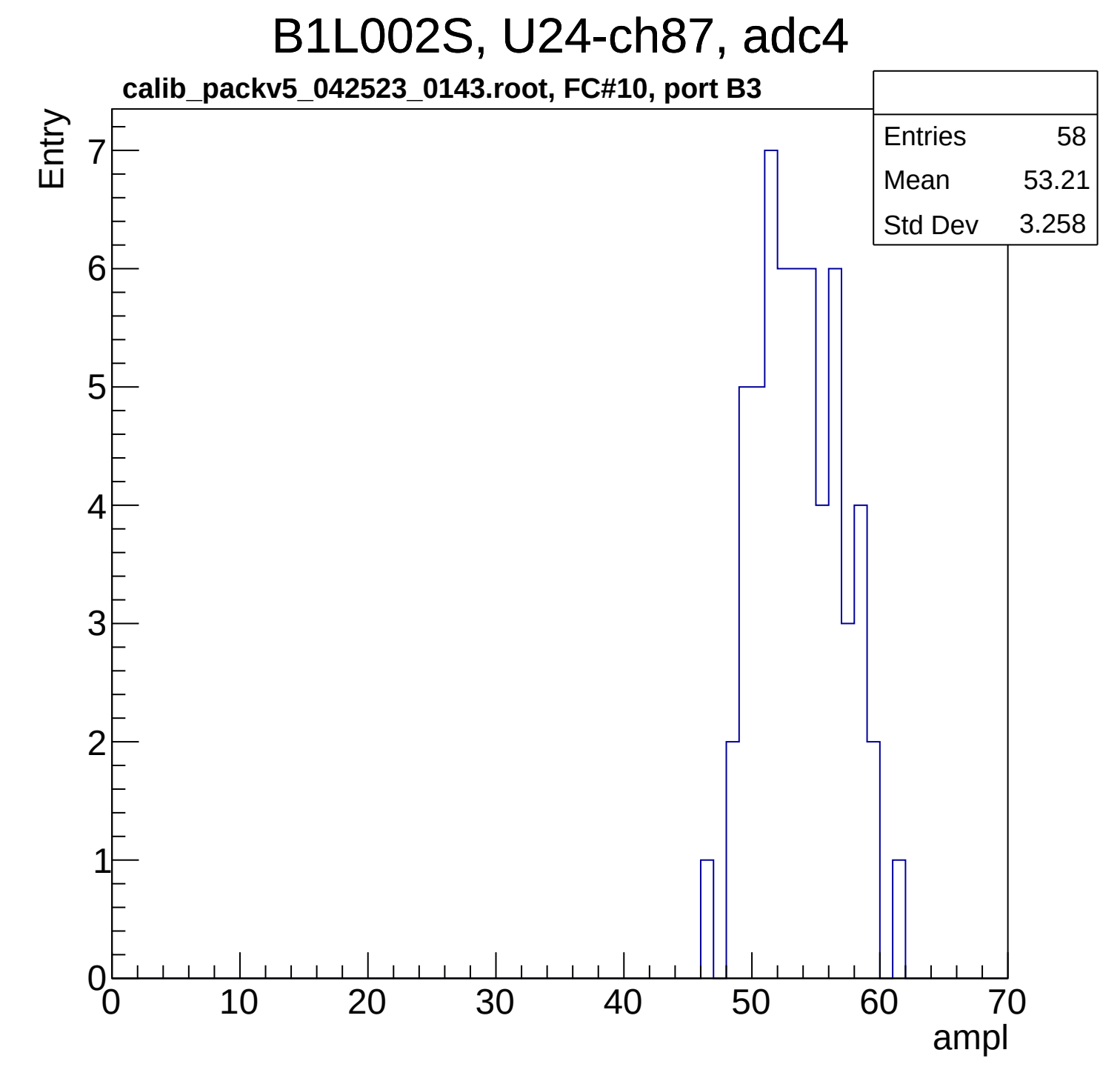
B1L002S, U24-ch87, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 53.21
Std Dev 3.258
Entry
ampl
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4
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7
0
10
20
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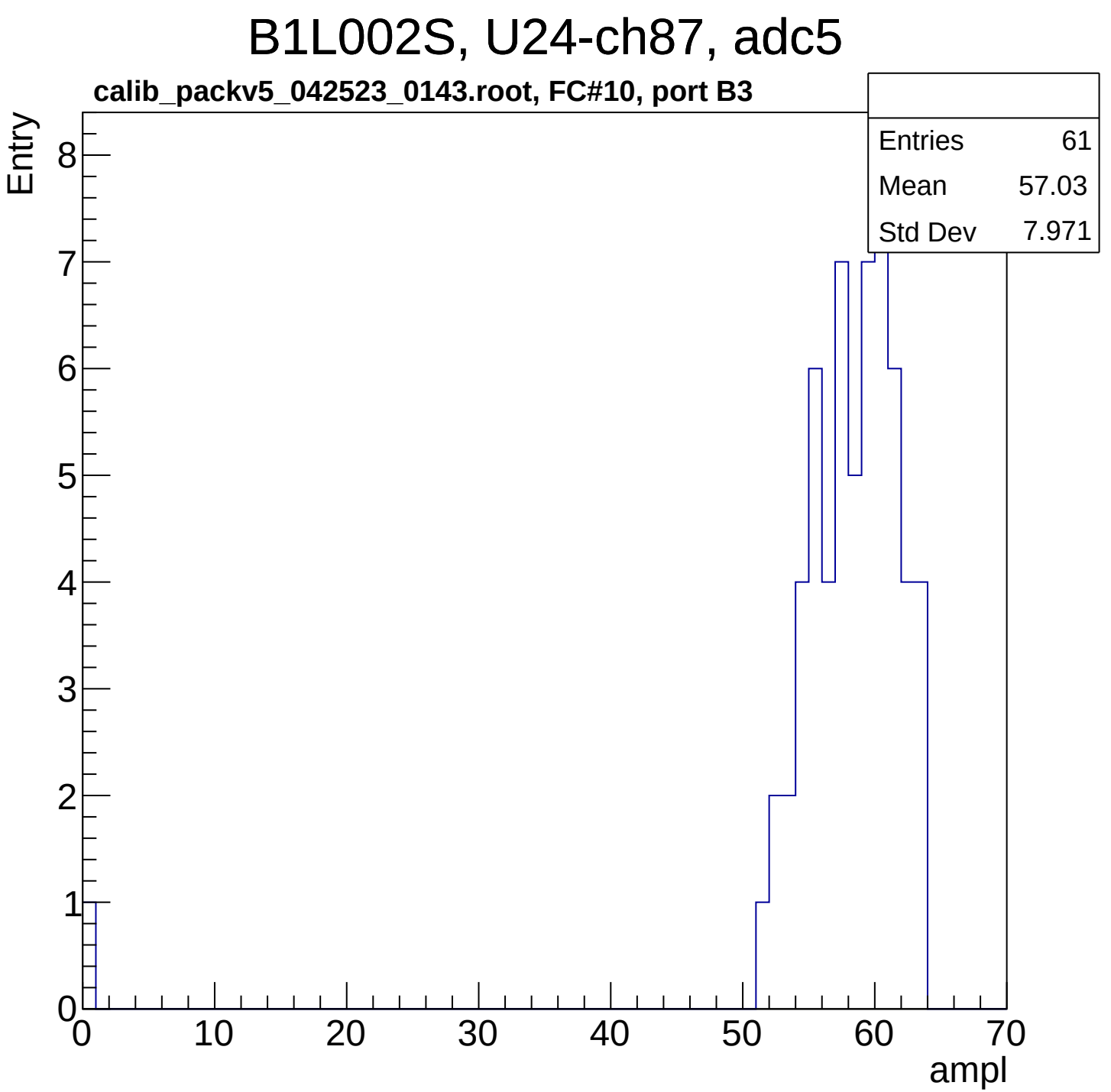
40
50
60
70


B1L002S, U24-ch87, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 57.03
Std Dev 7.971
Entry
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2
3
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6
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8
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10
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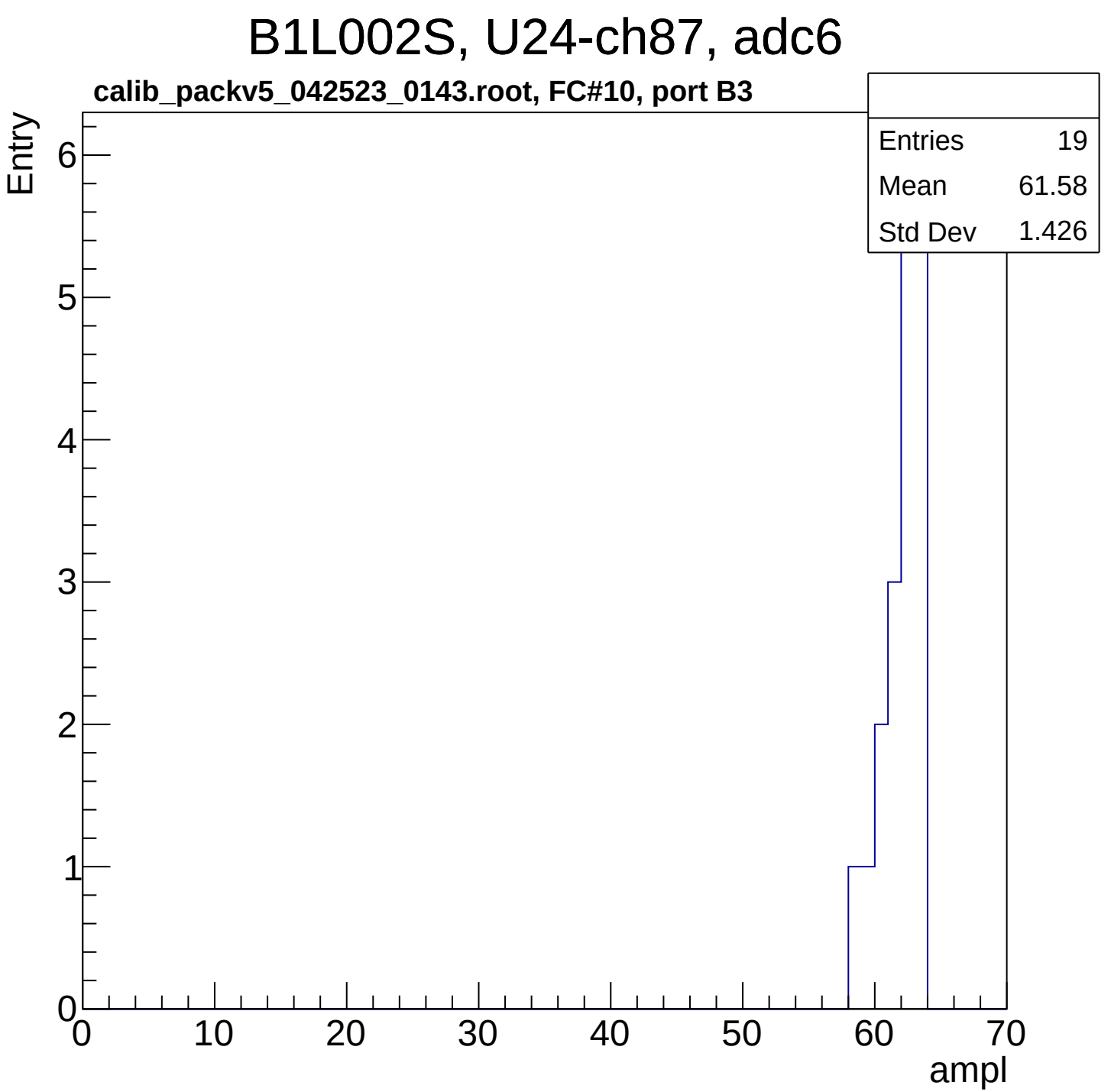
30
40
50
60
70
ampl


B1L002S, U24-ch87, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.58
Std Dev 1.426
Entry
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1
2
3
4
5
6
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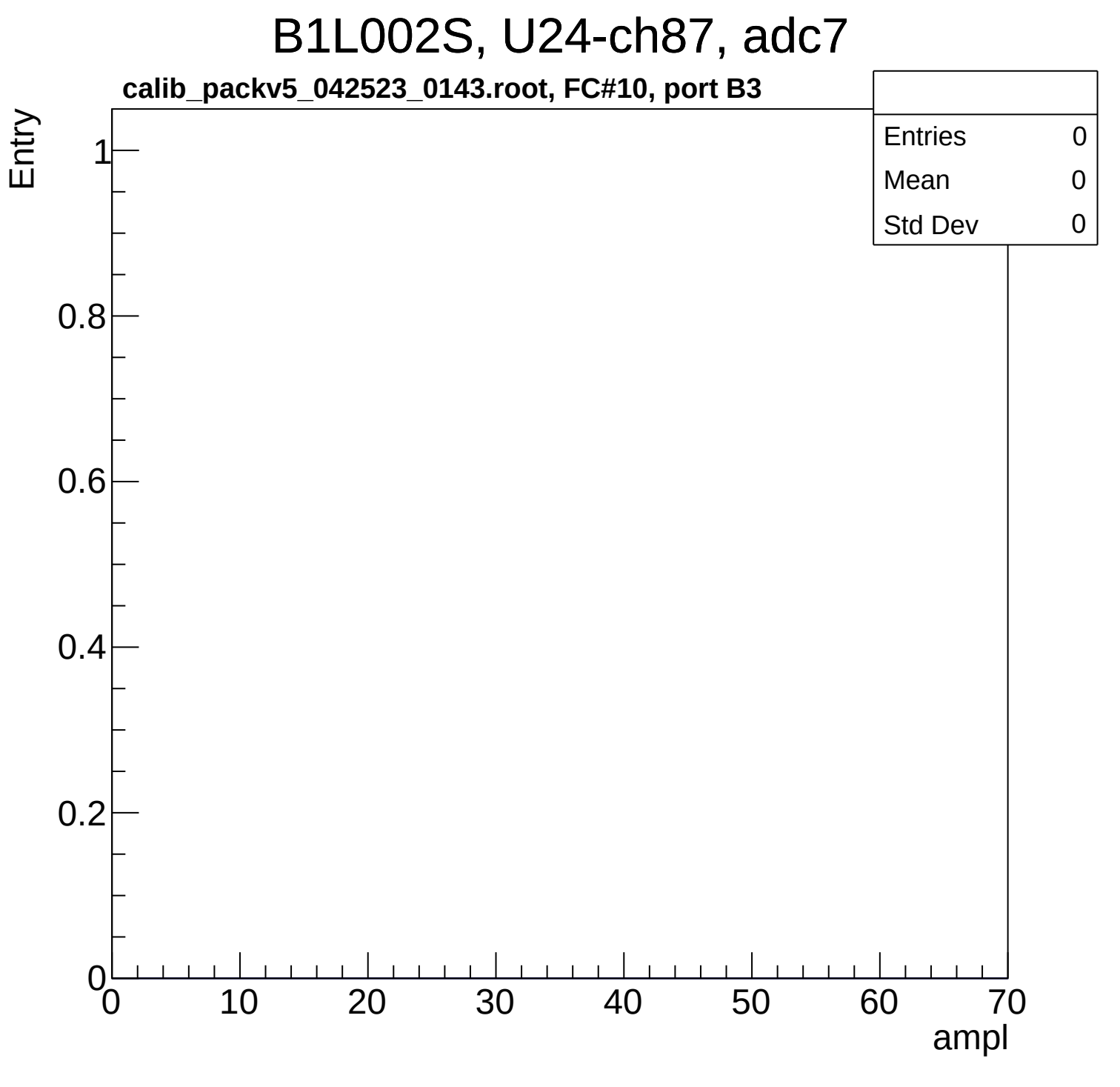
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch87, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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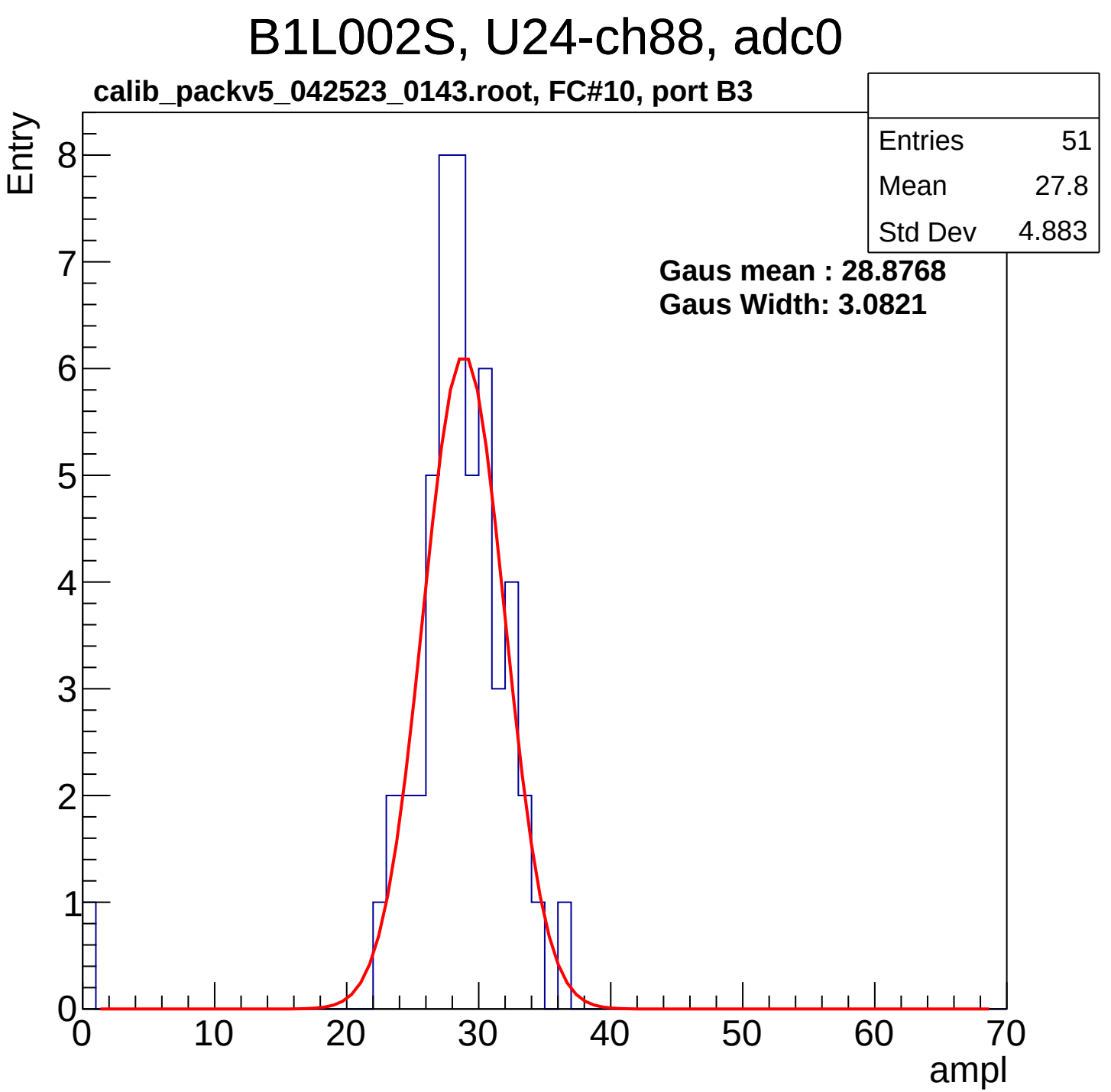
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch88, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27.8
Std Dev 4.883
Gaus mean : 28.8768
Gaus Width: 3.0821
Entry
ampl
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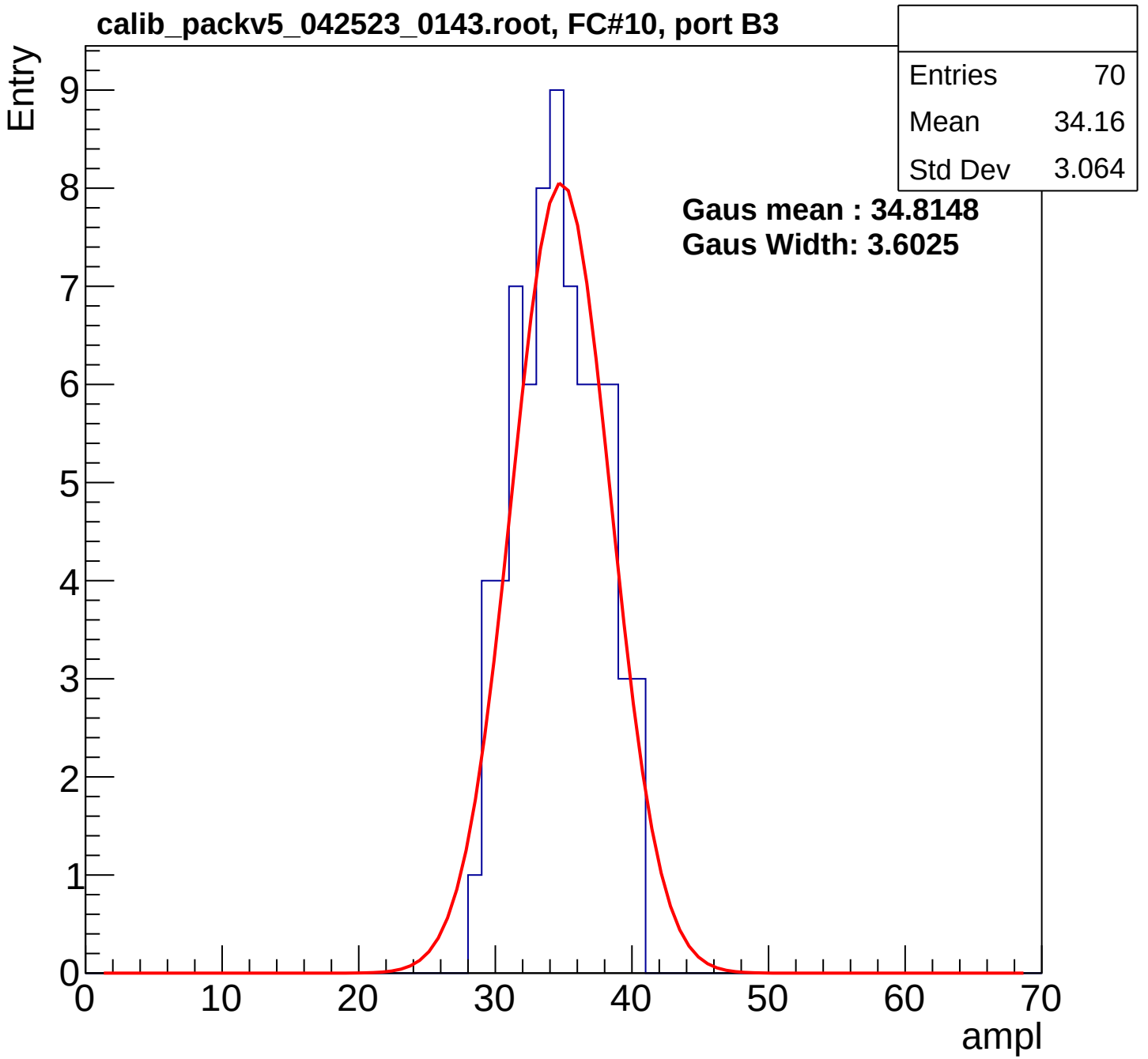
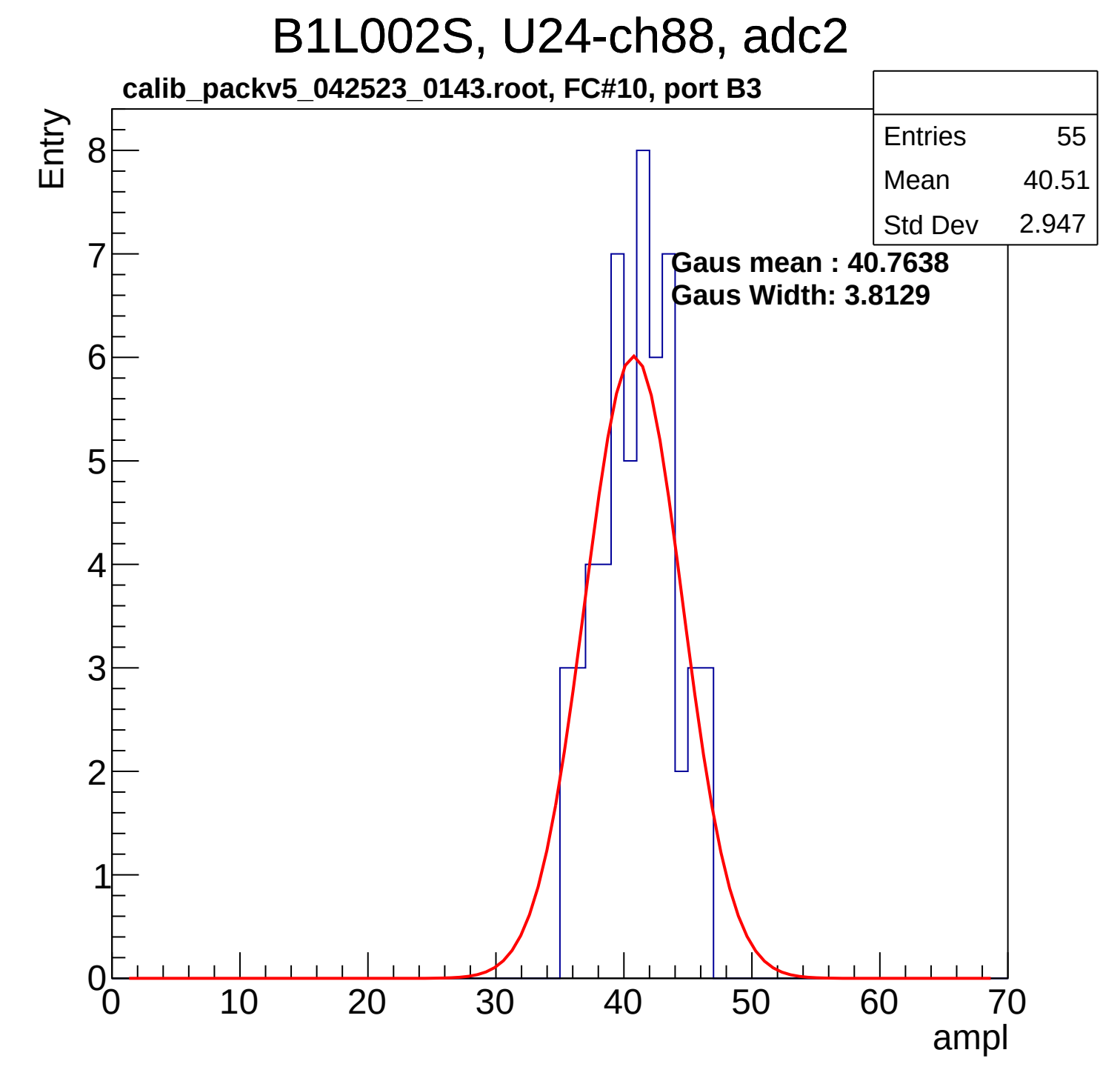
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch88, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 34.16
Std Dev 3.064
Gaus mean : 34.8148
Gaus Width: 3.6025
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch88, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 40.51
Std Dev 2.947
Gaus mean : 40.7638
Gaus Width: 3.8129
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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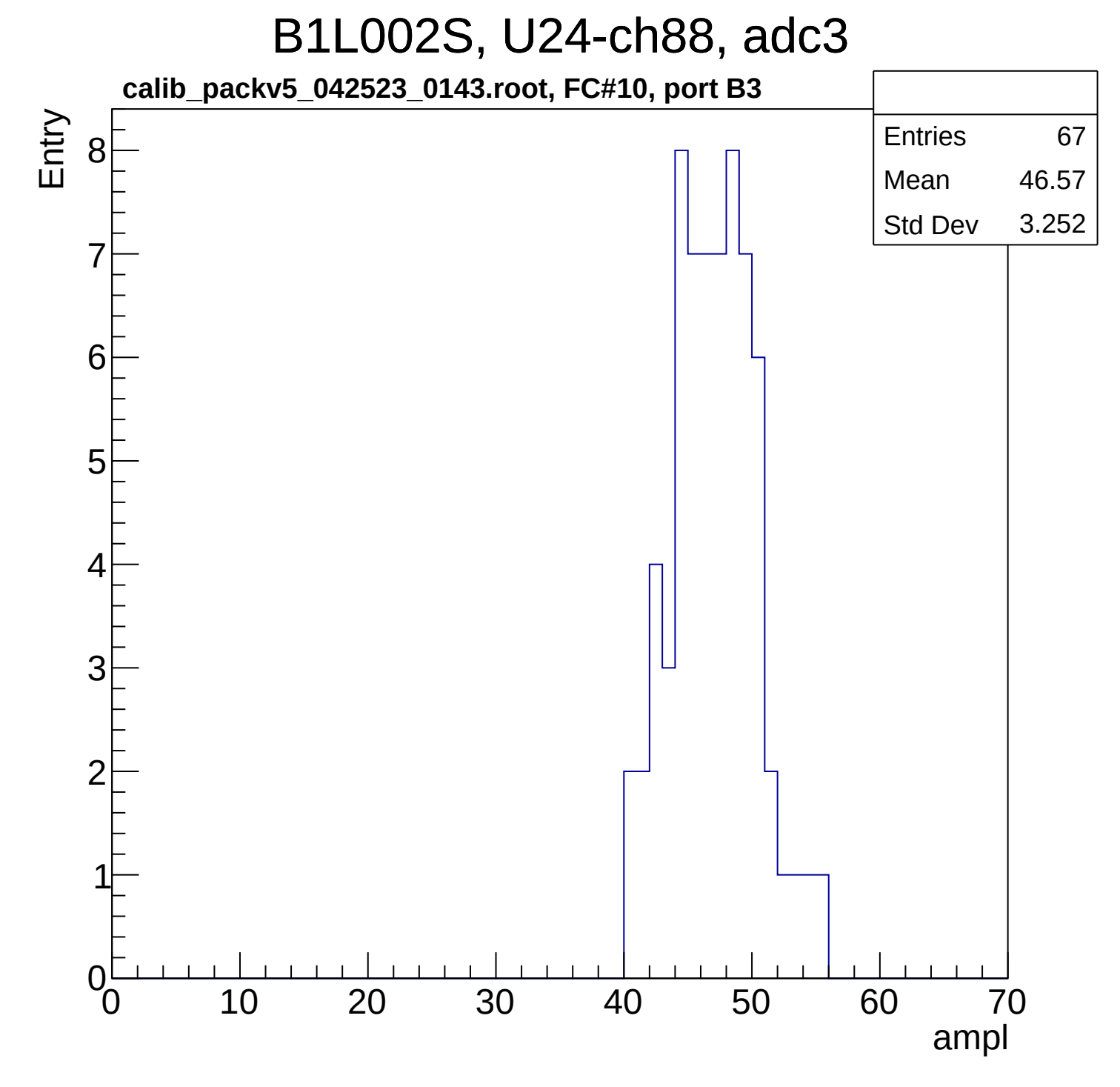
B1L002S, U24-ch88, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 46.57
Std Dev 3.252
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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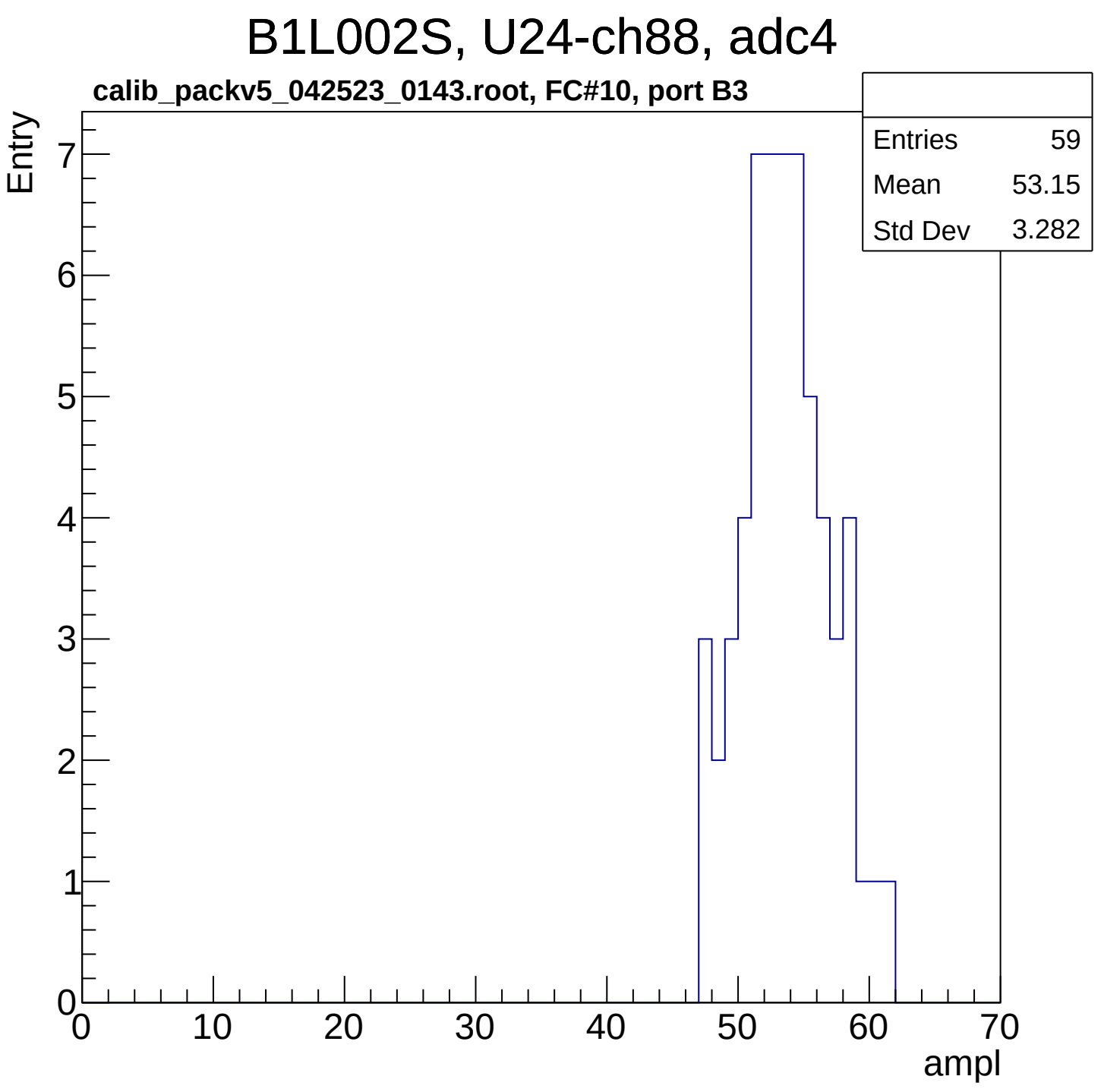
B1L002S, U24-ch88, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.15
Std Dev 3.282
Entry
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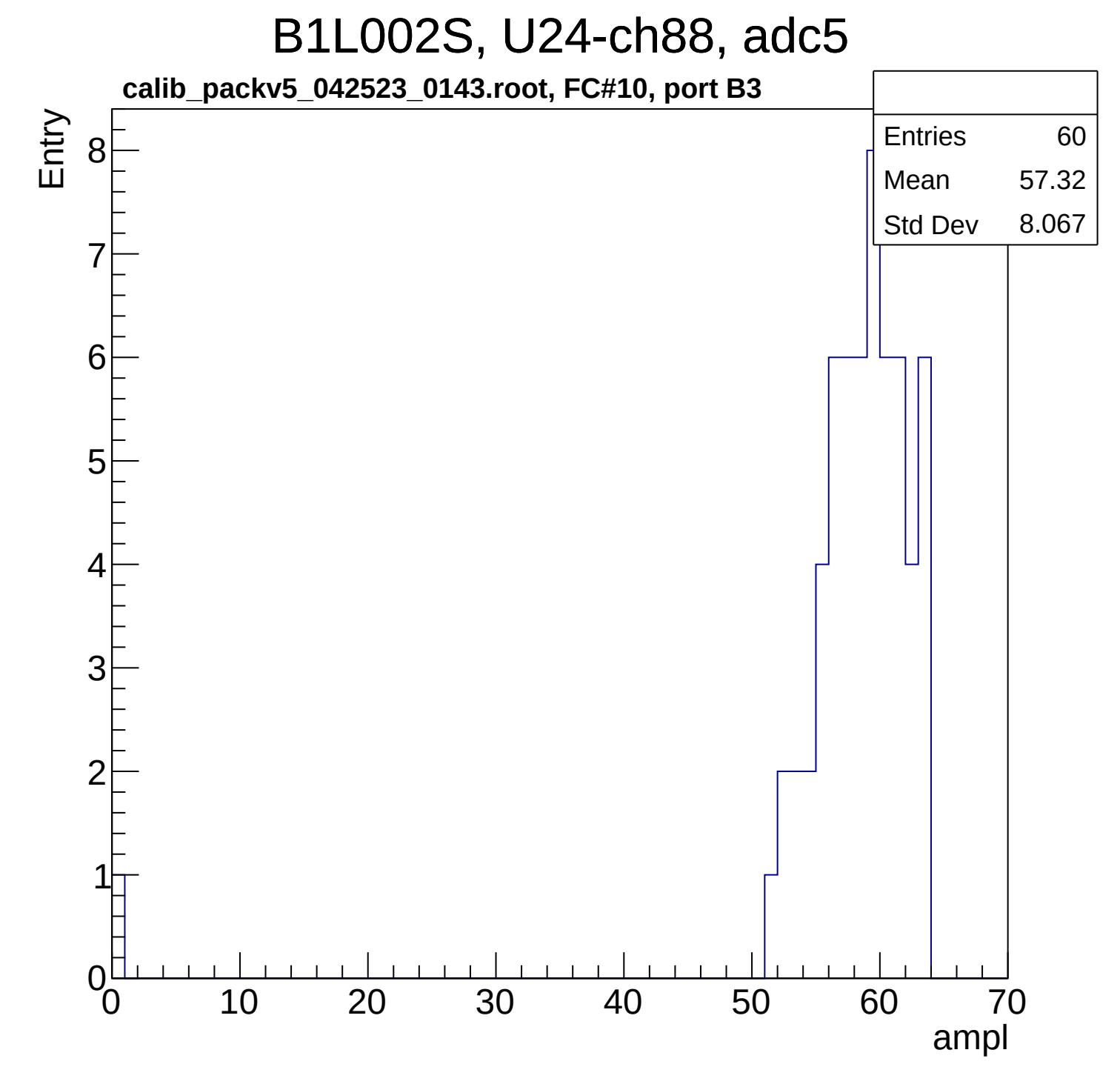
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch88, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 57.32
Std Dev 8.067
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
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60
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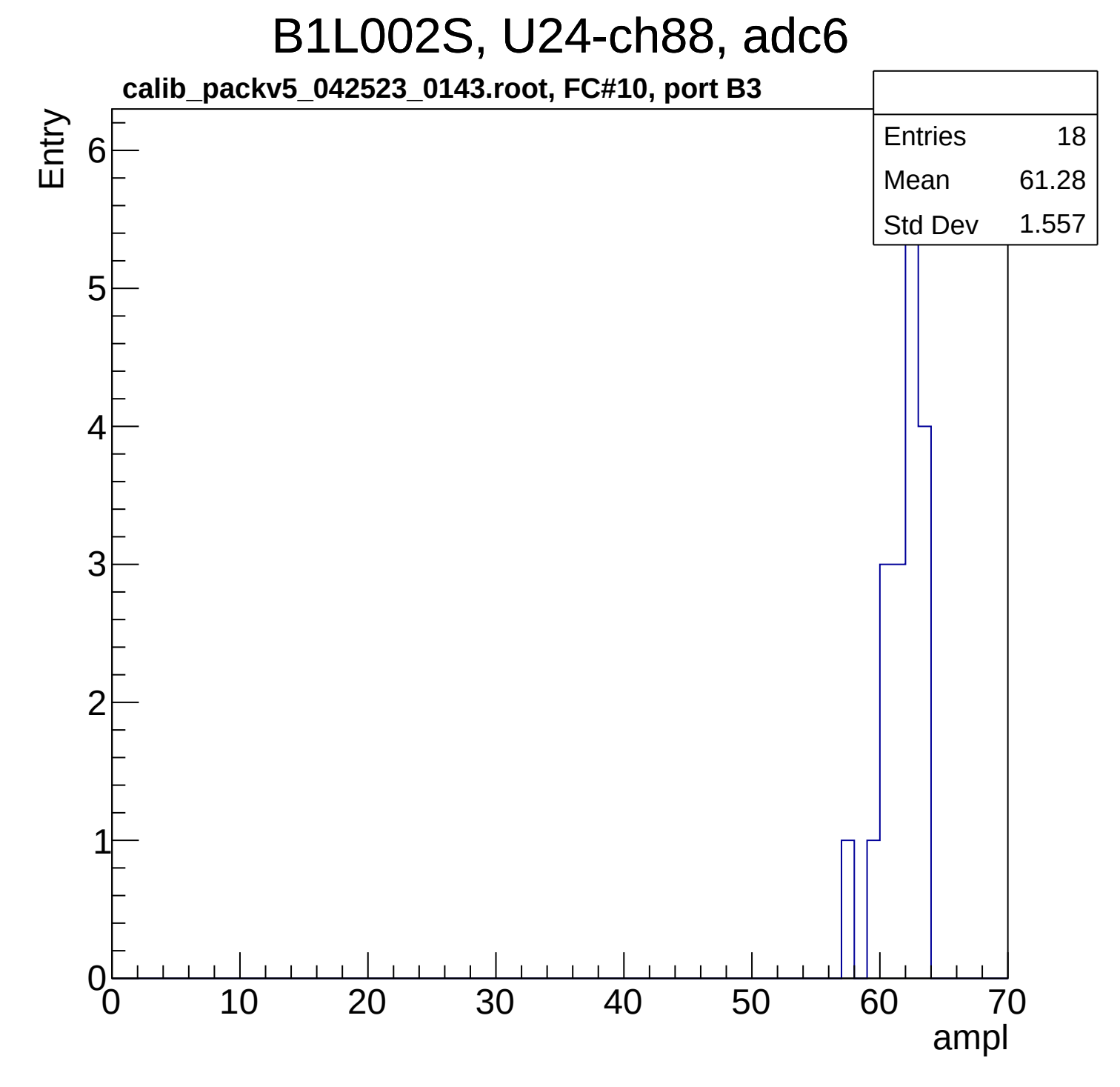
B1L002S, U24-ch88, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 18
Mean 61.28
Std Dev 1.557
Entry
ampl
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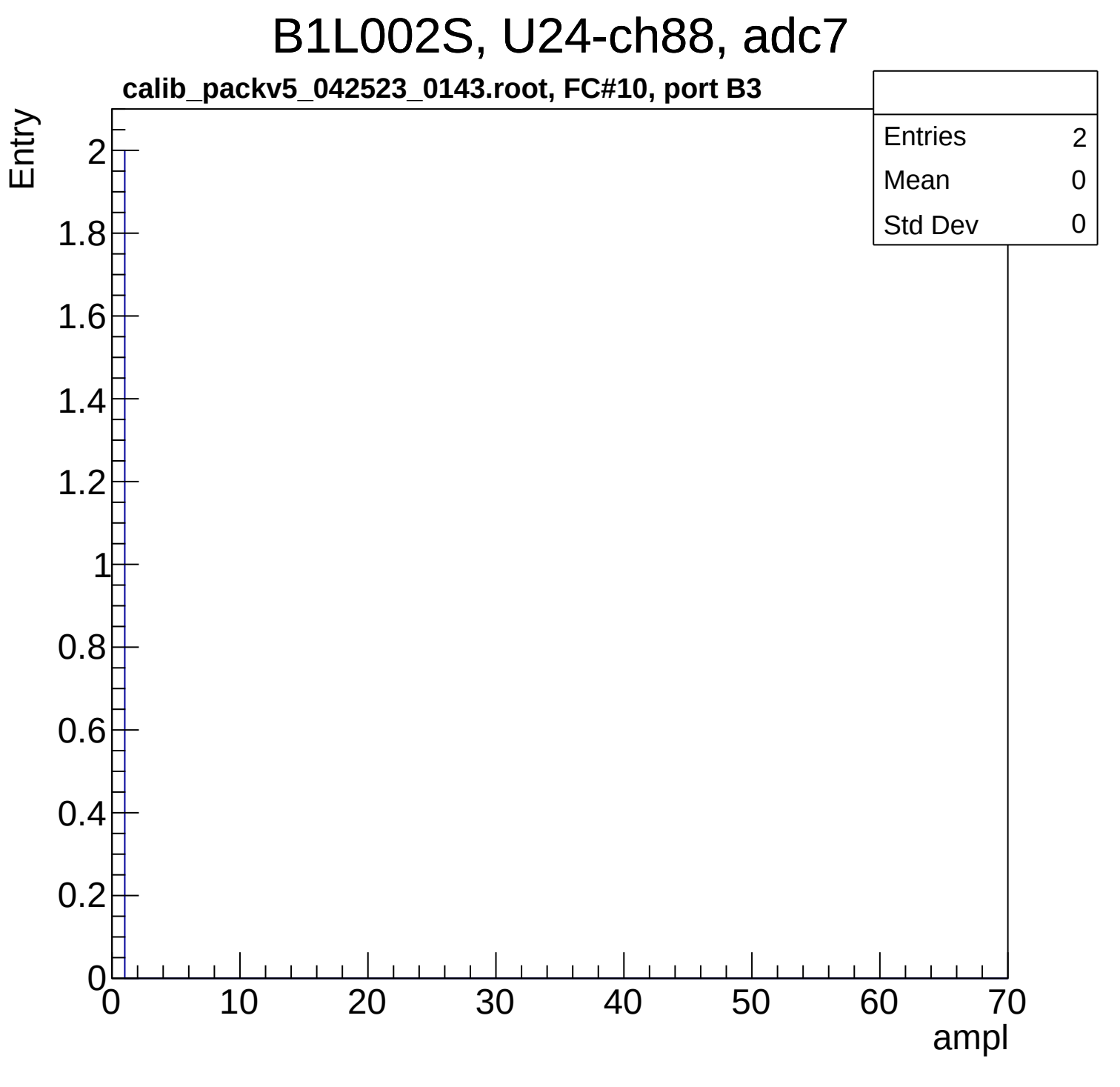
B1L002S, U24-ch88, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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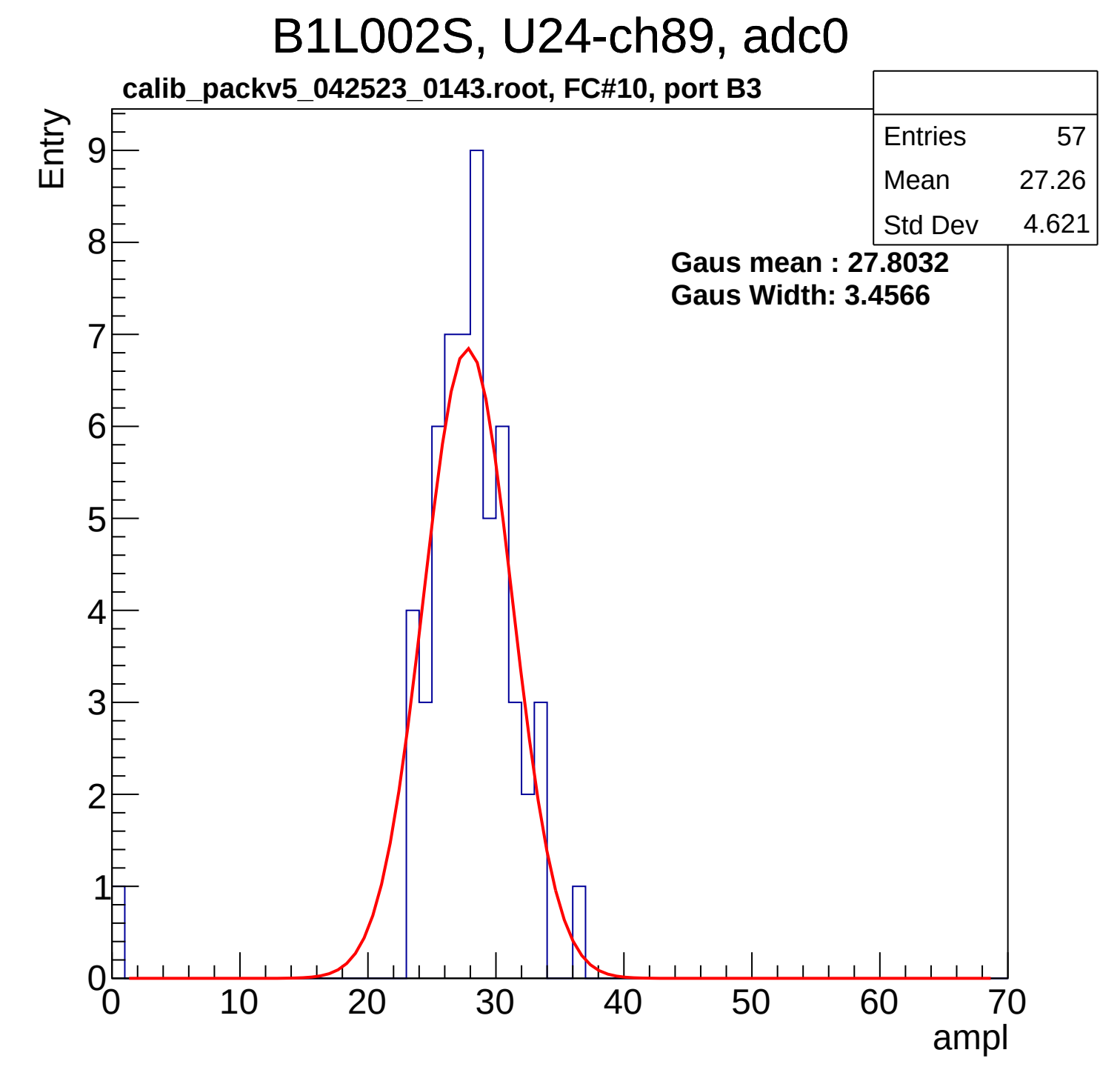
B1L002S, U24-ch89, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 27.26
Std Dev 4.621
Gaus mean : 27.8032
Gaus Width: 3.4566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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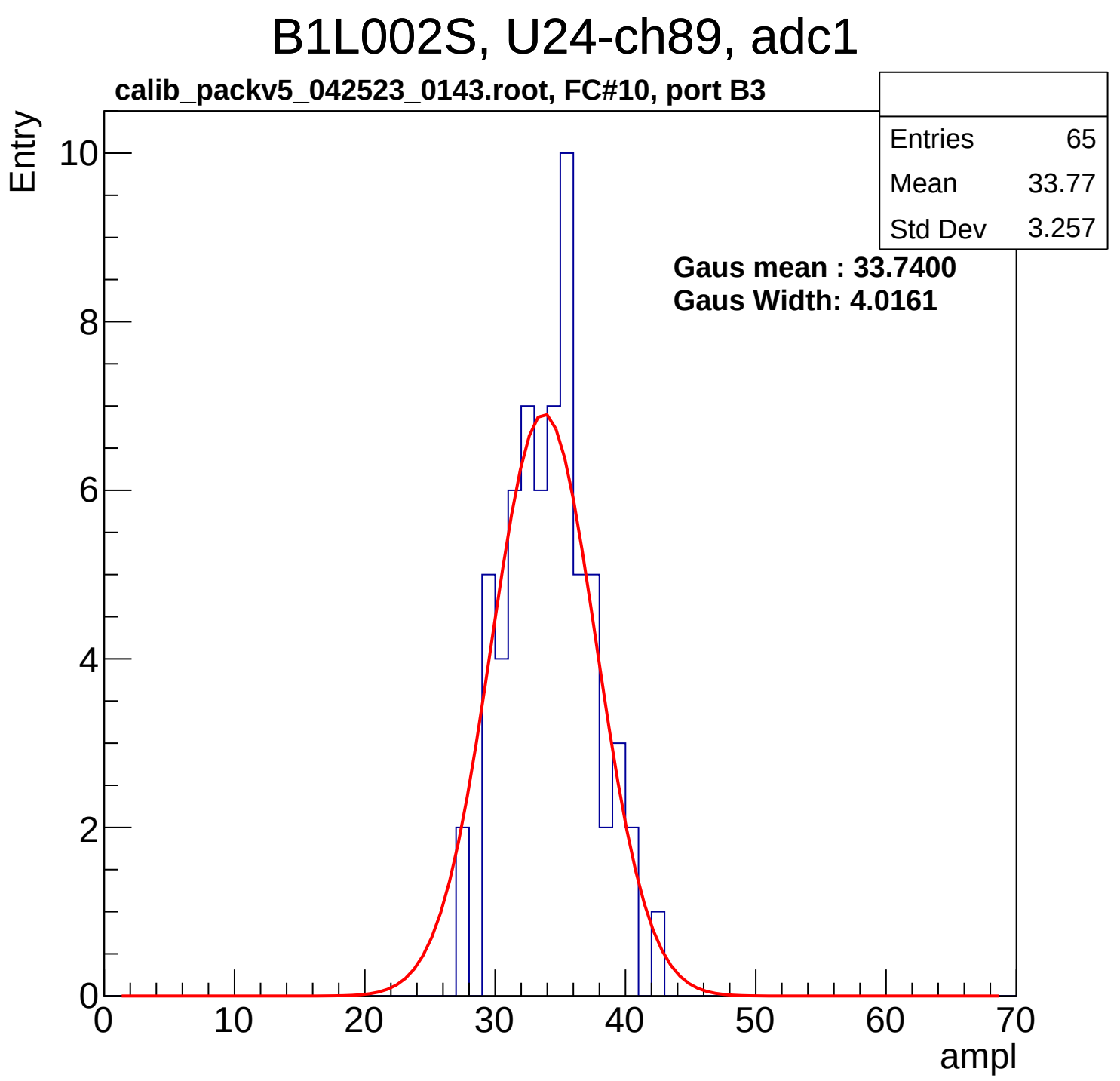
40
50
60
70


B1L002S, U24-ch89, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 33.77
Std Dev 3.257
Gaus mean : 33.7400
Gaus Width: 4.0161
Entry
ampl
0
2
4
6
8
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0
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40
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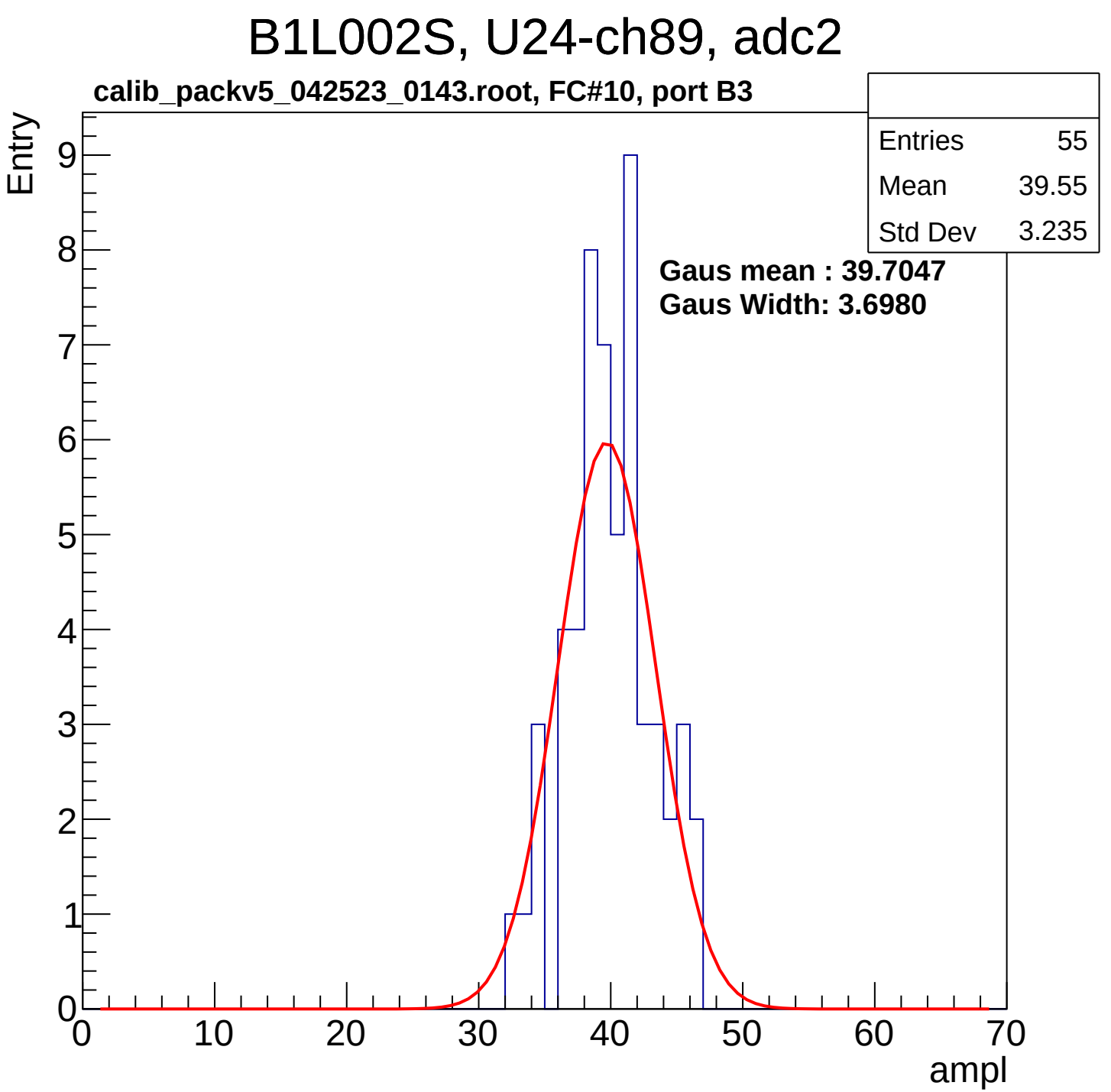
B1L002S, U24-ch89, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 39.55
Std Dev 3.235
Gaus mean : 39.7047
Gaus Width: 3.6980
Entry
ampl
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10
20
30
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2
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8
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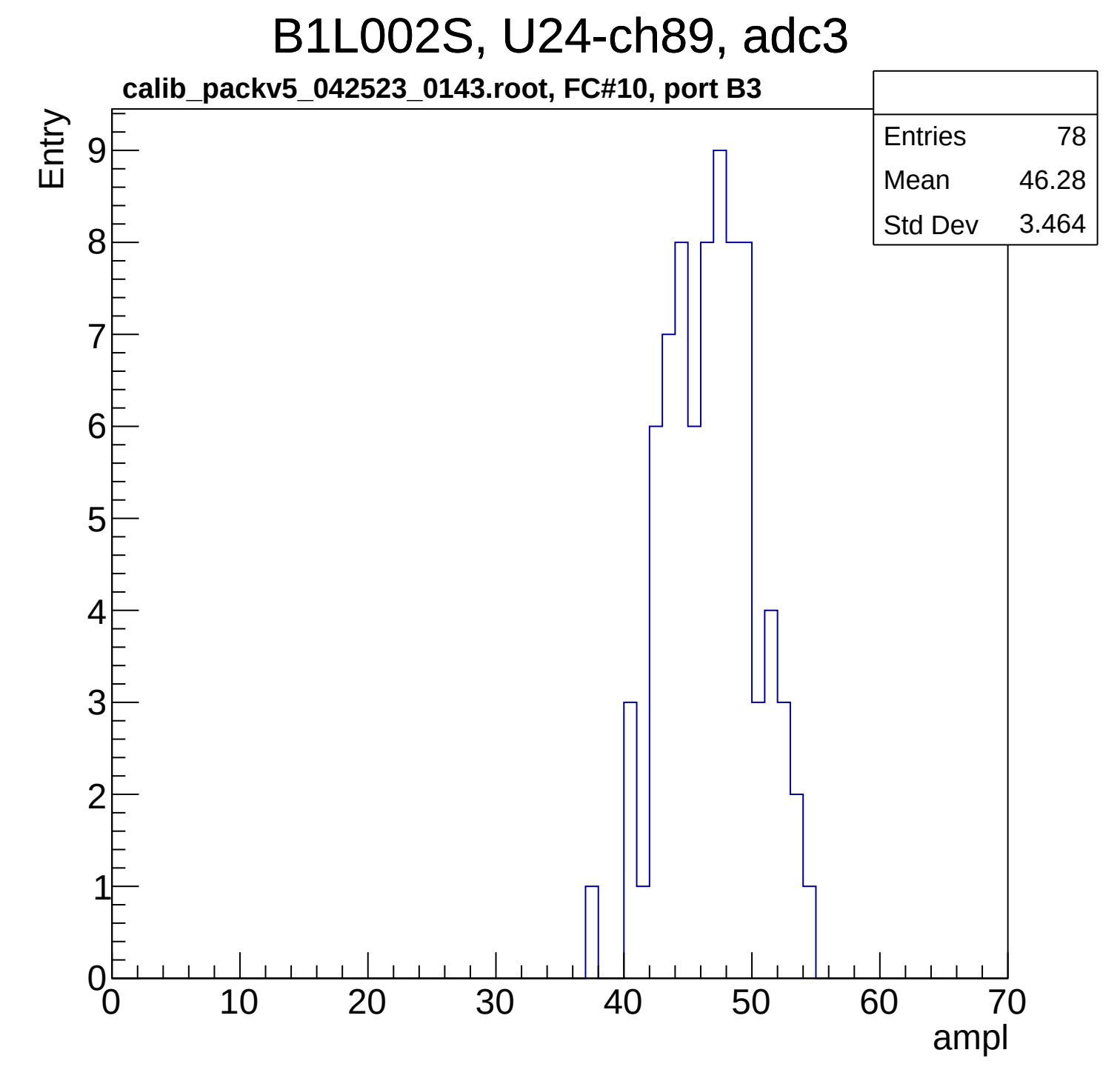
B1L002S, U24-ch89, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 46.28
Std Dev 3.464
Entry
ampl
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2
3
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6
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8
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0
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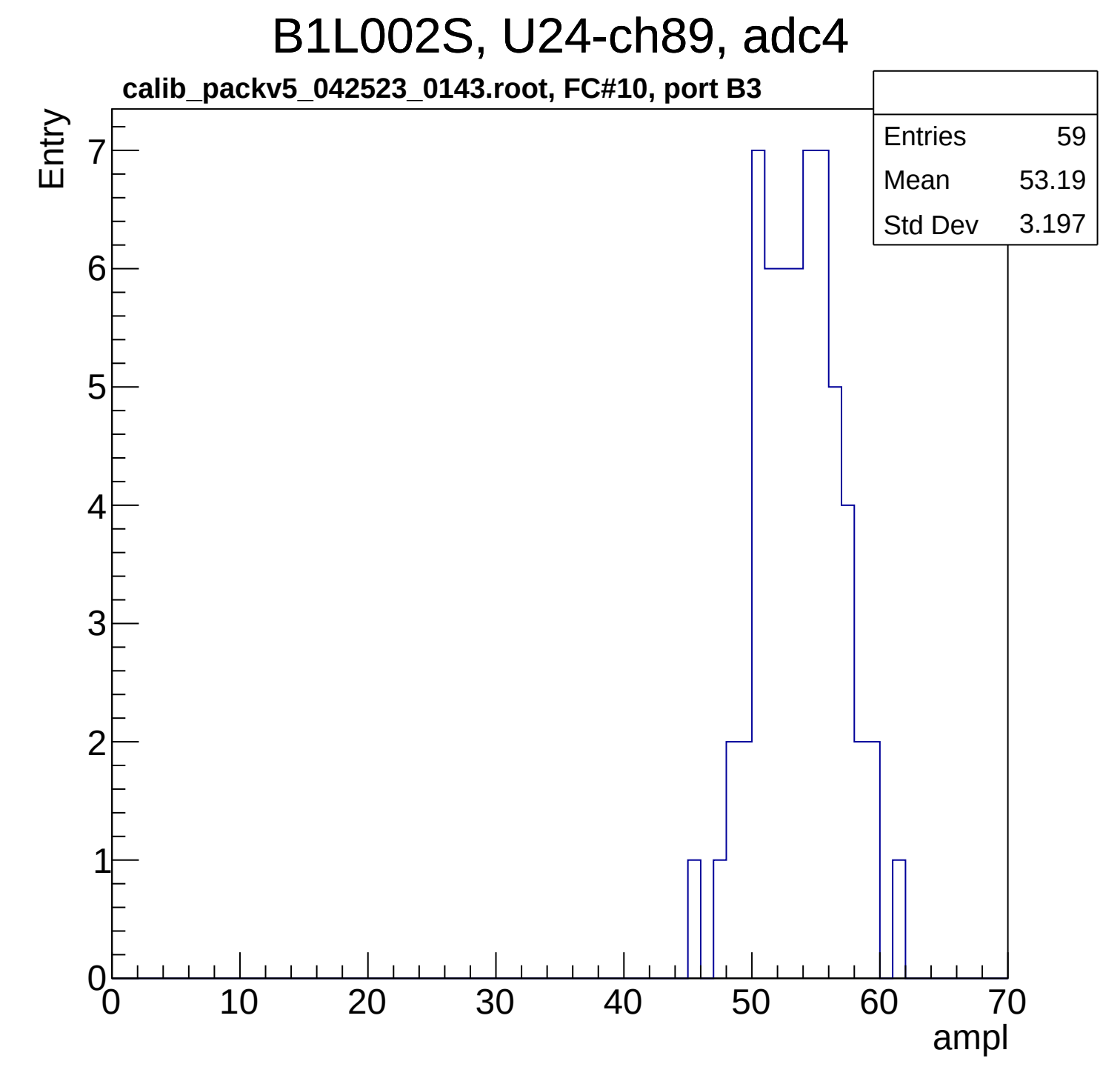
B1L002S, U24-ch89, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.19
Std Dev 3.197
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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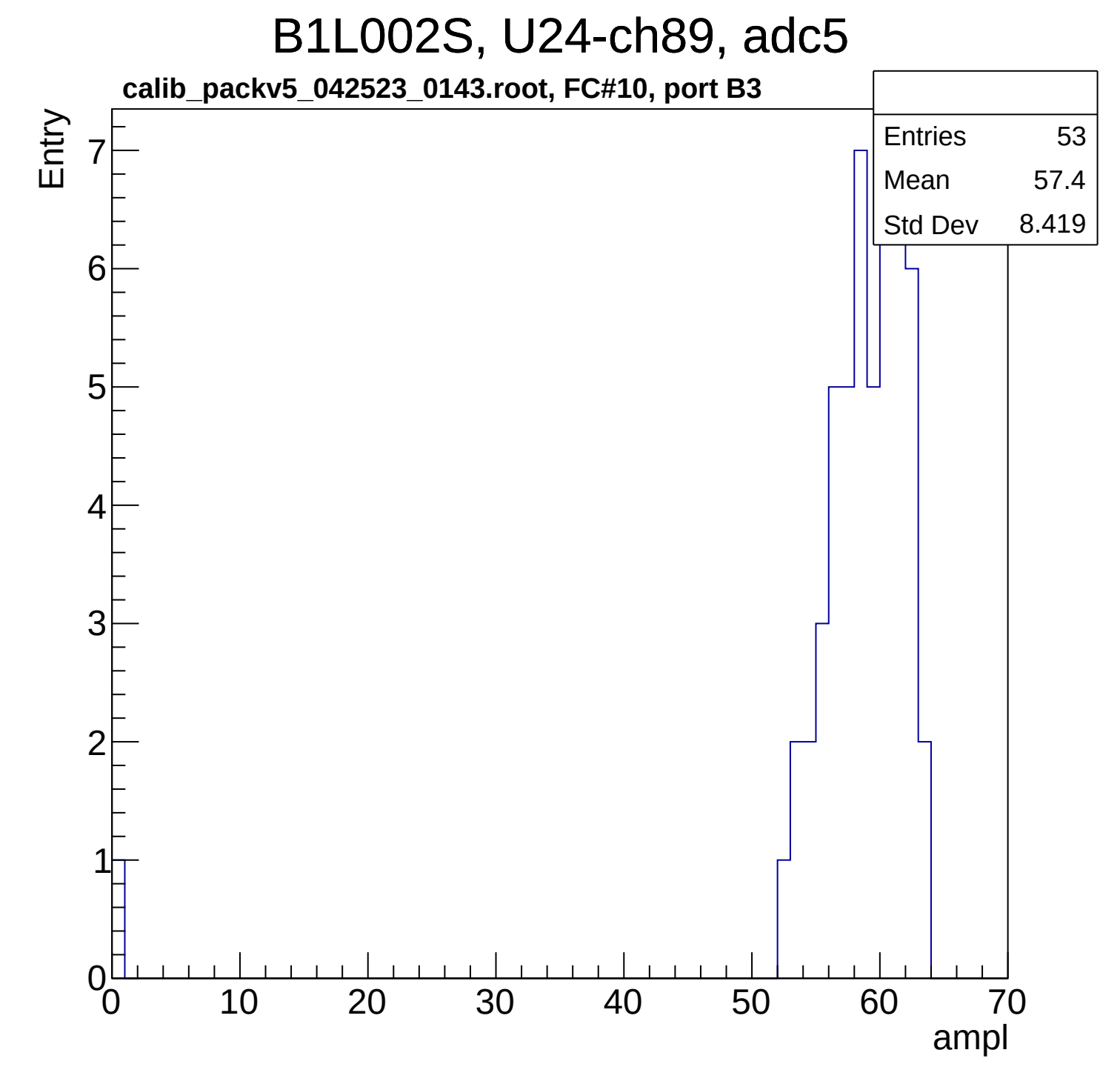
B1L002S, U24-ch89, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 57.4
Std Dev 8.419
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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60
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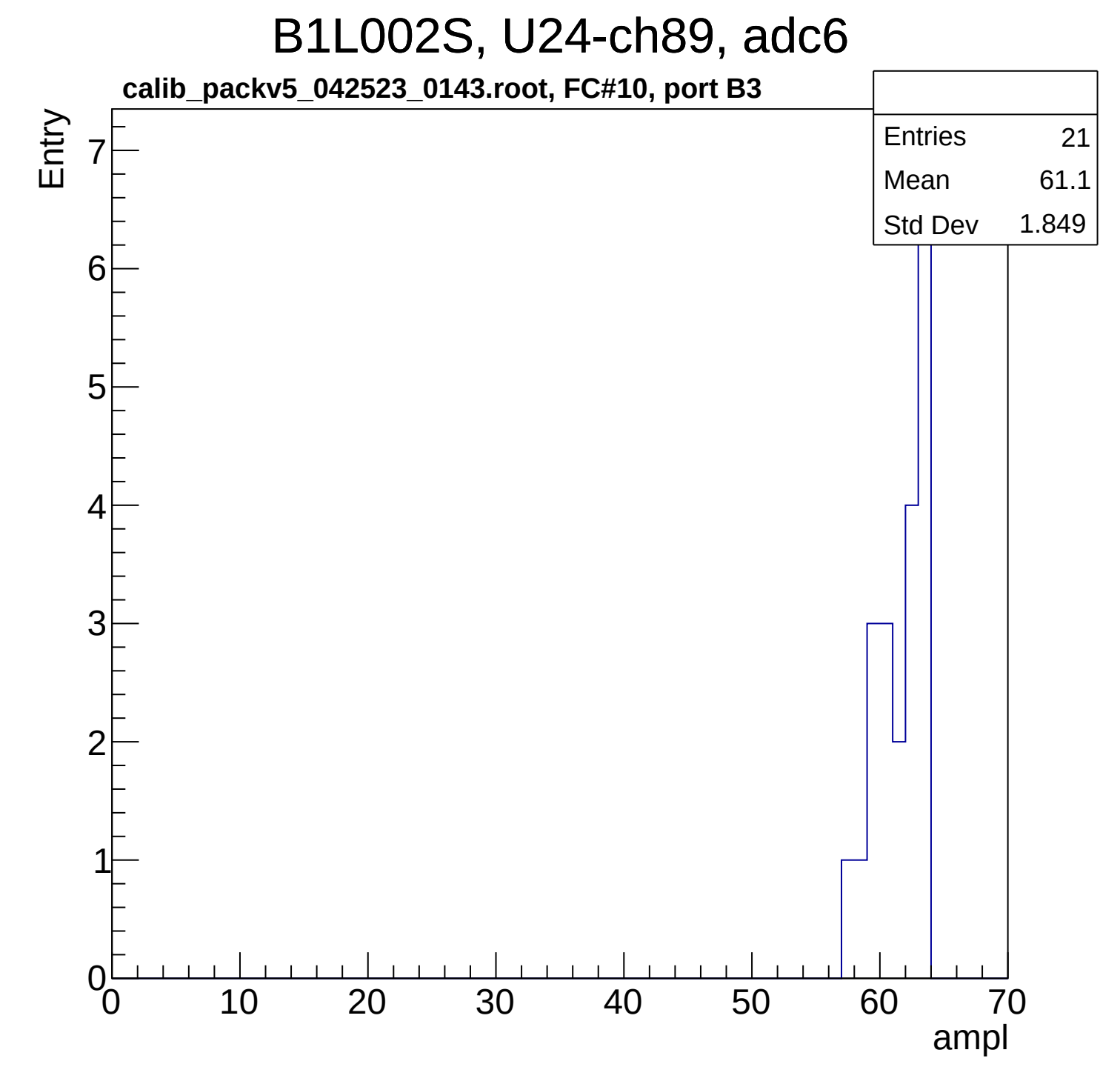
B1L002S, U24-ch89, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.1
Std Dev 1.849
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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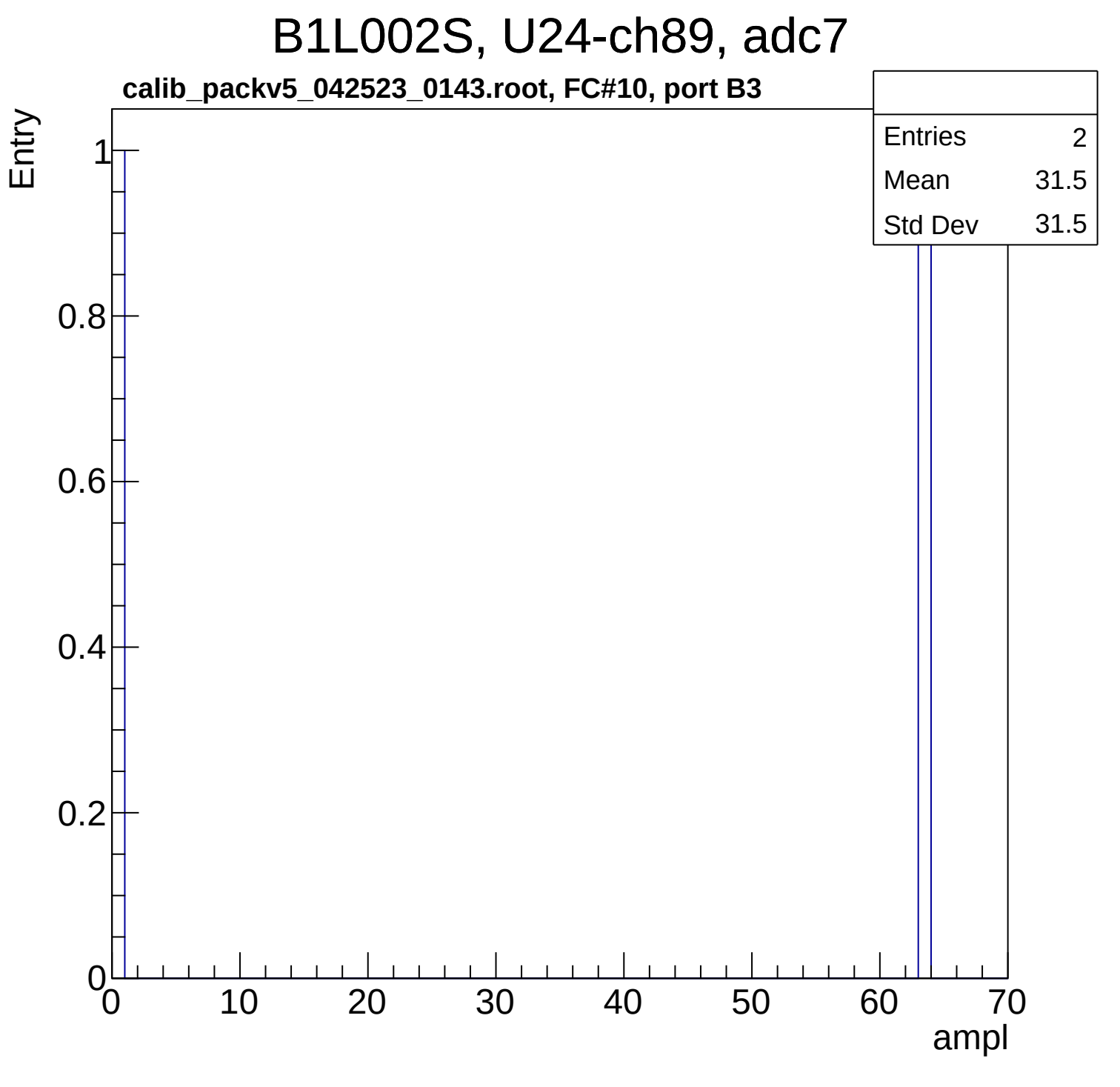
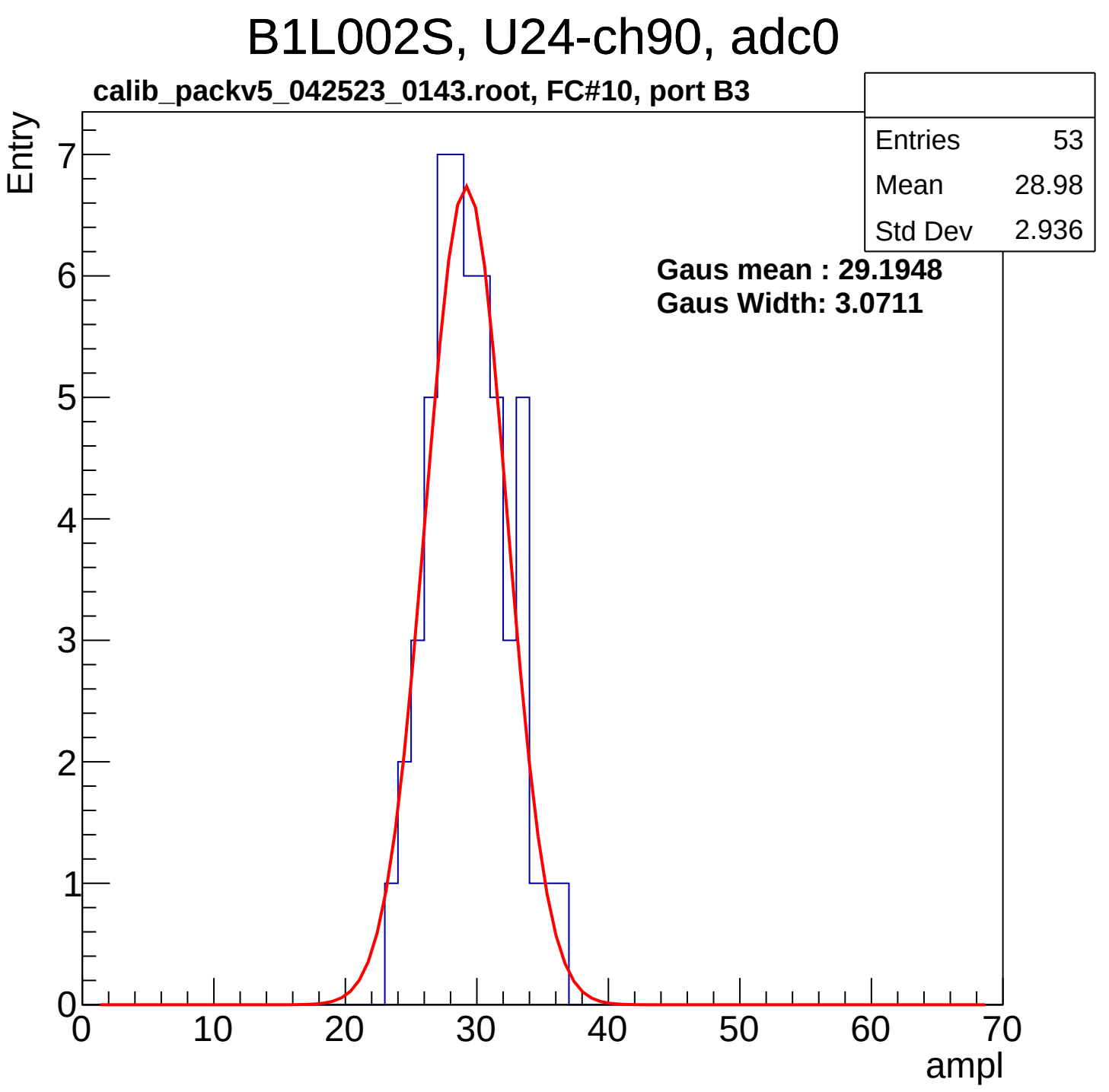

B1L002S, U24-ch89, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch90, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 28.98
Std Dev 2.936
Gaus mean : 29.1948
Gaus Width: 3.0711
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
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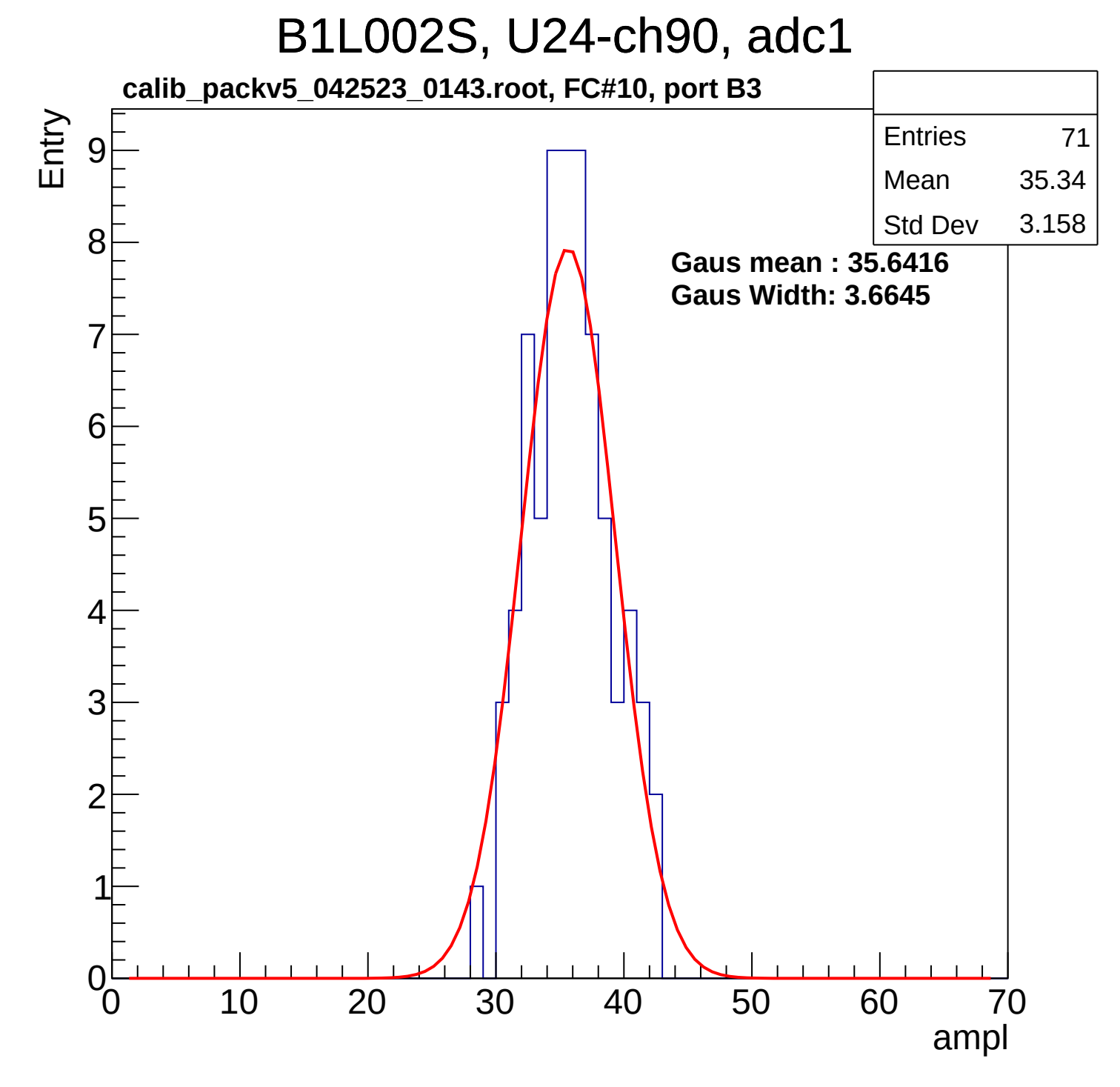
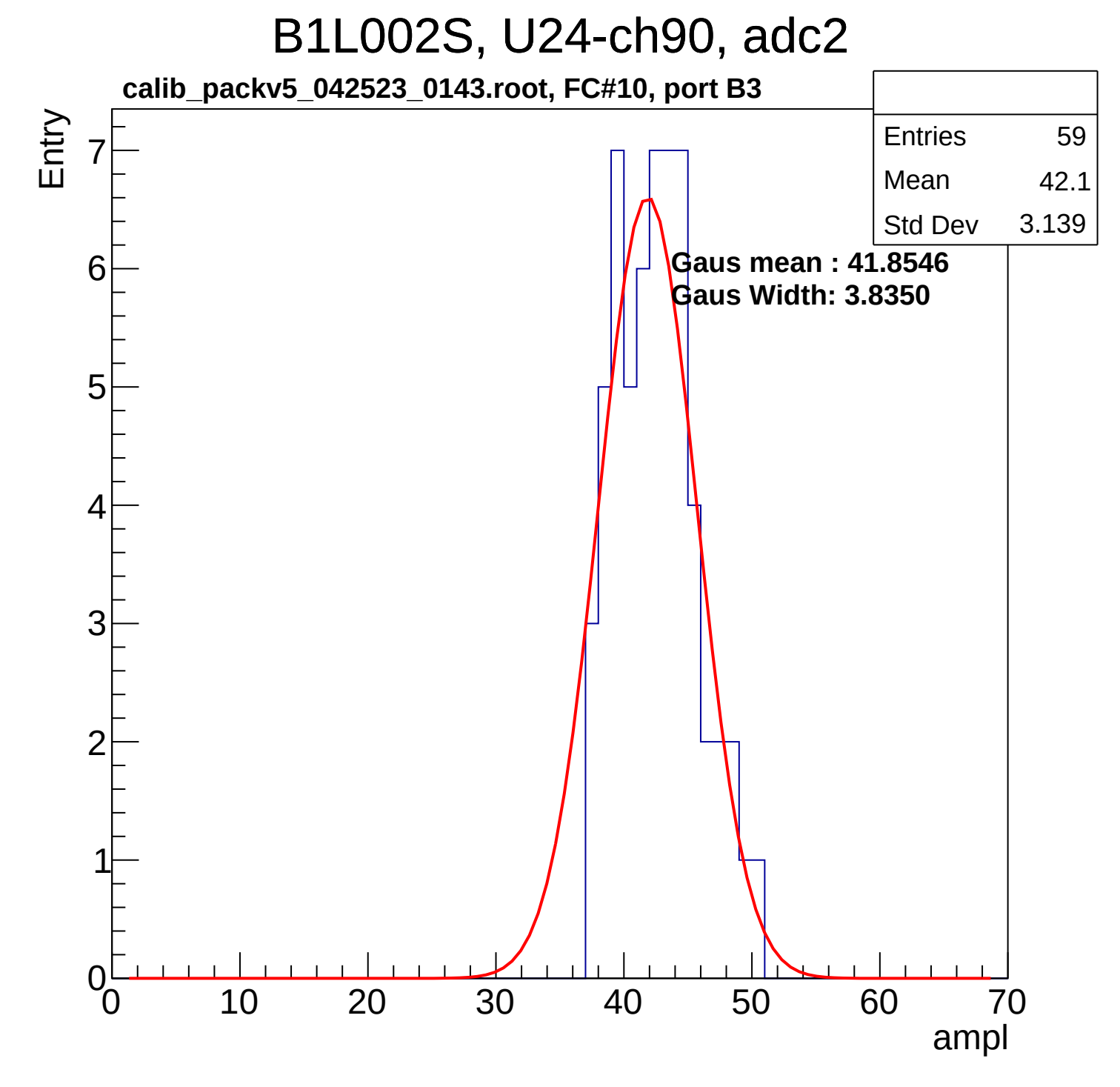
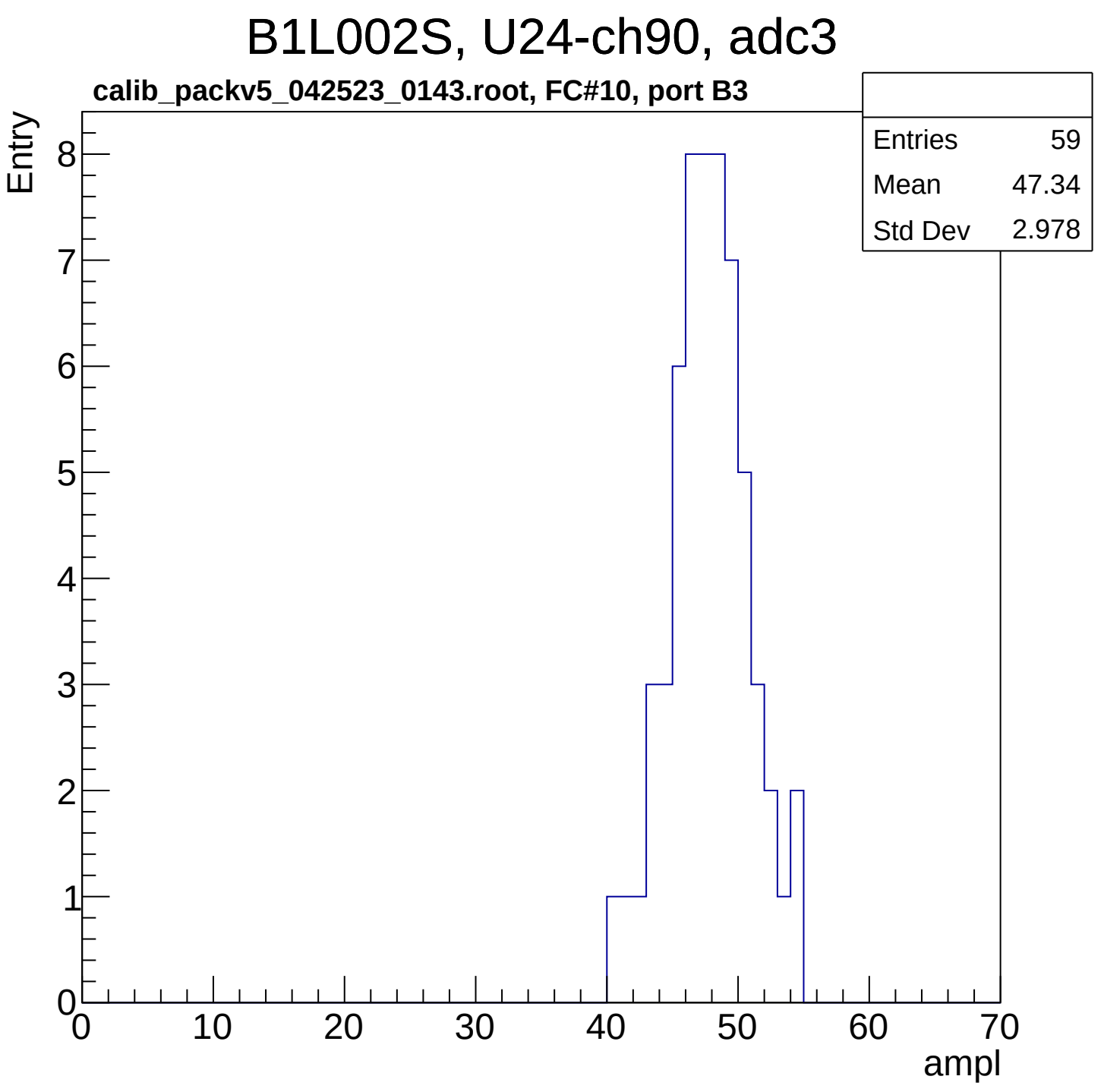

B1L002S, U24-ch90, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.34
Std Dev 3.158
Gaus mean : 35.6416
Gaus Width: 3.6645
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U24-ch90, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 42.1
Std Dev 3.139
Gaus mean : 41.8546
Gaus Width: 3.8350
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch90, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.34
Std Dev 2.978
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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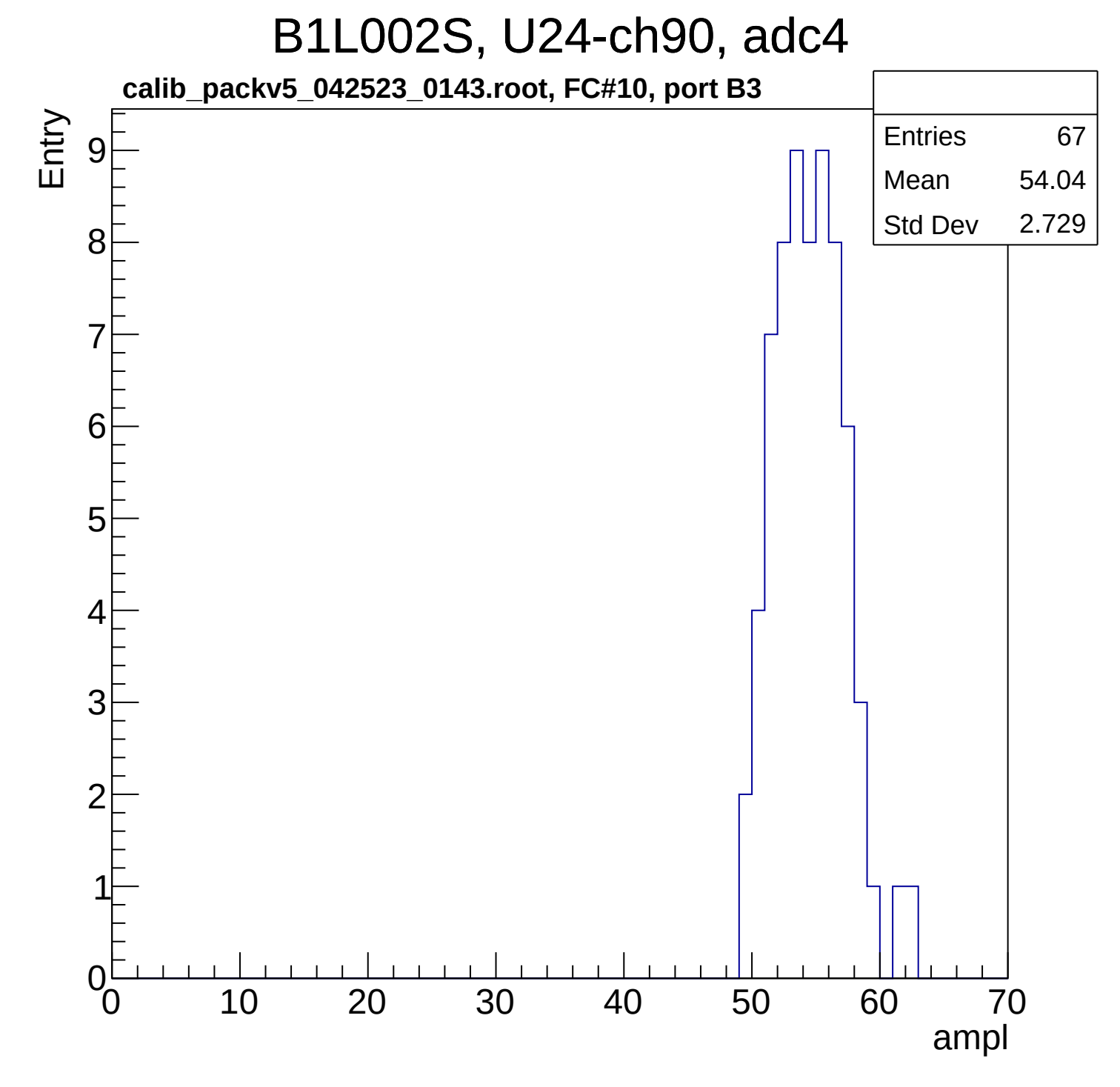
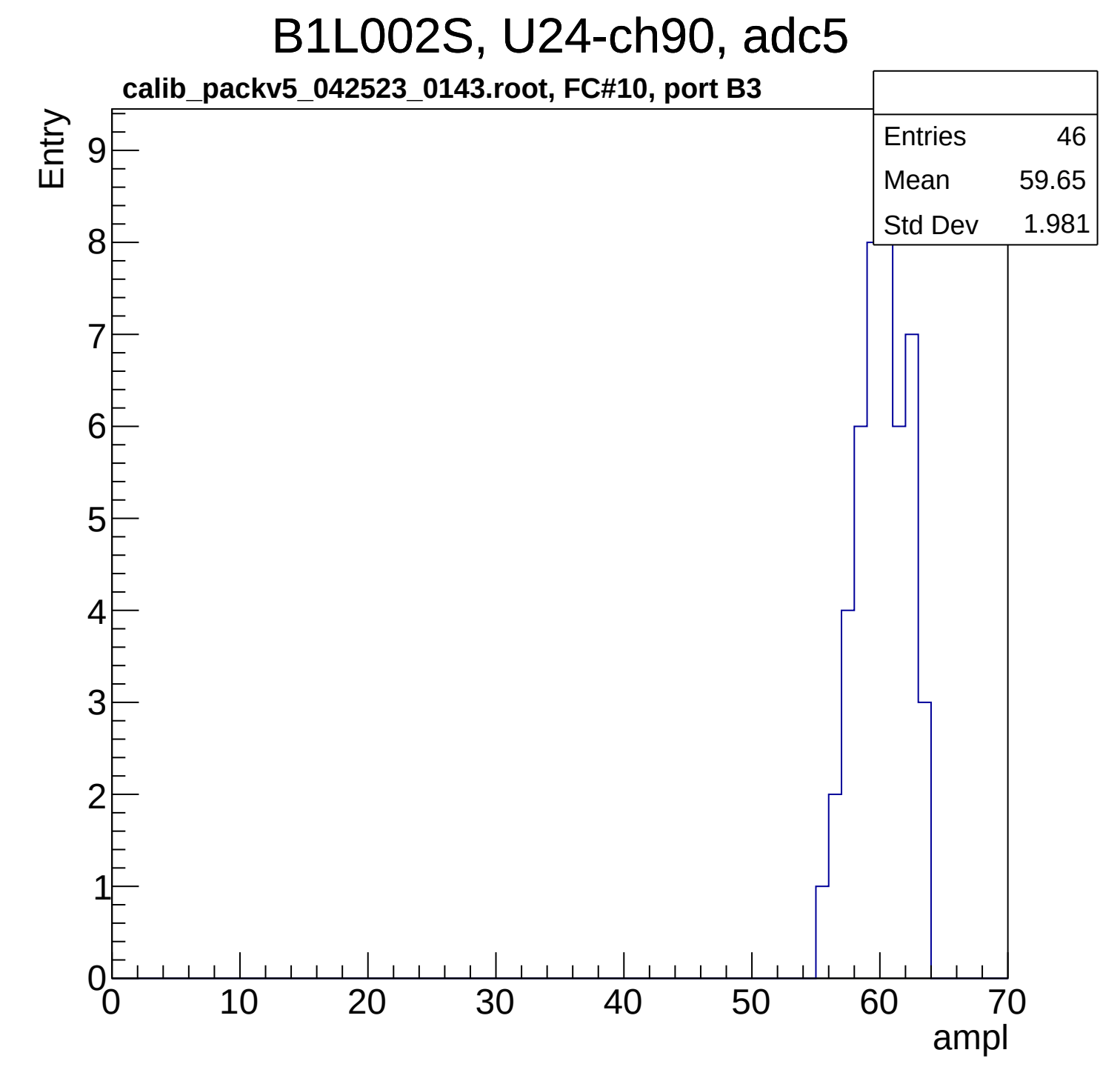
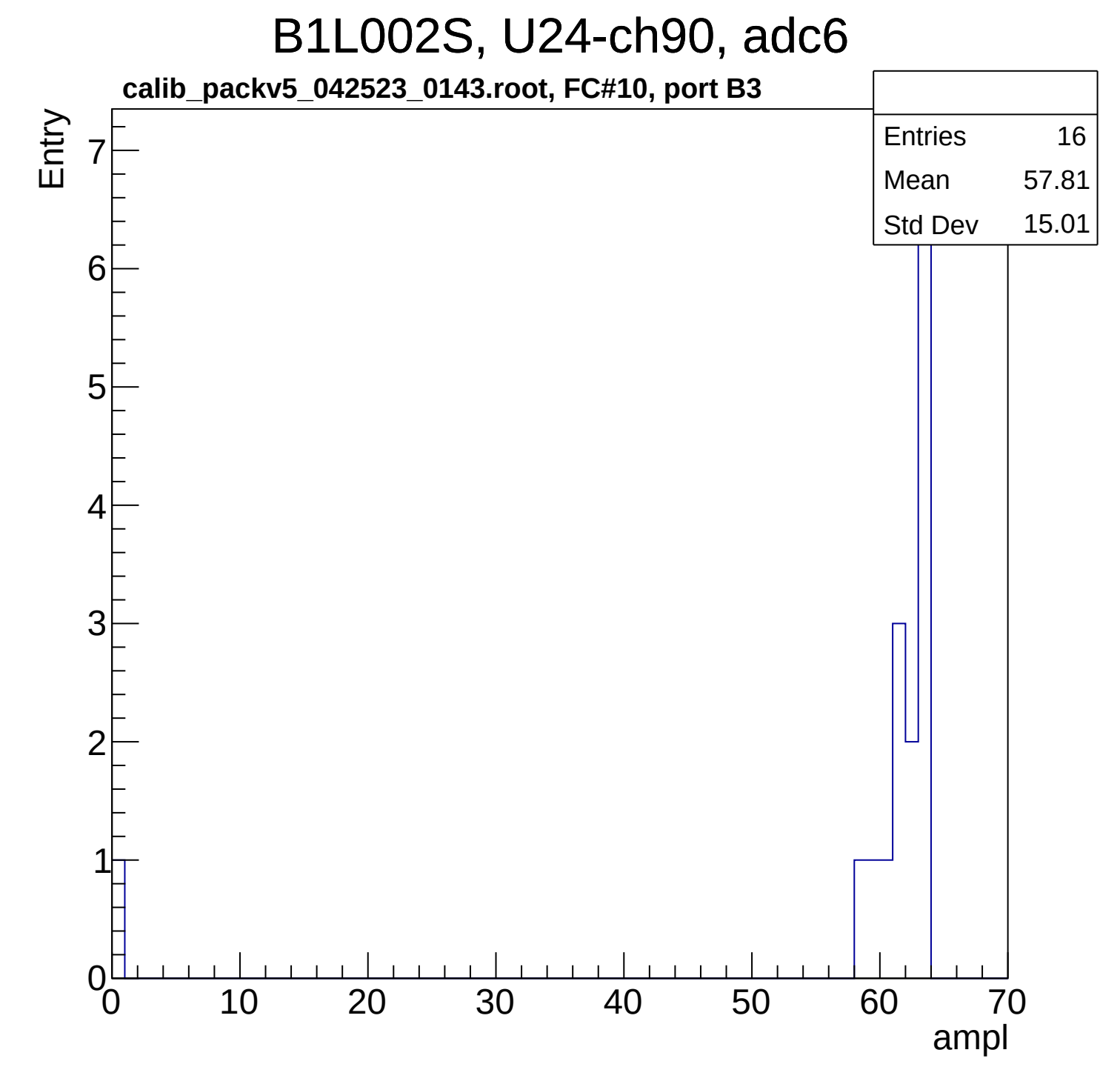
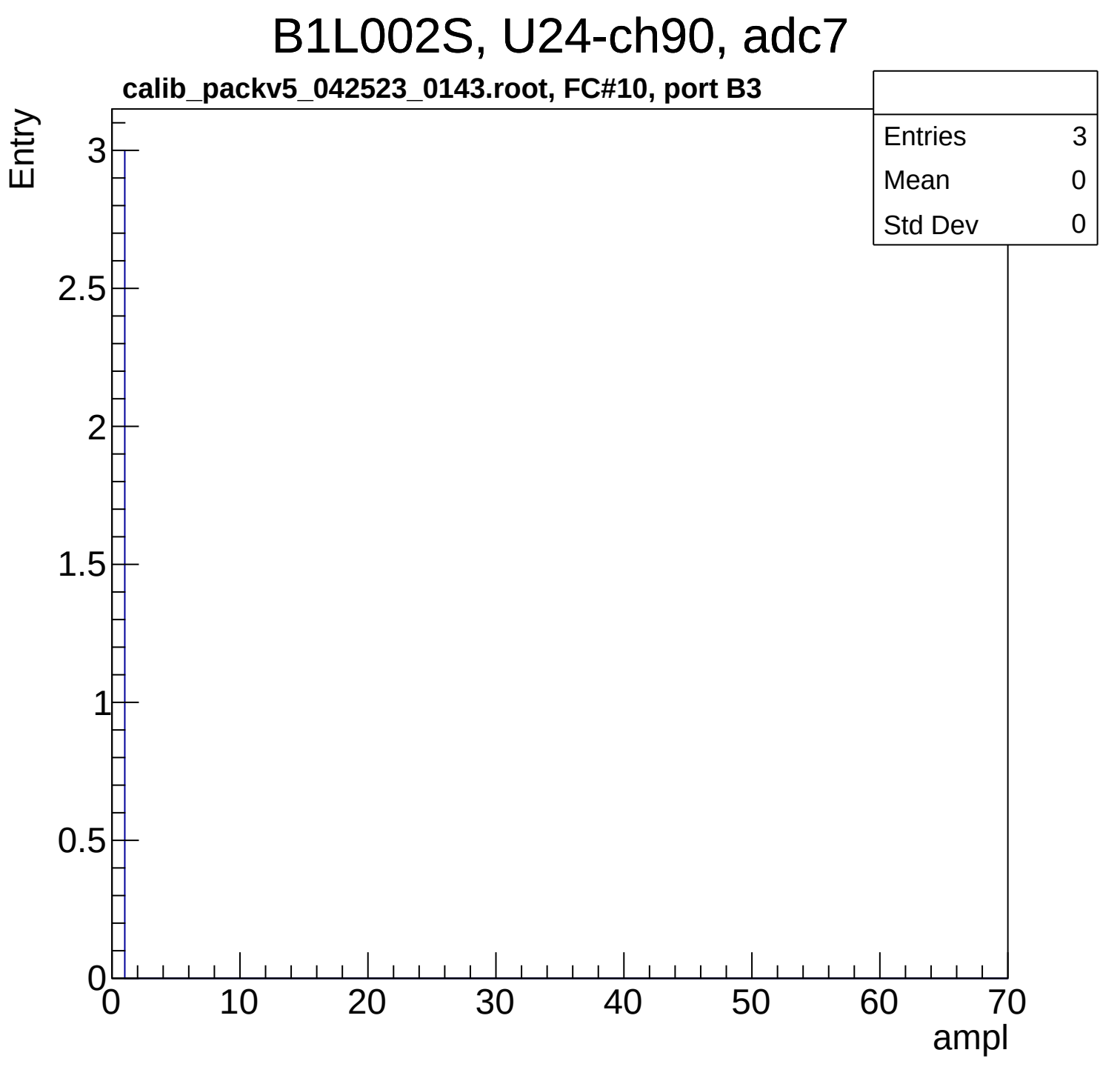
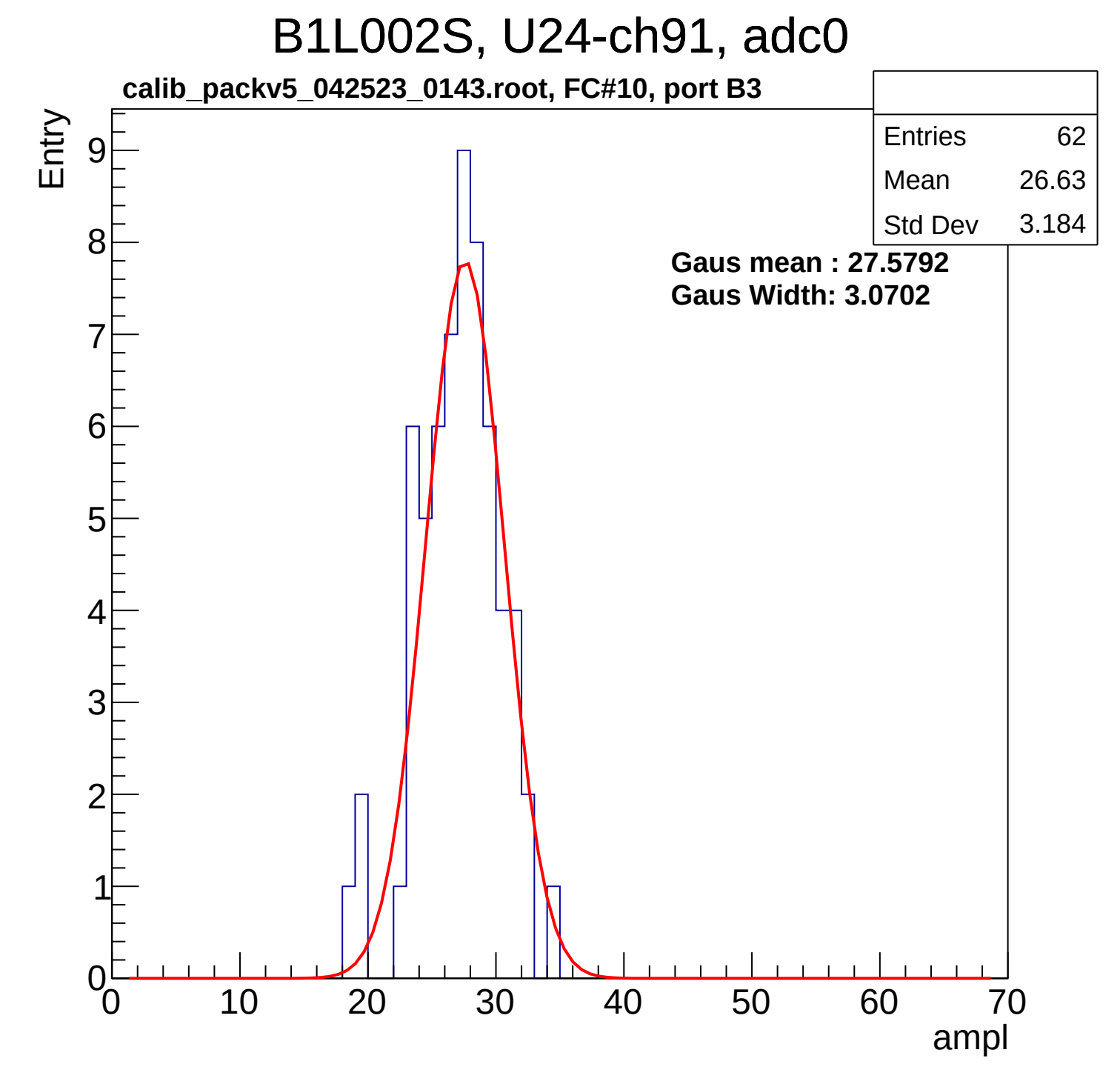
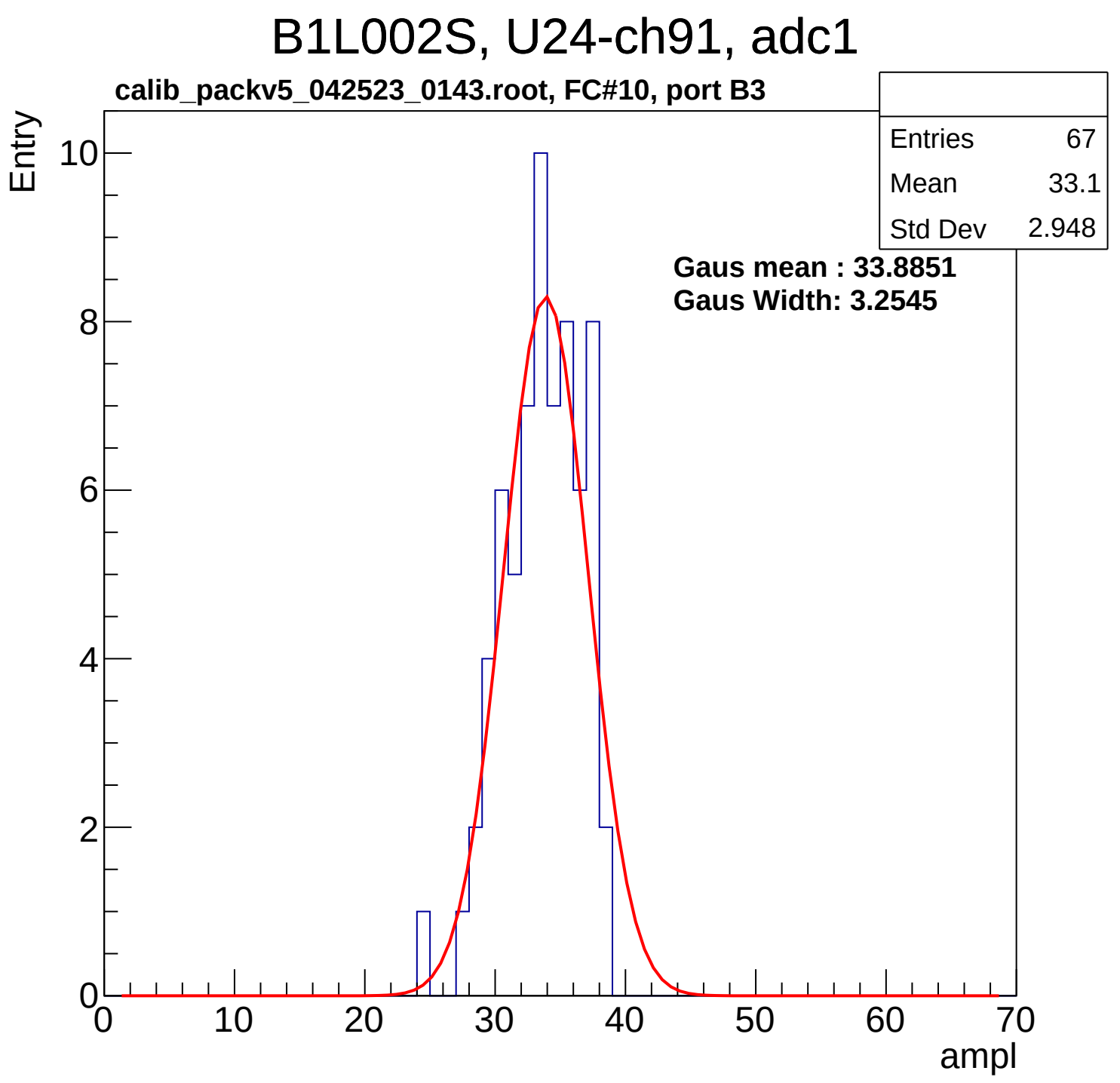

B1L002S, U24-ch90, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 54.04
Std Dev 2.729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

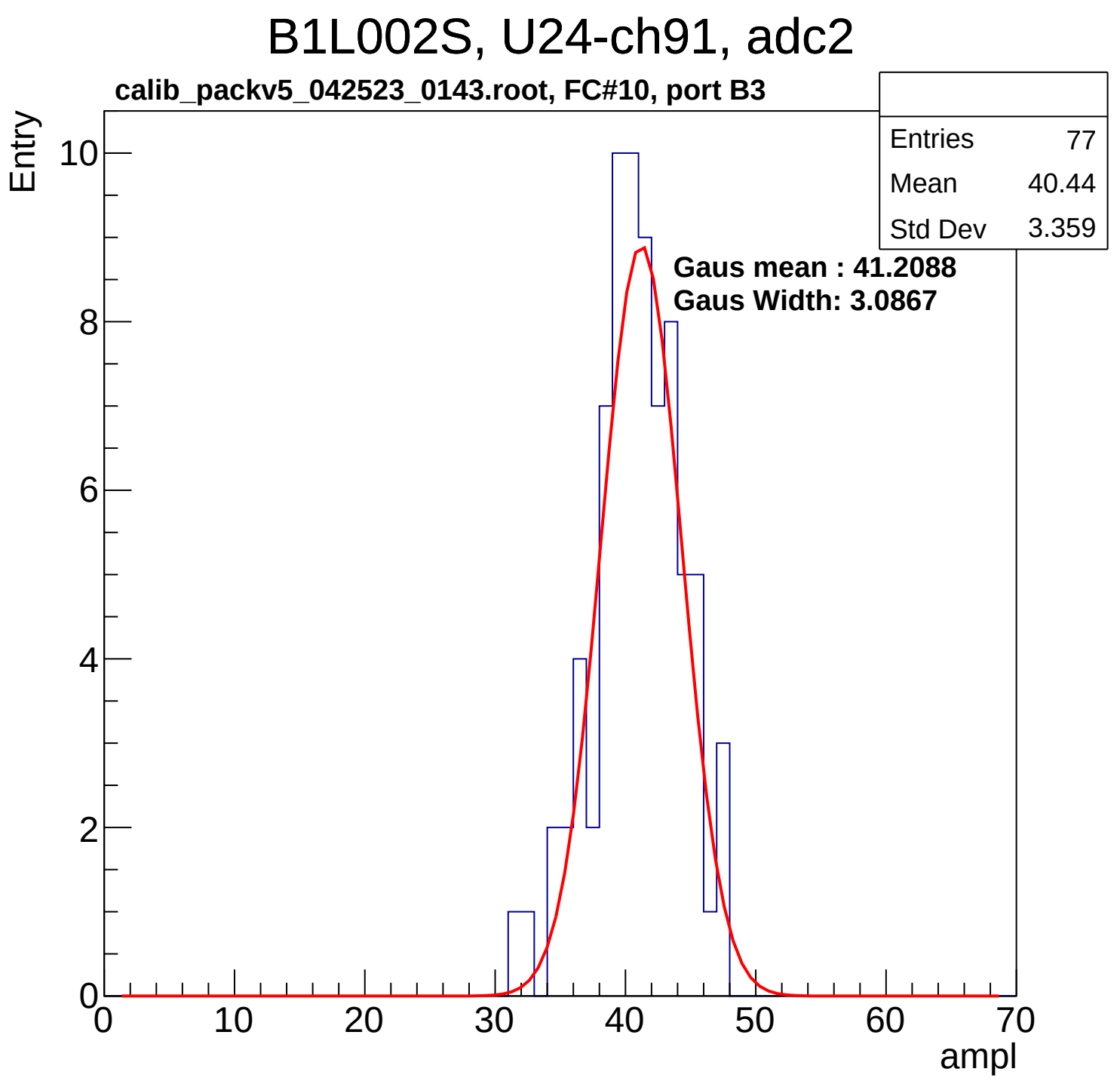

B1L002S, U24-ch90, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 59.65
Std Dev 1.981
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch90, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.81
Std Dev 15.01
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch90, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch91, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 26.63
Std Dev 3.184
Gaus mean : 27.5792
Gaus Width: 3.0702
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch91, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 33.1
Std Dev 2.948
Gaus mean : 33.8851
Gaus Width: 3.2545
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch91, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 40.44
Std Dev 3.359
Gaus mean : 41.2088
Gaus Width: 3.0867
Entry
ampl
0
2
4
6
8
10
0
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20
30
40
50
60
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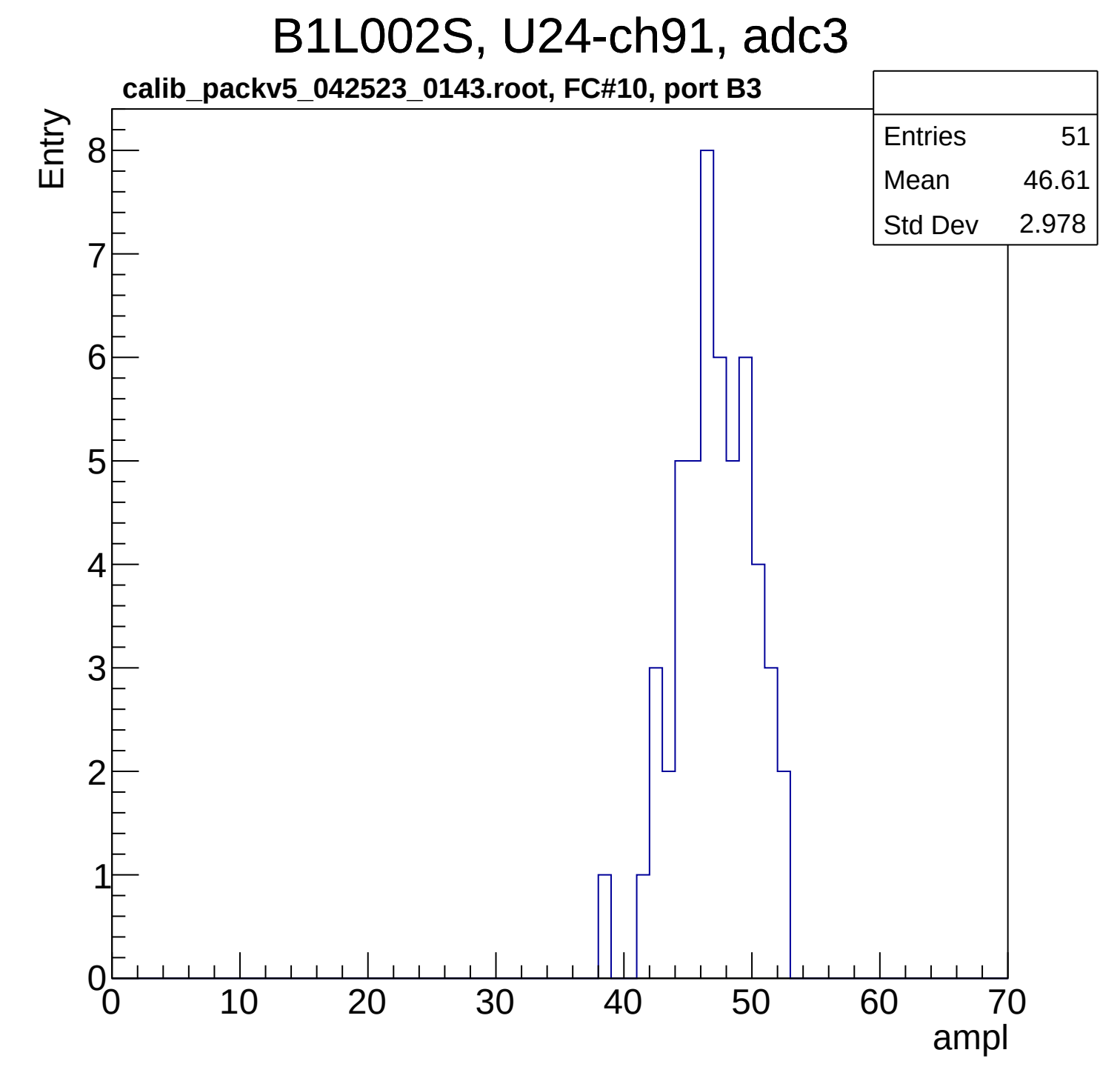
B1L002S, U24-ch91, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 46.61
Std Dev 2.978
Entry
ampl
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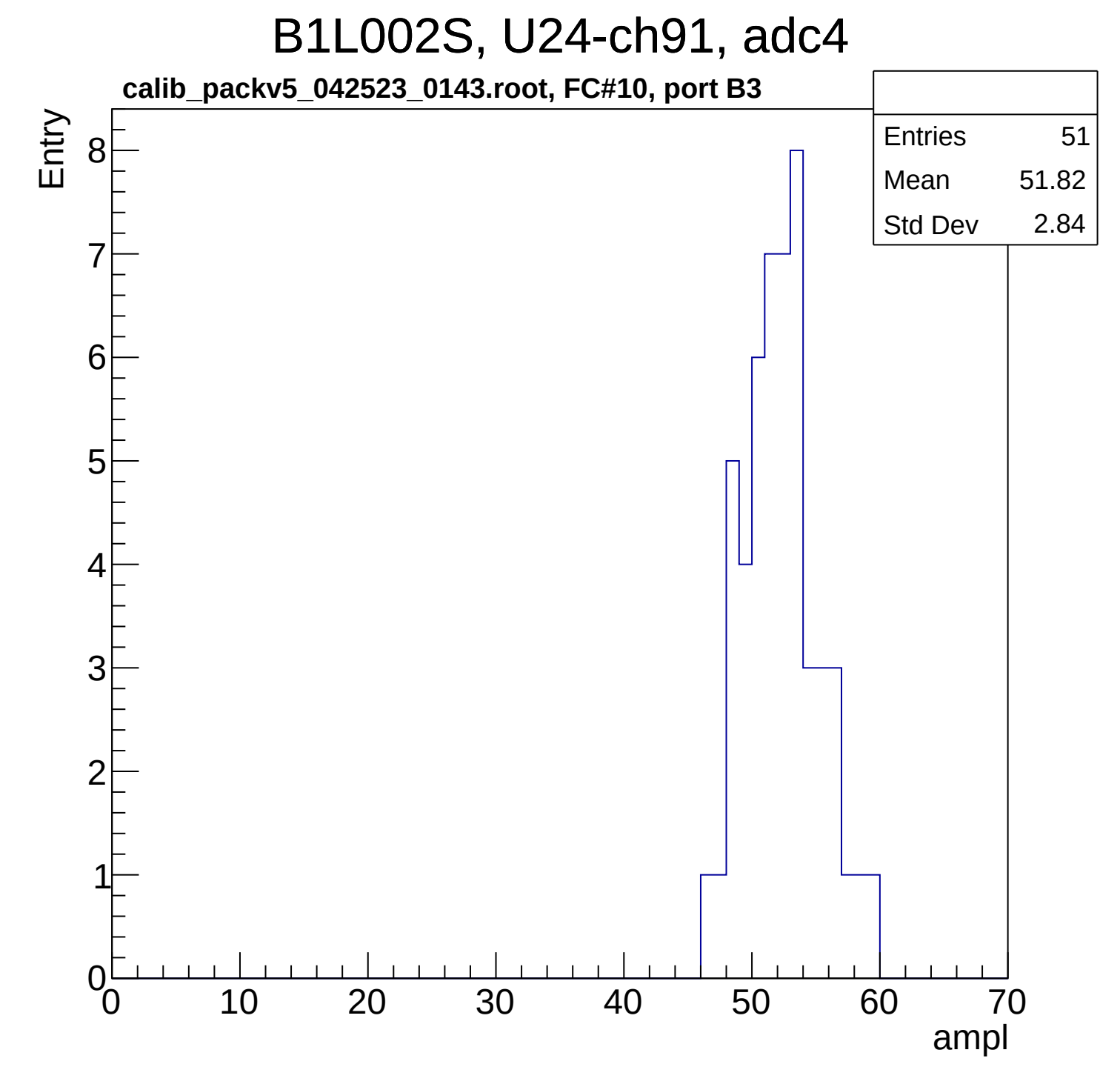
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch91, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 51.82
Std Dev 2.84
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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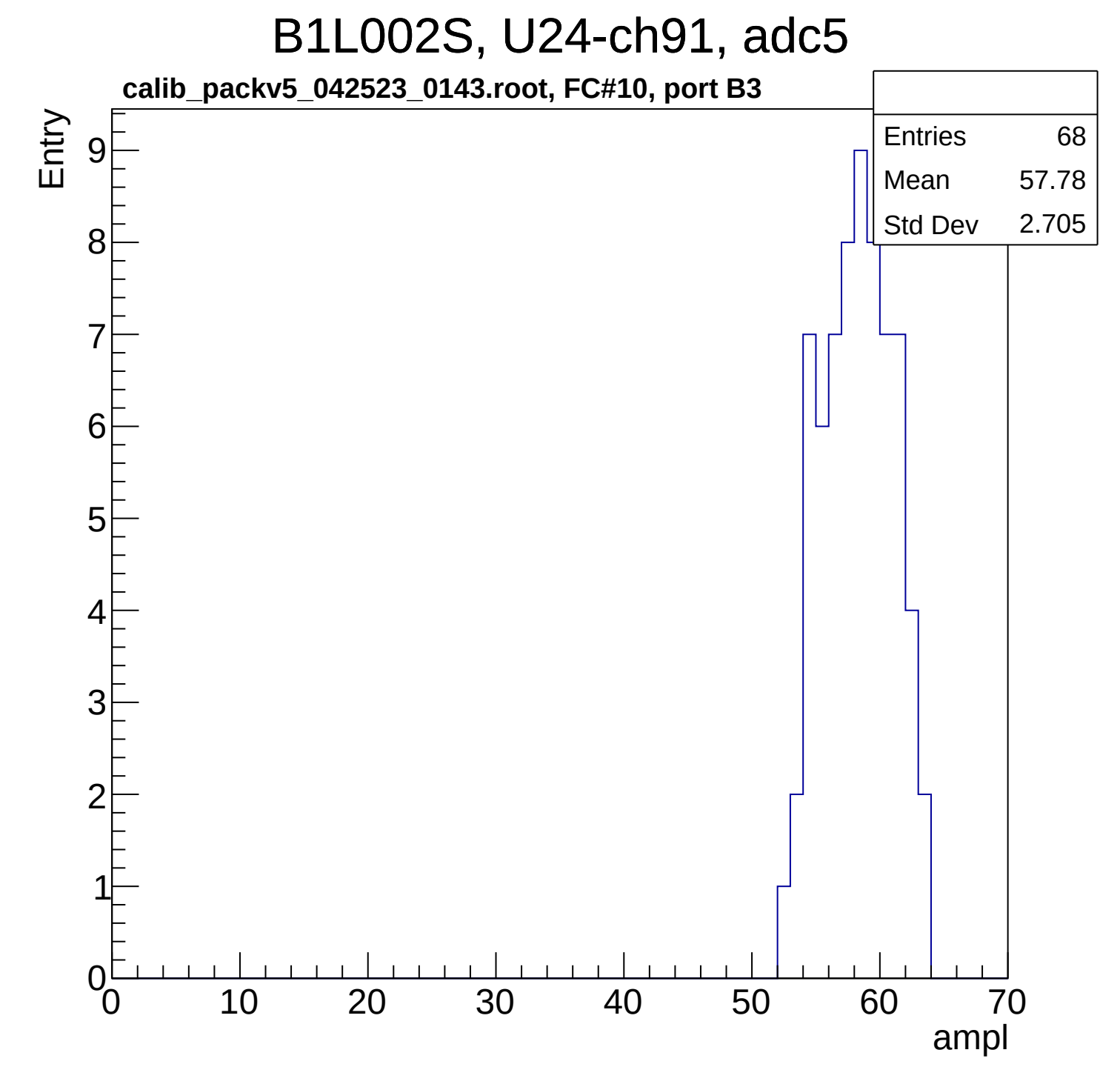
B1L002S, U24-ch91, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.78
Std Dev 2.705
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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50
60
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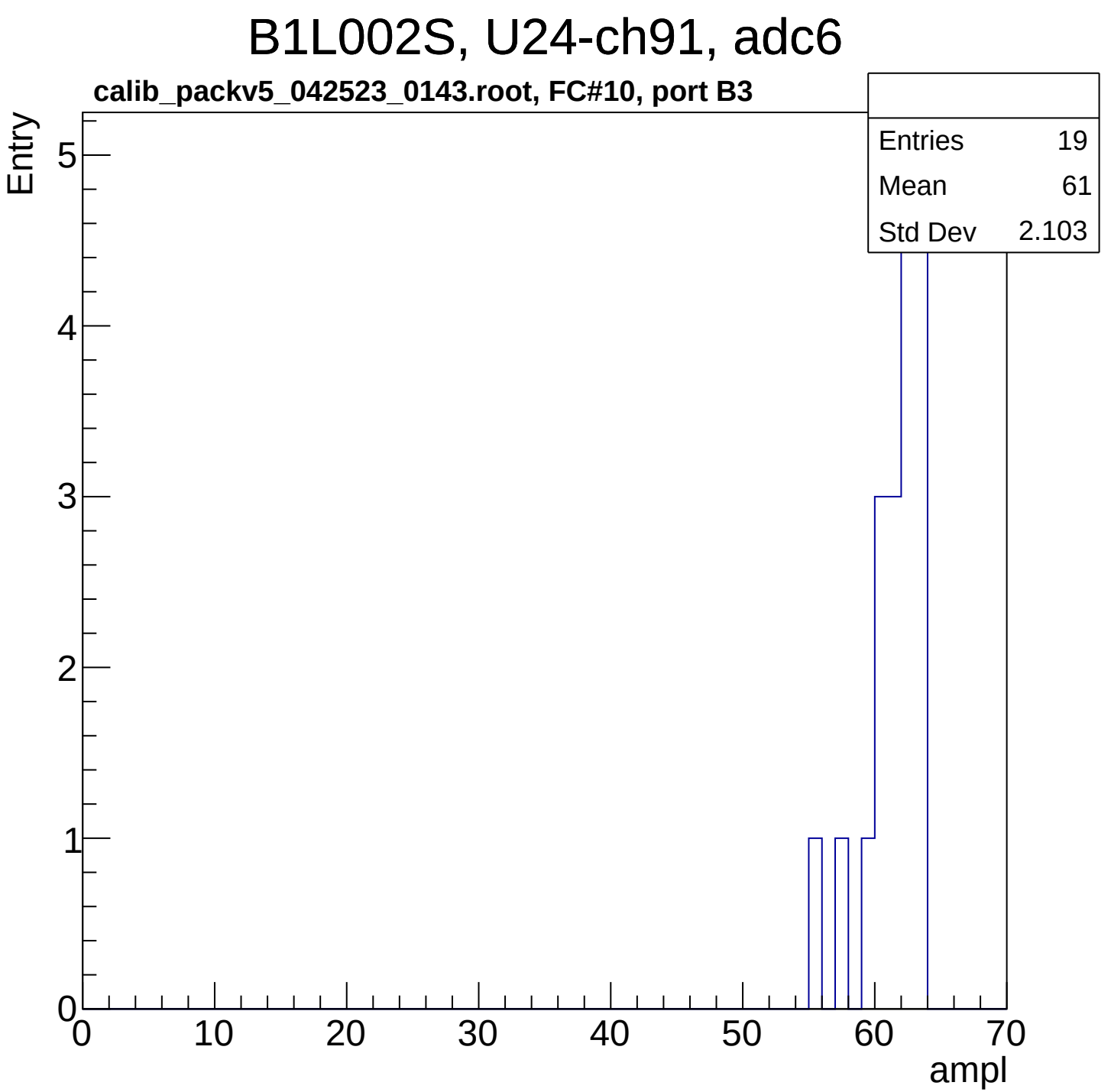
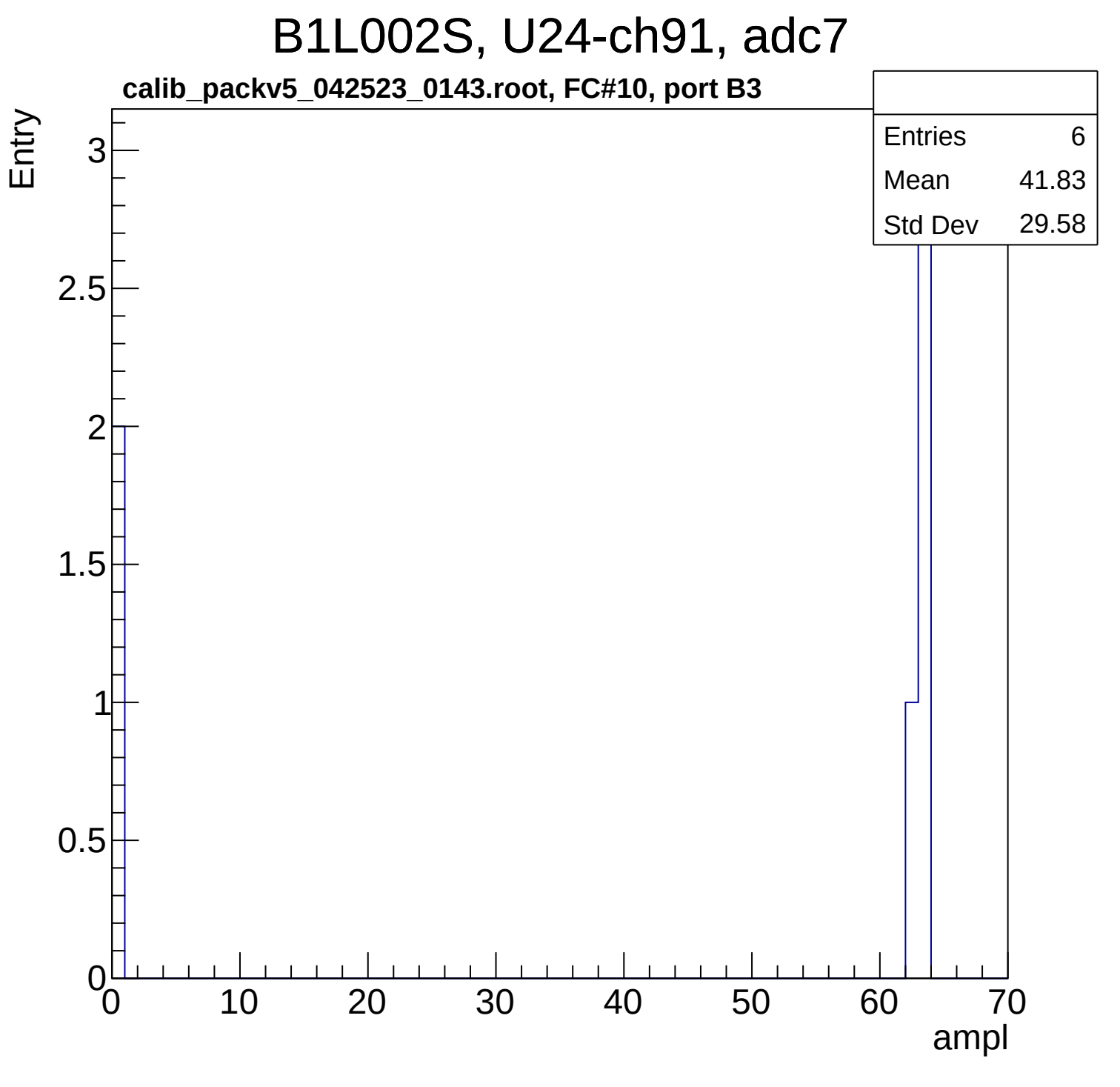

B1L002S, U24-ch91, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61
Std Dev 2.103
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch91, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 41.83
Std Dev 29.58
Entry
ampl
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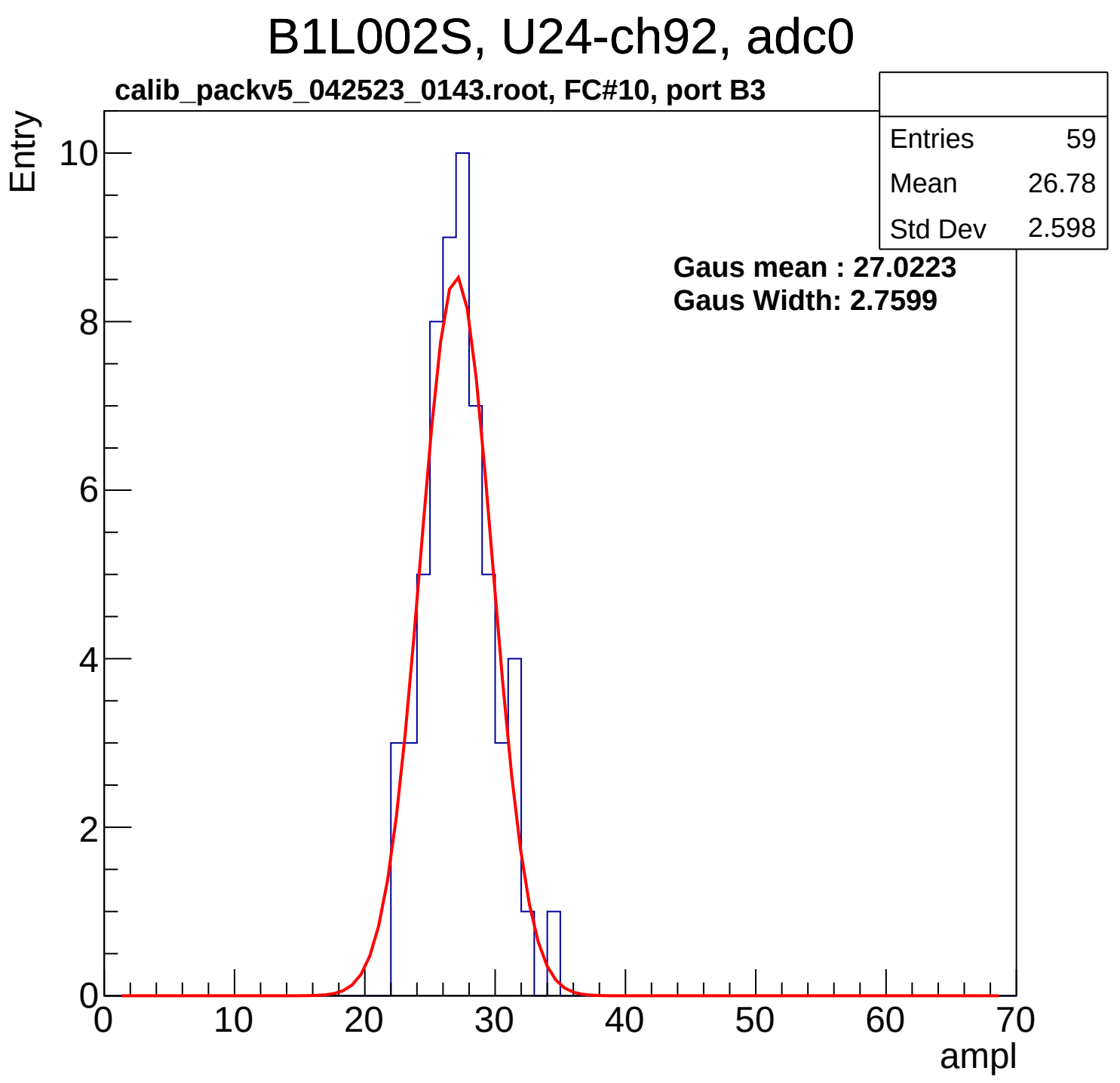
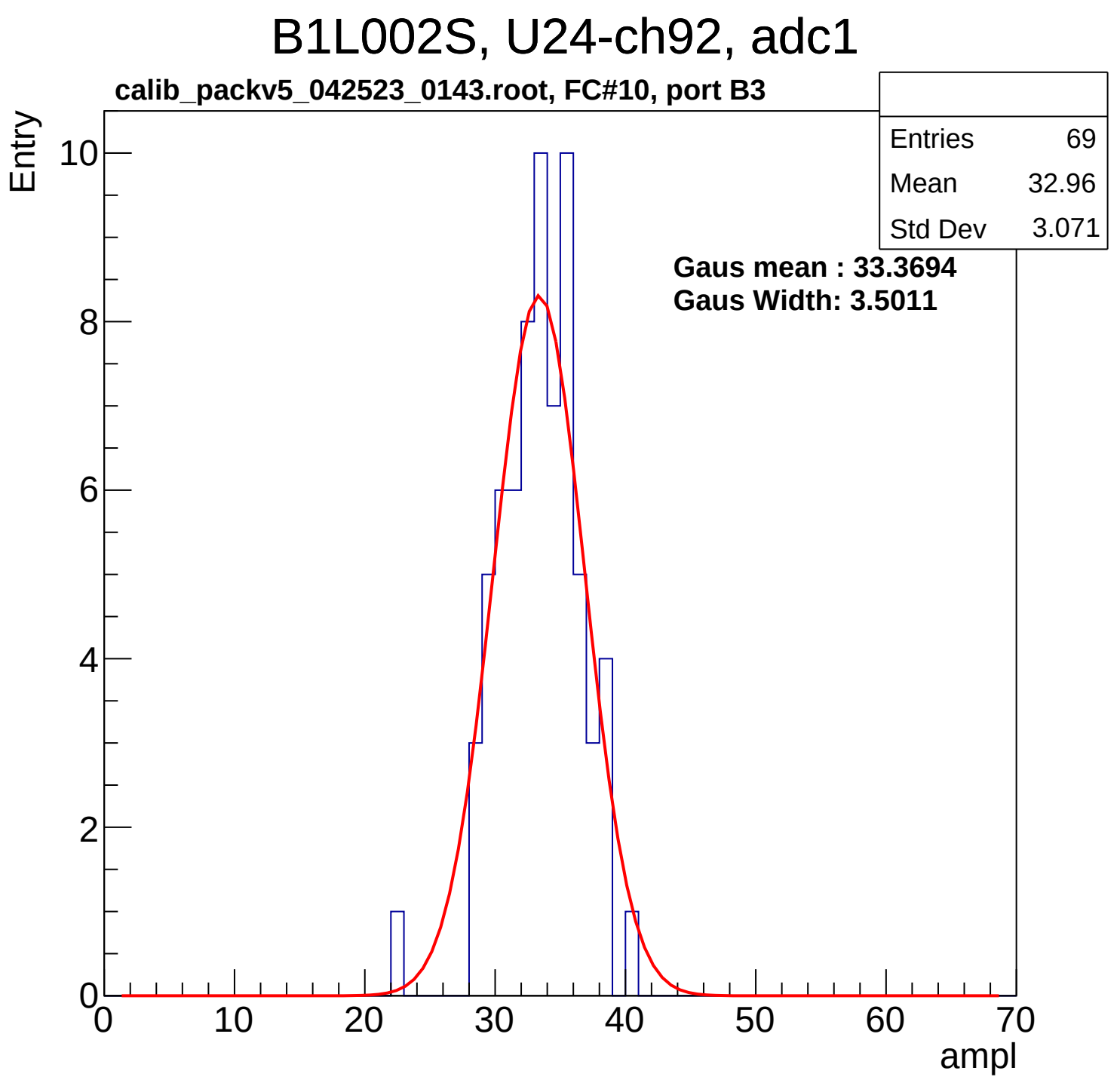
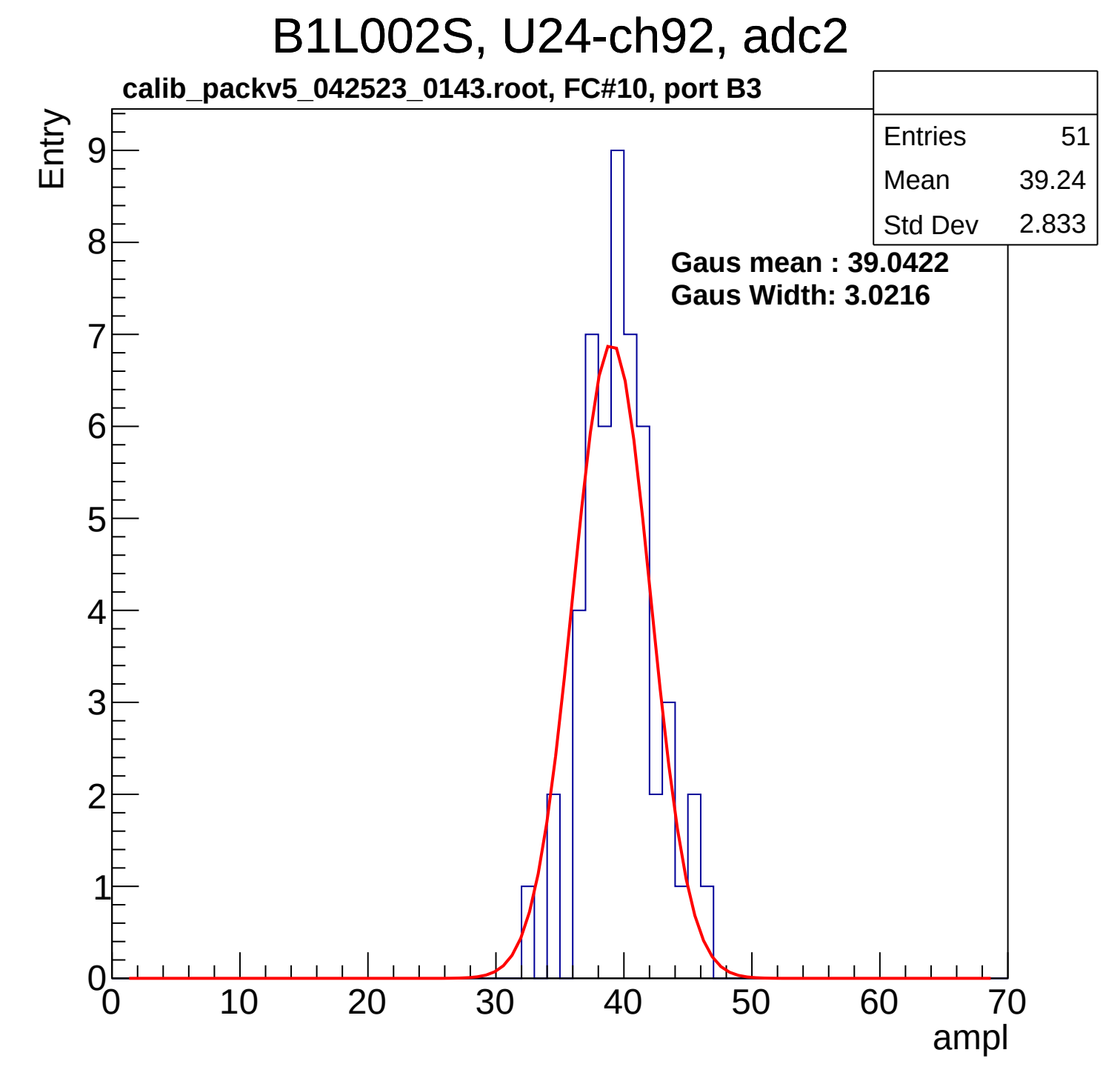
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch92, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 26.78
Std Dev 2.598
Gaus mean : 27.0223
Gaus Width: 2.7599
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch92, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 32.96
Std Dev 3.071
Gaus mean : 33.3694
Gaus Width: 3.5011
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch92, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 39.24
Std Dev 2.833
Gaus mean : 39.0422
Gaus Width: 3.0216
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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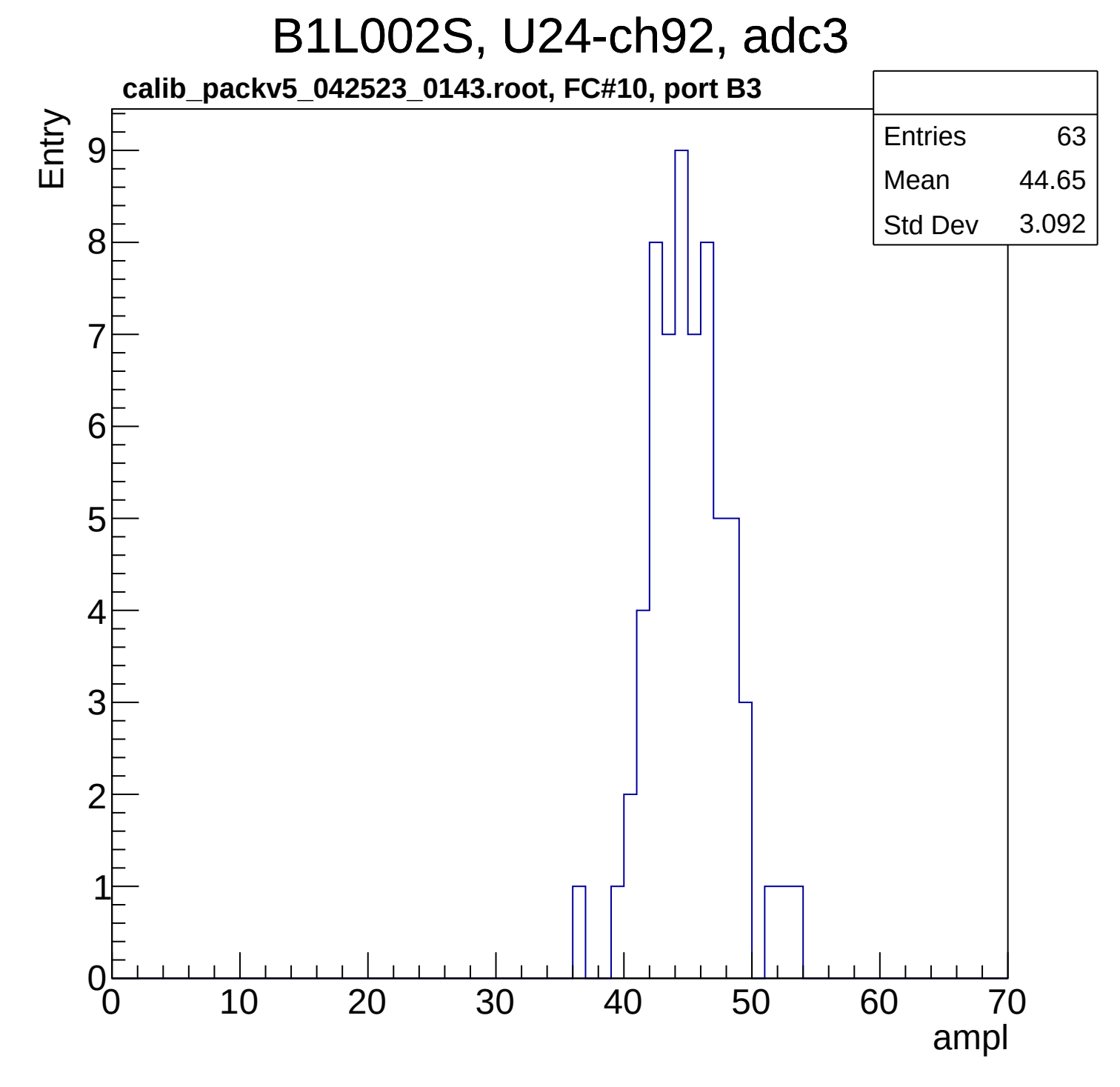
B1L002S, U24-ch92, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 44.65
Std Dev 3.092
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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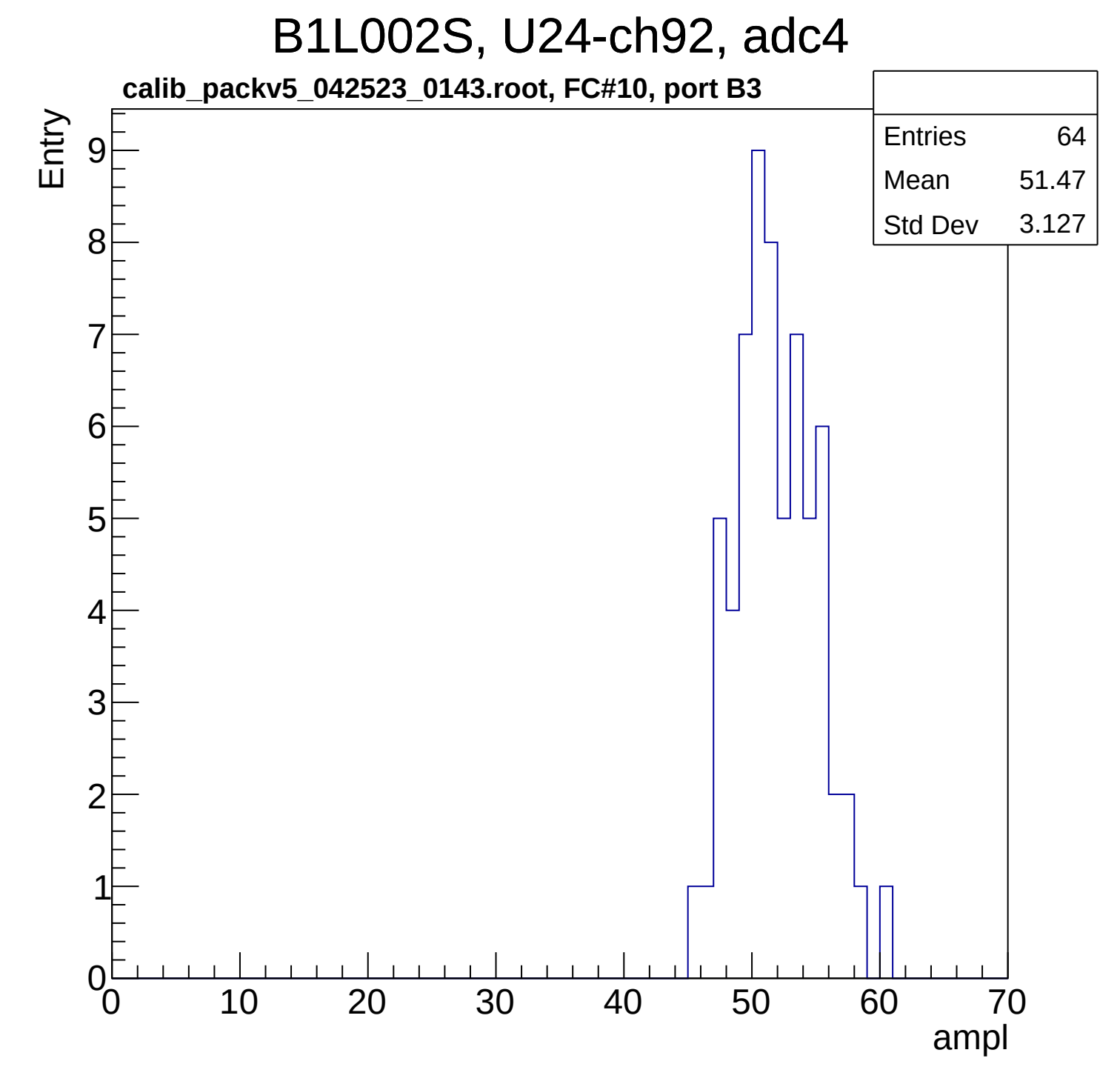
B1L002S, U24-ch92, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 51.47
Std Dev 3.127
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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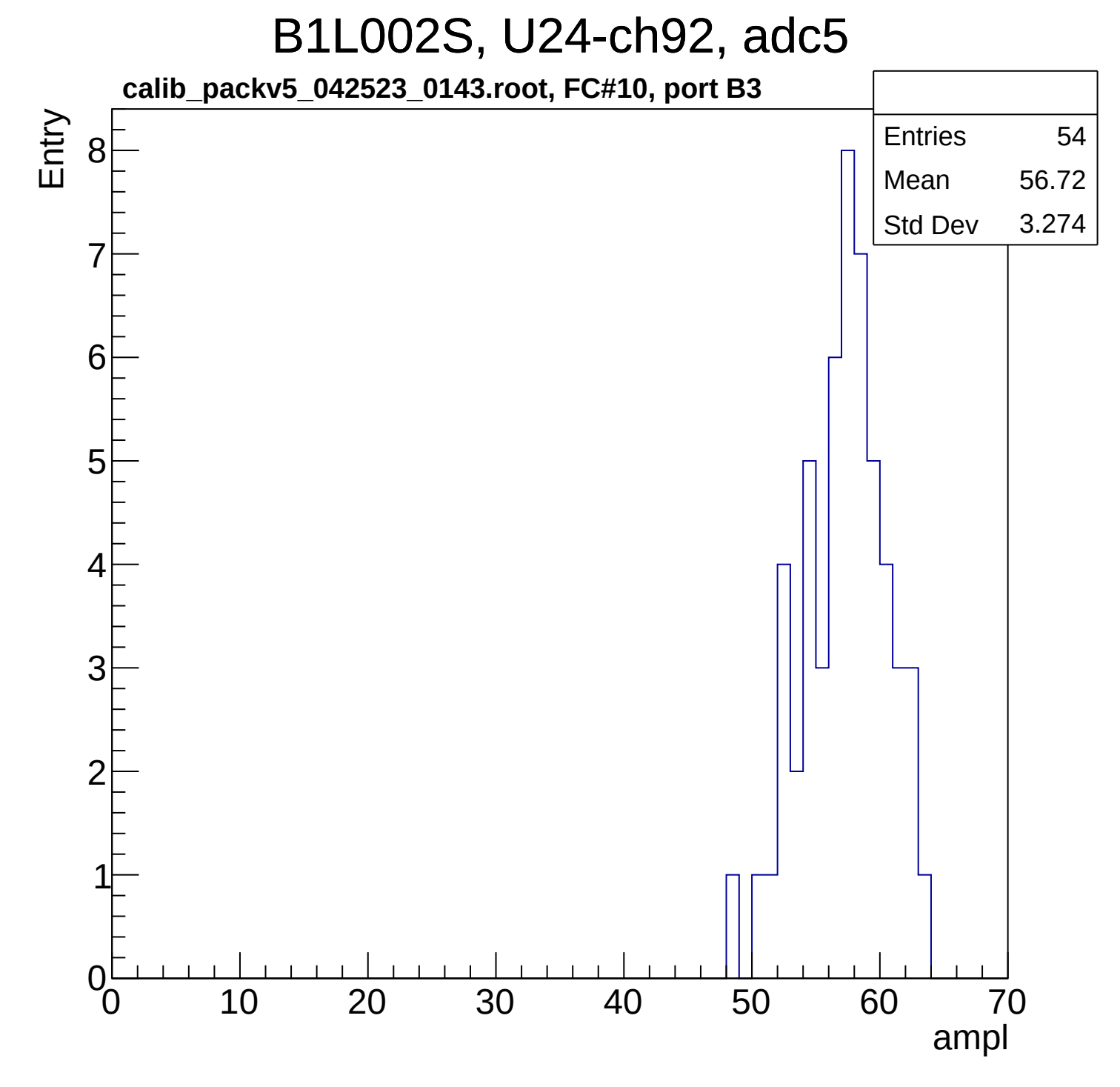
B1L002S, U24-ch92, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 56.72
Std Dev 3.274
Entry
ampl
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3
4
5
6
7
8
0
10
20
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40
50
60
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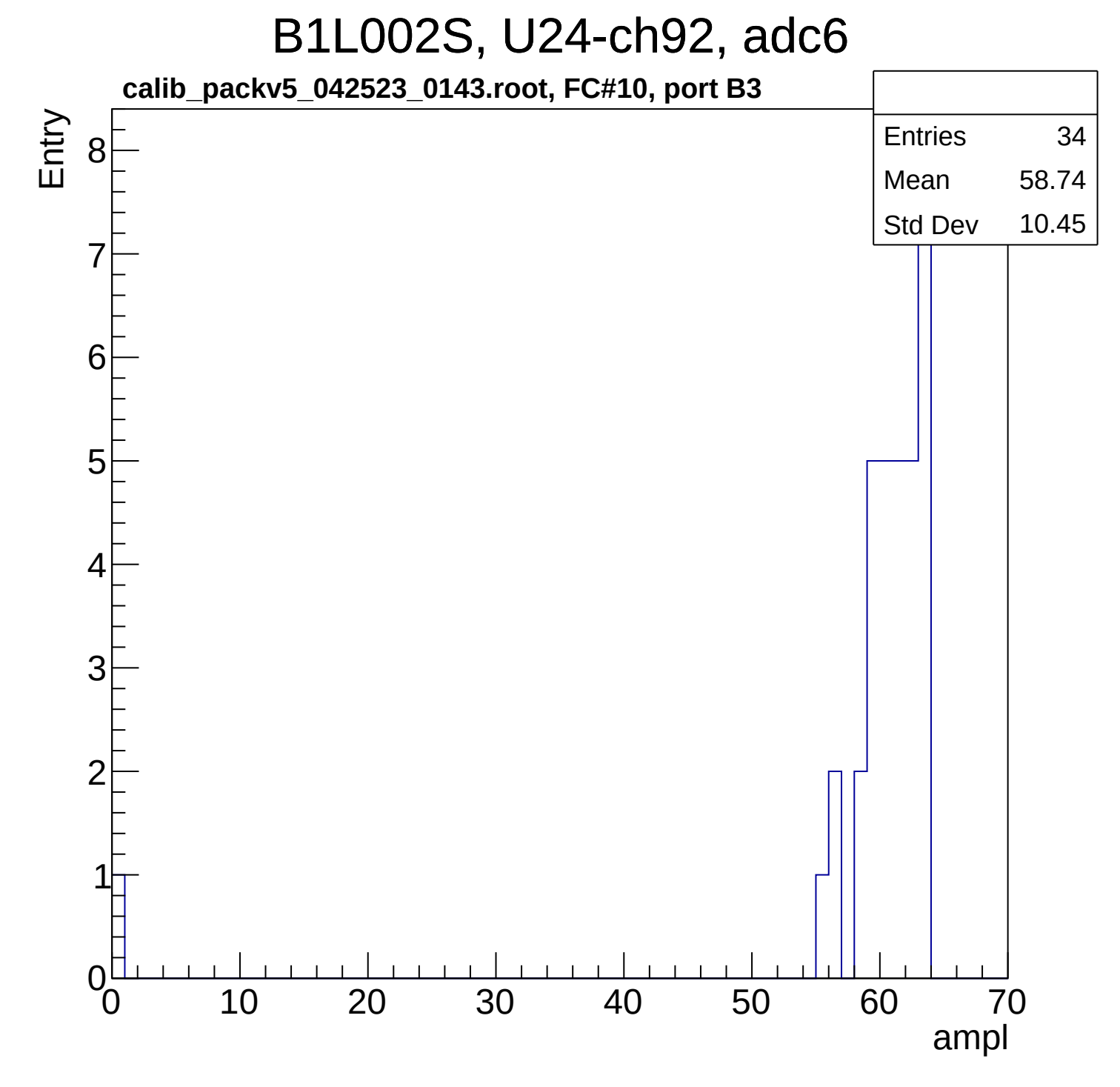
B1L002S, U24-ch92, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 58.74
Std Dev 10.45
Entry
ampl
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3
4
5
6
7
8
0
10
20
30
40
50
60
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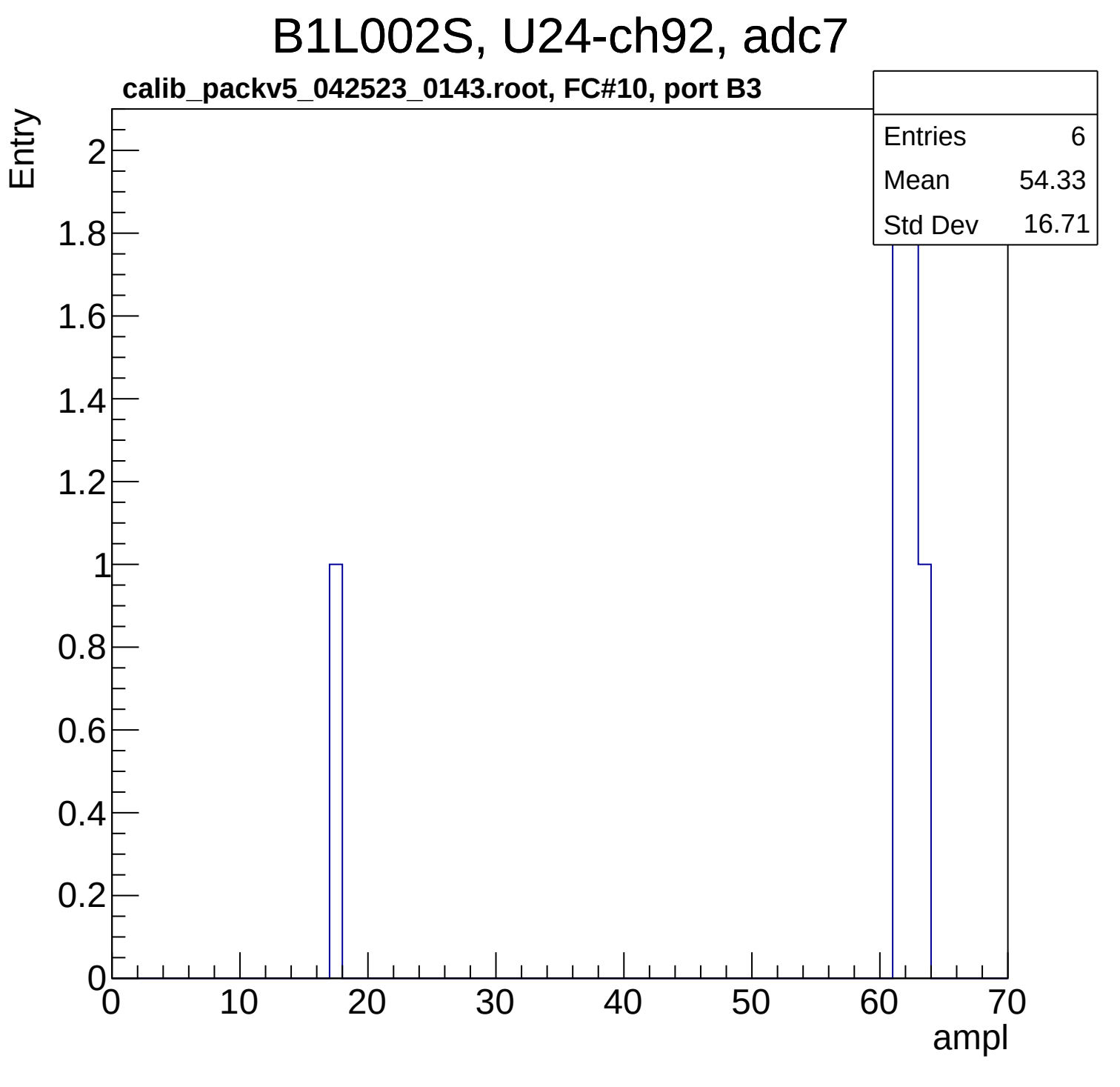
B1L002S, U24-ch92, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 54.33
Std Dev 16.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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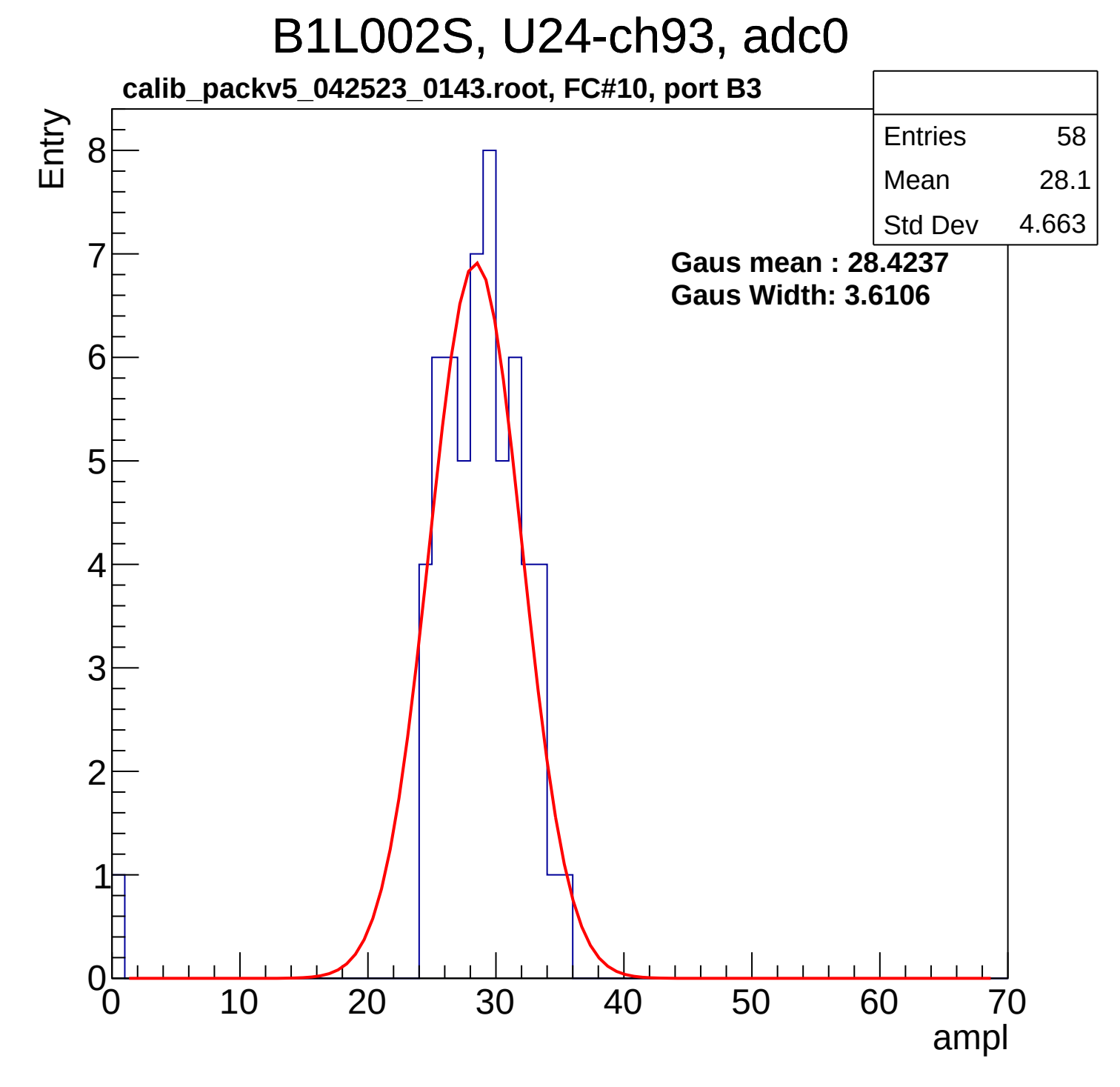
40
50
60
70


B1L002S, U24-ch93, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 28.1
Std Dev 4.663
Gaus mean : 28.4237
Gaus Width: 3.6106
Entry
ampl
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1
2
3
4
5
6
7
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0
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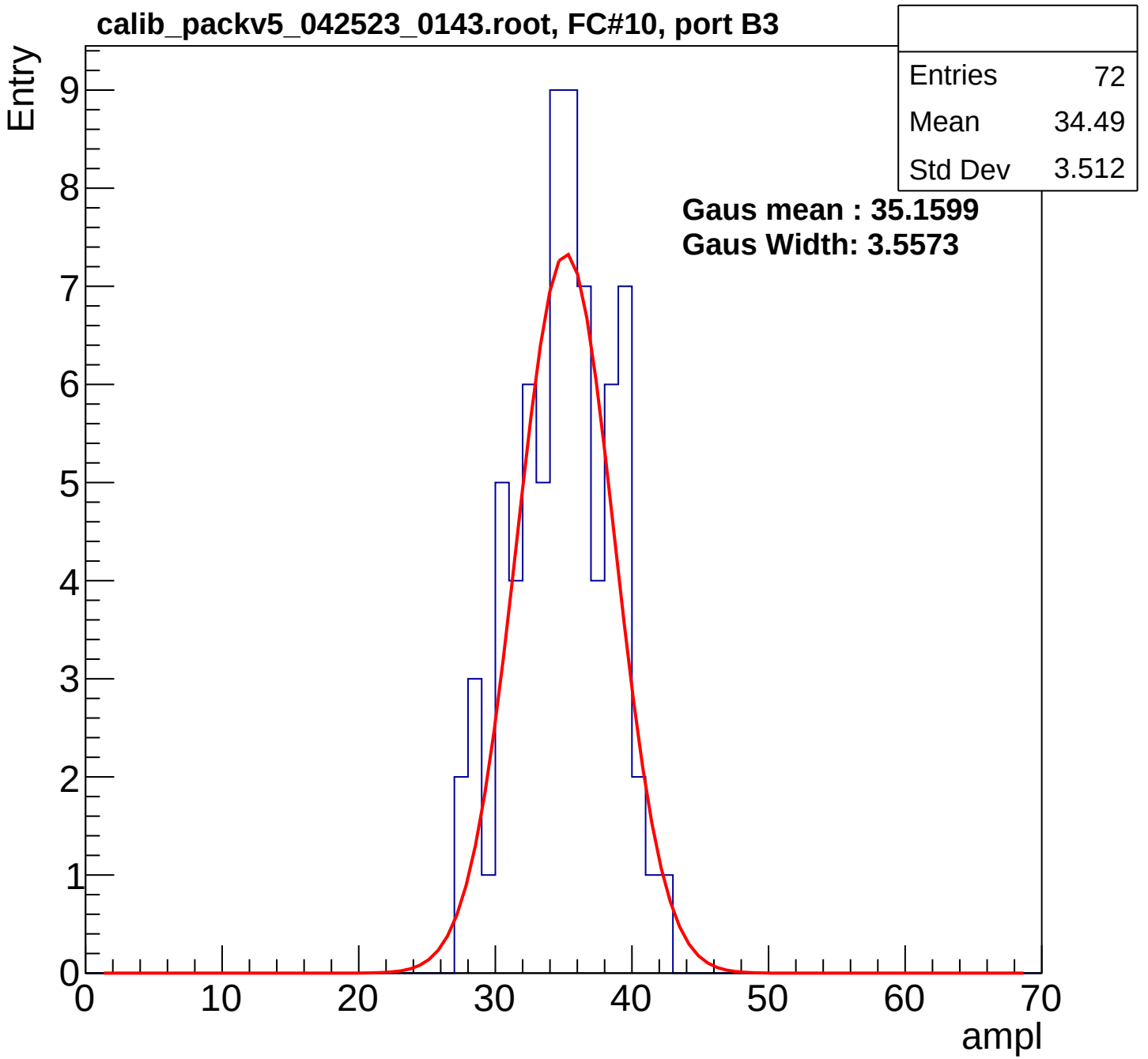
B1L002S, U24-ch93, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 34.49
Std Dev 3.512
Gaus mean : 35.1599
Gaus Width: 3.5573
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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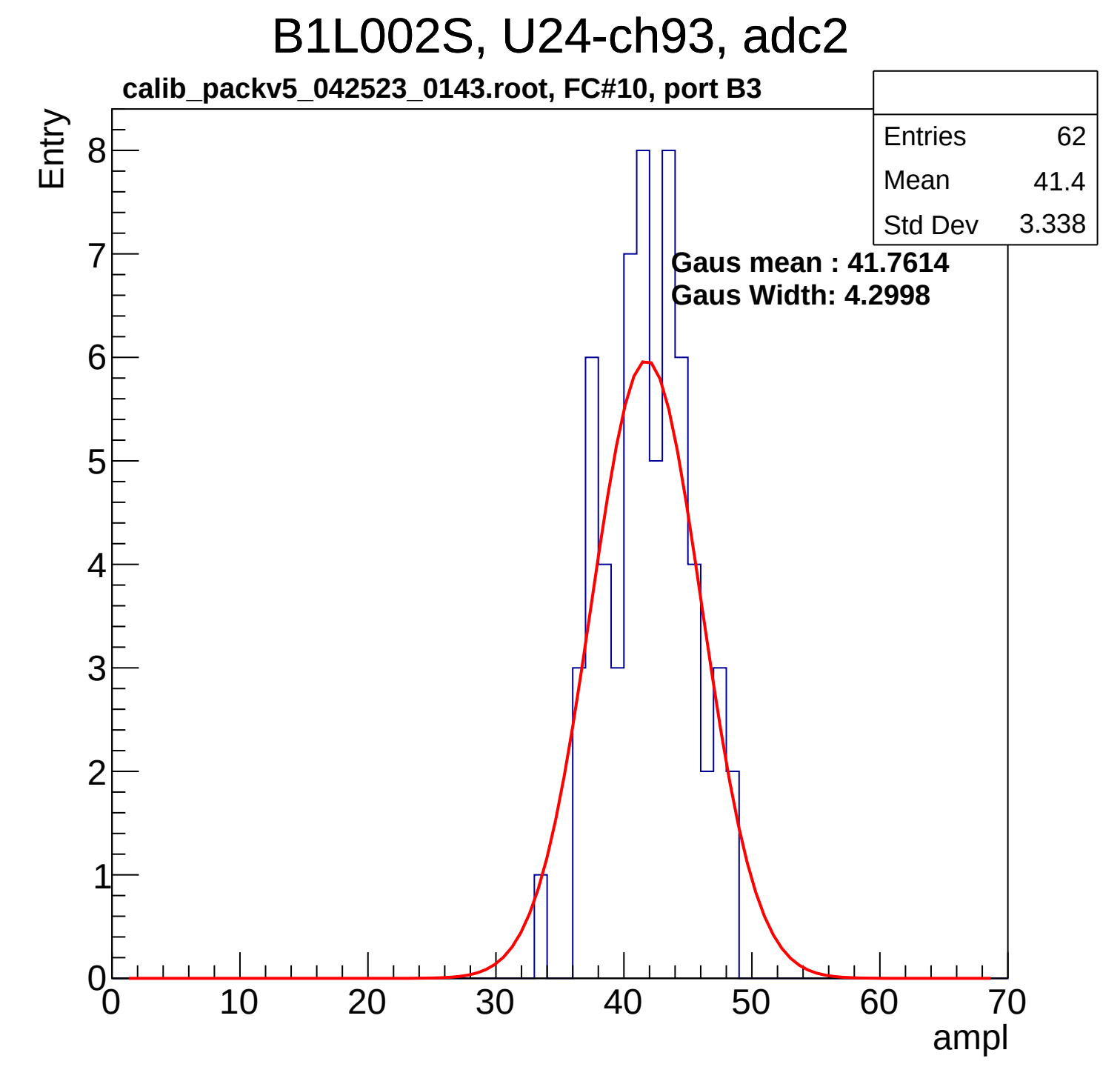
B1L002S, U24-ch93, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 41.4
Std Dev 3.338
Gaus mean : 41.7614
Gaus Width: 4.2998
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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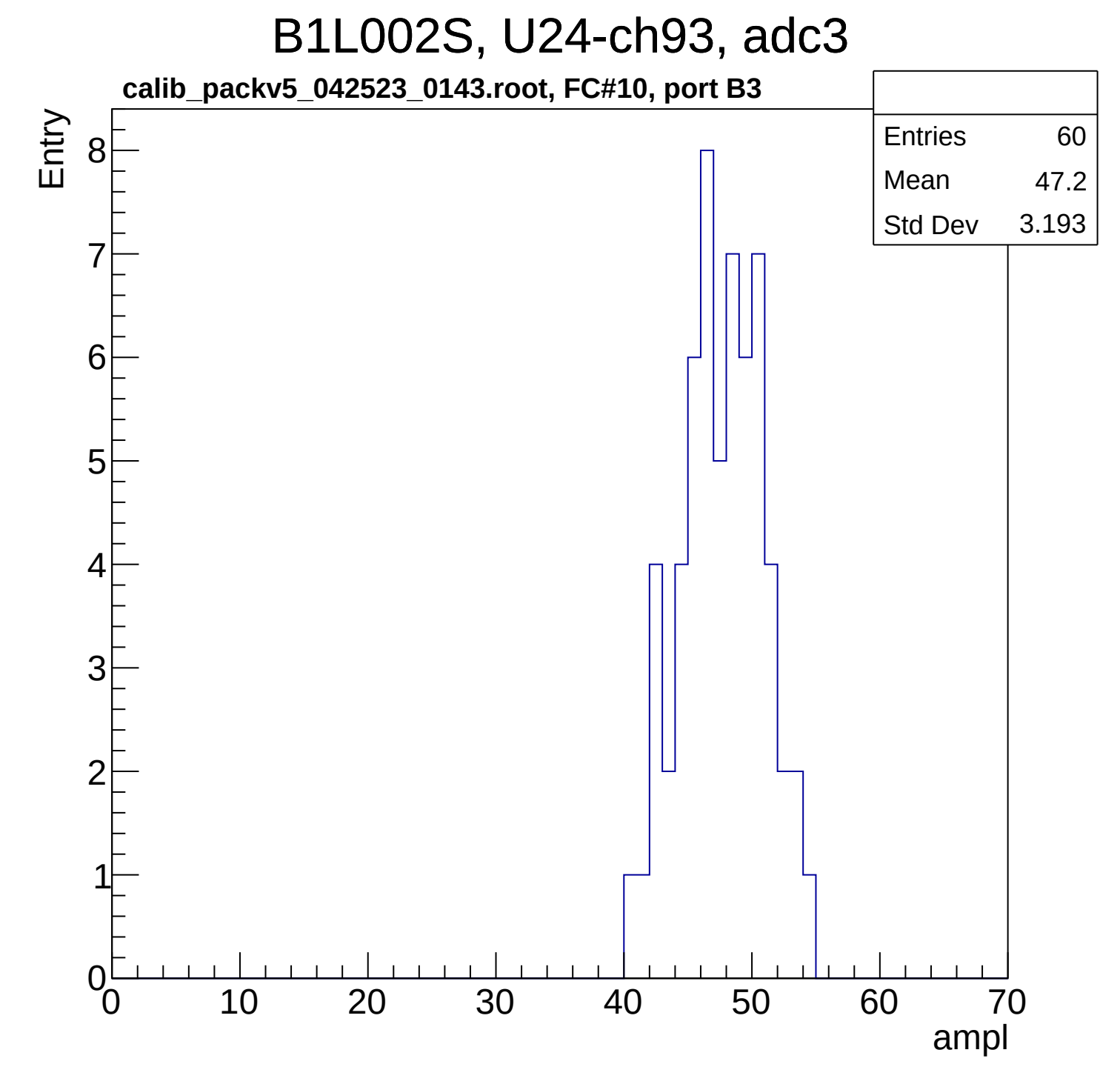
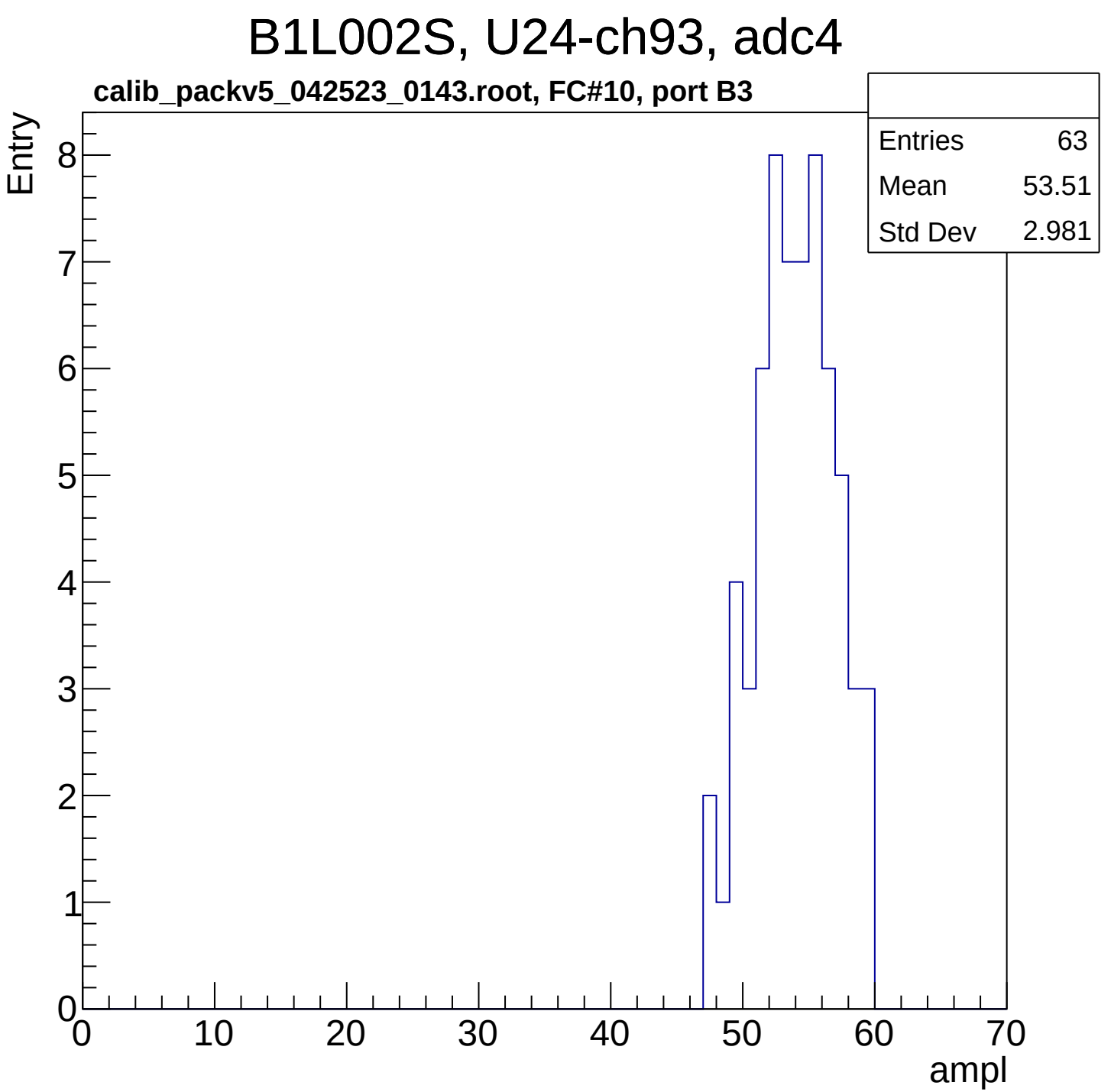

B1L002S, U24-ch93, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 47.2
Std Dev 3.193
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch93, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 53.51
Std Dev 2.981
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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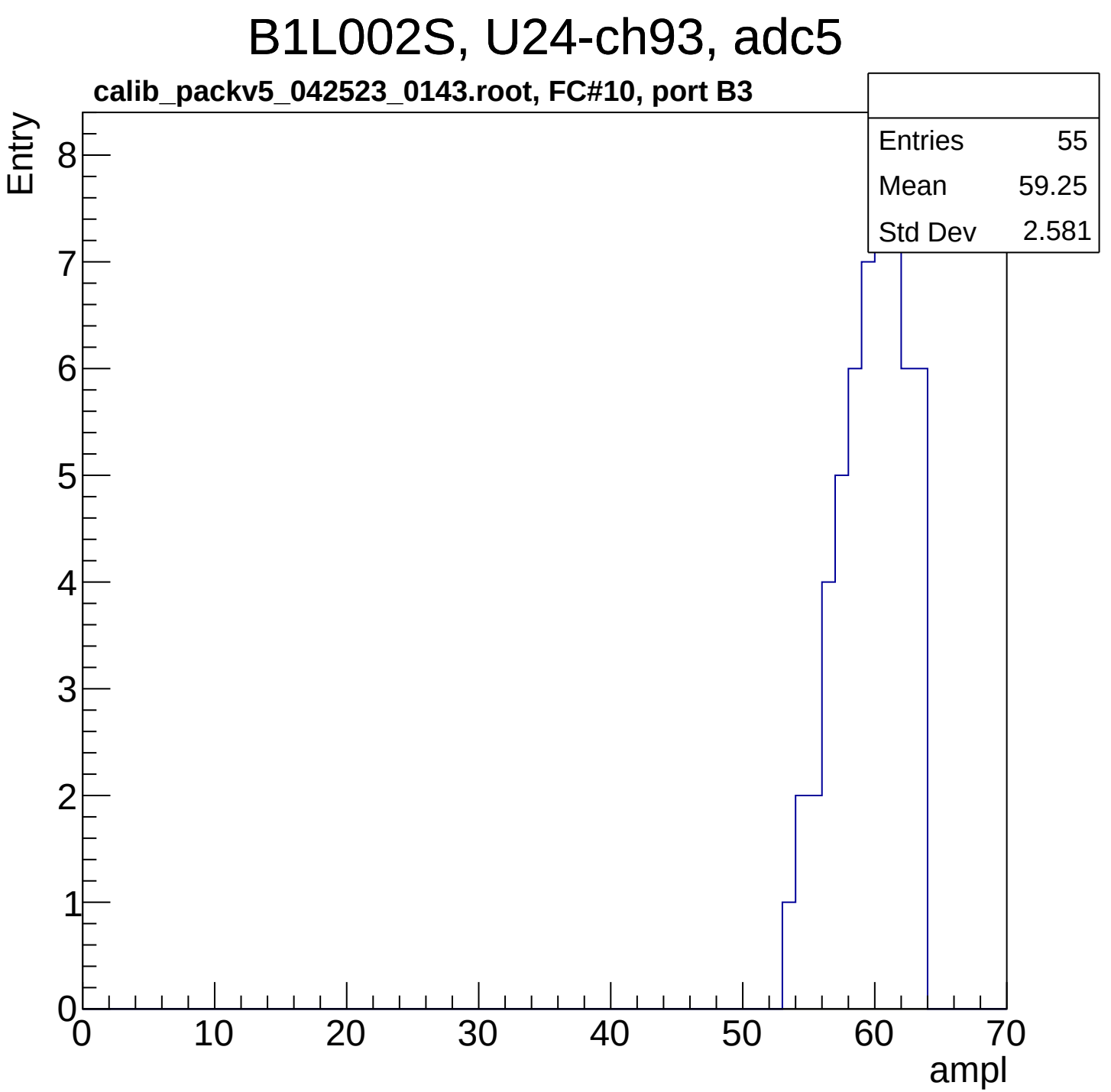
B1L002S, U24-ch93, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 59.25
Std Dev 2.581
Entry
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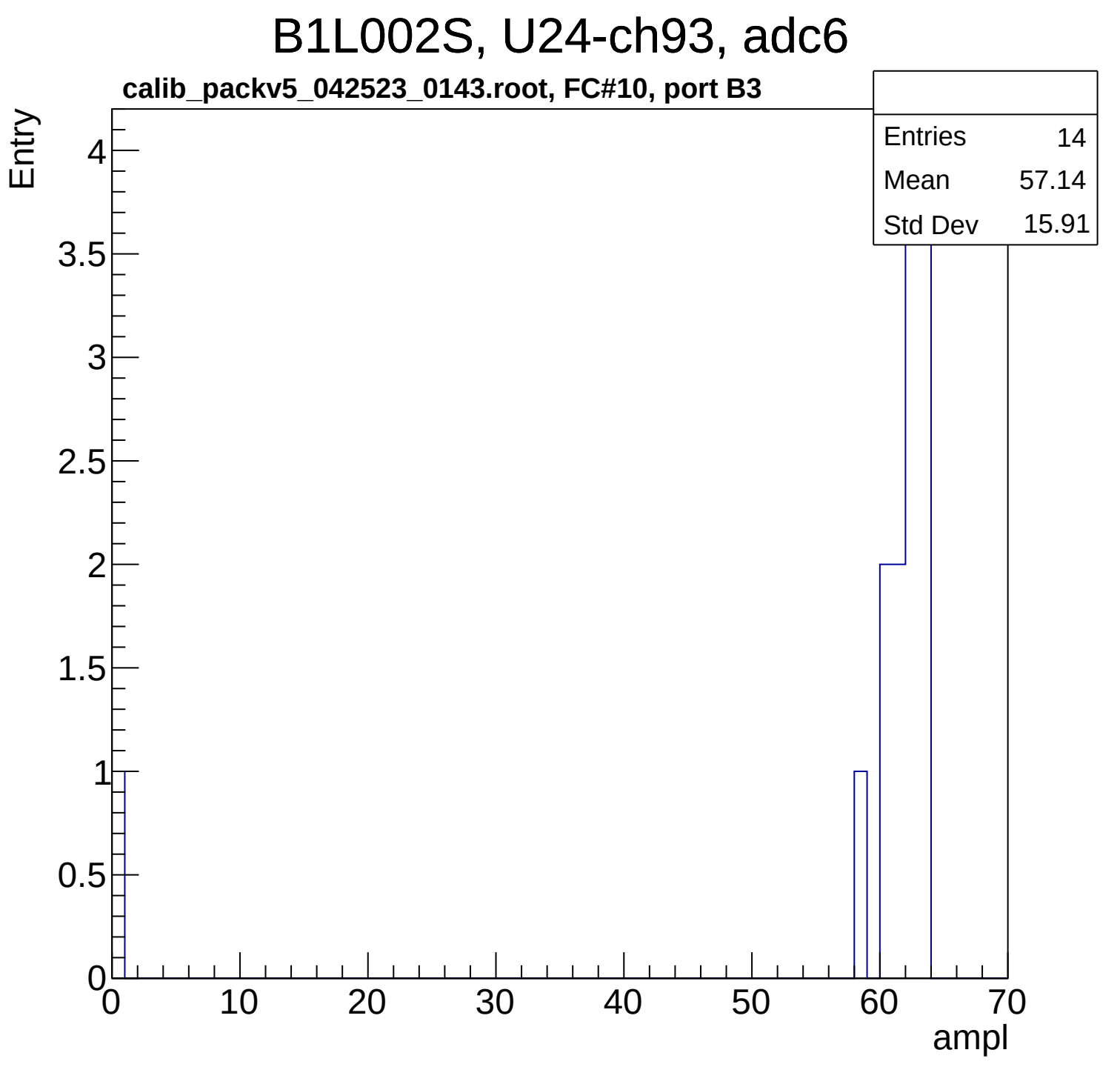
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch93, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 14
Mean 57.14
Std Dev 15.91
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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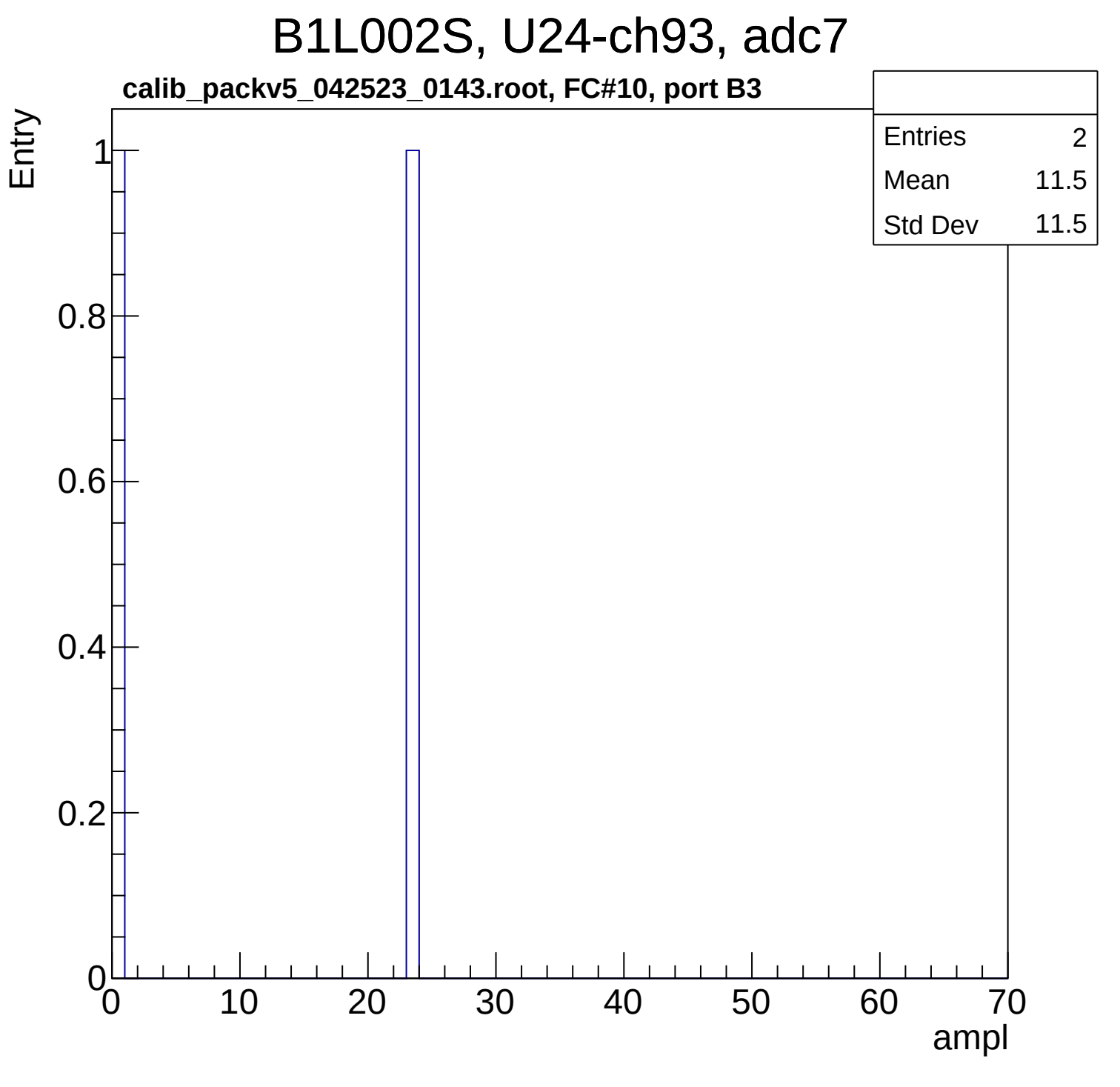
B1L002S, U24-ch93, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
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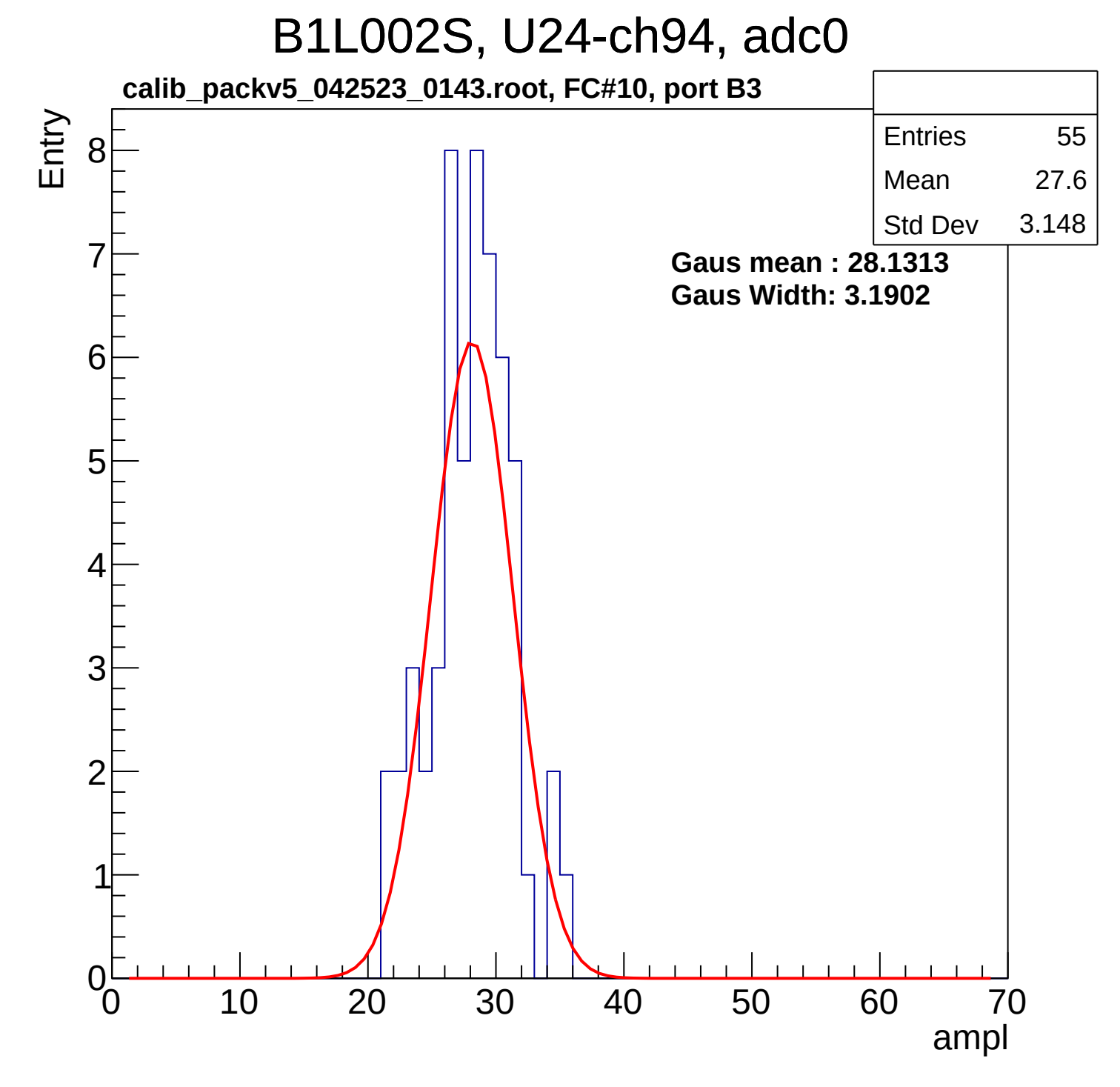
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch94, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 27.6
Std Dev 3.148
Gaus mean : 28.1313
Gaus Width: 3.1902
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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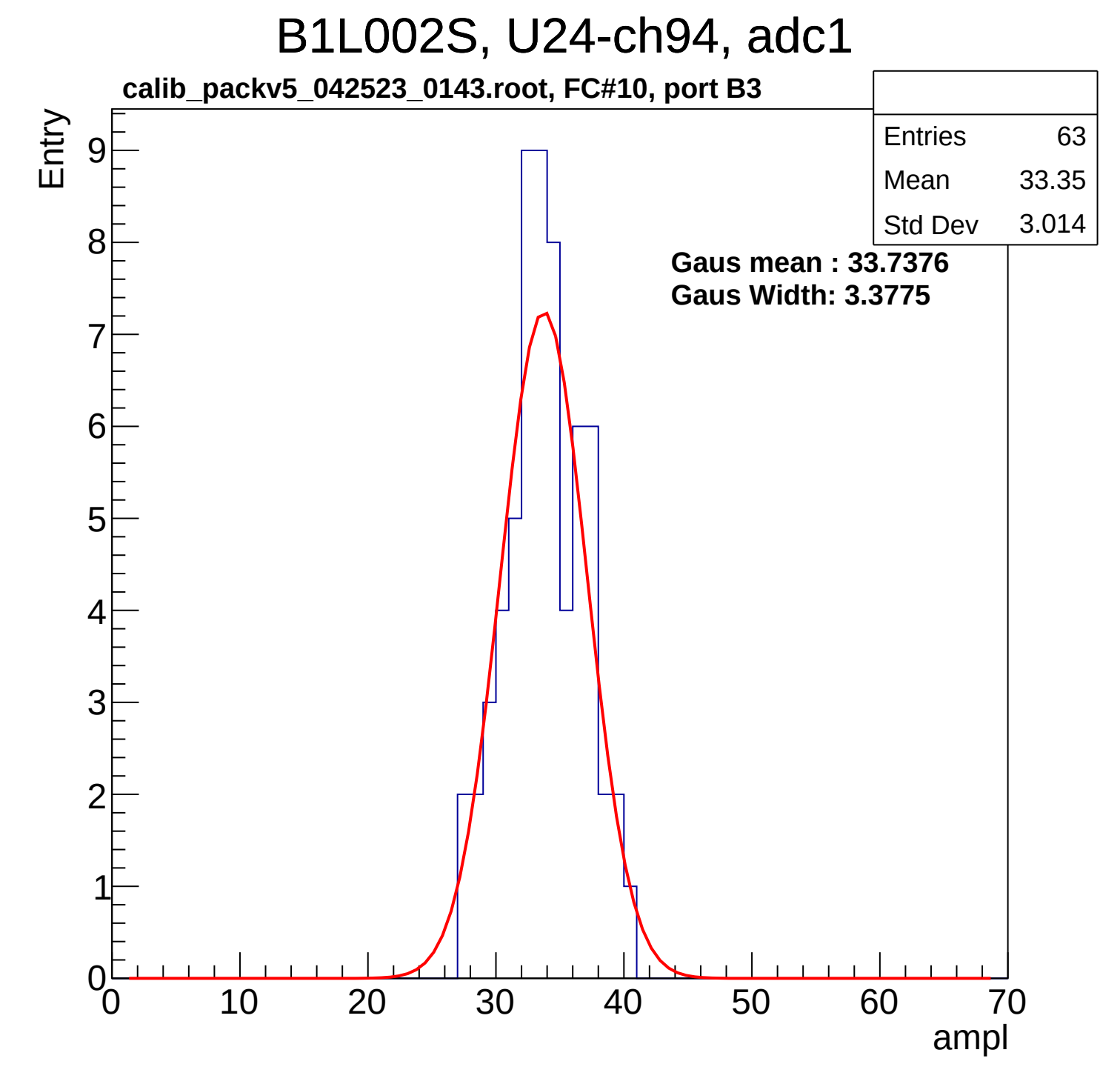
B1L002S, U24-ch94, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 33.35
Std Dev 3.014
Gaus mean : 33.7376
Gaus Width: 3.3775
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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40
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60
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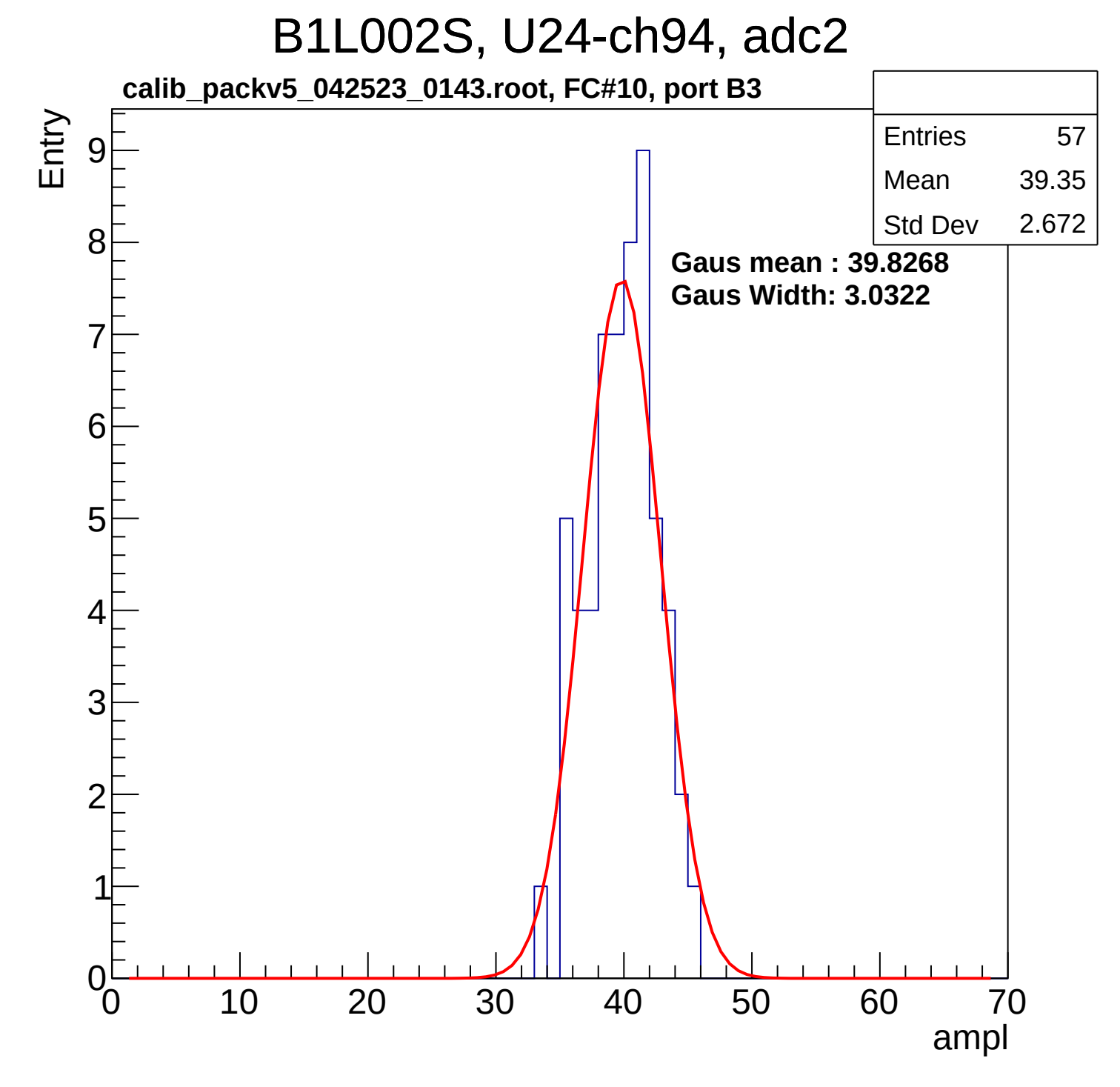
B1L002S, U24-ch94, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 39.35
Std Dev 2.672
Gaus mean : 39.8268
Gaus Width: 3.0322
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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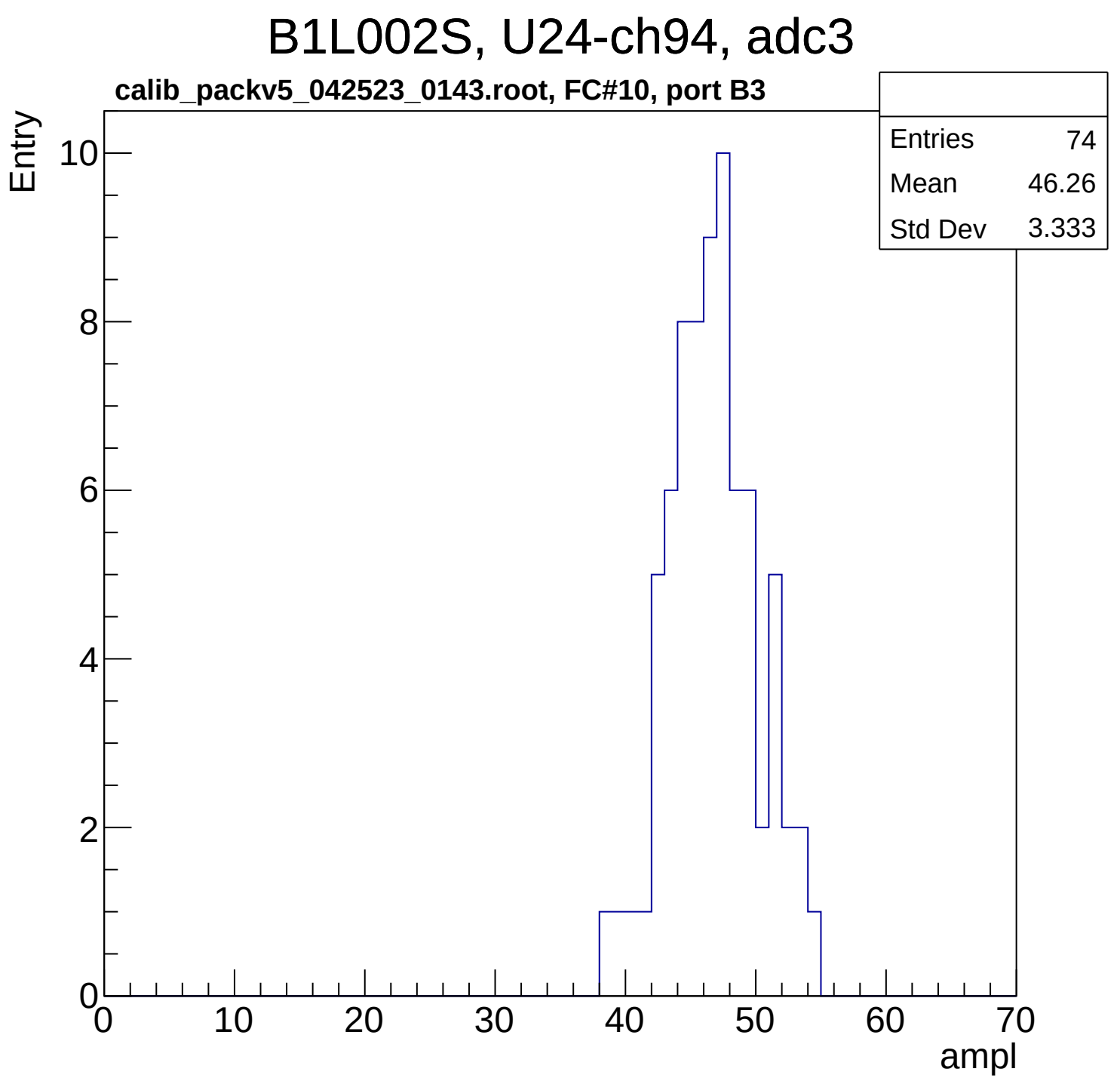
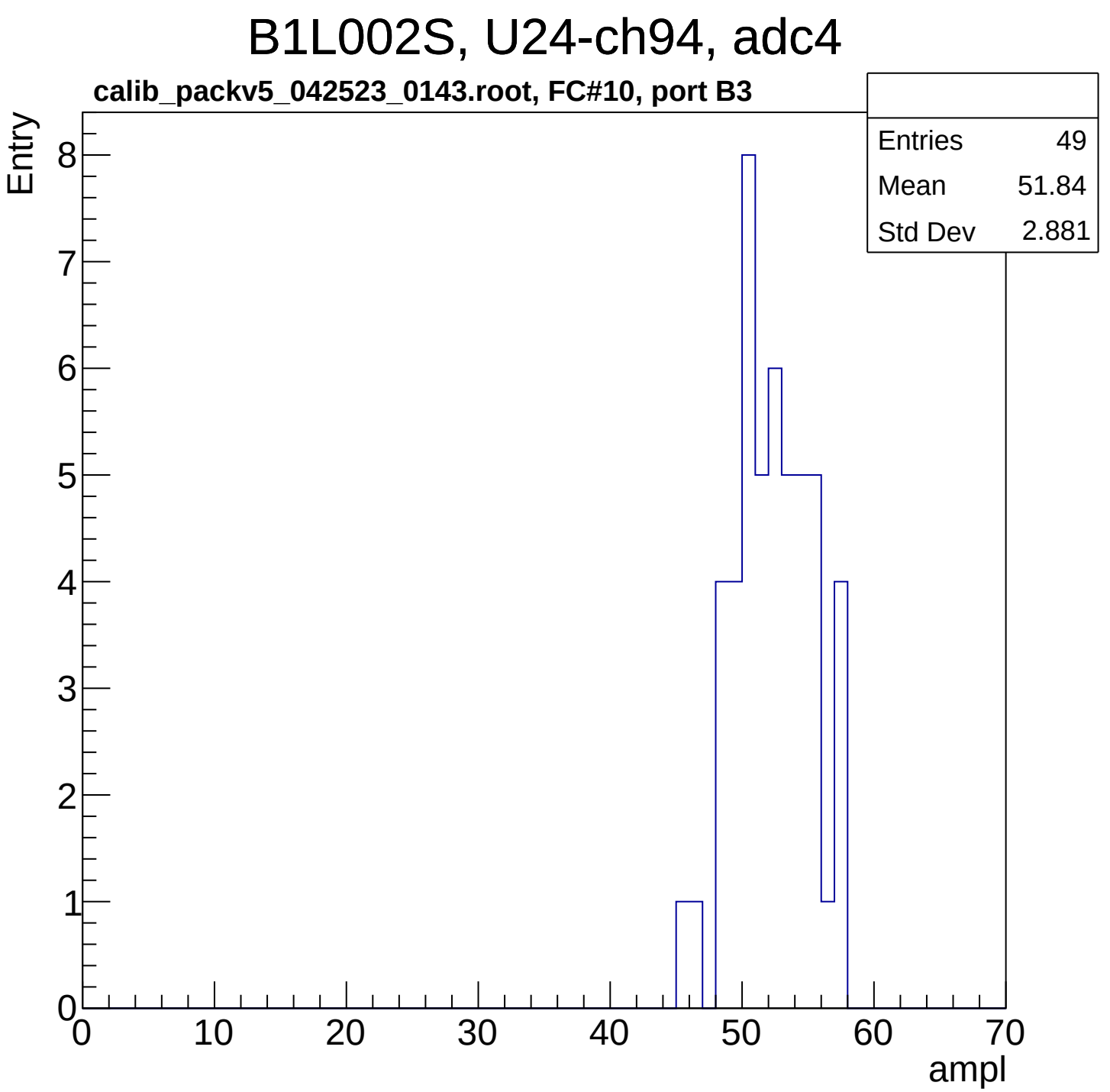

B1L002S, U24-ch94, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 46.26
Std Dev 3.333
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch94, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 51.84
Std Dev 2.881
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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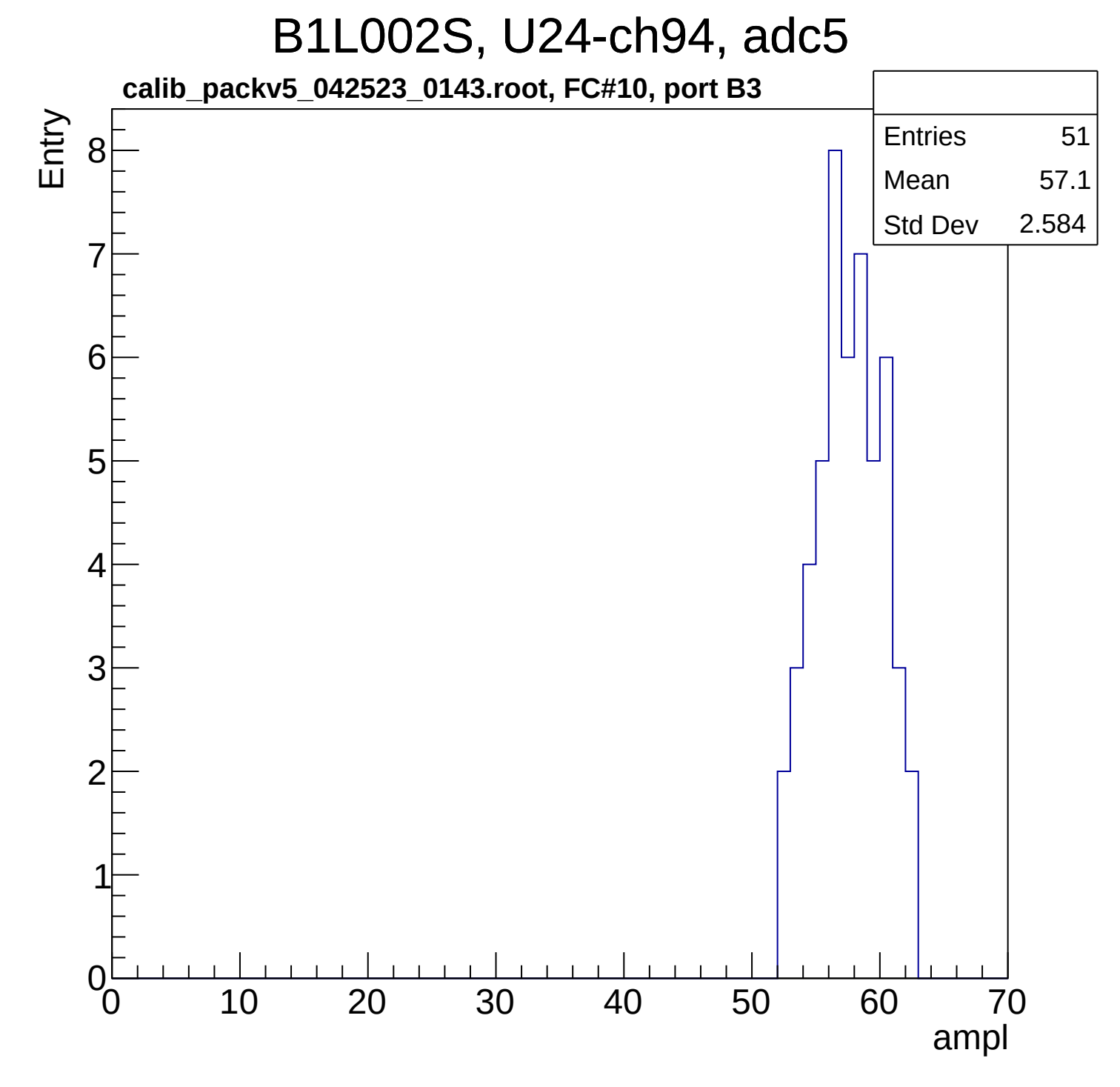
B1L002S, U24-ch94, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.1
Std Dev 2.584
Entry
ampl
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2
3
4
5
6
7
8
0
10
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60
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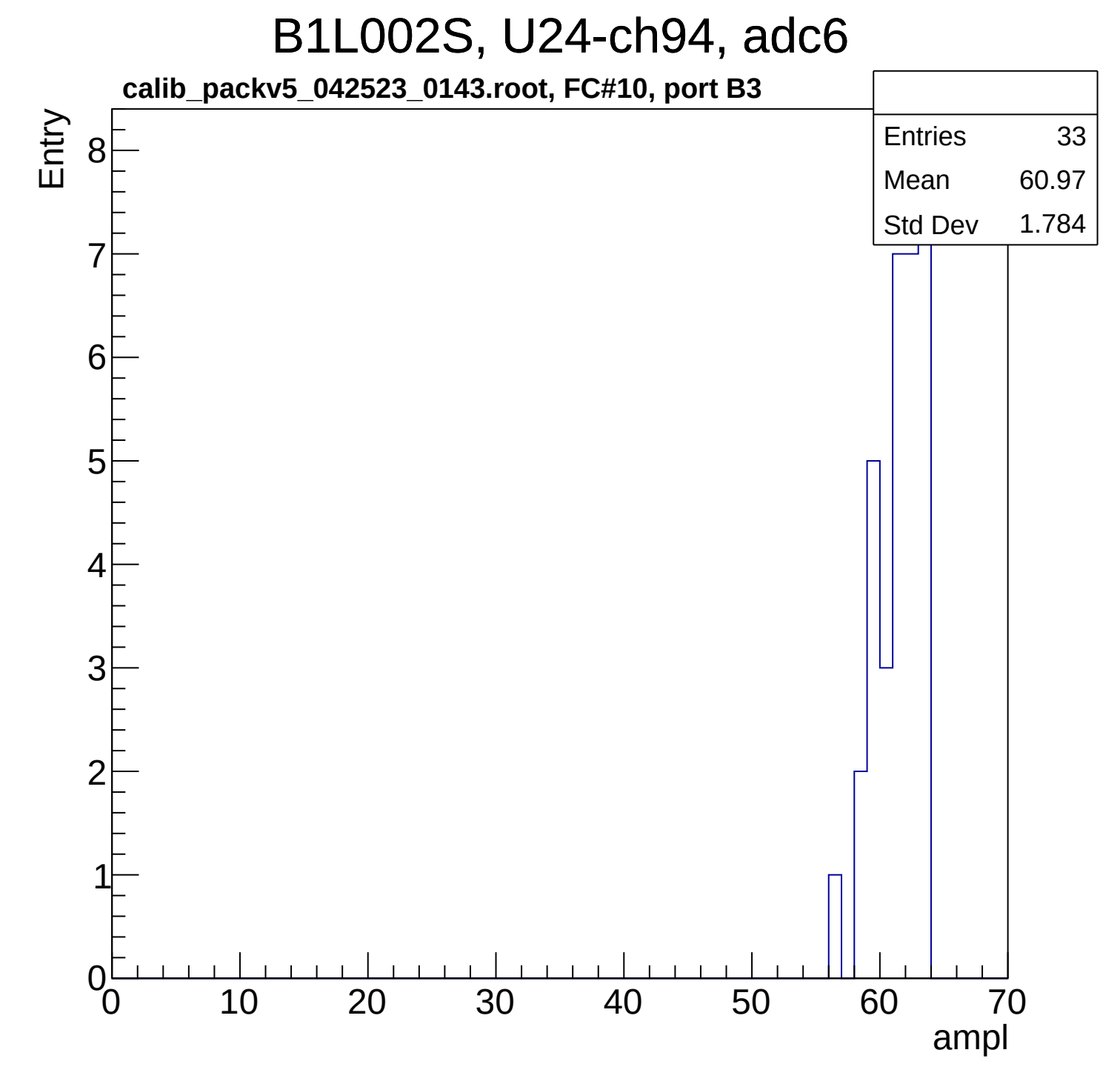
B1L002S, U24-ch94, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 33
Mean 60.97
Std Dev 1.784
Entry
ampl
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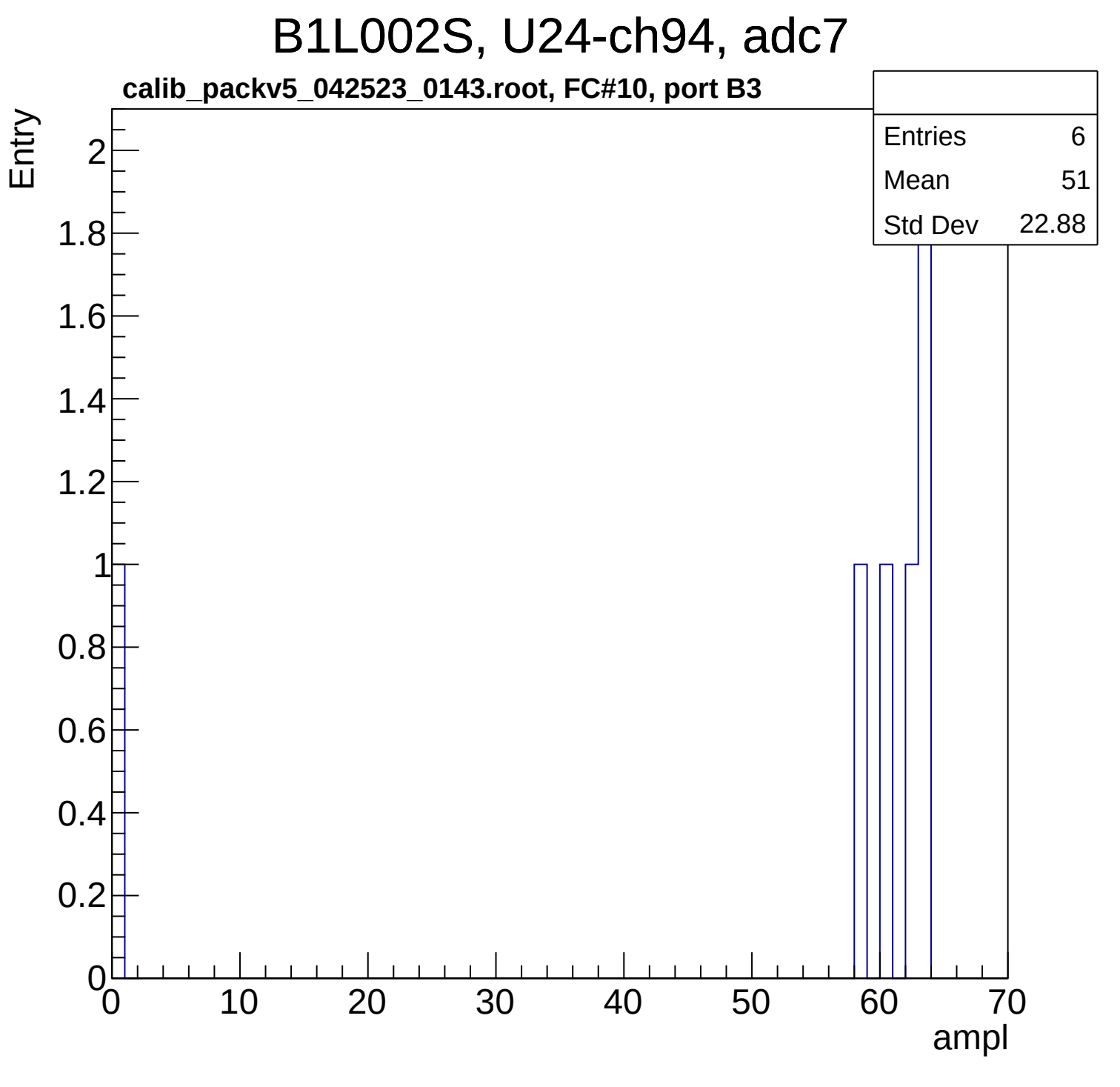
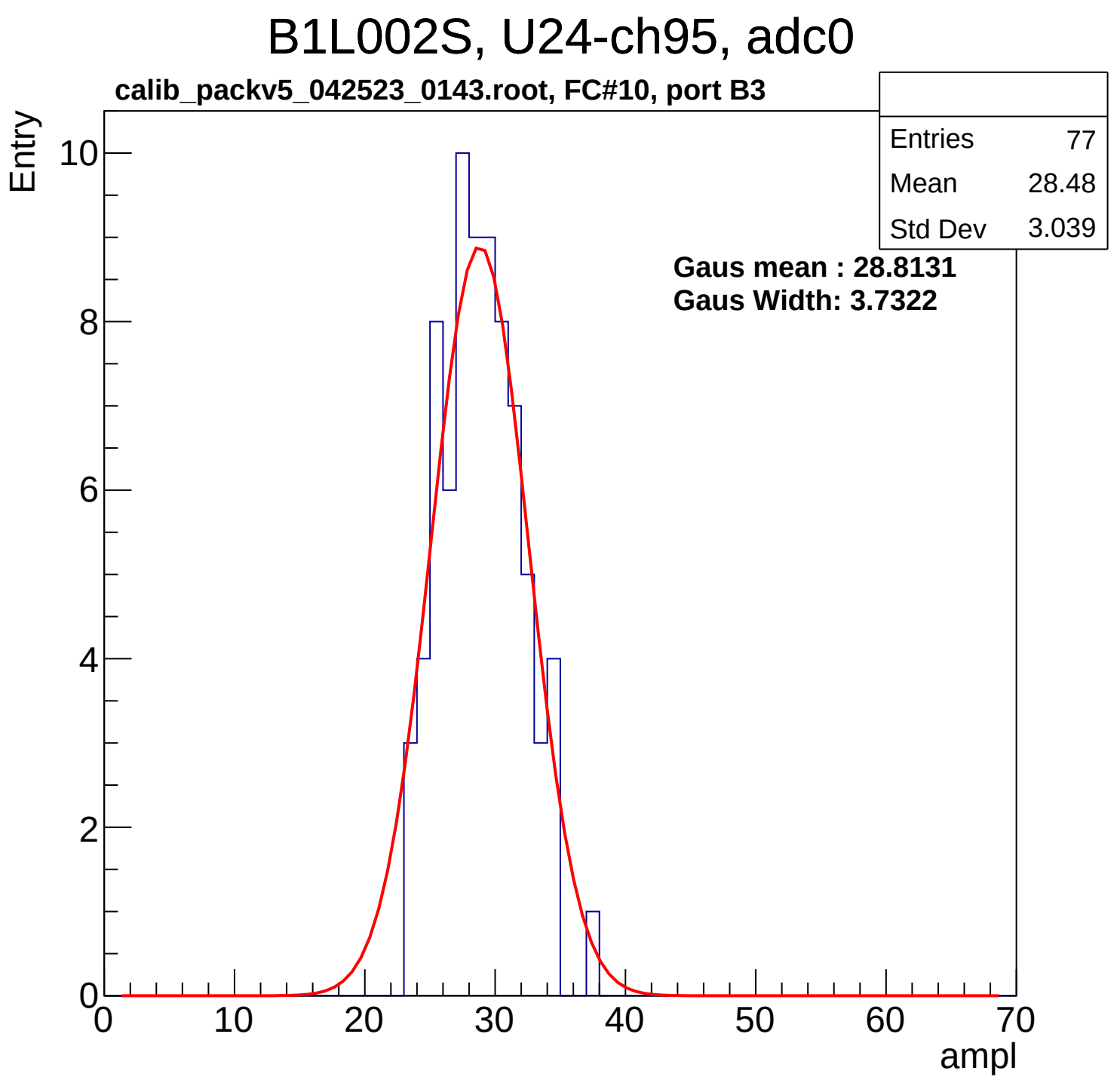
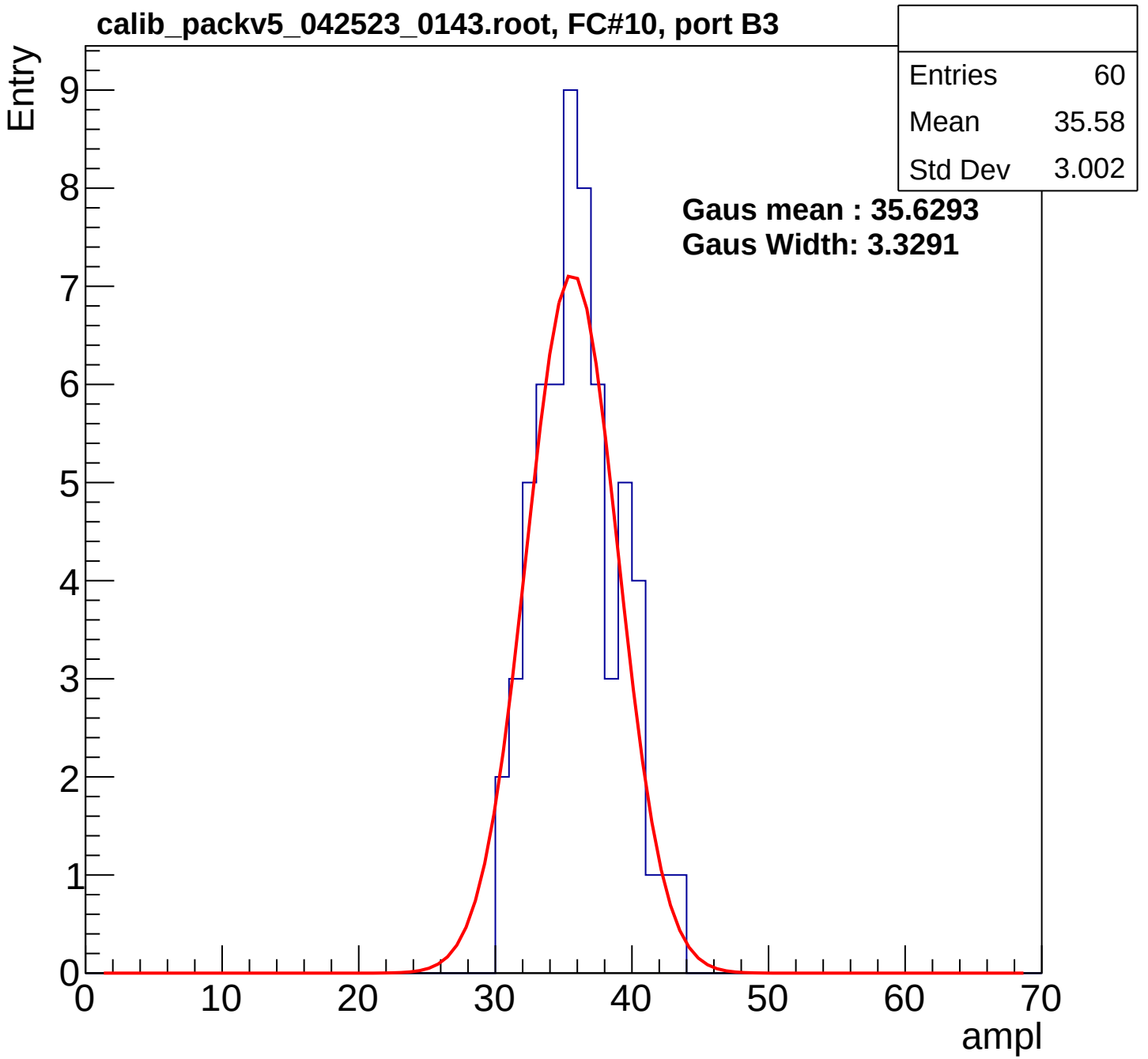
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch94, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 51
Std Dev 22.88
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch95, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 28.48
Std Dev 3.039
Gaus mean : 28.8131
Gaus Width: 3.7322
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch95, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 35.58
Std Dev 3.002
Gaus mean : 35.6293
Gaus Width: 3.3291
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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40
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60
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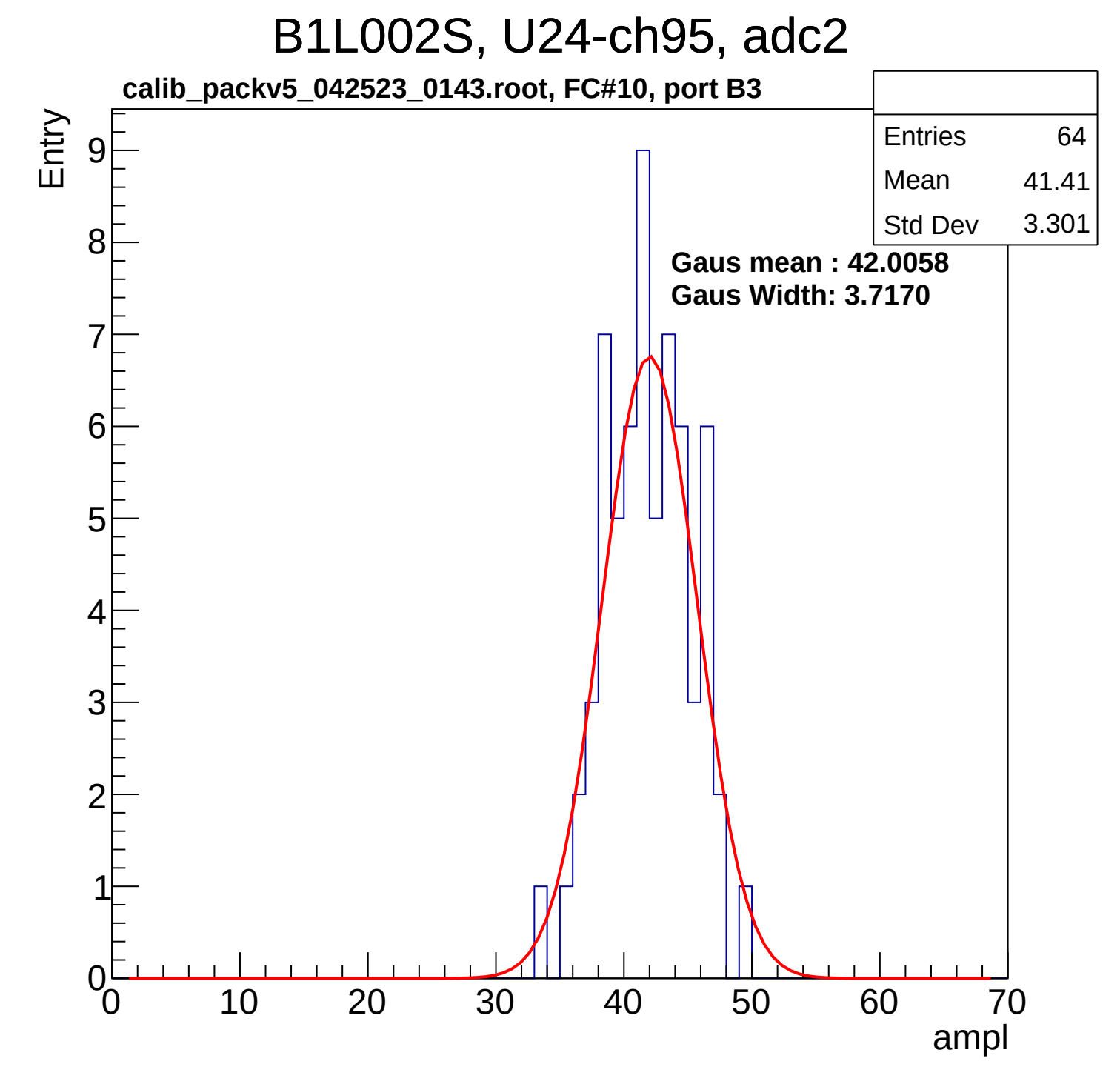
B1L002S, U24-ch95, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 41.41
Std Dev 3.301
Gaus mean : 42.0058
Gaus Width: 3.7170
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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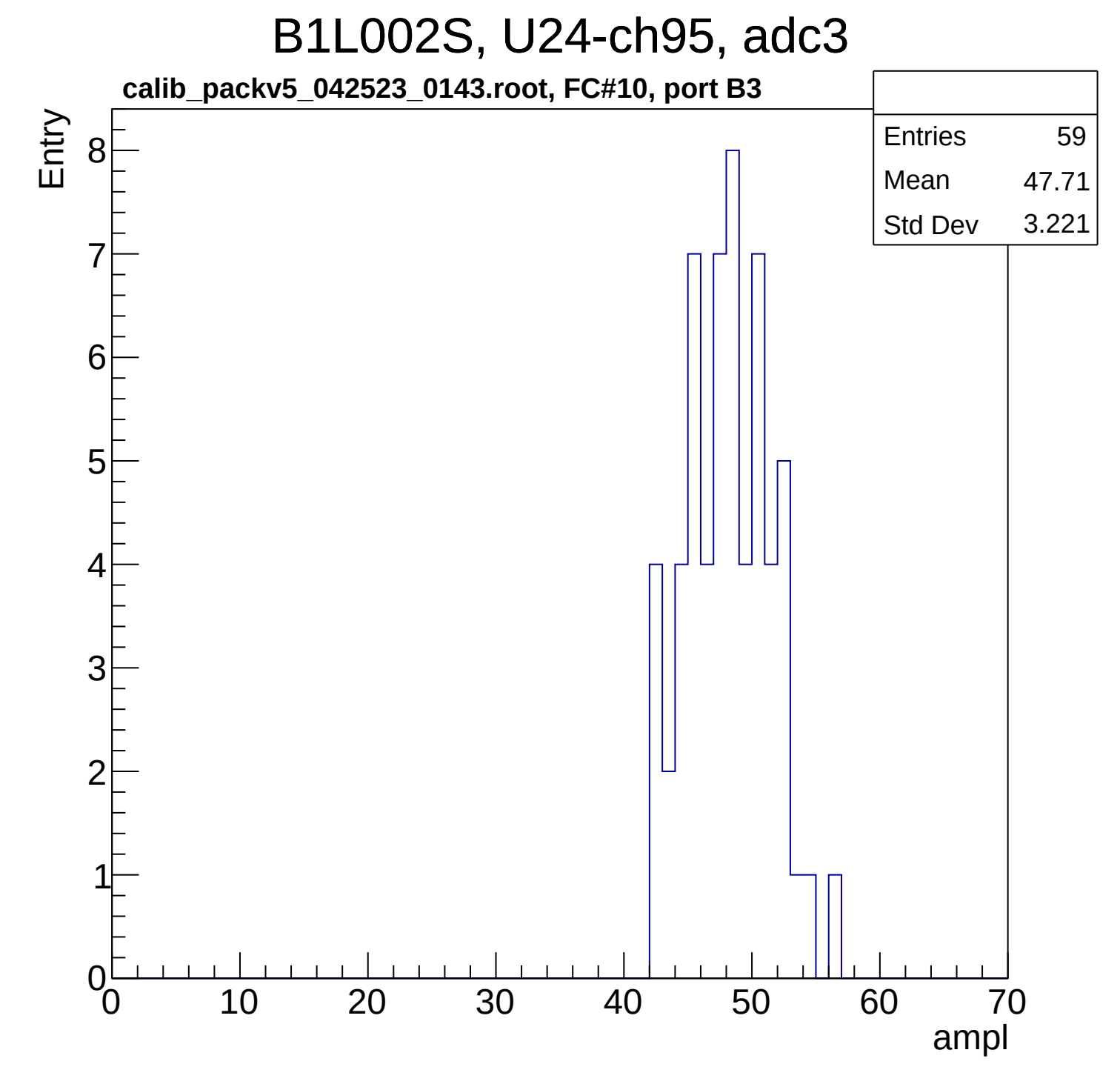
B1L002S, U24-ch95, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 47.71
Std Dev 3.221
Entry
ampl
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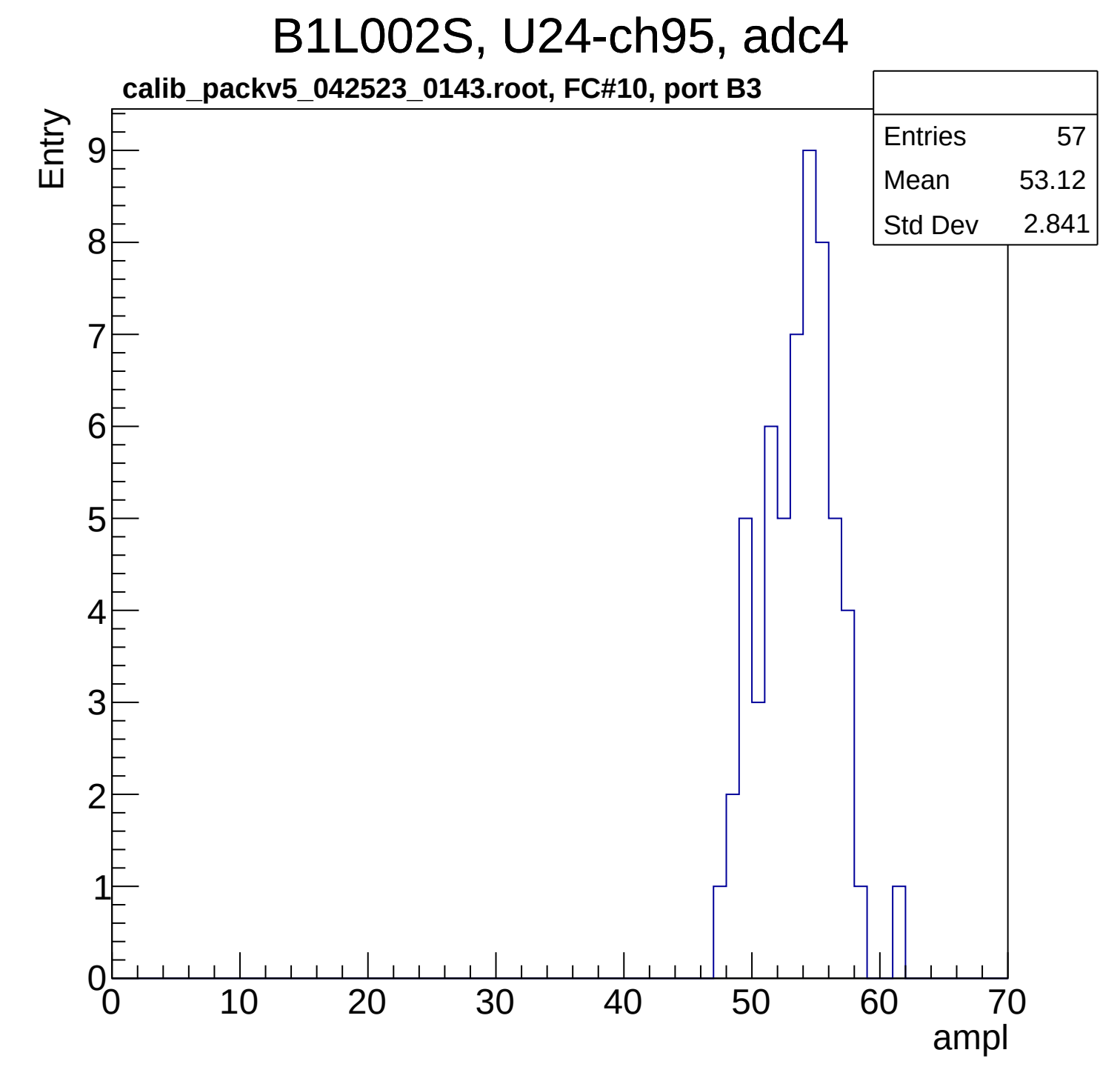
B1L002S, U24-ch95, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 53.12
Std Dev 2.841
Entry
ampl
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3
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6
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0
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60
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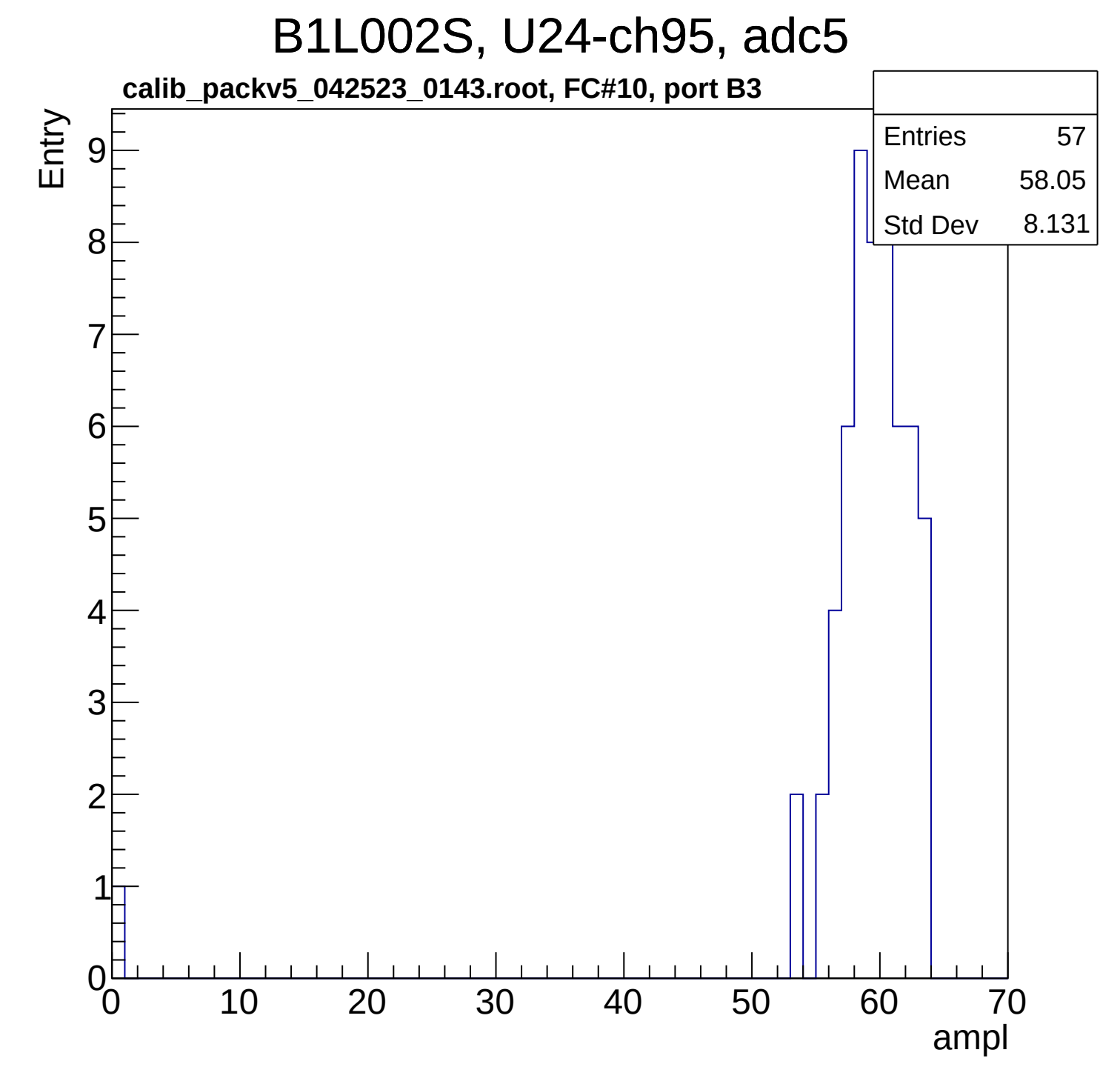
B1L002S, U24-ch95, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 58.05
Std Dev 8.131
Entry
ampl
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0
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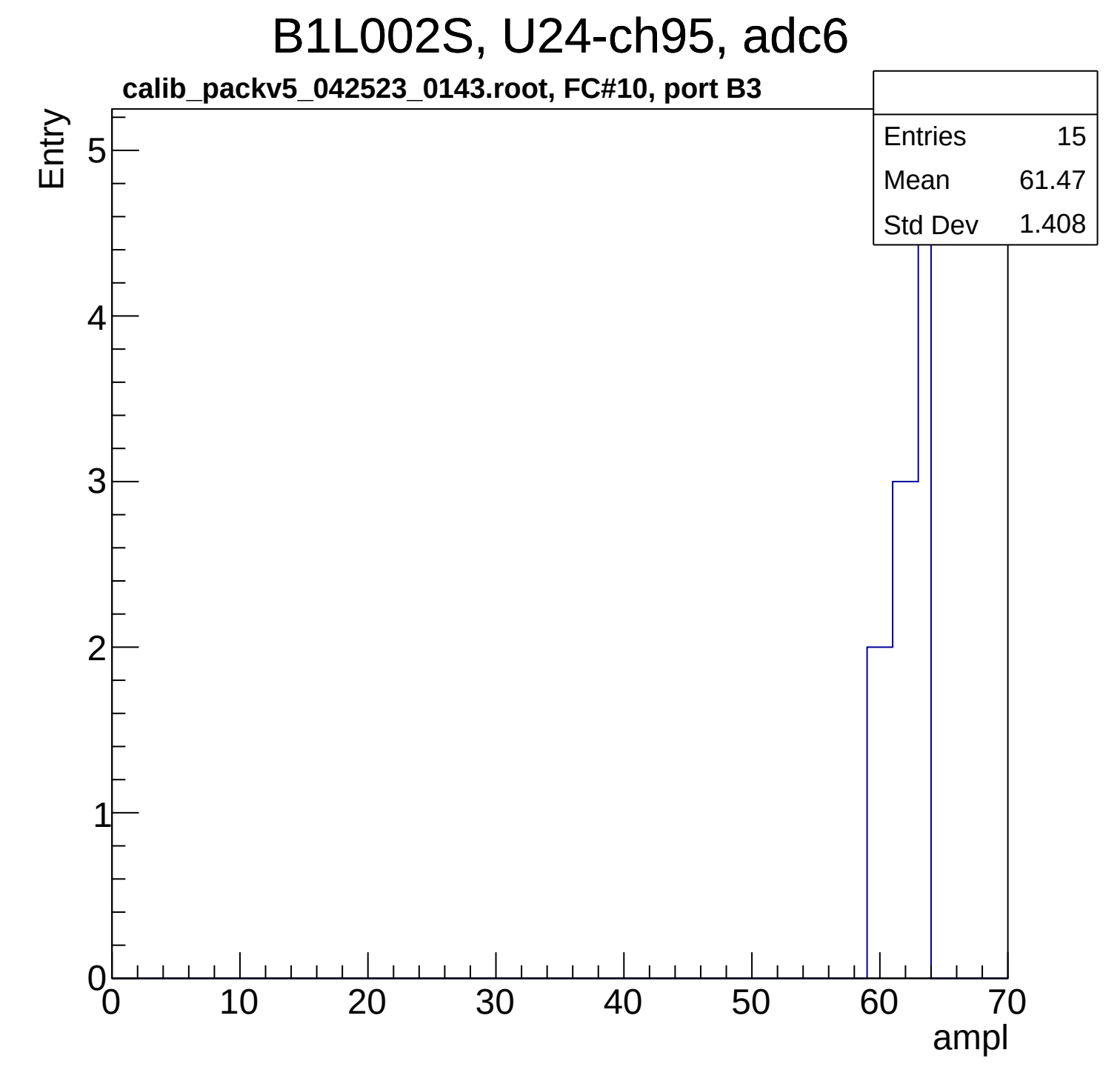
B1L002S, U24-ch95, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 61.47
Std Dev 1.408
Entry
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1
2
3
4
5
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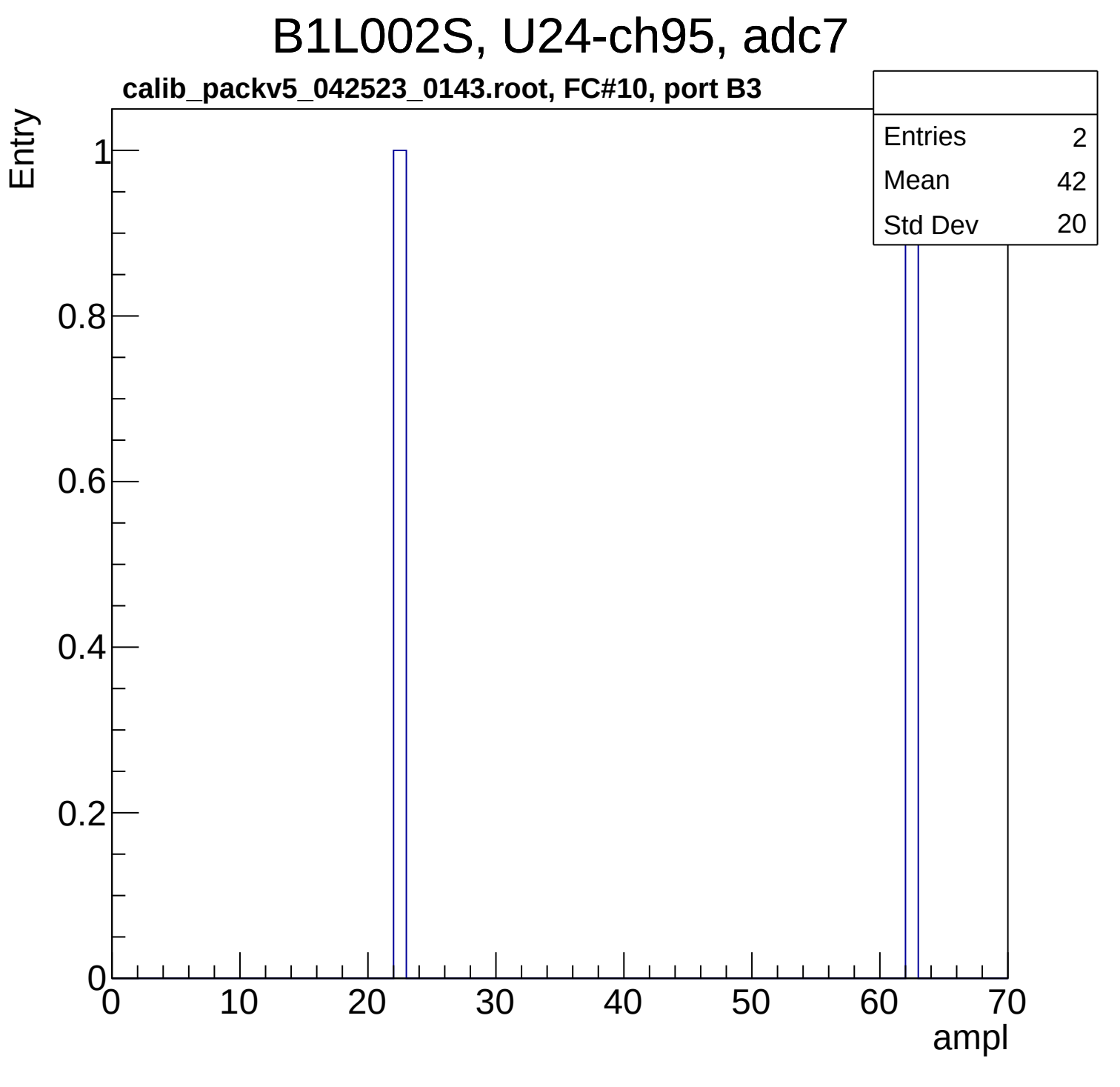
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch95, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 42
Std Dev 20
Entry
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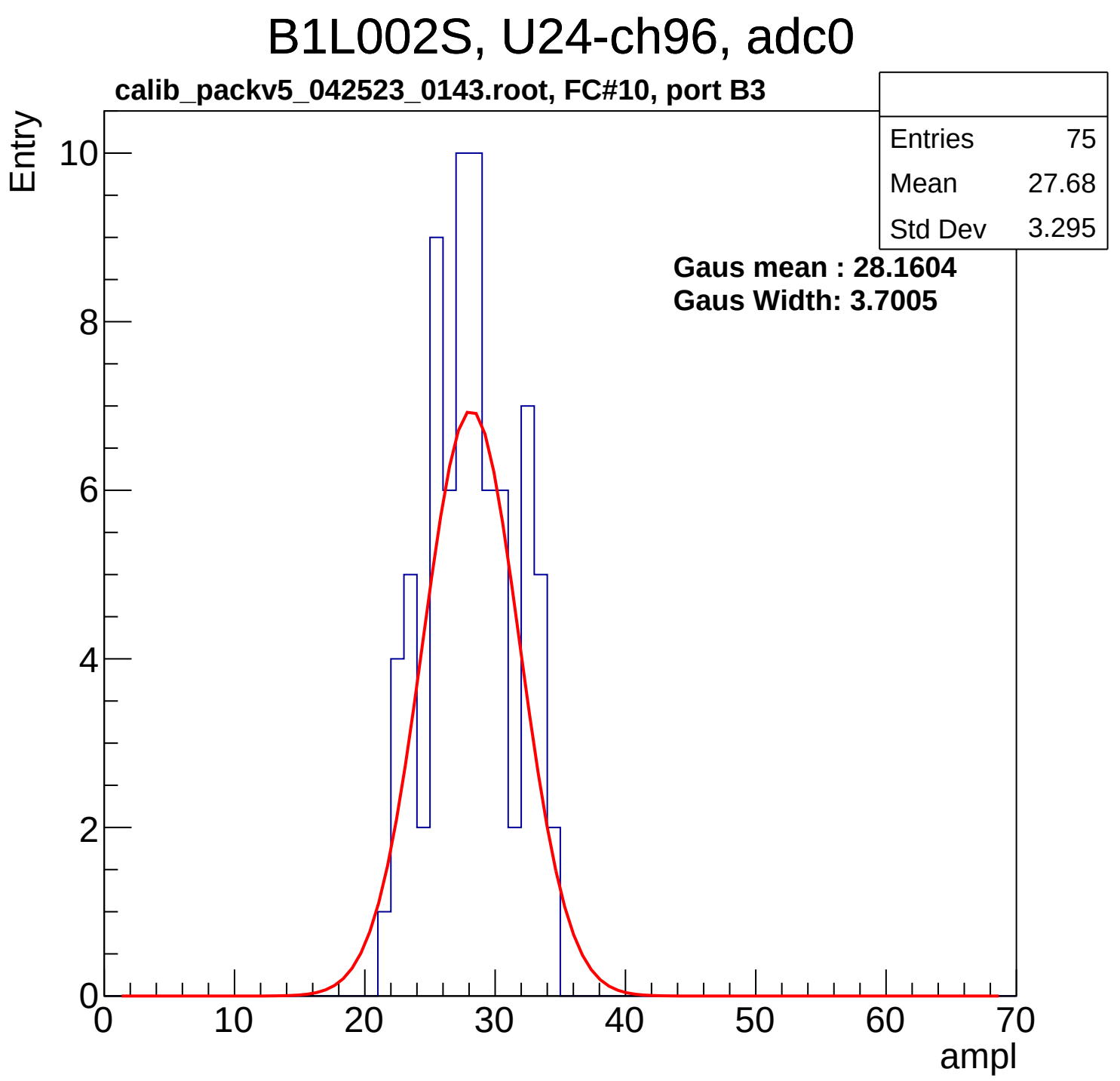
50
60
70
ampl


B1L002S, U24-ch96, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 27.68
Std Dev 3.295
Gaus mean : 28.1604
Gaus Width: 3.7005
Entry
ampl
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4
6
8
10
0
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60
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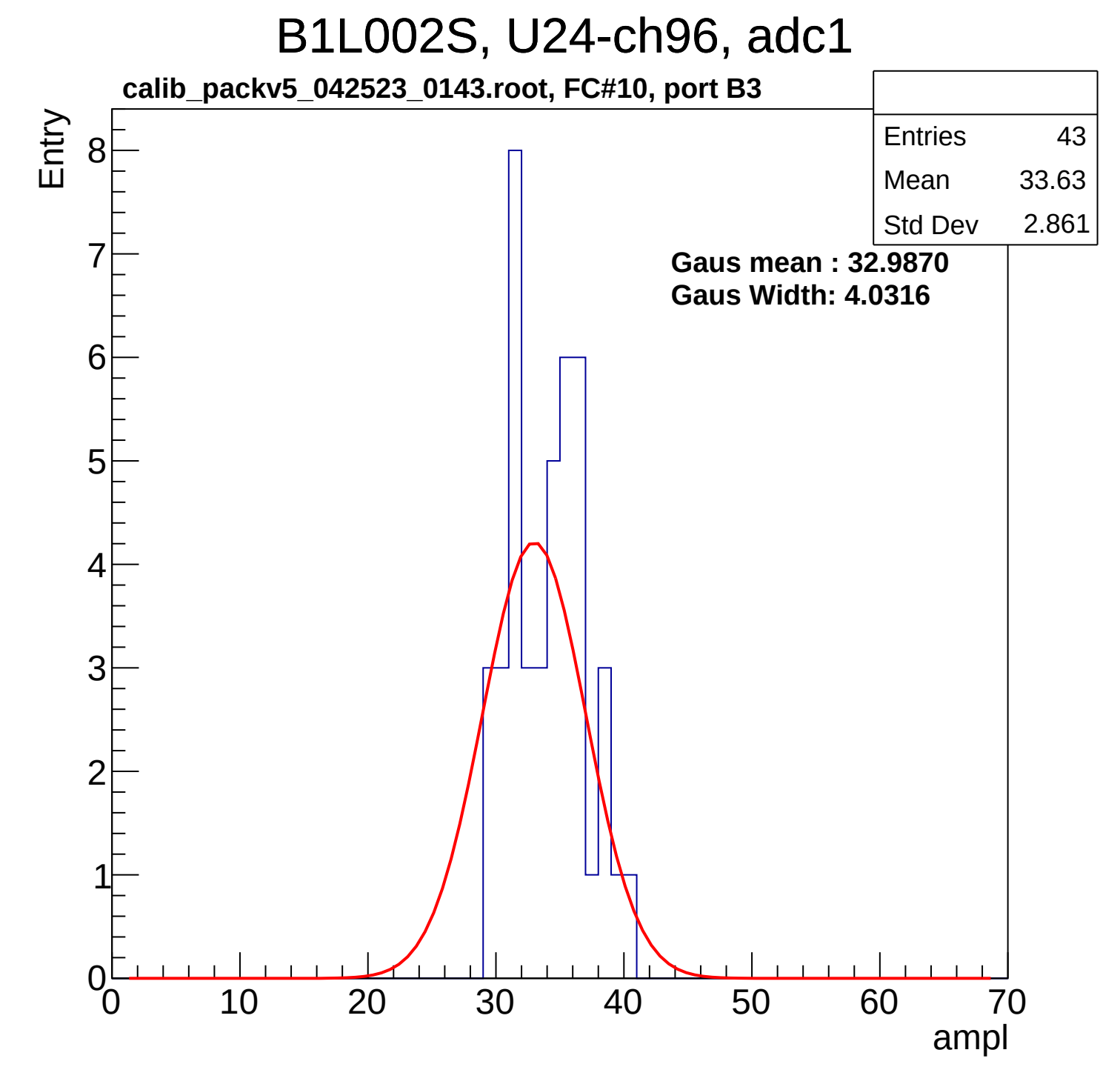
B1L002S, U24-ch96, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 33.63
Std Dev 2.861
Gaus mean : 32.9870
Gaus Width: 4.0316
Entry
ampl
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2
3
4
5
6
7
8
0
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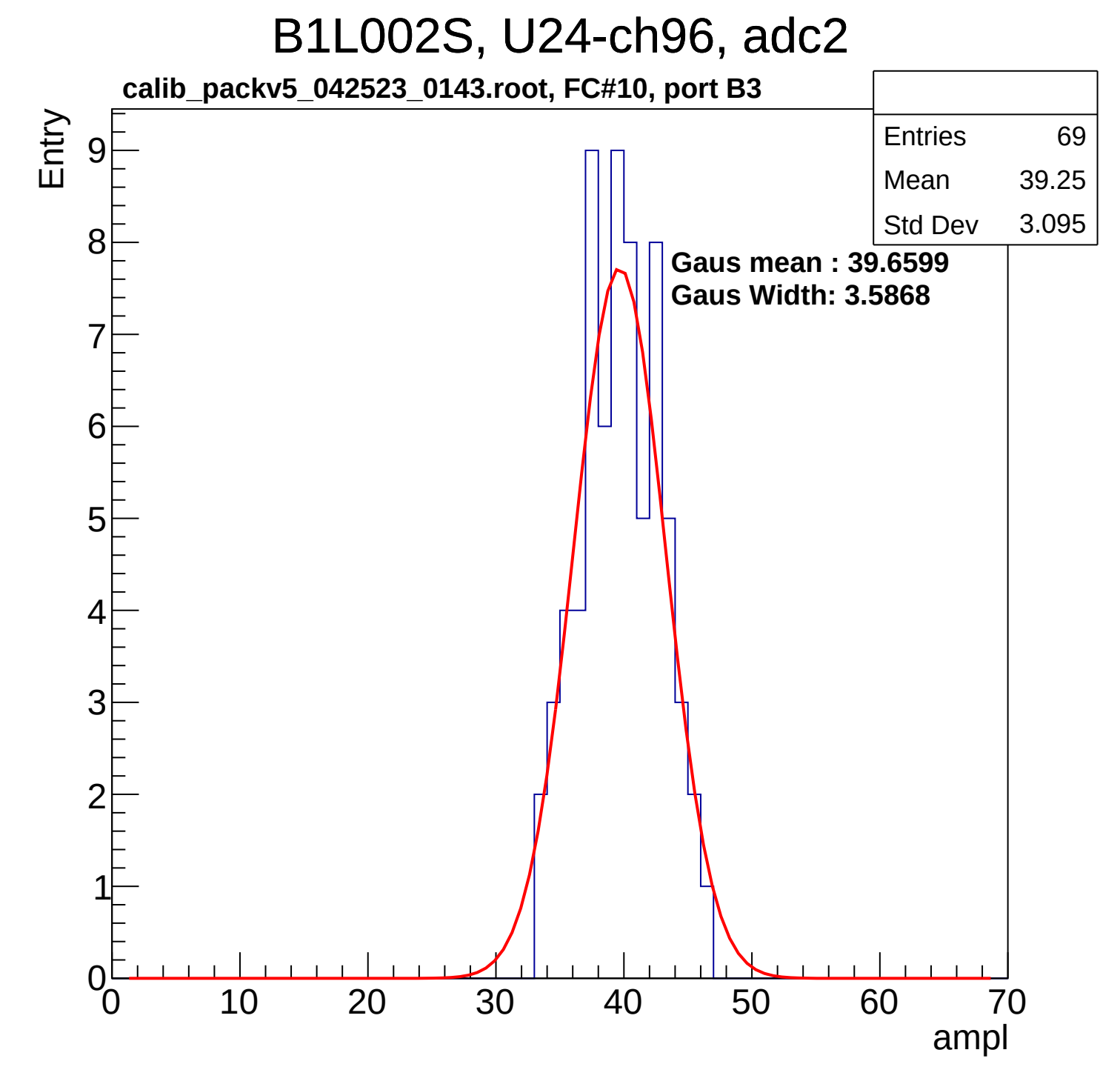
B1L002S, U24-ch96, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 39.25
Std Dev 3.095
Gaus mean : 39.6599
Gaus Width: 3.5868
Entry
ampl
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2
3
4
5
6
7
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0
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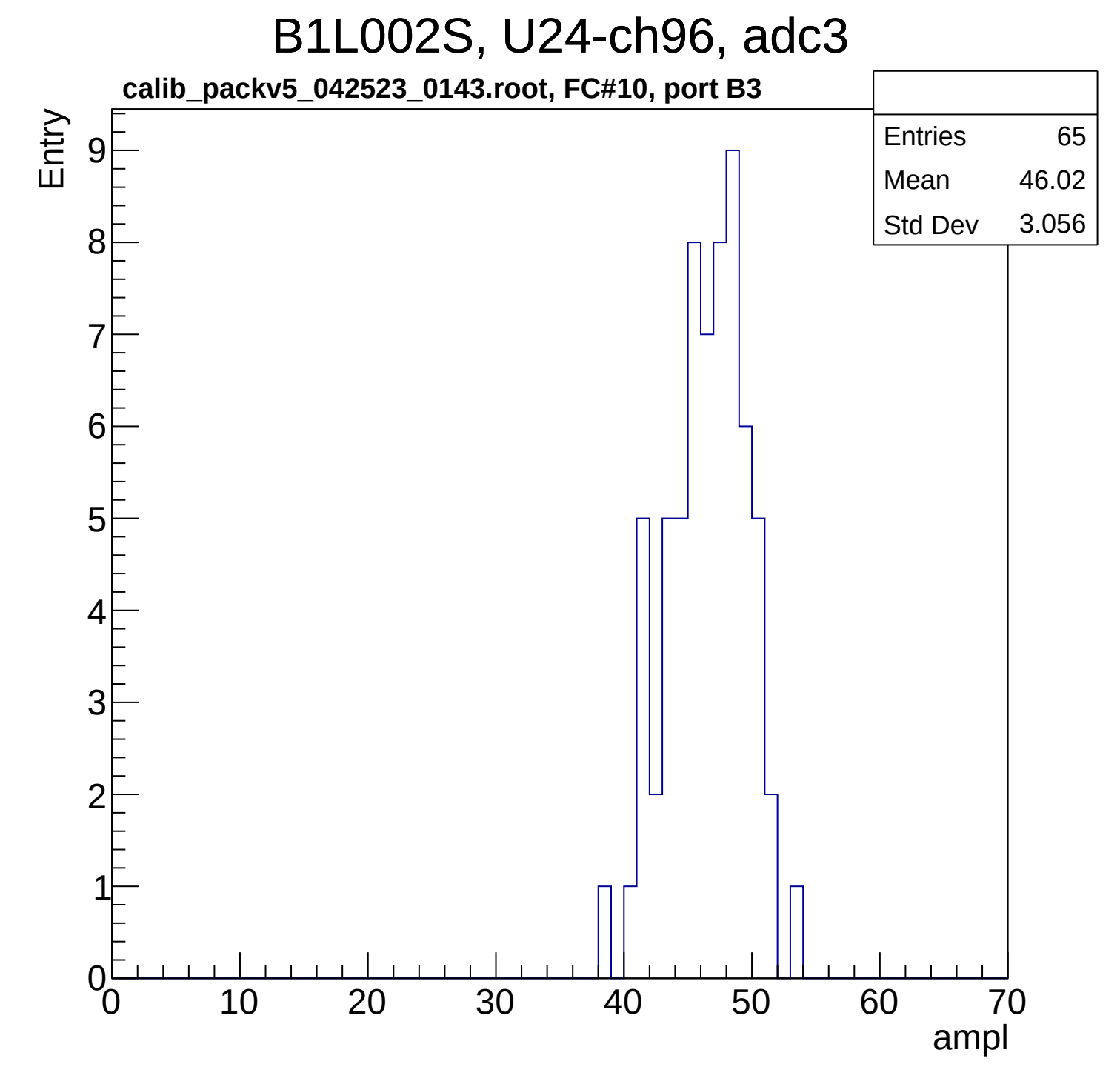
B1L002S, U24-ch96, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 46.02
Std Dev 3.056
Entry
ampl
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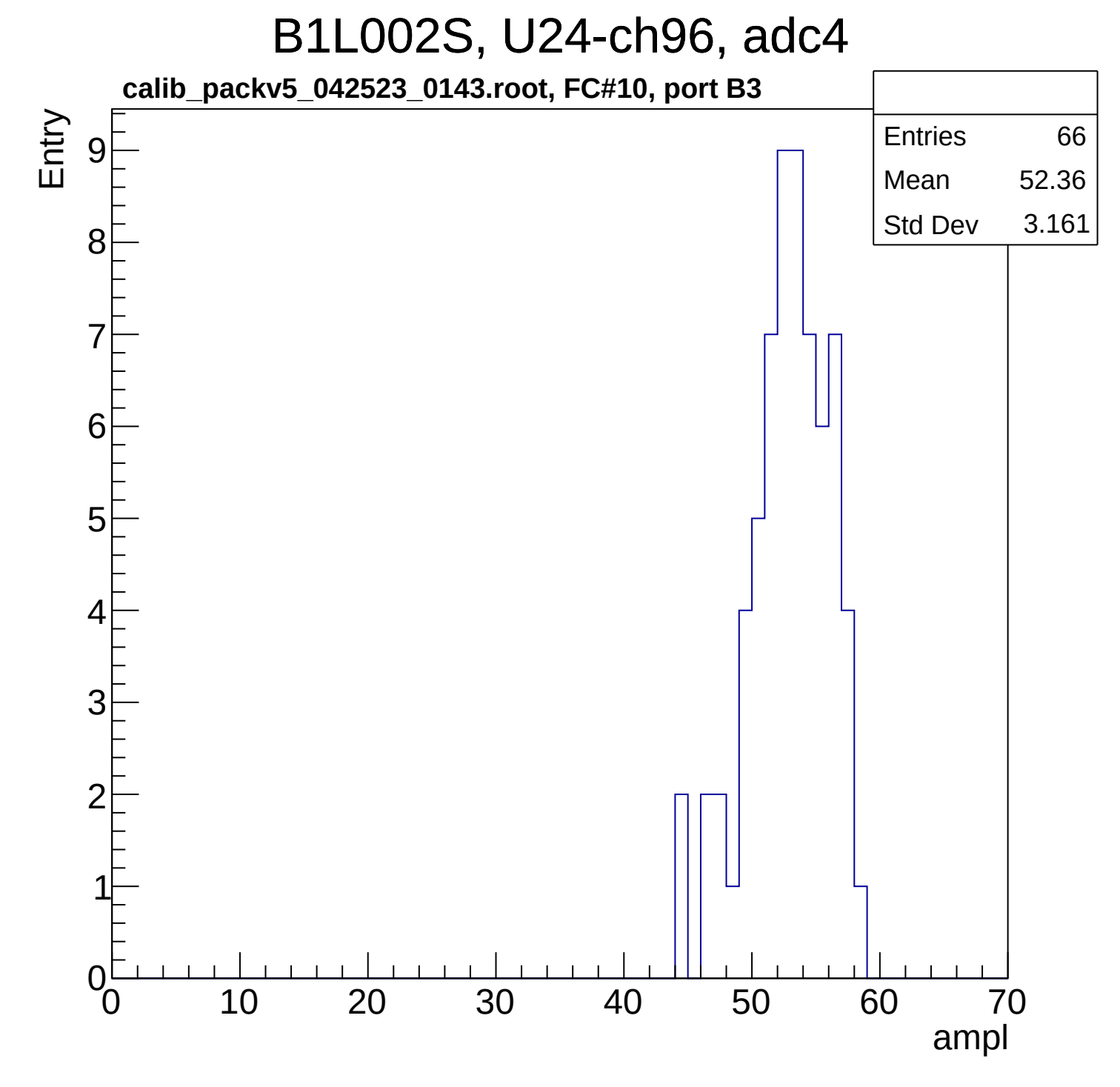
B1L002S, U24-ch96, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 52.36
Std Dev 3.161
Entry
ampl
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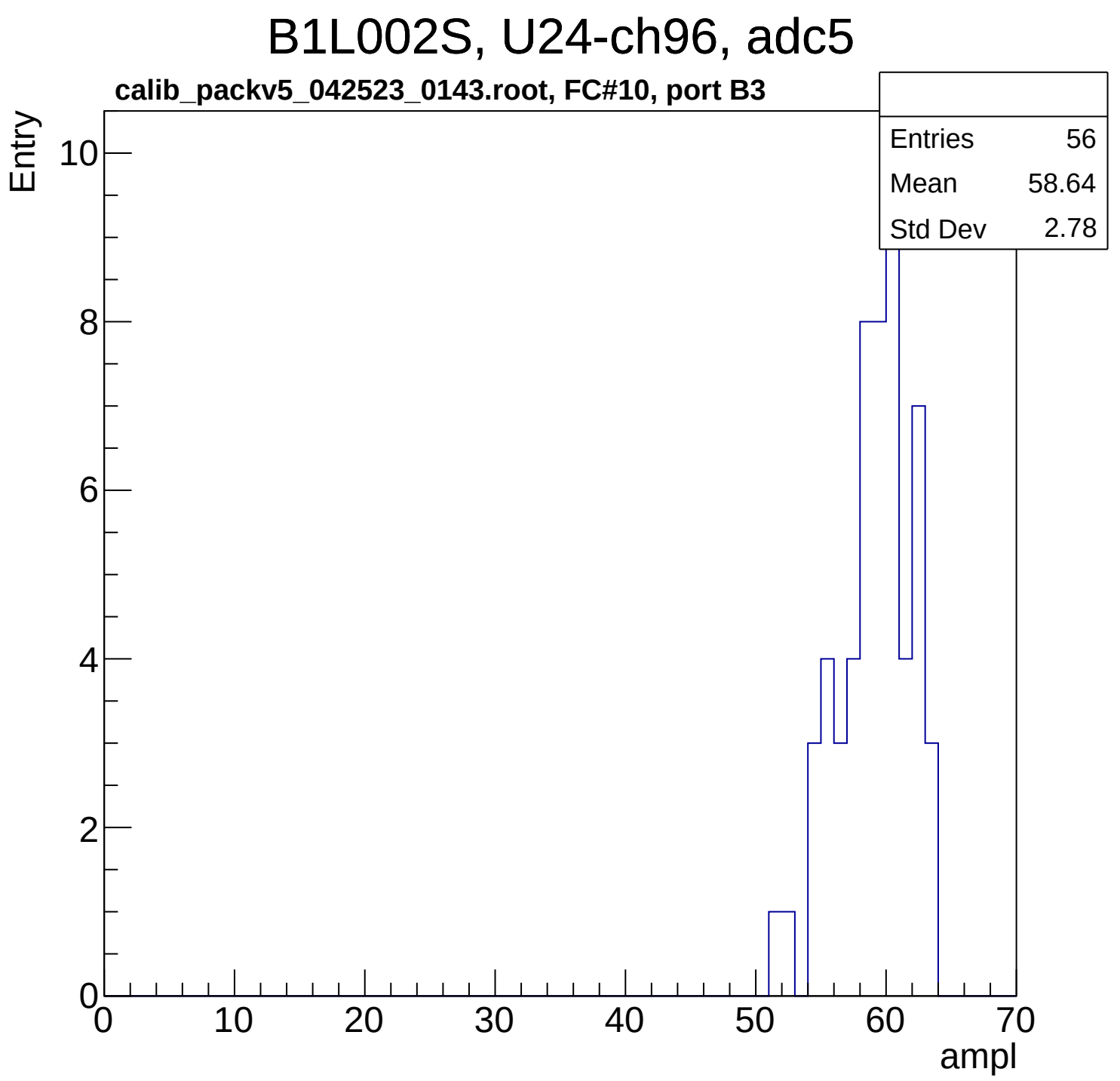
B1L002S, U24-ch96, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 58.64
Std Dev 2.78
Entry
ampl
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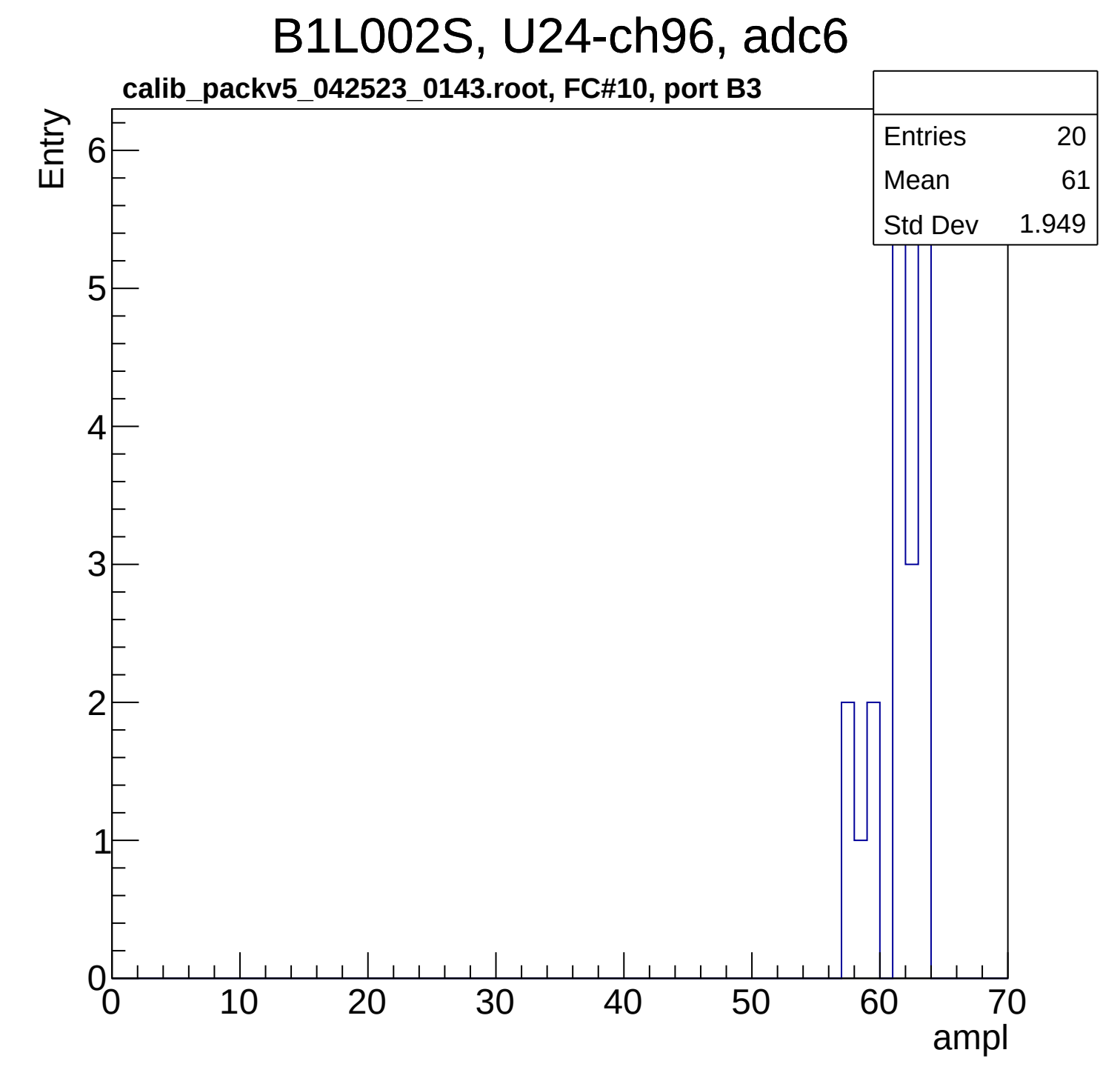
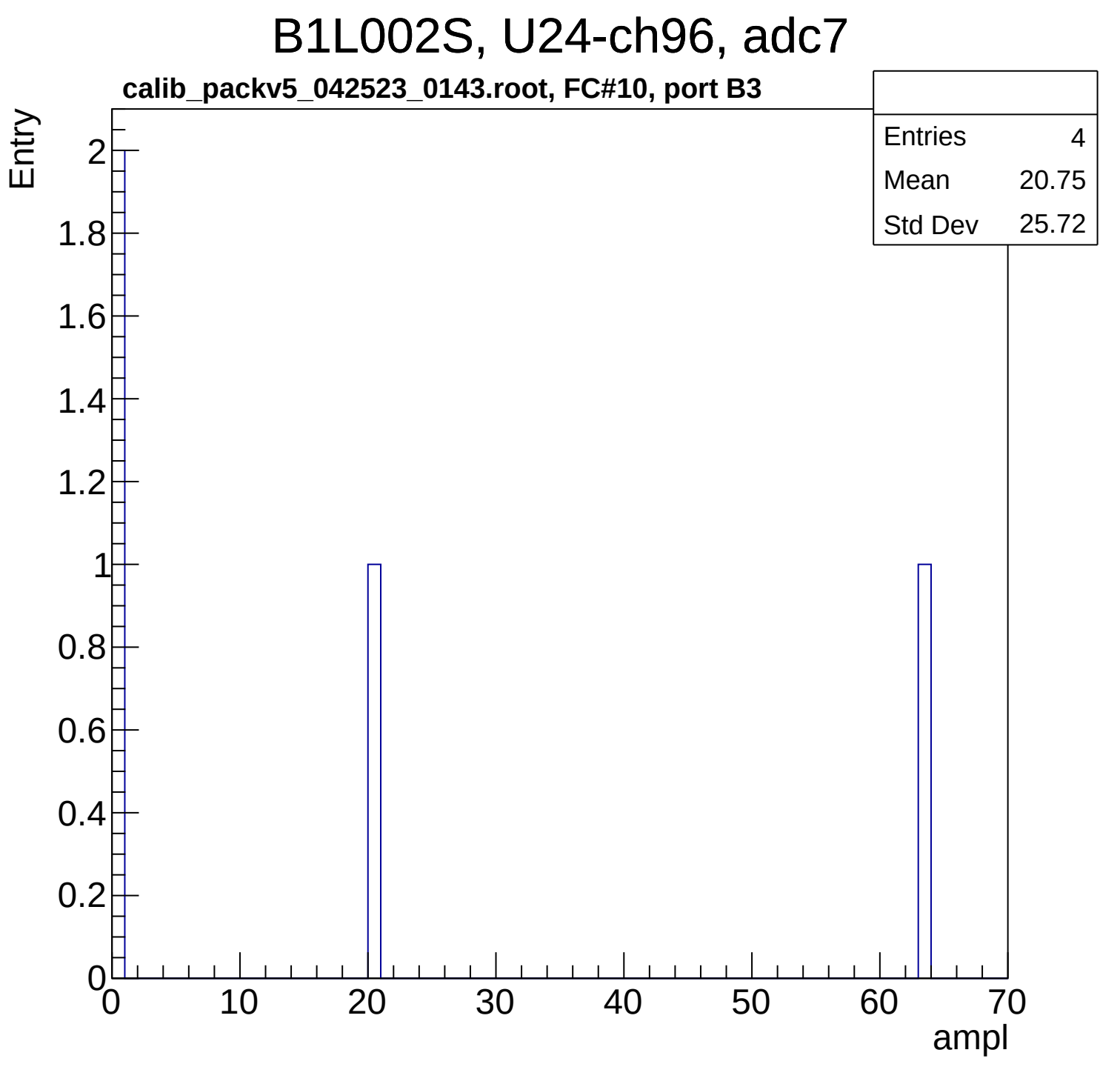

B1L002S, U24-ch96, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 61
Std Dev 1.949
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch96, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 20.75
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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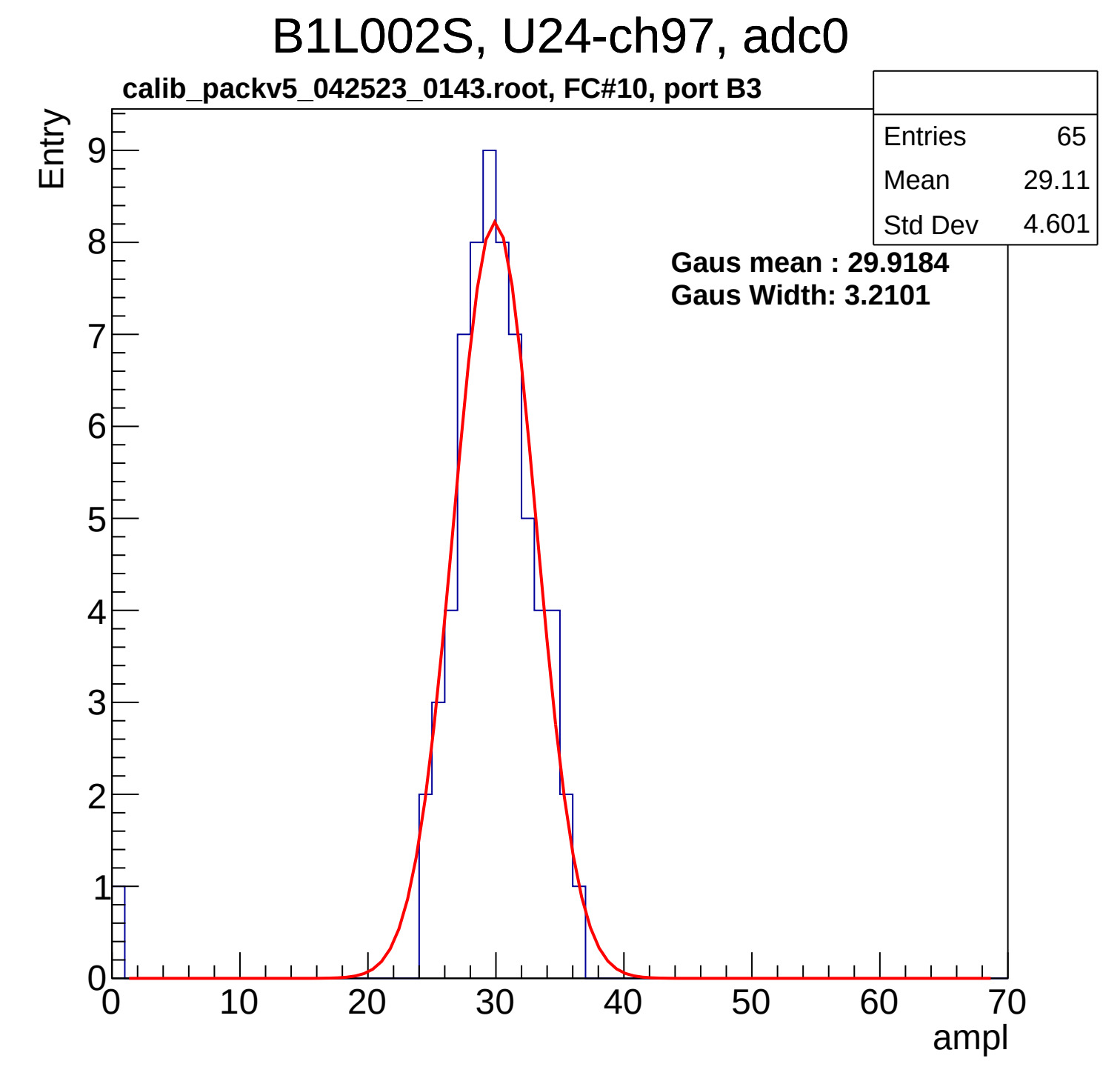
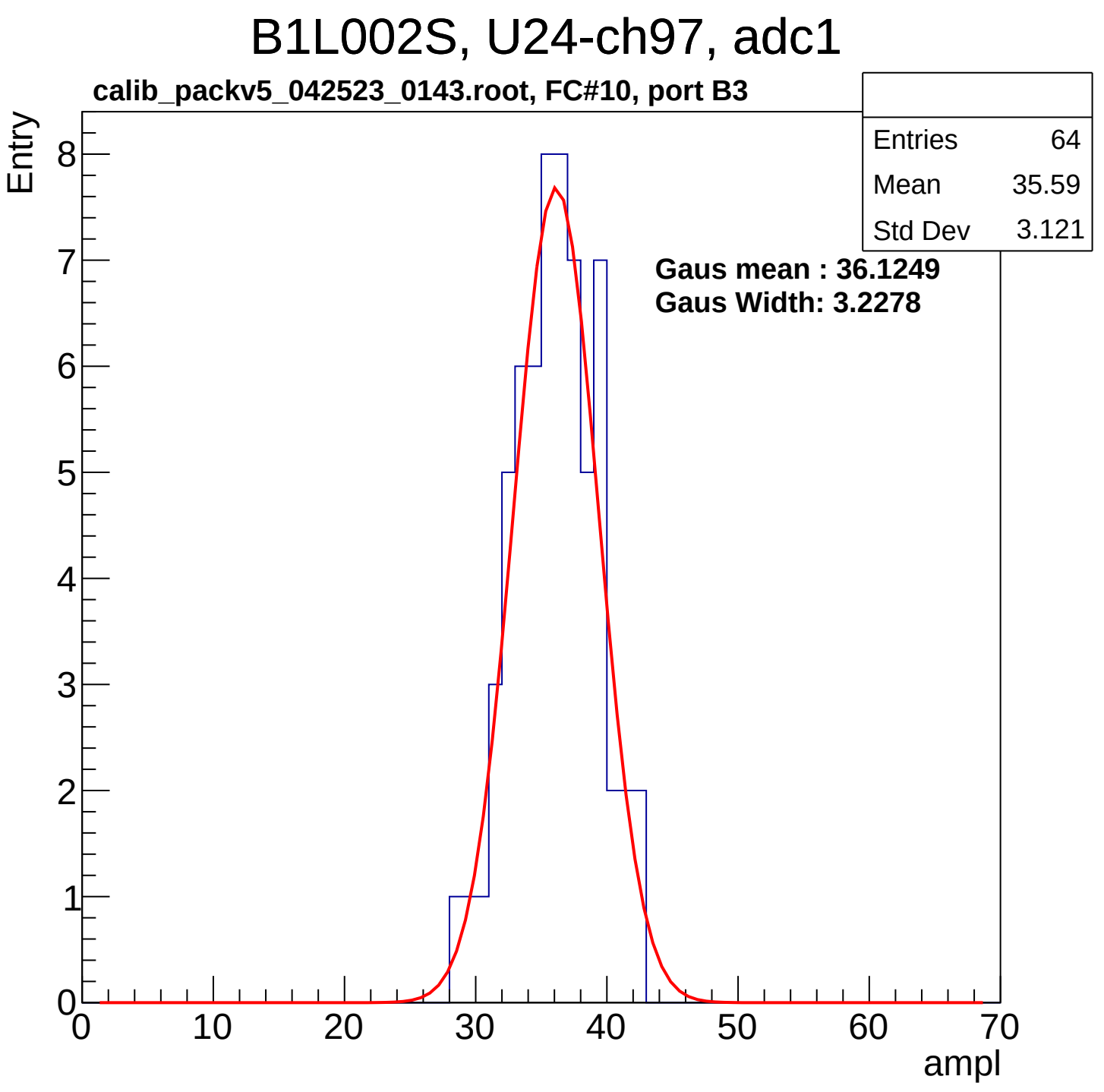
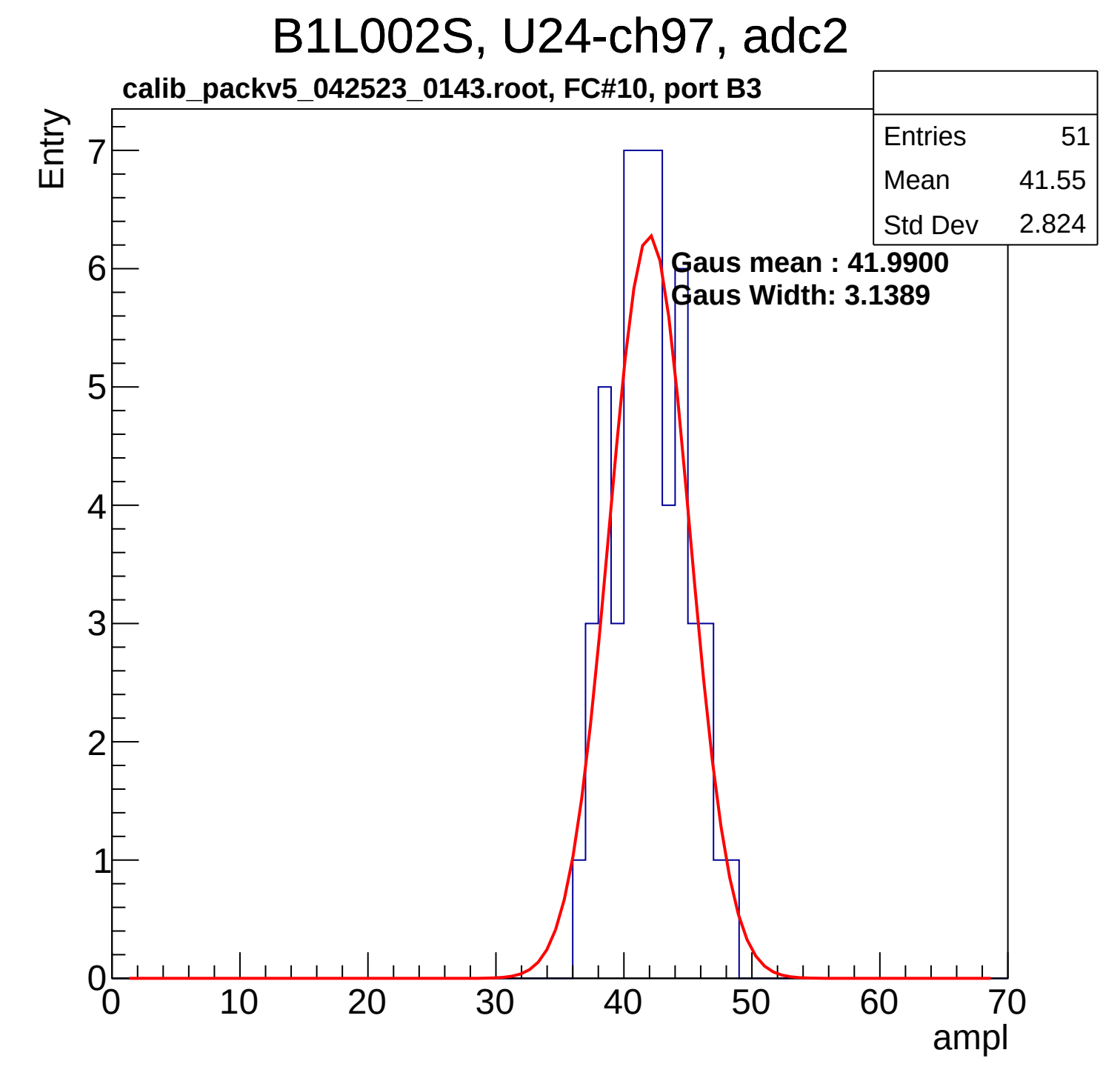
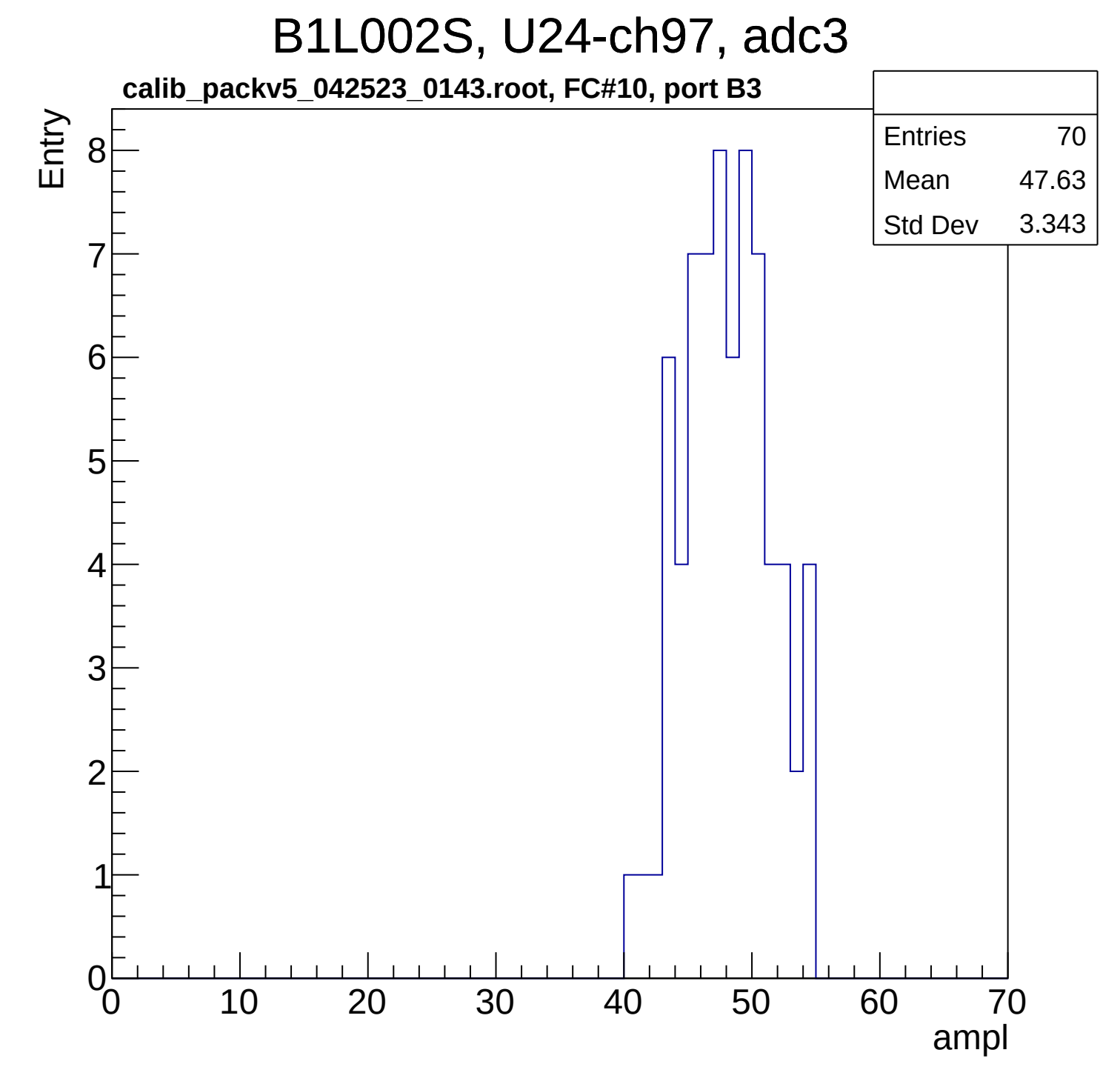
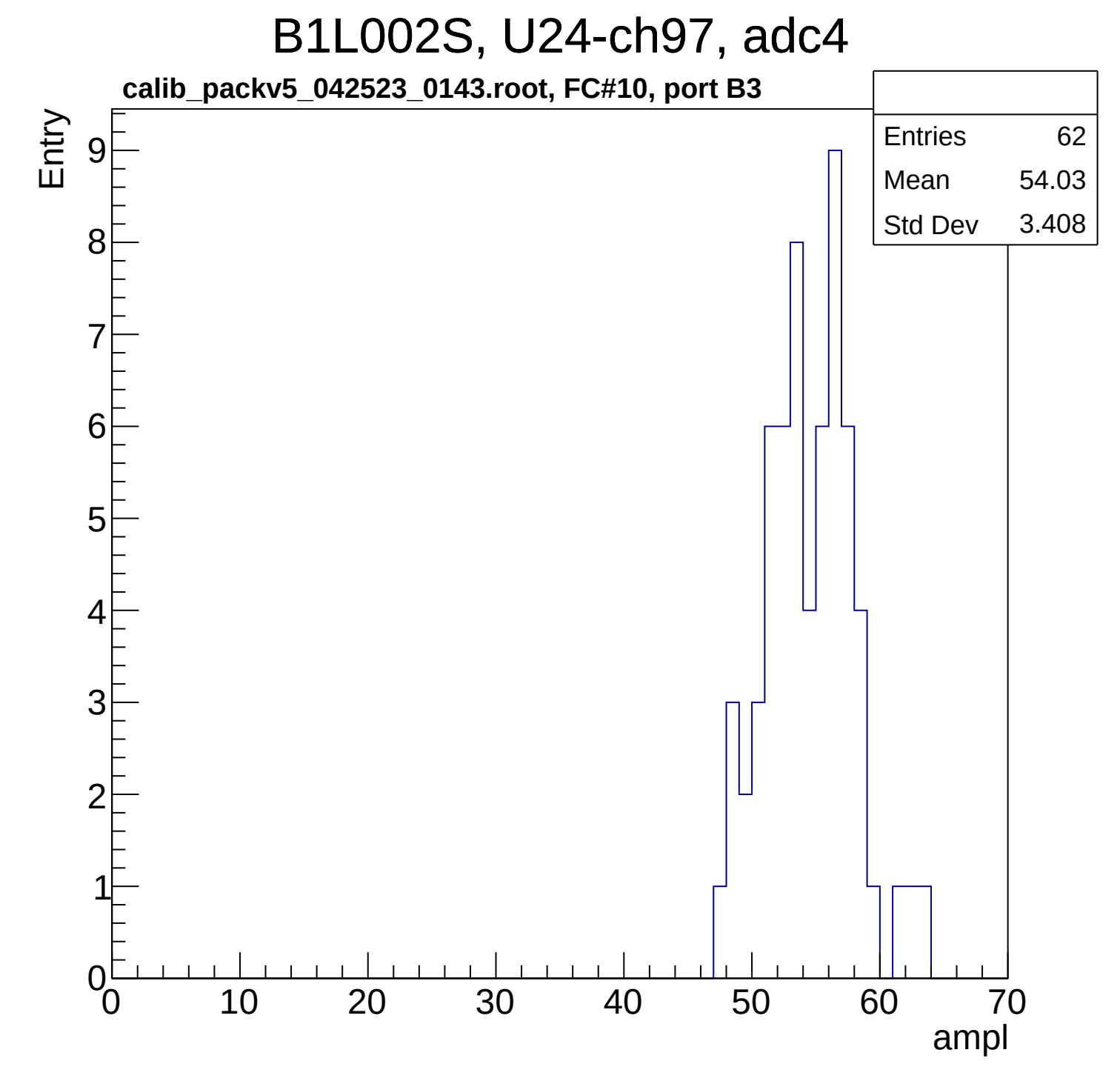
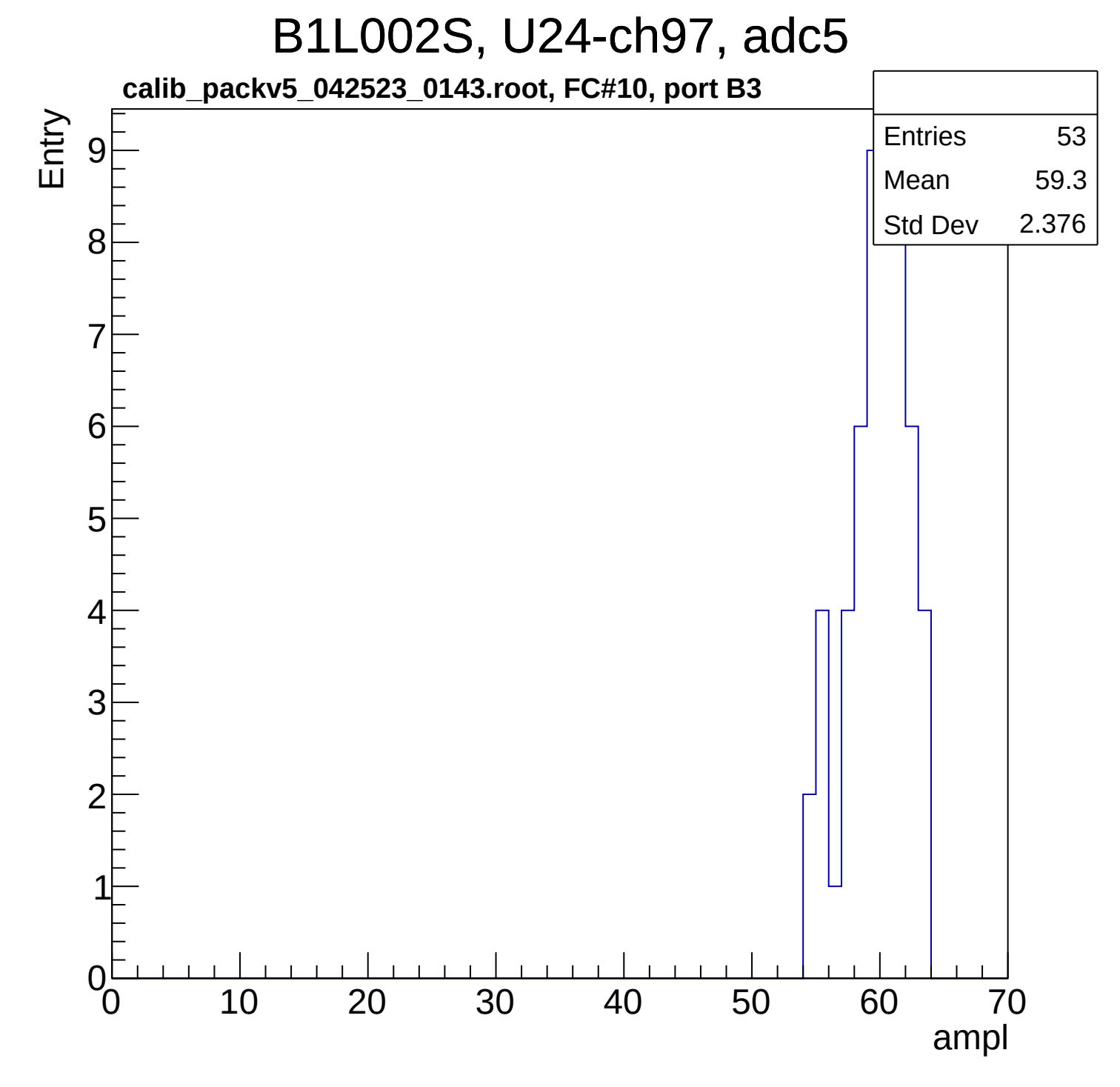

B1L002S, U24-ch97, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.11
Std Dev 4.601
Gaus mean : 29.9184
Gaus Width: 3.2101
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

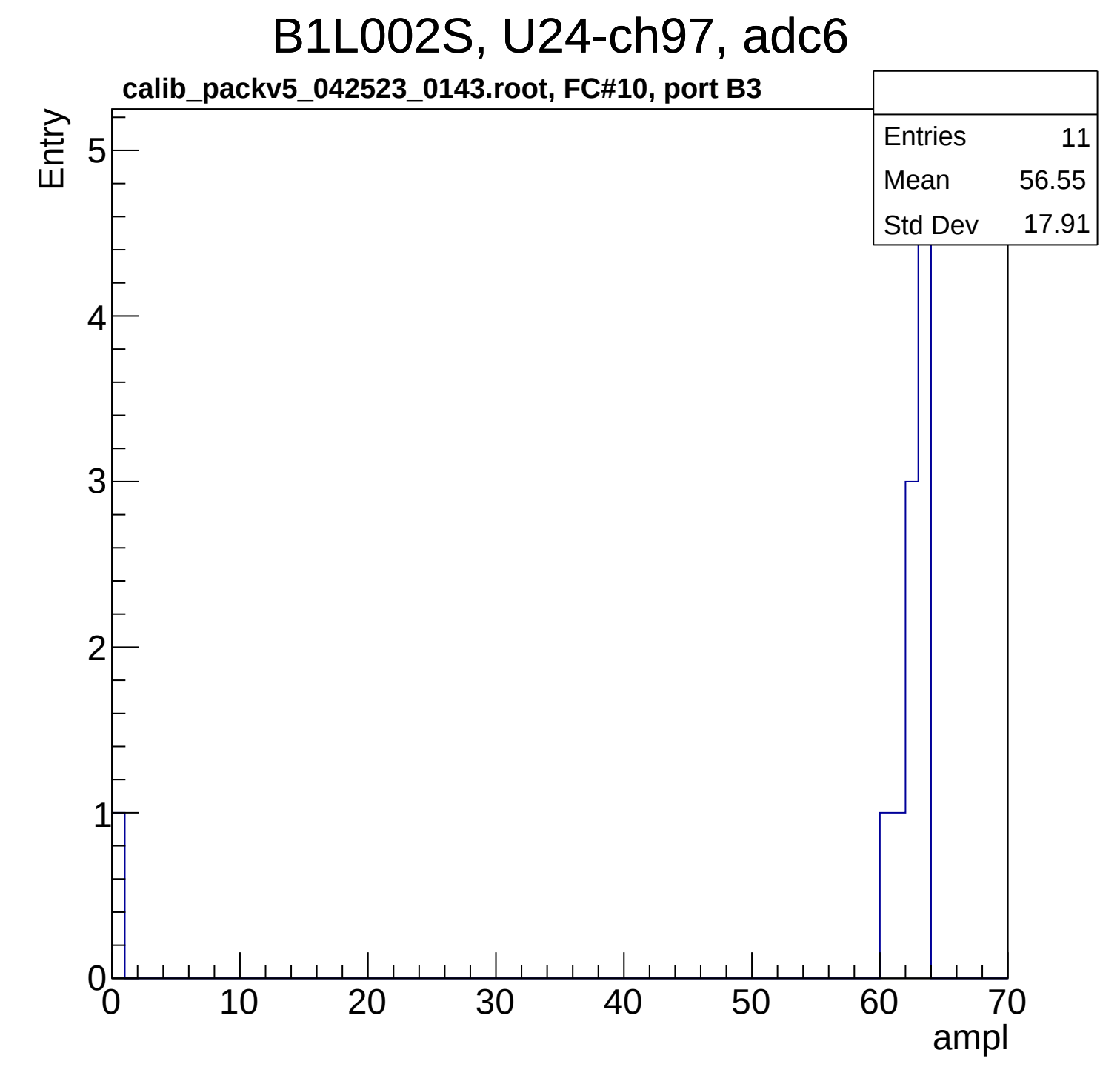

B1L002S, U24-ch97, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 35.59
Std Dev 3.121
Gaus mean : 36.1249
Gaus Width: 3.2278
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

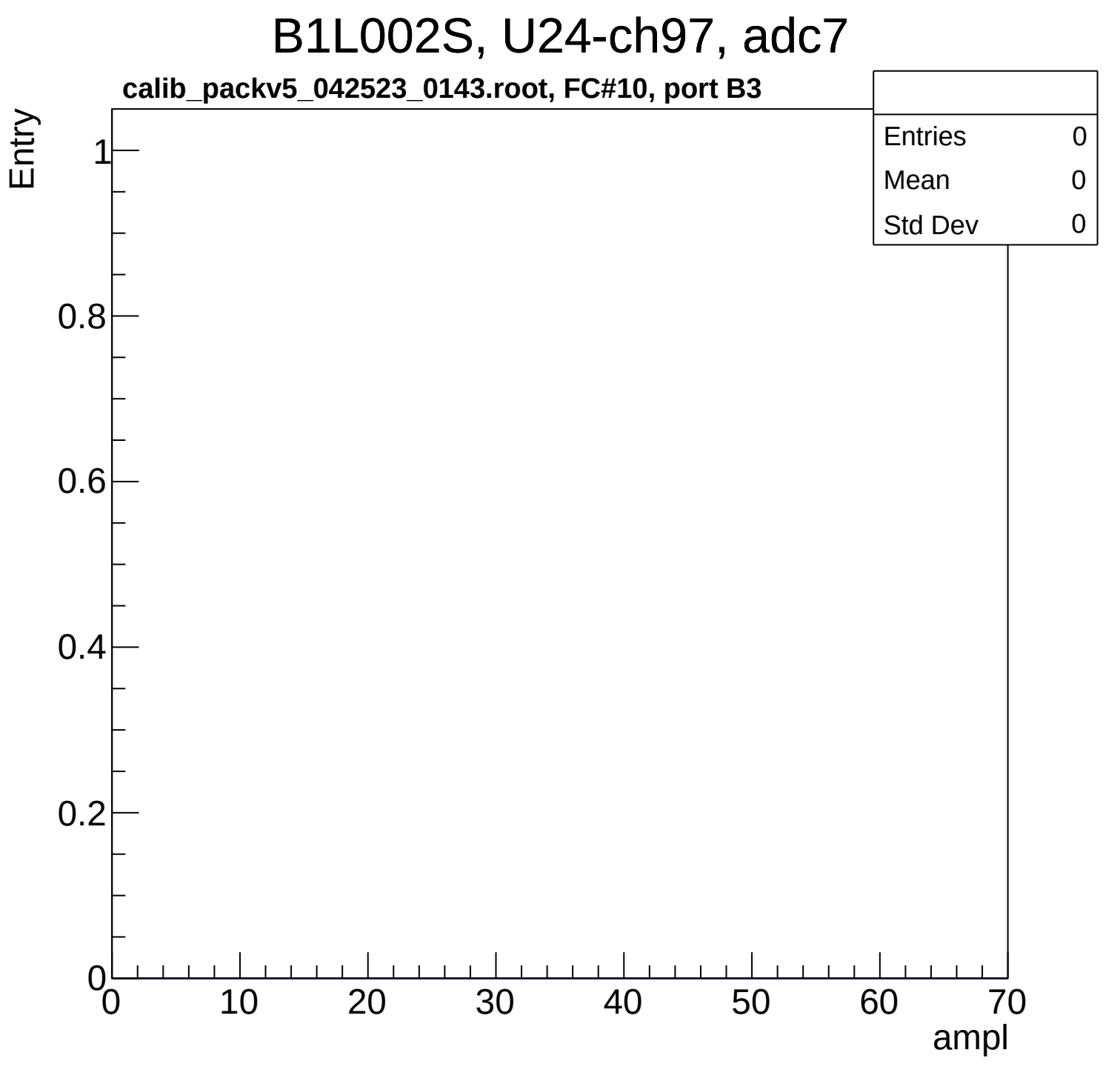

B1L002S, U24-ch97, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 41.55
Std Dev 2.824
Gaus mean : 41.9900
Gaus Width: 3.1389
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

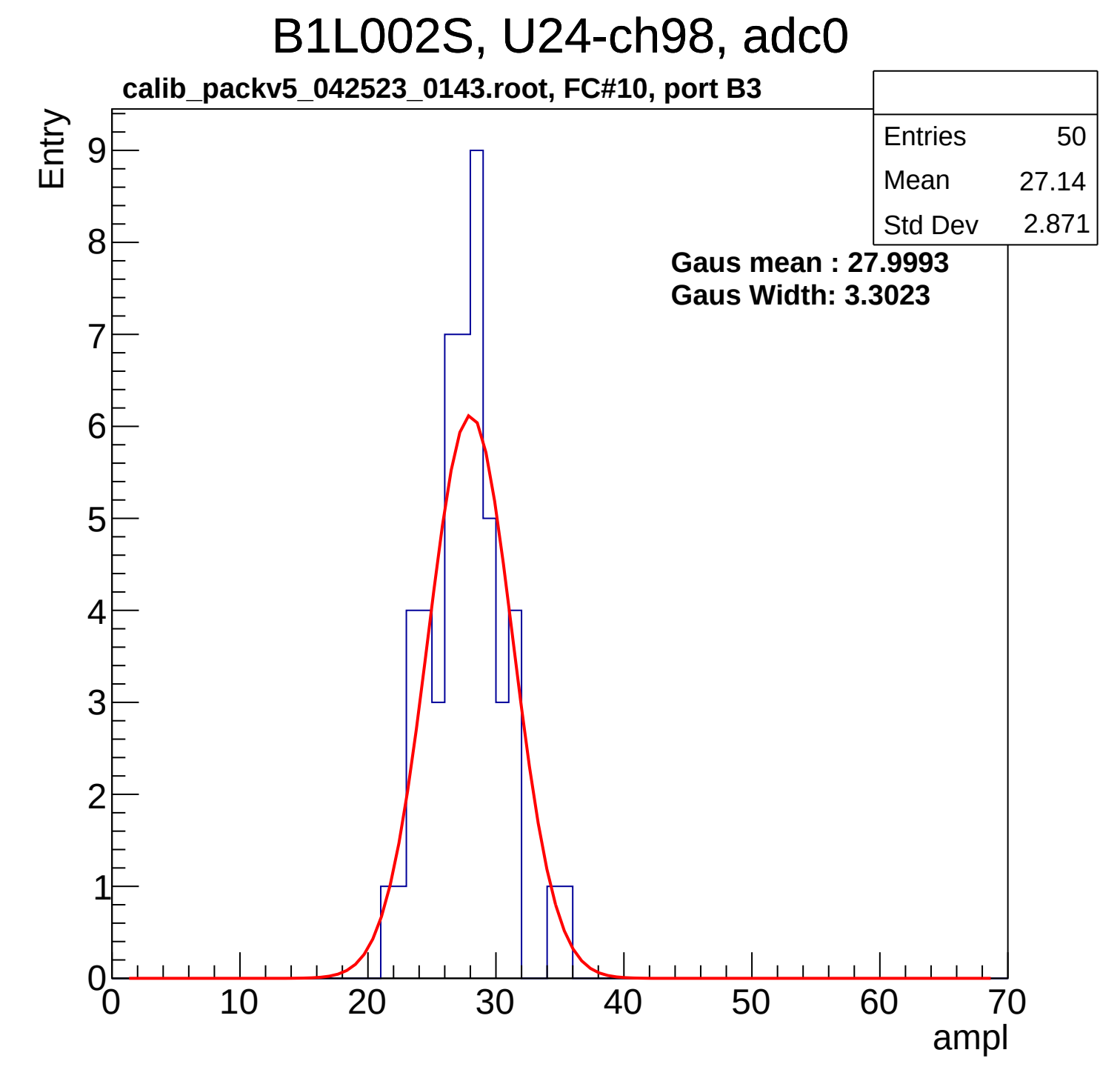

B1L002S, U24-ch97, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 47.63
Std Dev 3.343
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

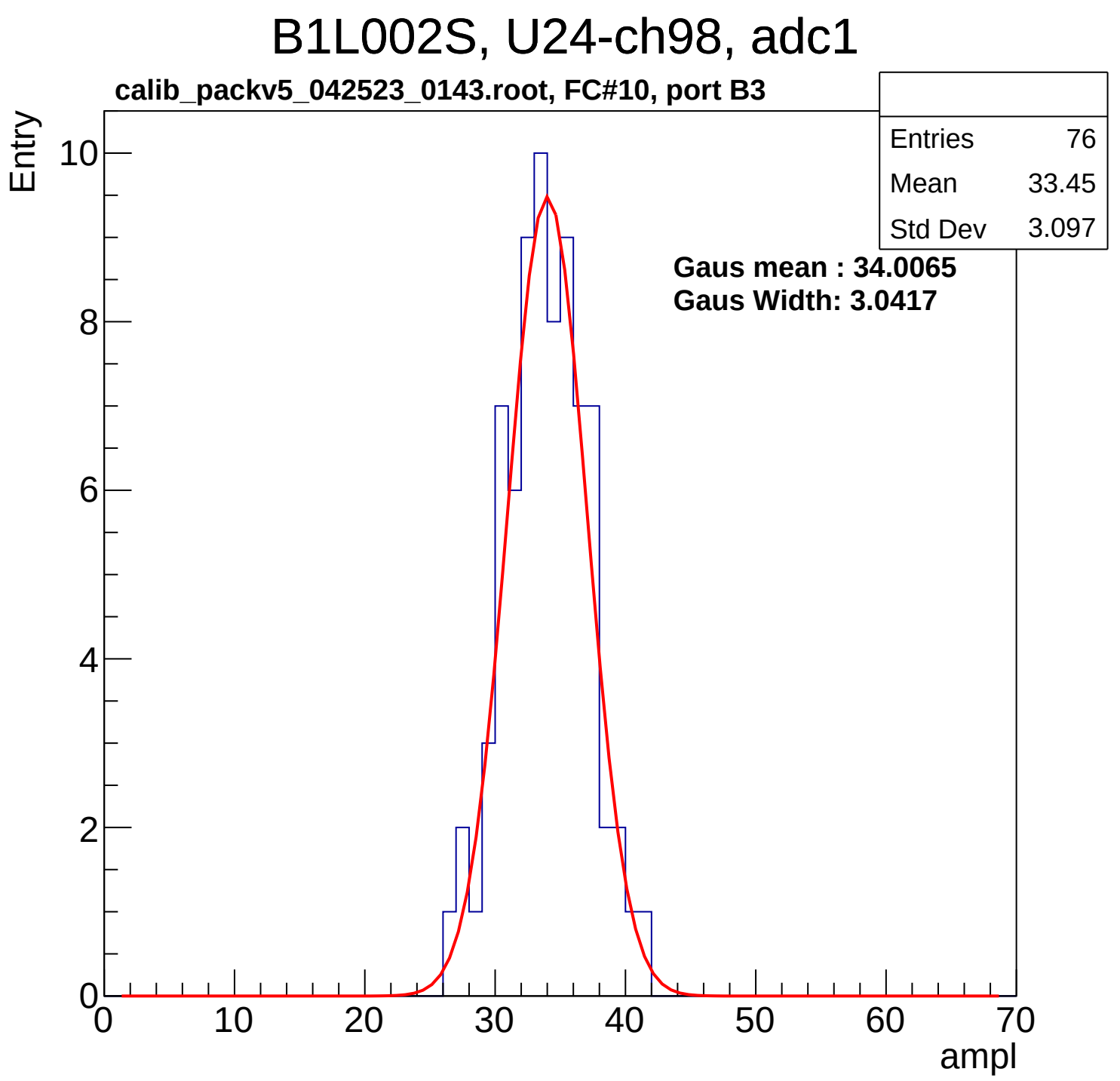

B1L002S, U24-ch97, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 54.03
Std Dev 3.408
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

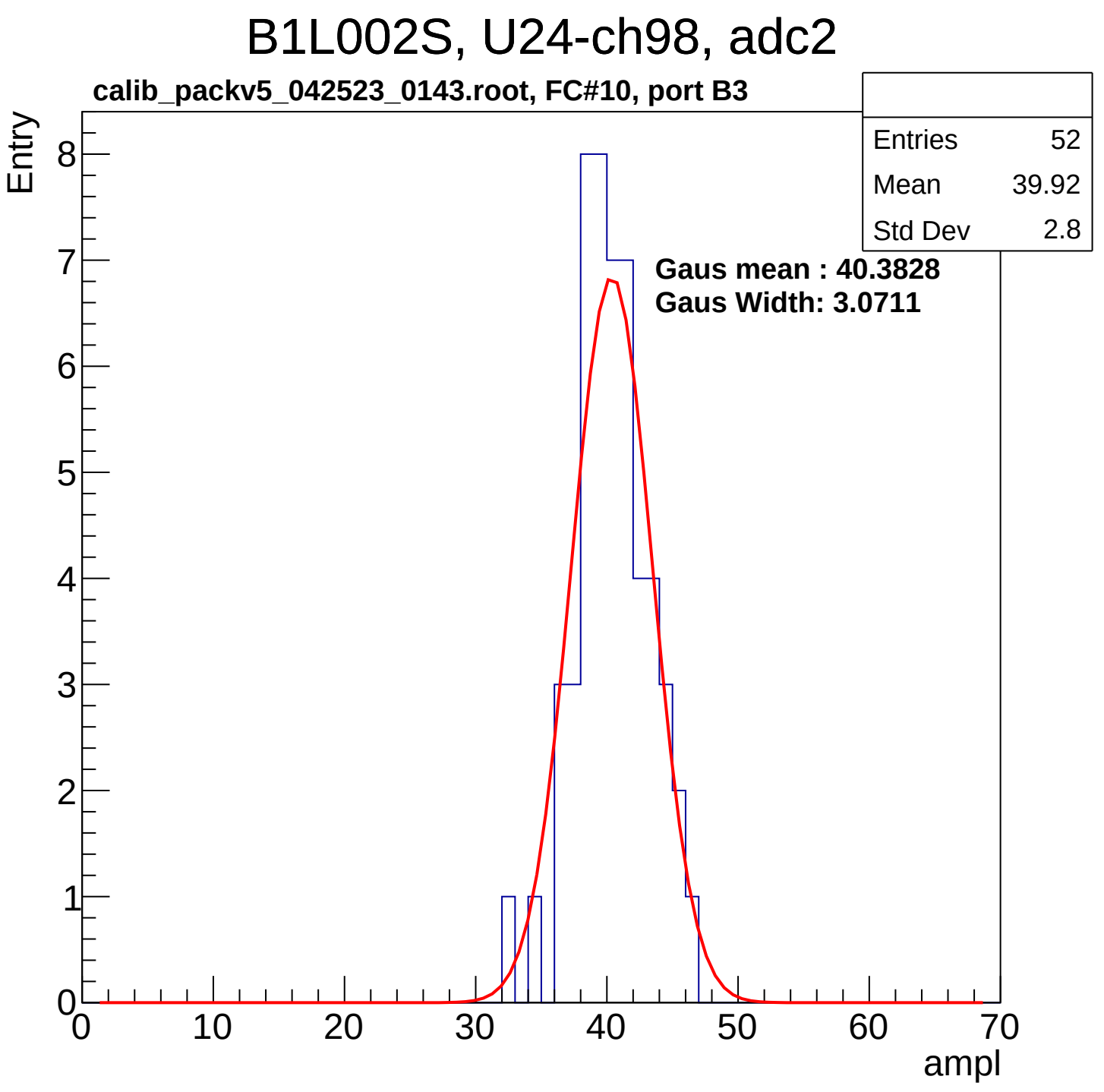

B1L002S, U24-ch97, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 59.3
Std Dev 2.376
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

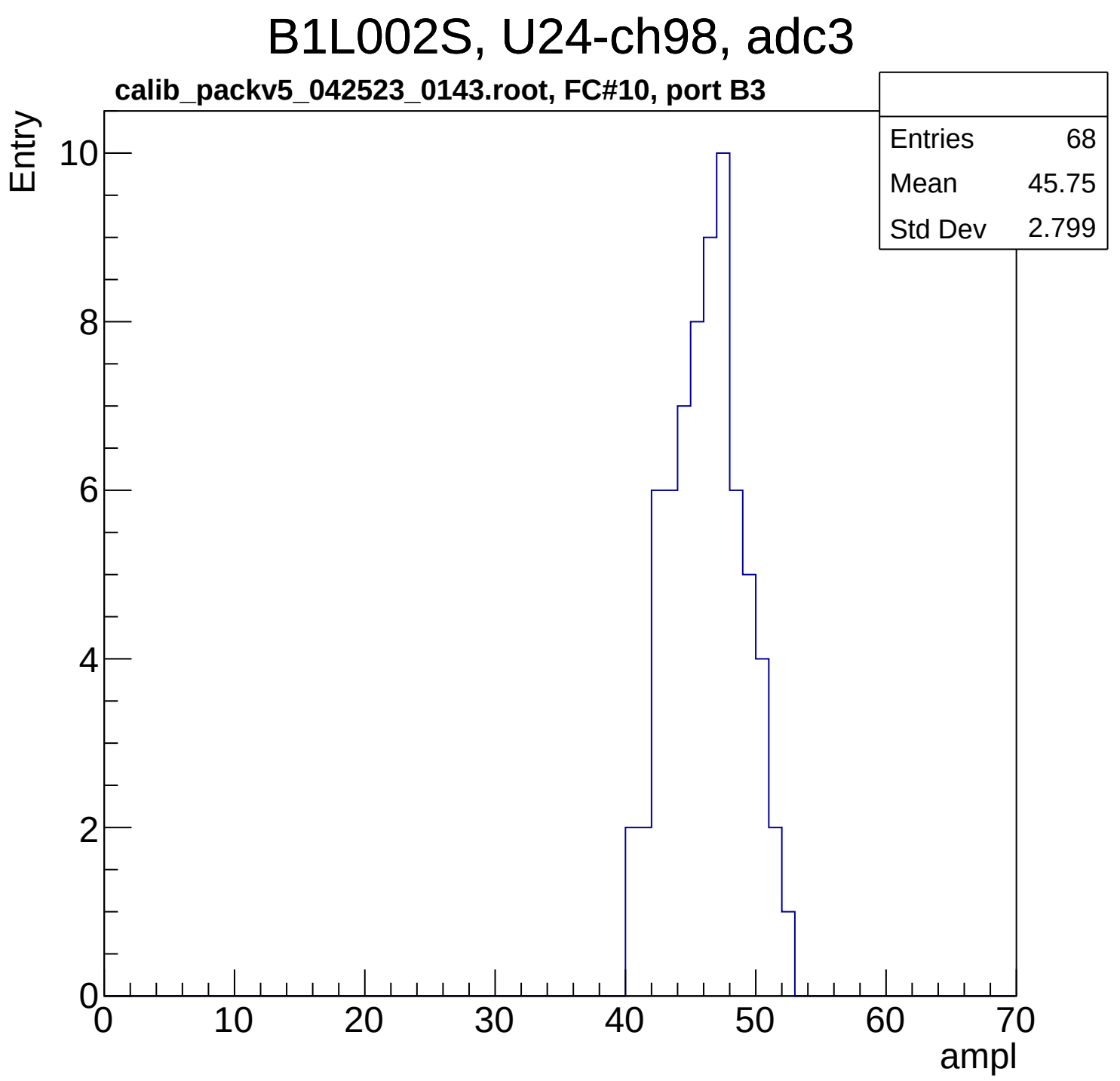

B1L002S, U24-ch97, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 56.55
Std Dev 17.91
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl

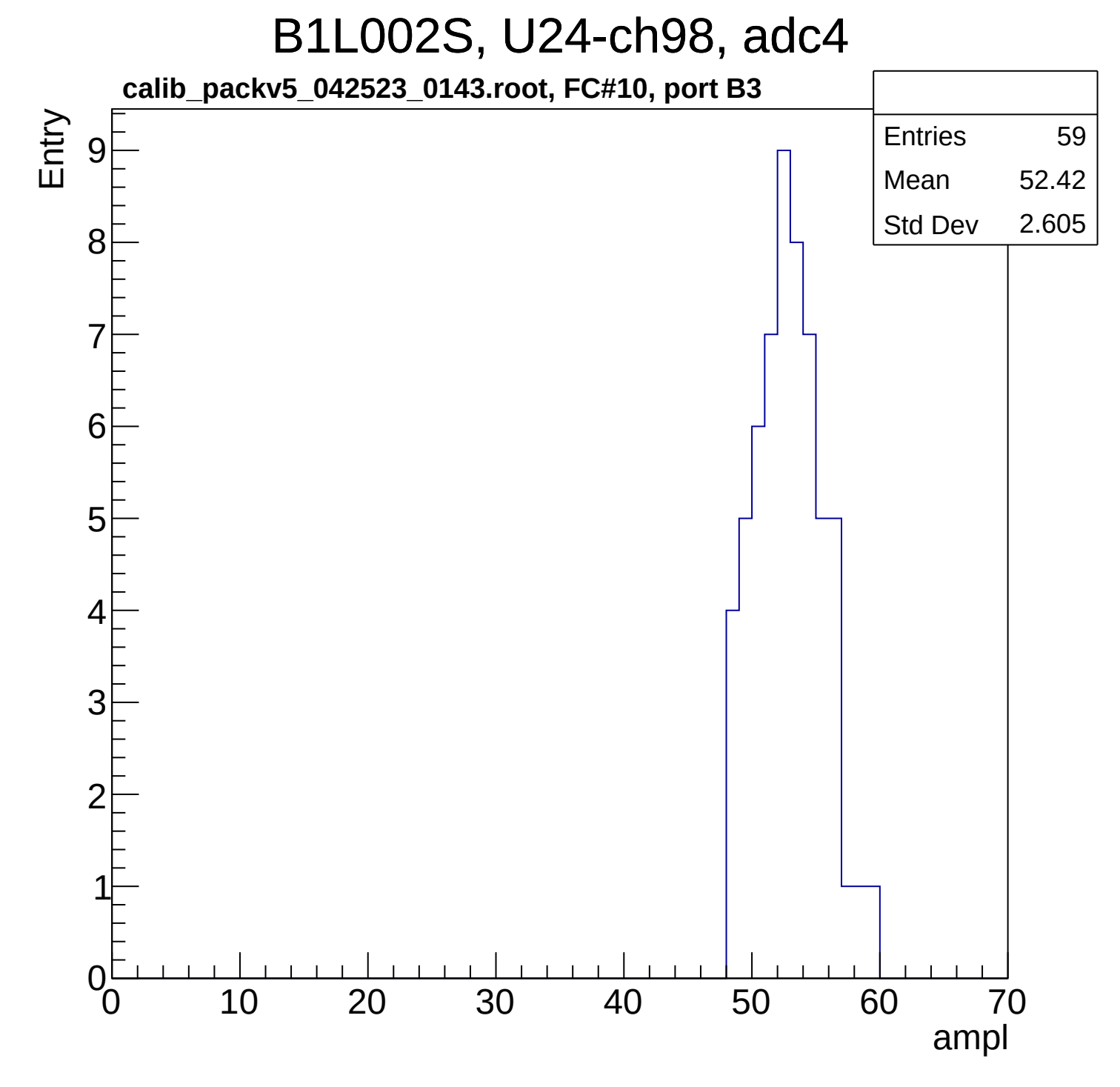

B1L002S, U24-ch97, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

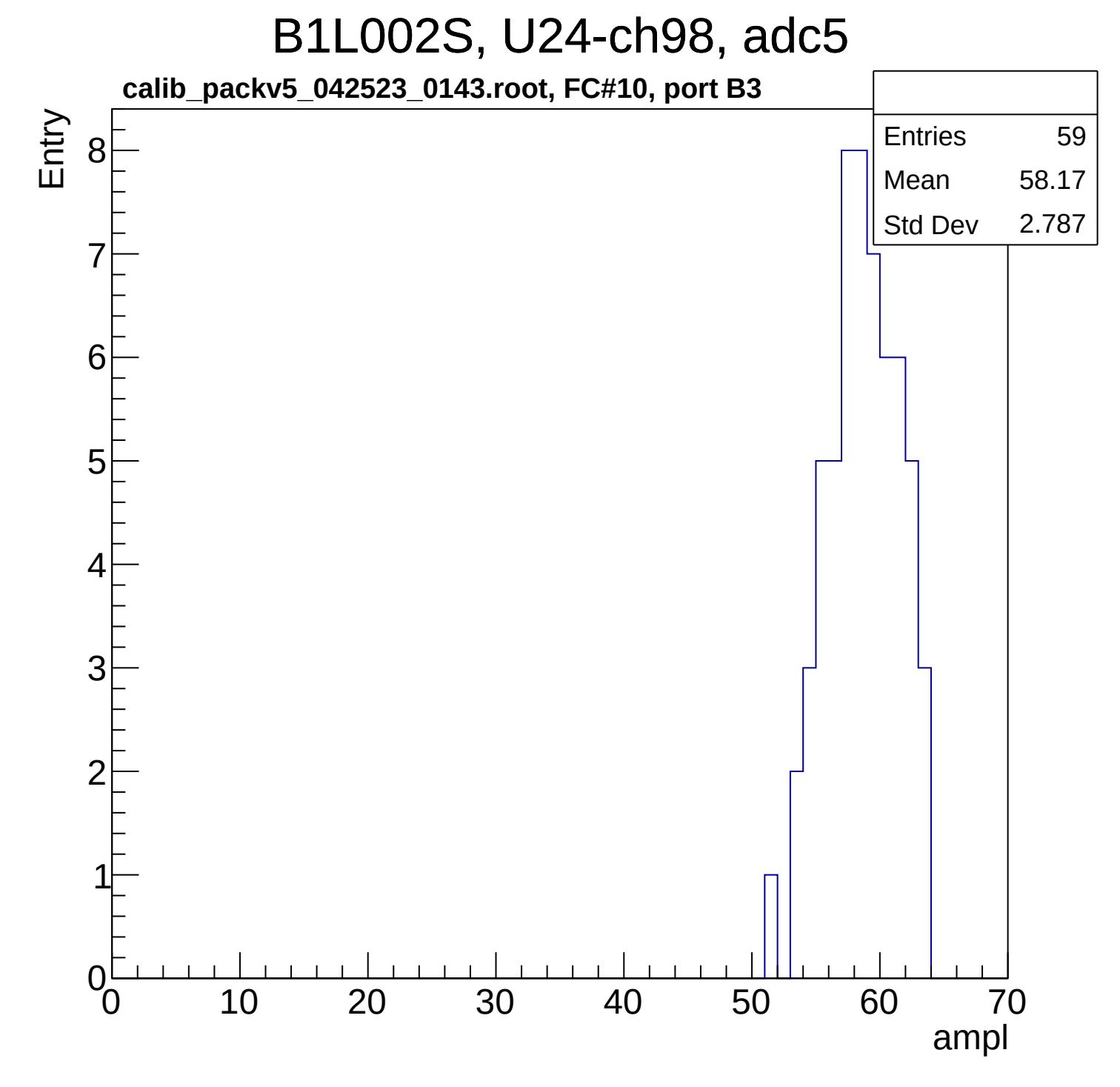

B1L002S, U24-ch98, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 27.14
Std Dev 2.871
Gaus mean : 27.9993
Gaus Width: 3.3023
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch98, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 33.45
Std Dev 3.097
Gaus mean : 34.0065
Gaus Width: 3.0417
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch98, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 39.92
Std Dev 2.8
Gaus mean : 40.3828
Gaus Width: 3.0711
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch98, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 45.75
Std Dev 2.799
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch98, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 52.42
Std Dev 2.605
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch98, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 58.17
Std Dev 2.787
Entry
ampl
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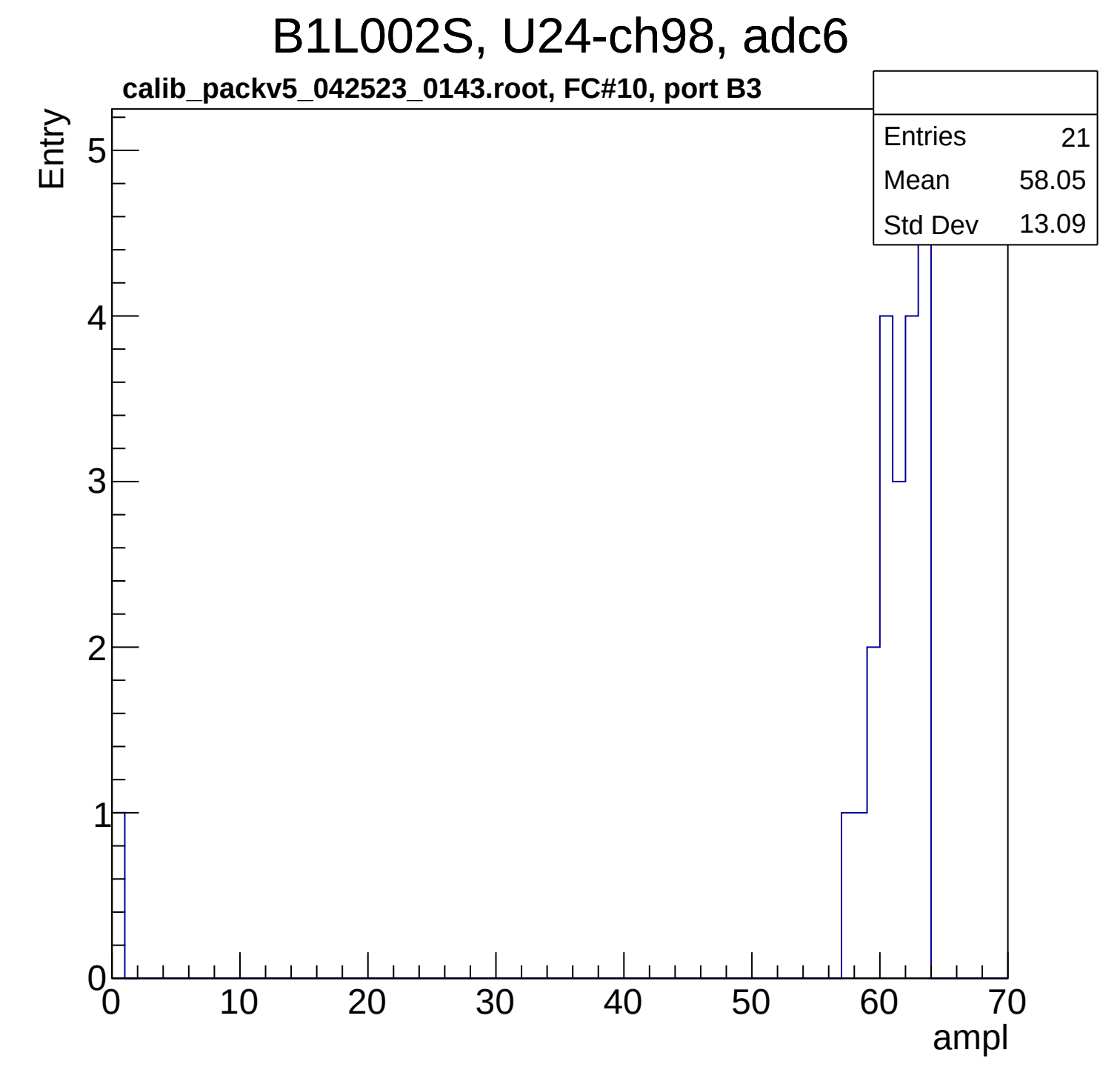
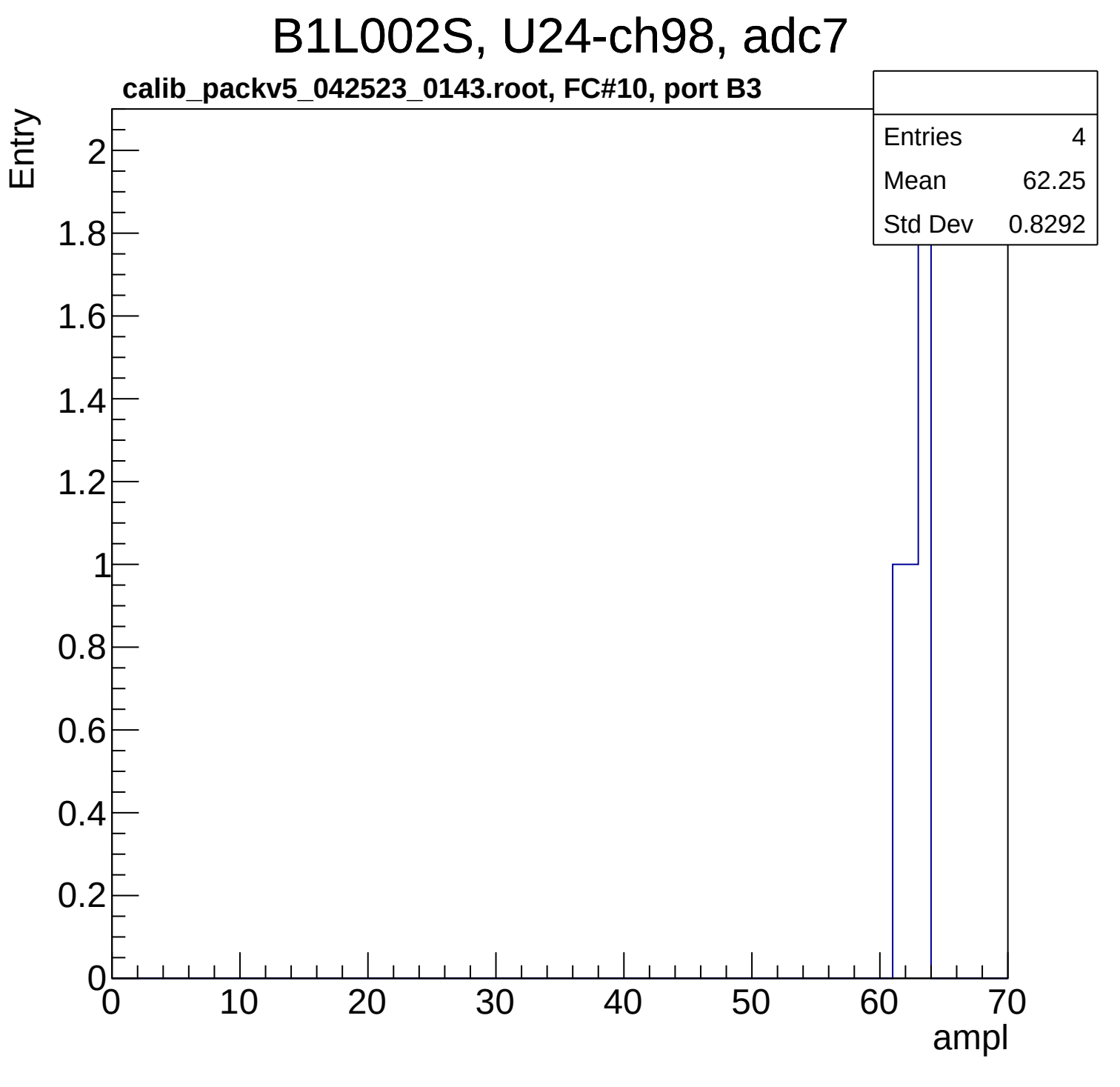
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch98, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 58.05
Std Dev 13.09
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch98, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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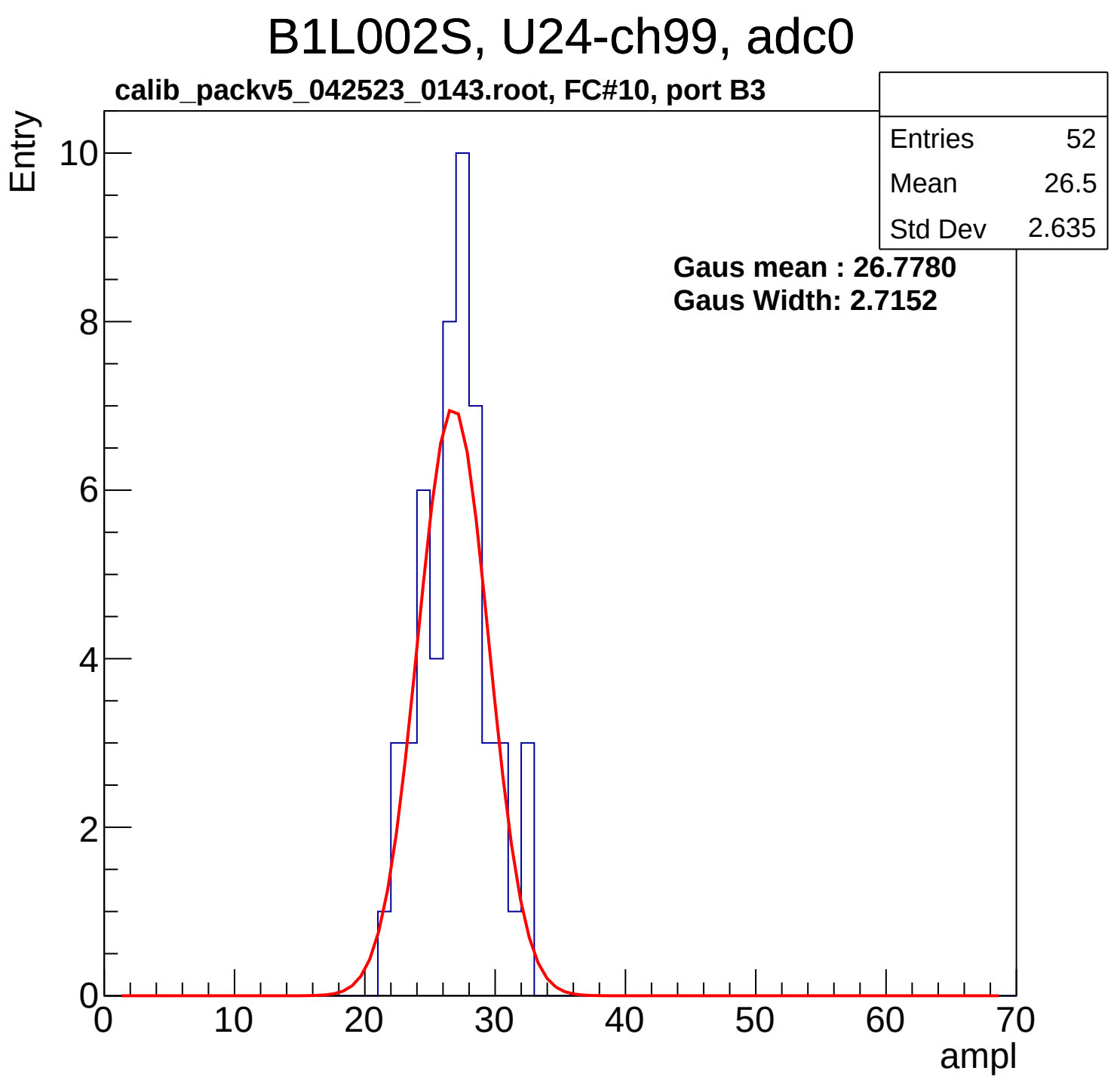
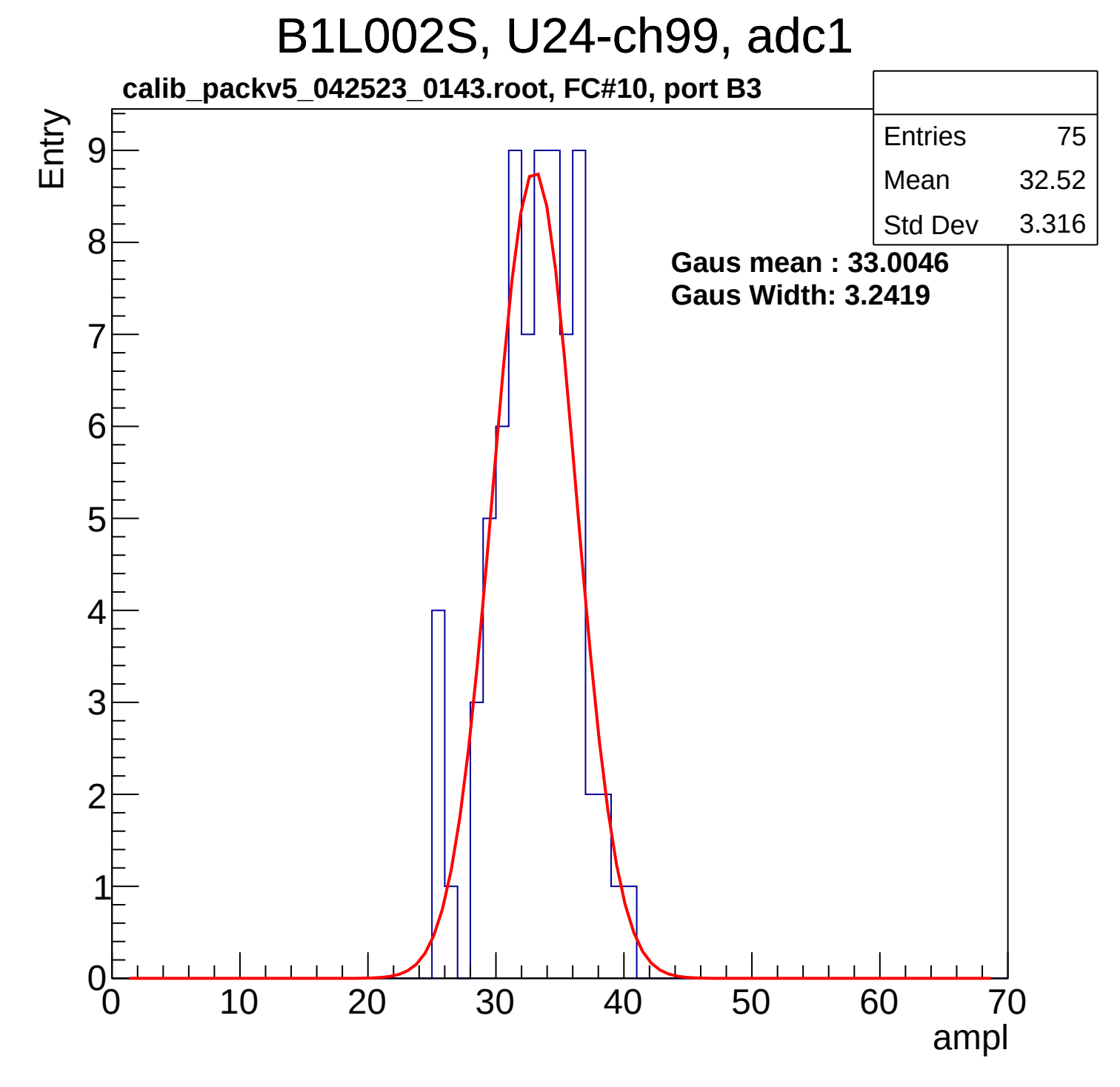
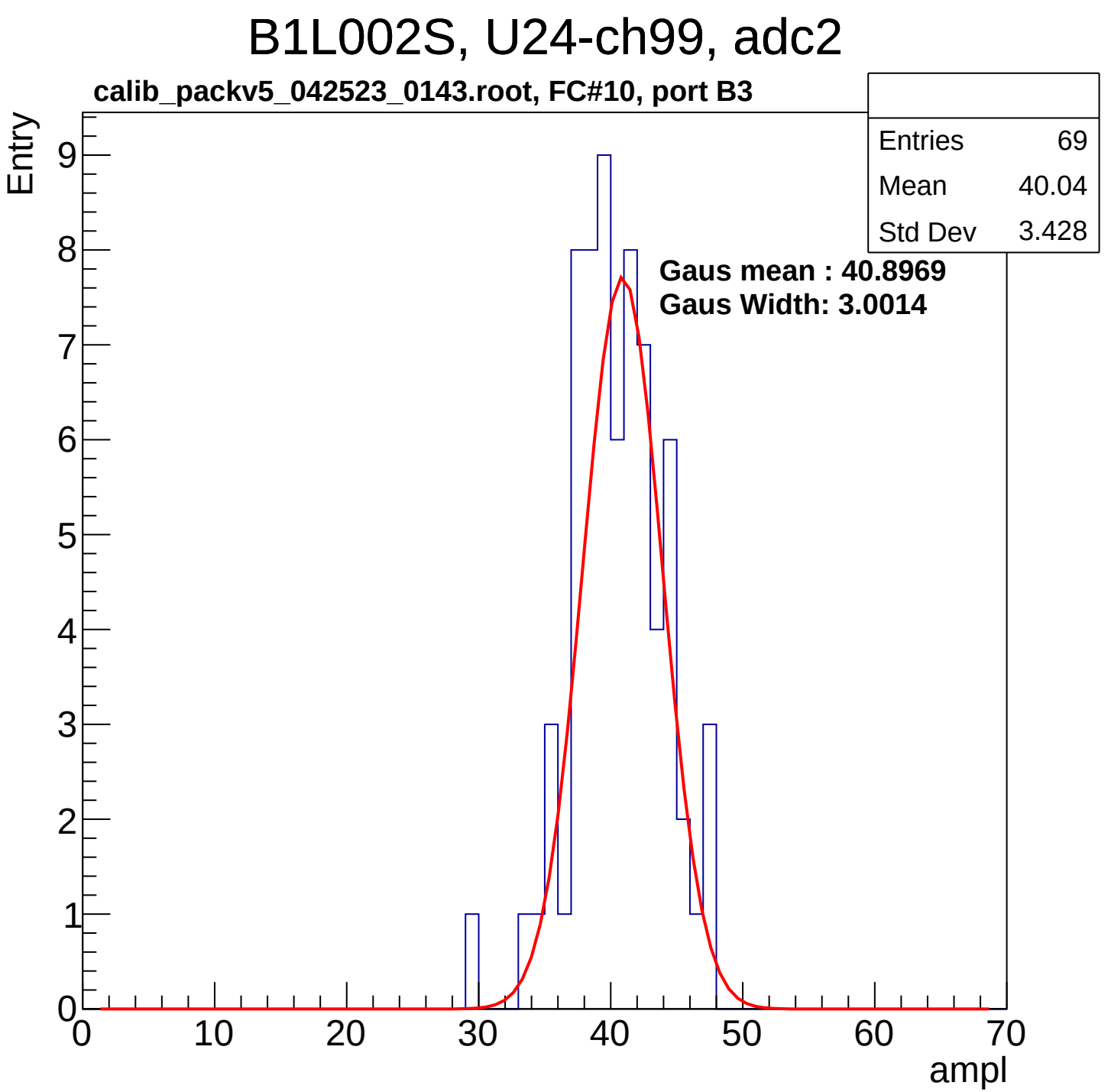
20
30
40
50
60
70


B1L002S, U24-ch99, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 26.5
Std Dev 2.635
Gaus mean : 26.7780
Gaus Width: 2.7152
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch99, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 32.52
Std Dev 3.316
Gaus mean : 33.0046
Gaus Width: 3.2419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch99, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 40.04
Std Dev 3.428
Gaus mean : 40.8969
Gaus Width: 3.0014
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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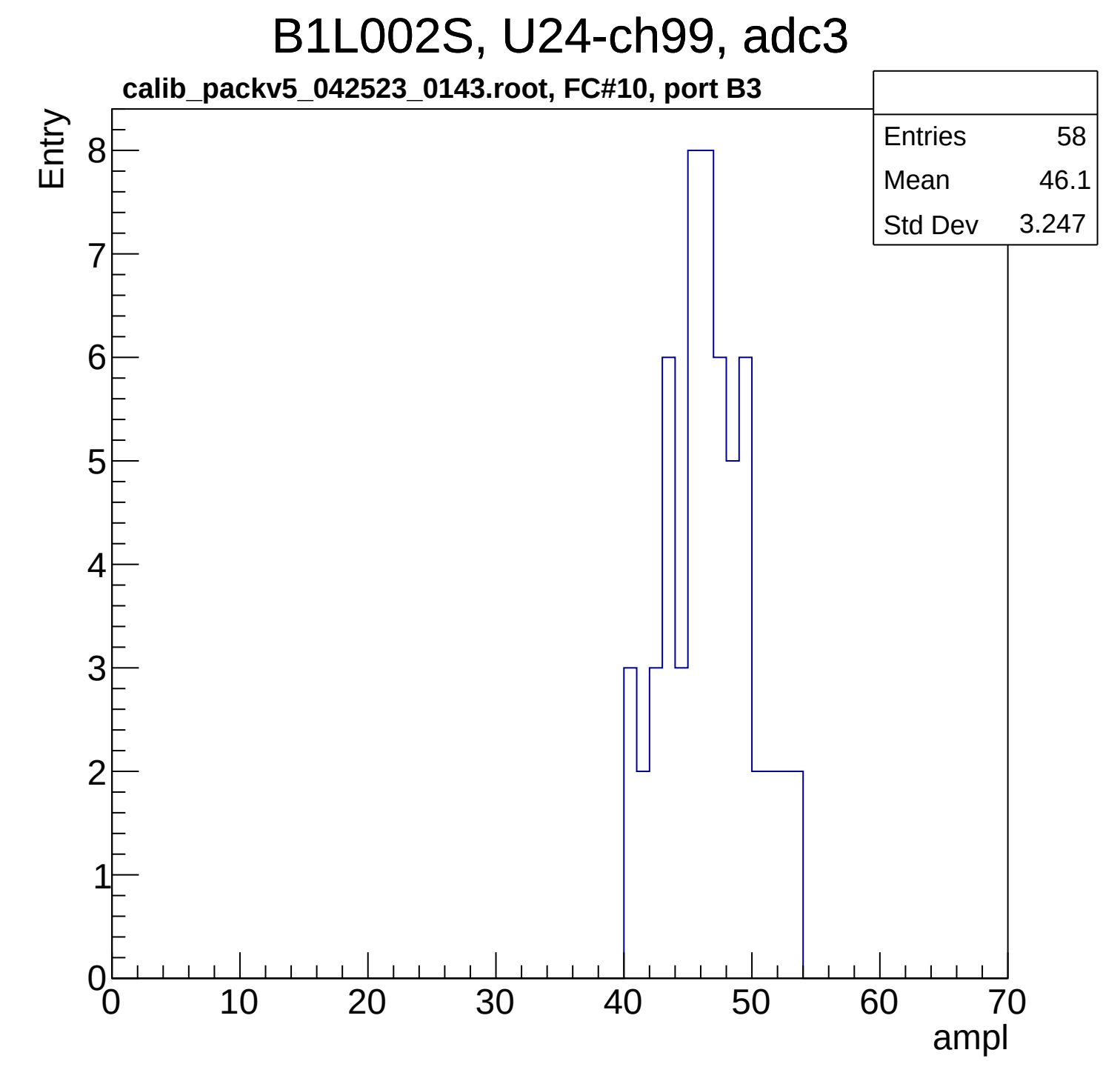
60
70


B1L002S, U24-ch99, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 46.1
Std Dev 3.247
Entry
ampl
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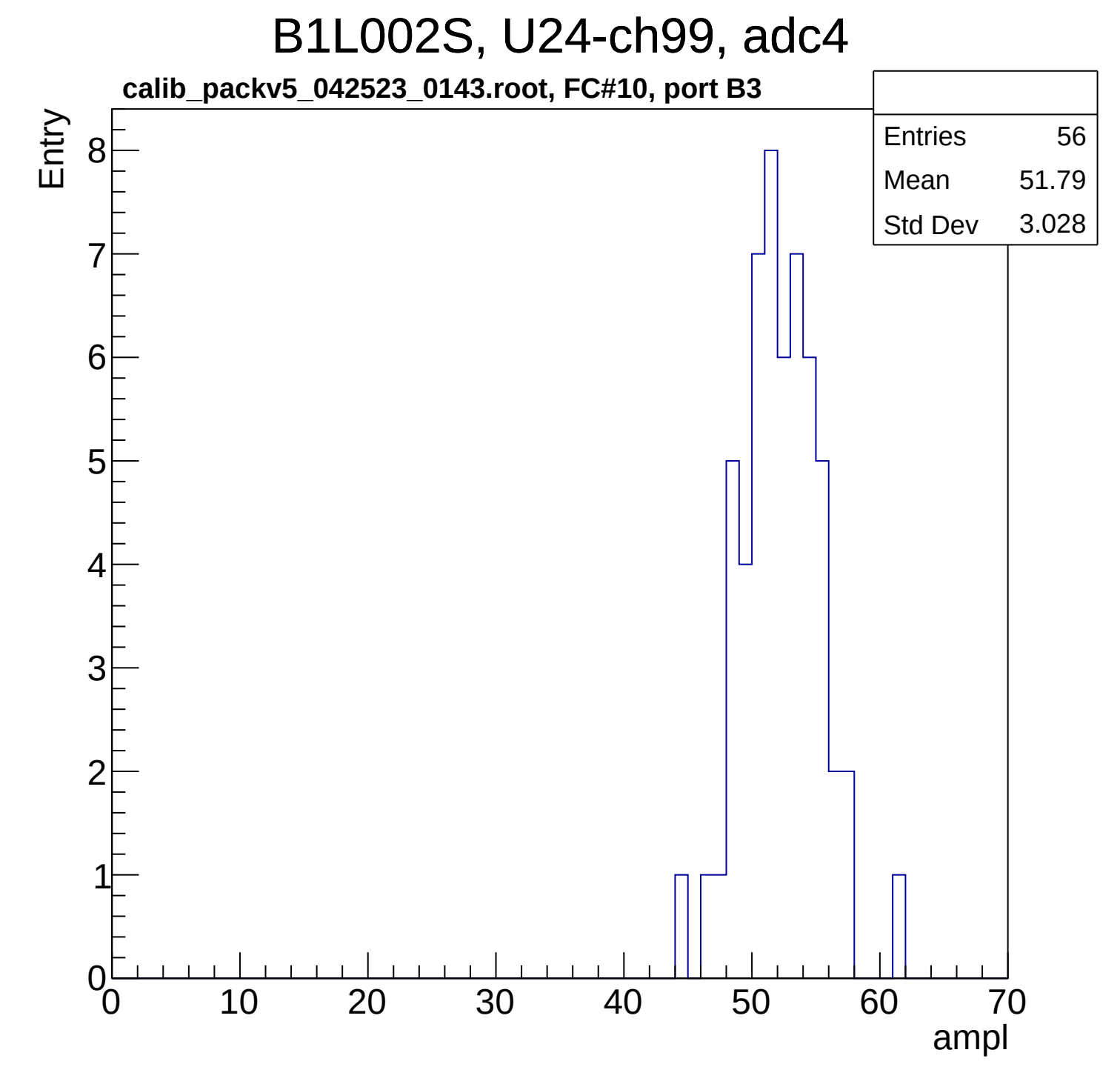
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch99, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 51.79
Std Dev 3.028
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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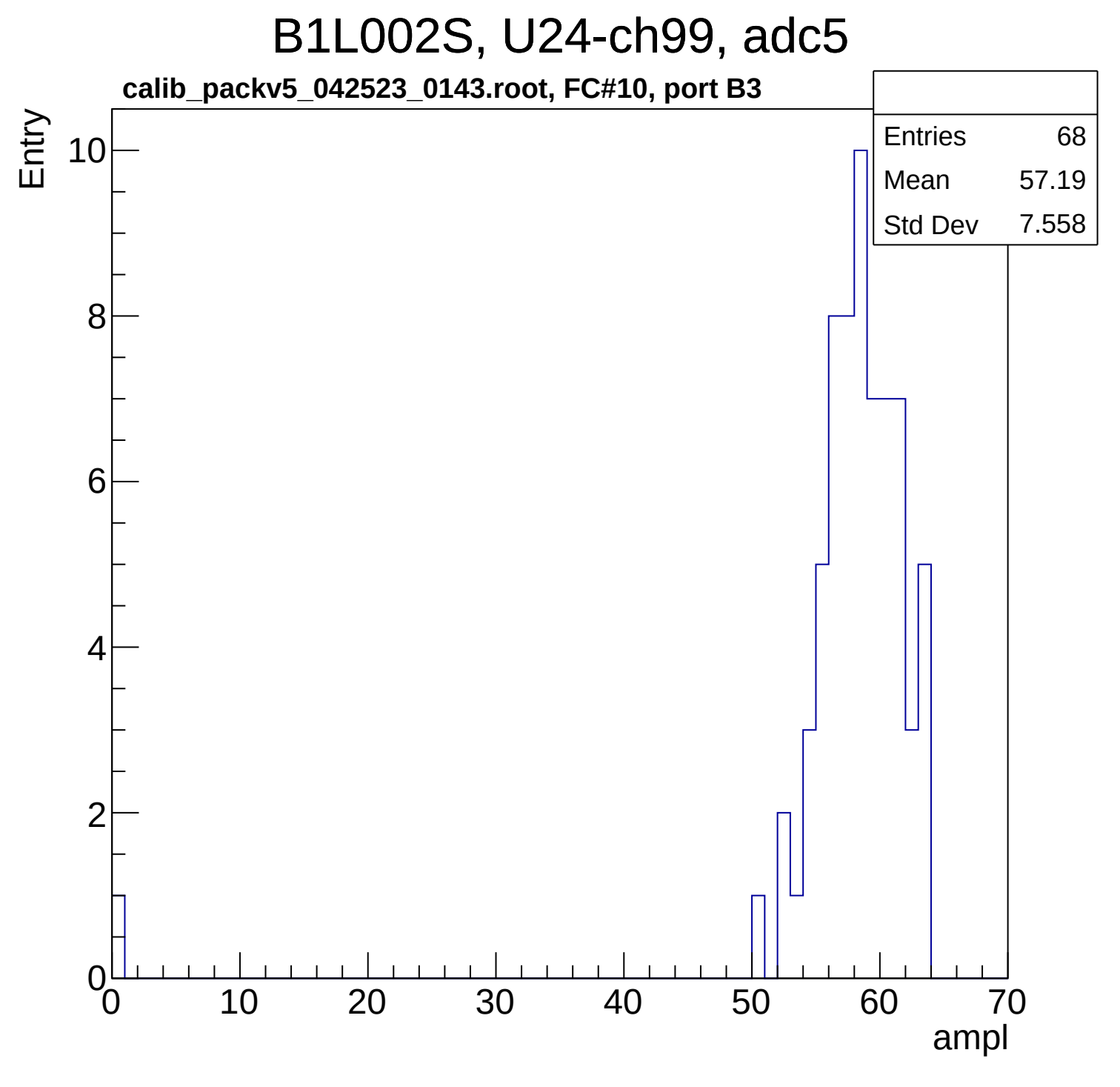
B1L002S, U24-ch99, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 57.19
Std Dev 7.558
Entry
ampl
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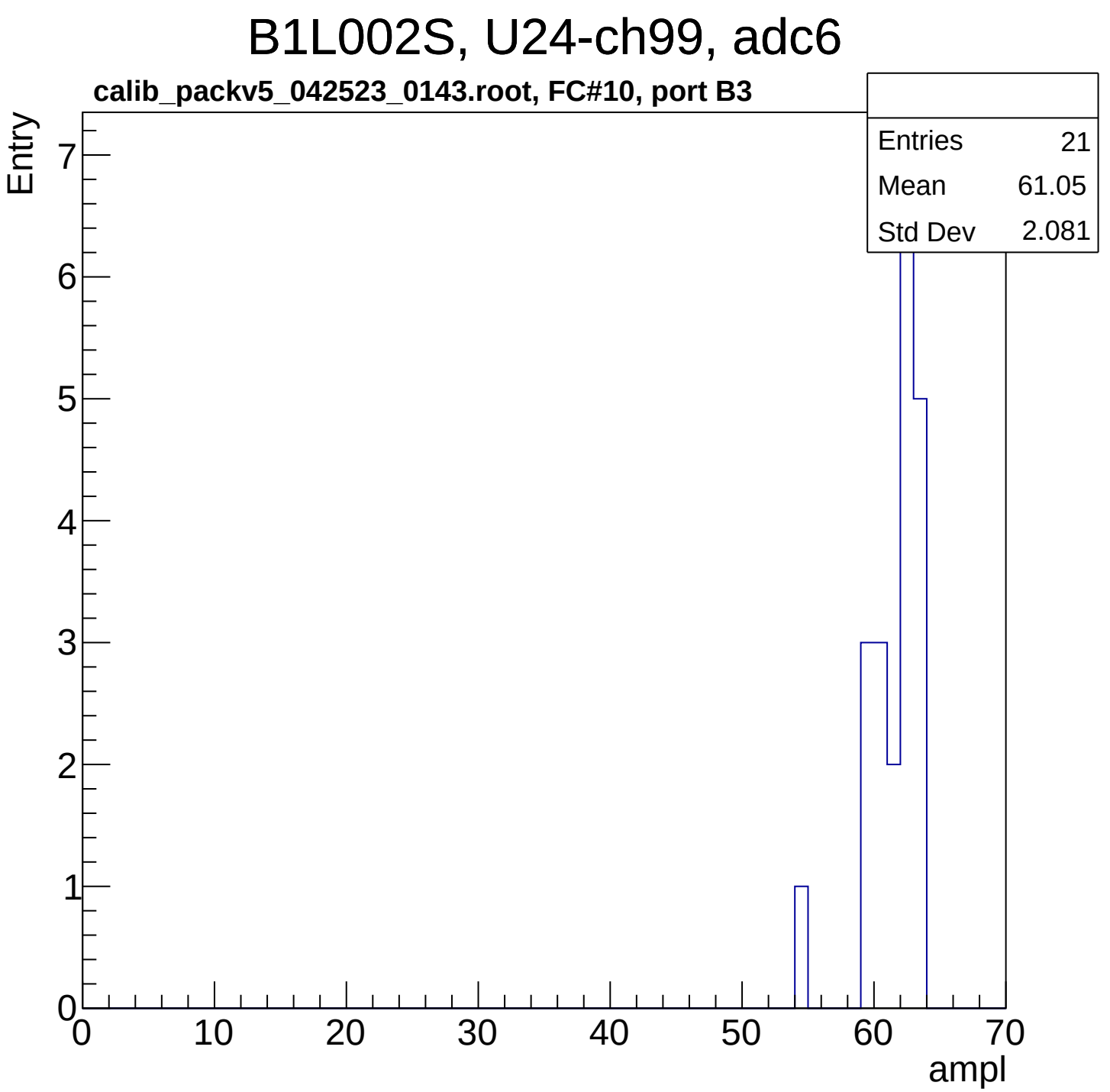
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch99, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 21
Mean 61.05
Std Dev 2.081
Entry
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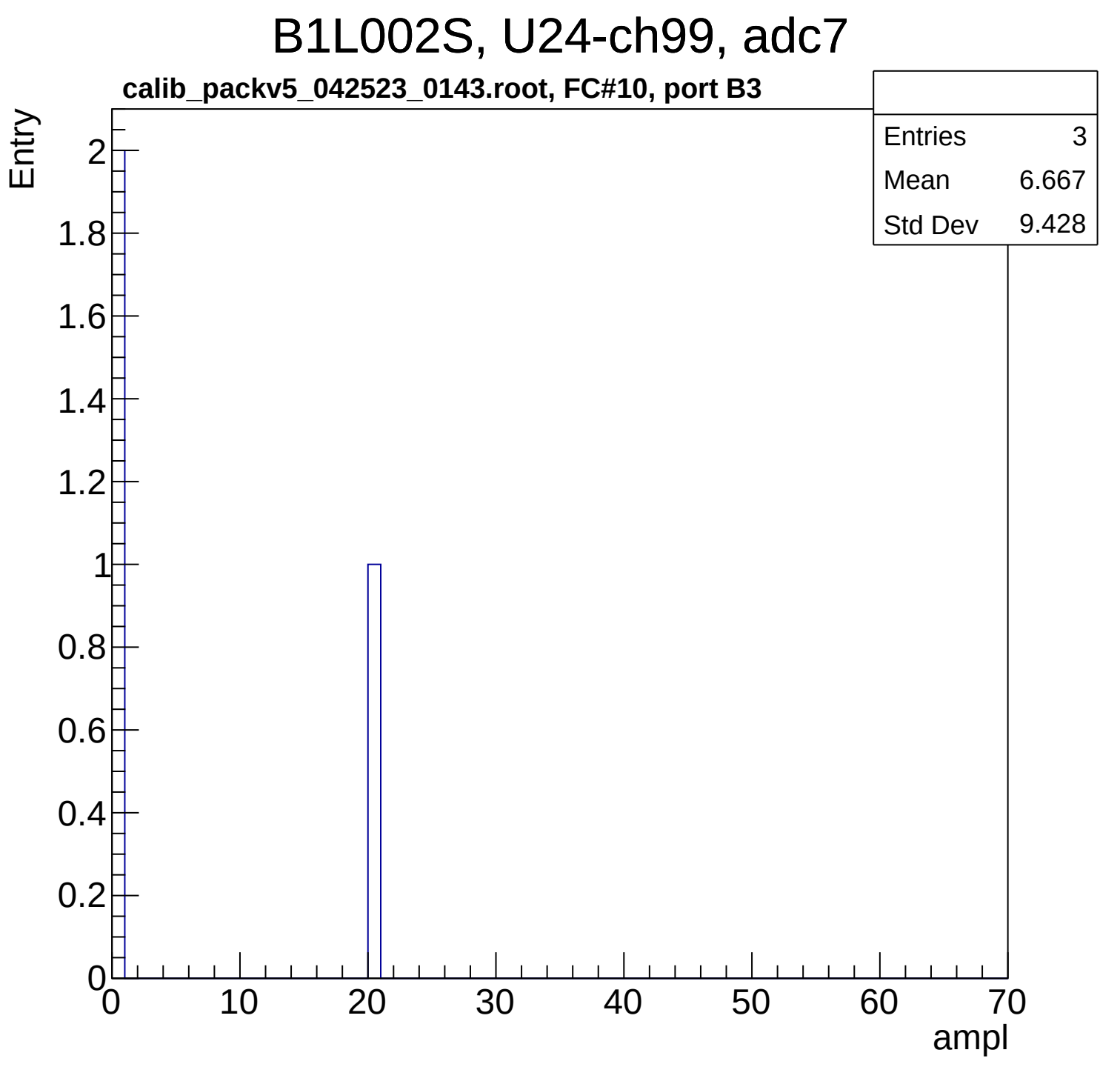
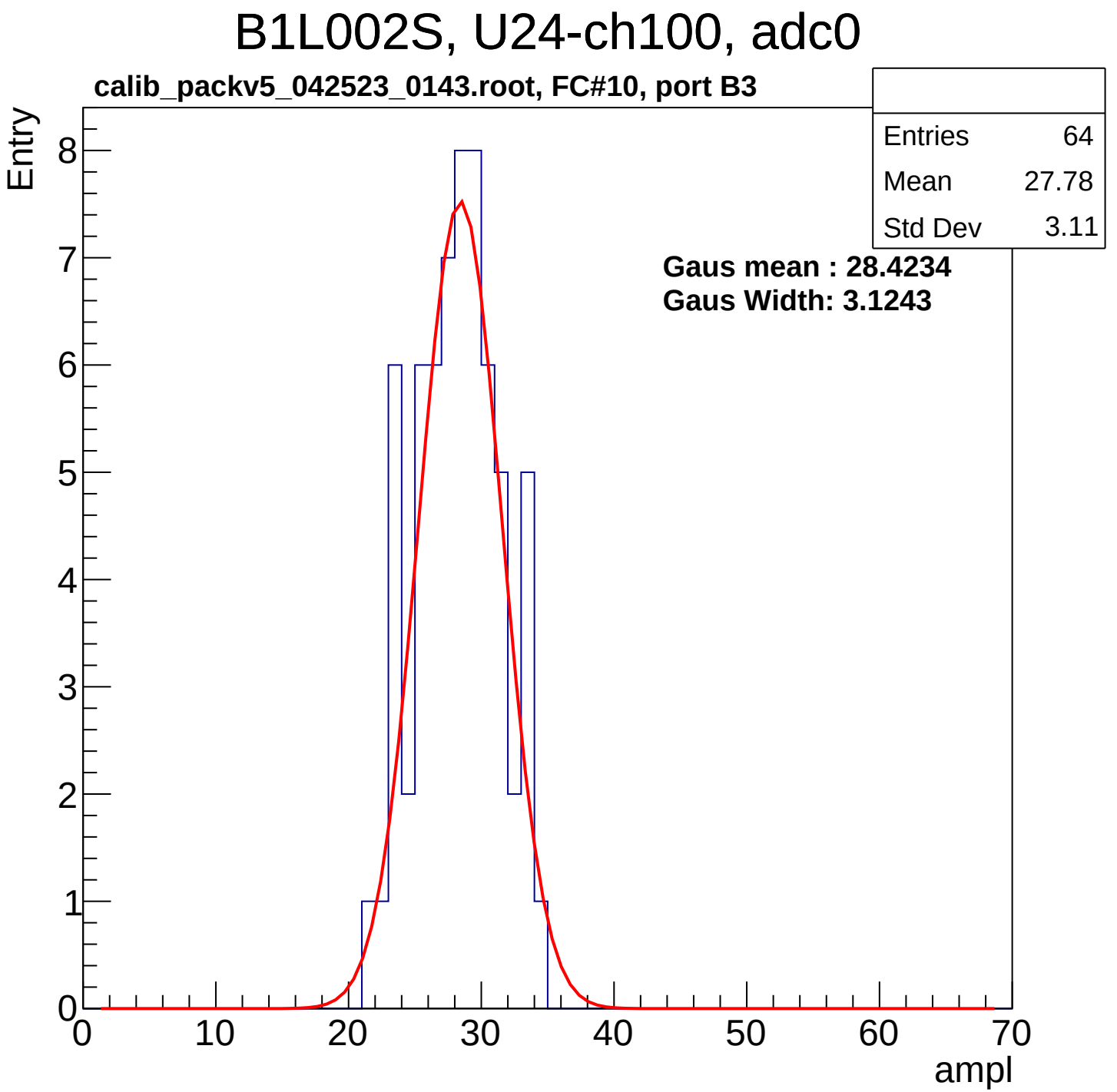
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch99, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 3
Mean 6.667
Std Dev 9.428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch100, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 27.78
Std Dev 3.11
Gaus mean : 28.4234
Gaus Width: 3.1243
Entry
ampl
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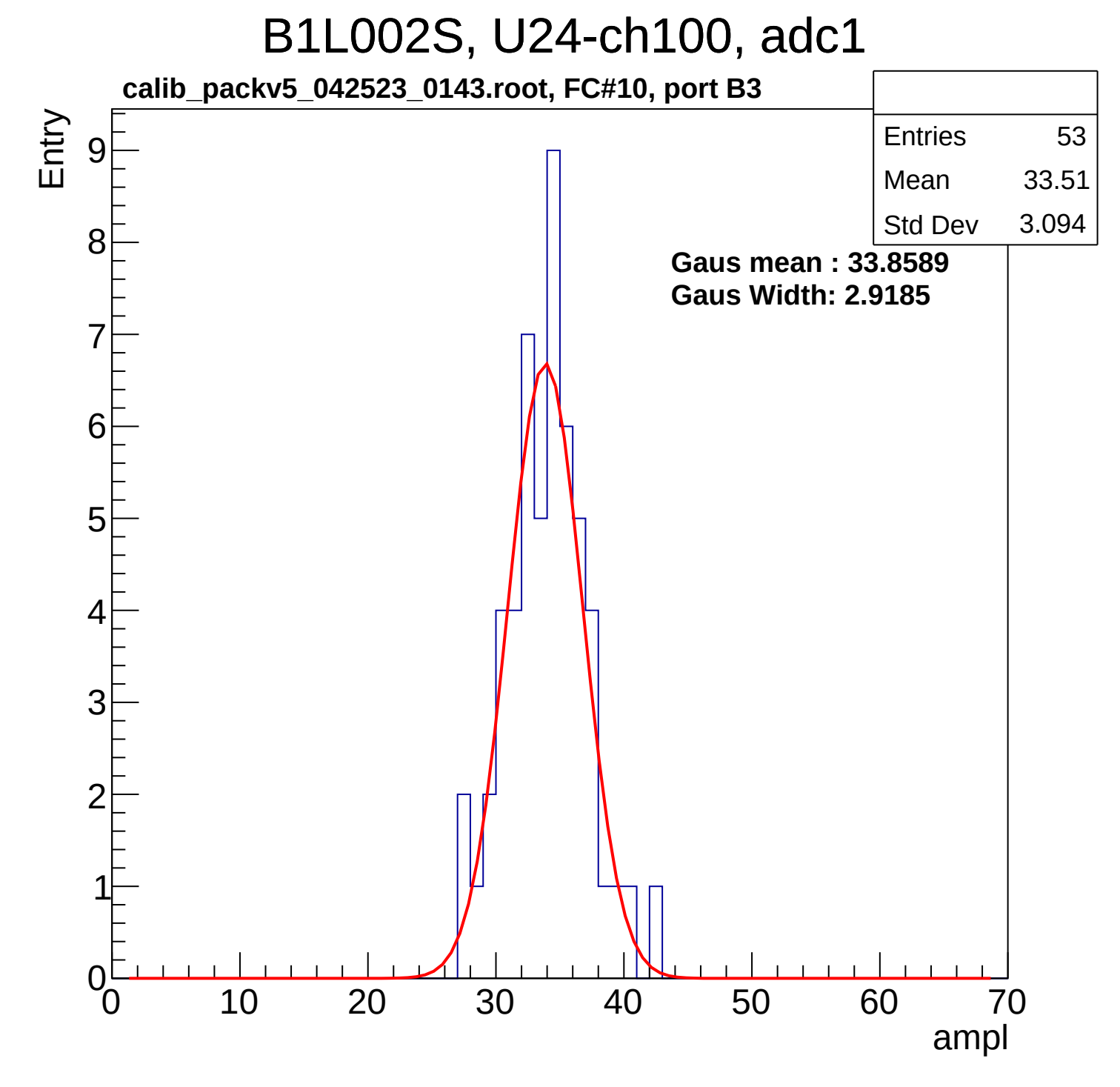
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch100, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 33.51
Std Dev 3.094
Gaus mean : 33.8589
Gaus Width: 2.9185
Entry
ampl
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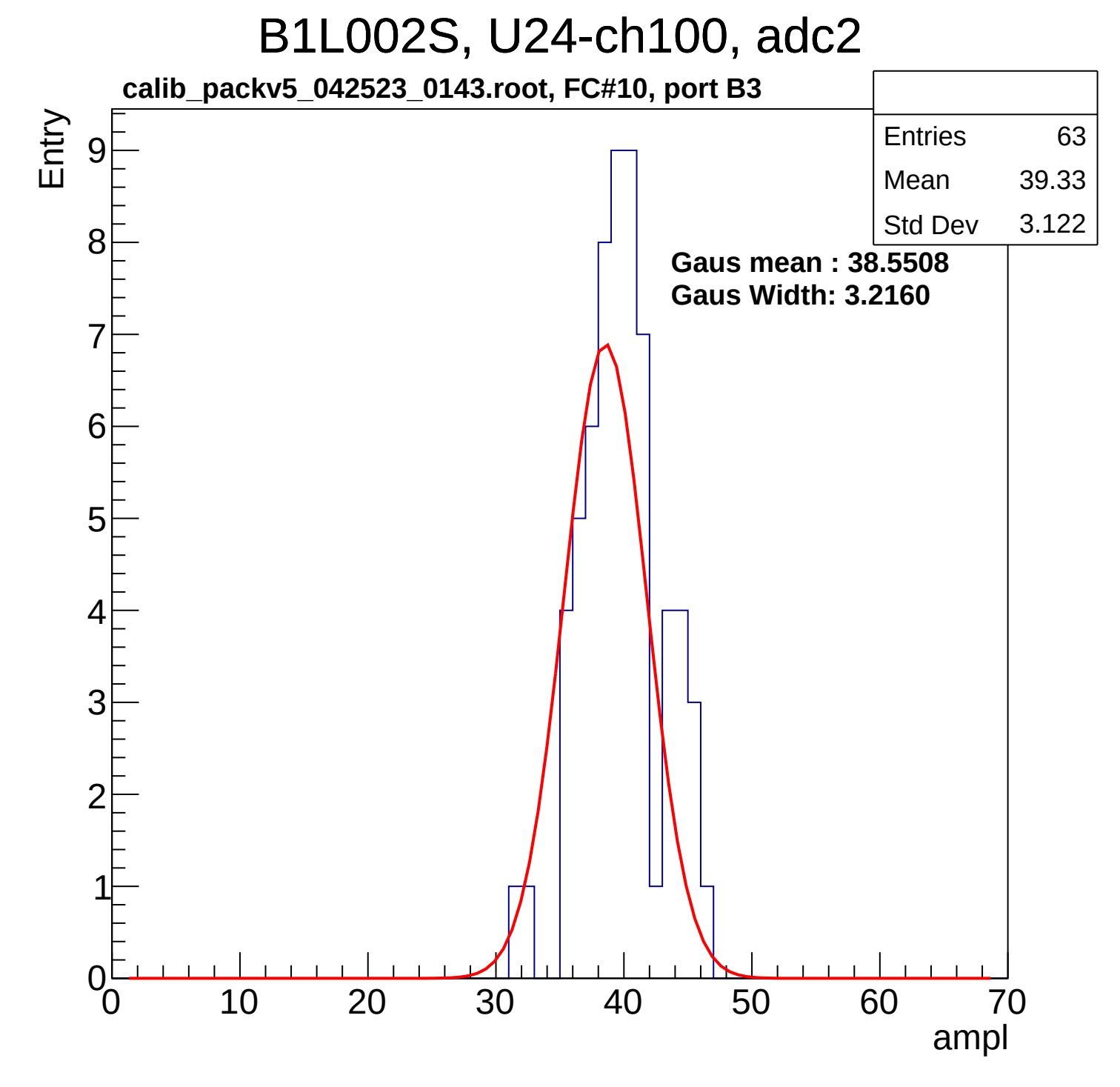
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch100, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 39.33
Std Dev 3.122
Gaus mean : 38.5508
Gaus Width: 3.2160
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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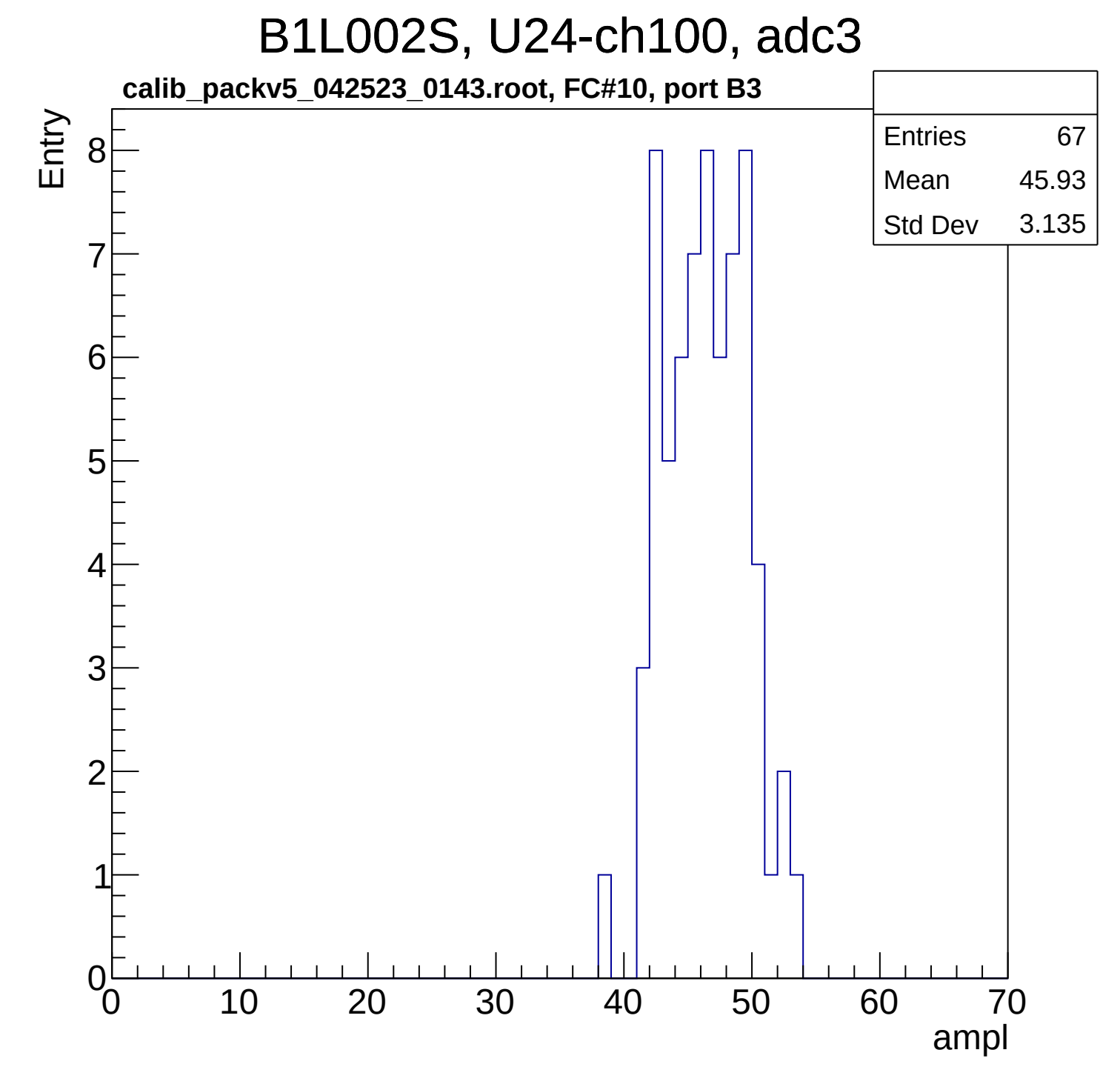
40
50
60
70


B1L002S, U24-ch100, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 45.93
Std Dev 3.135
Entry
ampl
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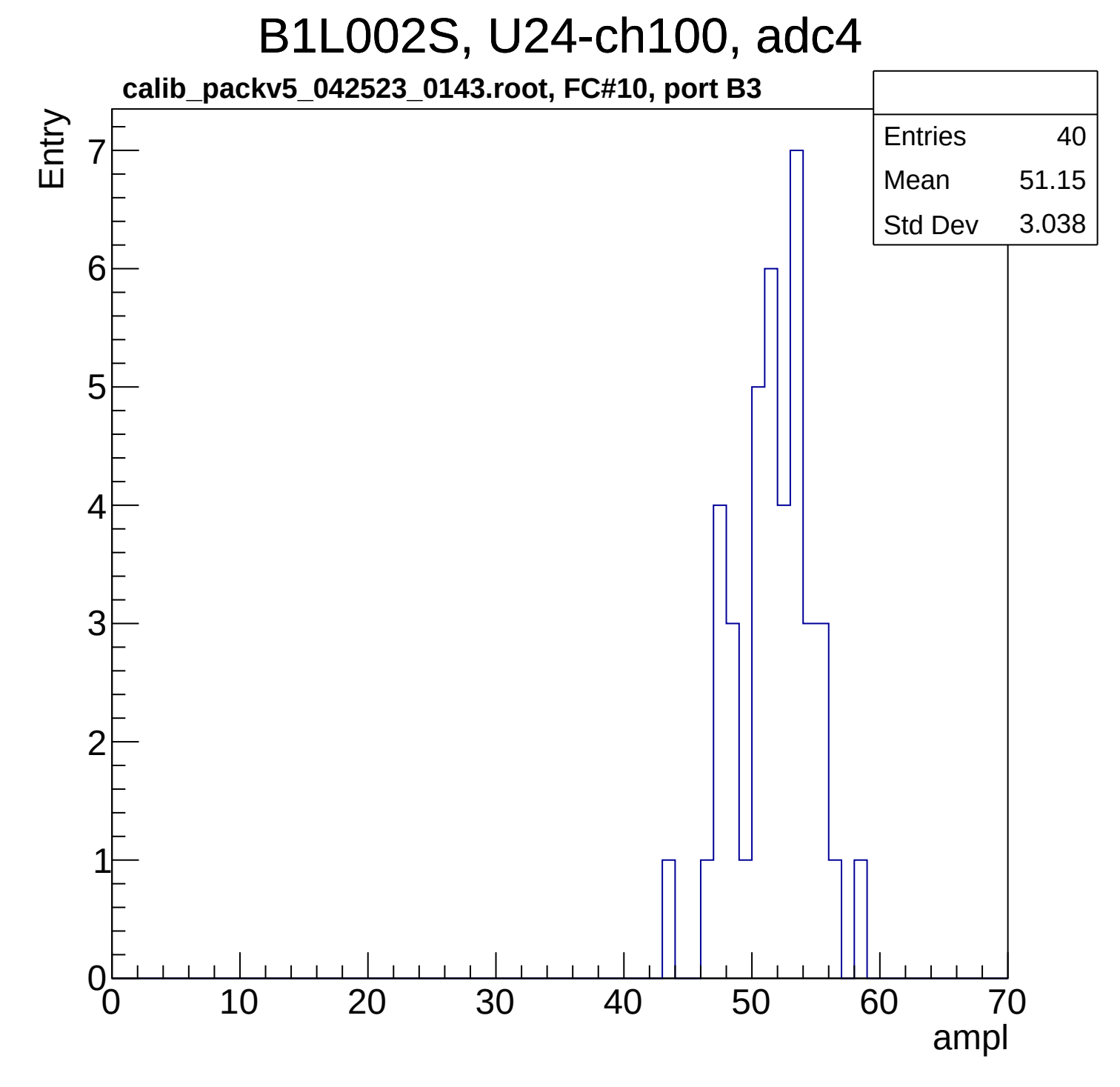
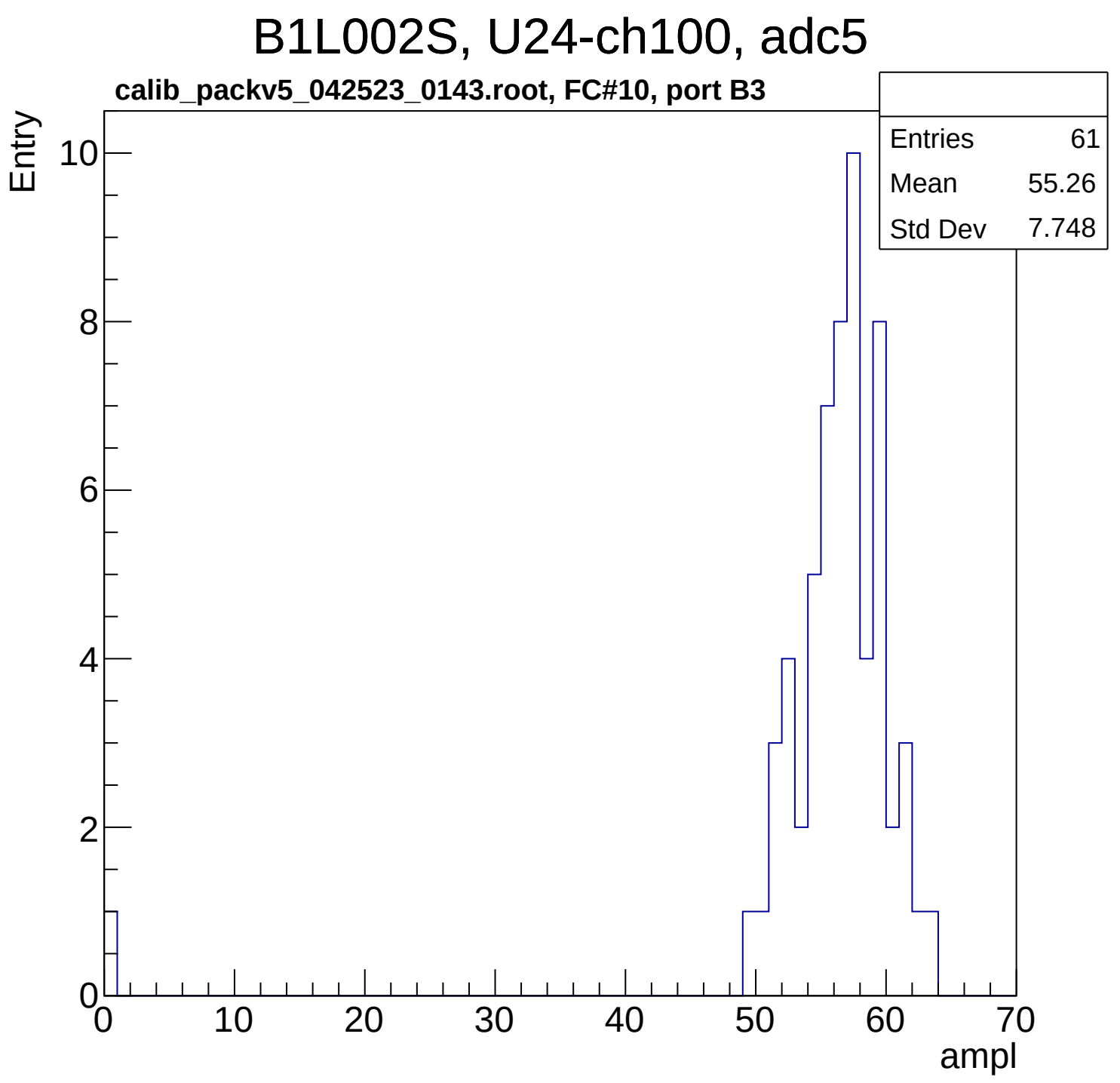
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch100, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 51.15
Std Dev 3.038
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch100, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 55.26
Std Dev 7.748
Entry
0
2
4
6
8
10
0
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20
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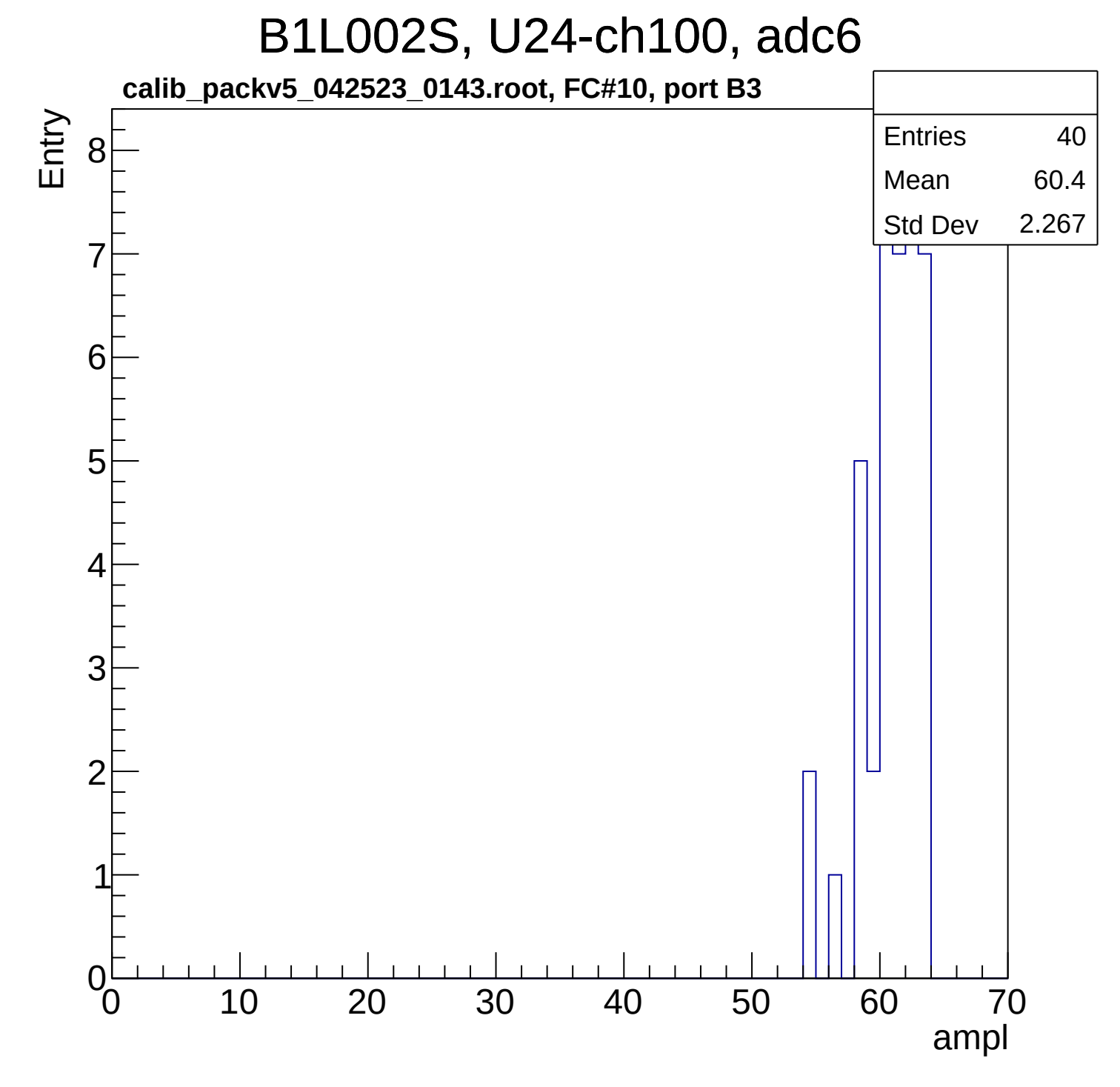
60
70
ampl


B1L002S, U24-ch100, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 40
Mean 60.4
Std Dev 2.267
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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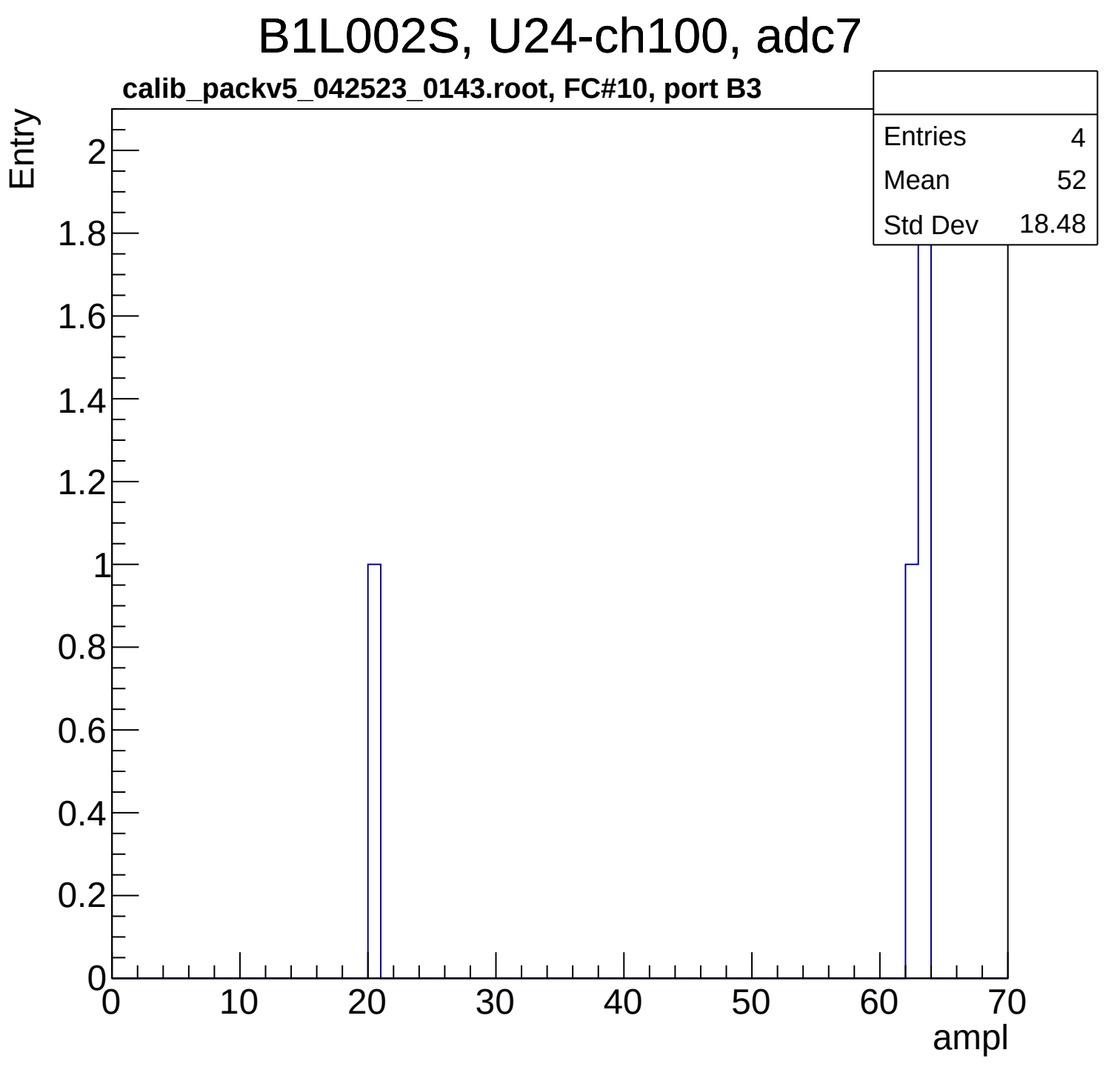
B1L002S, U24-ch100, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 52
Std Dev 18.48
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
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60
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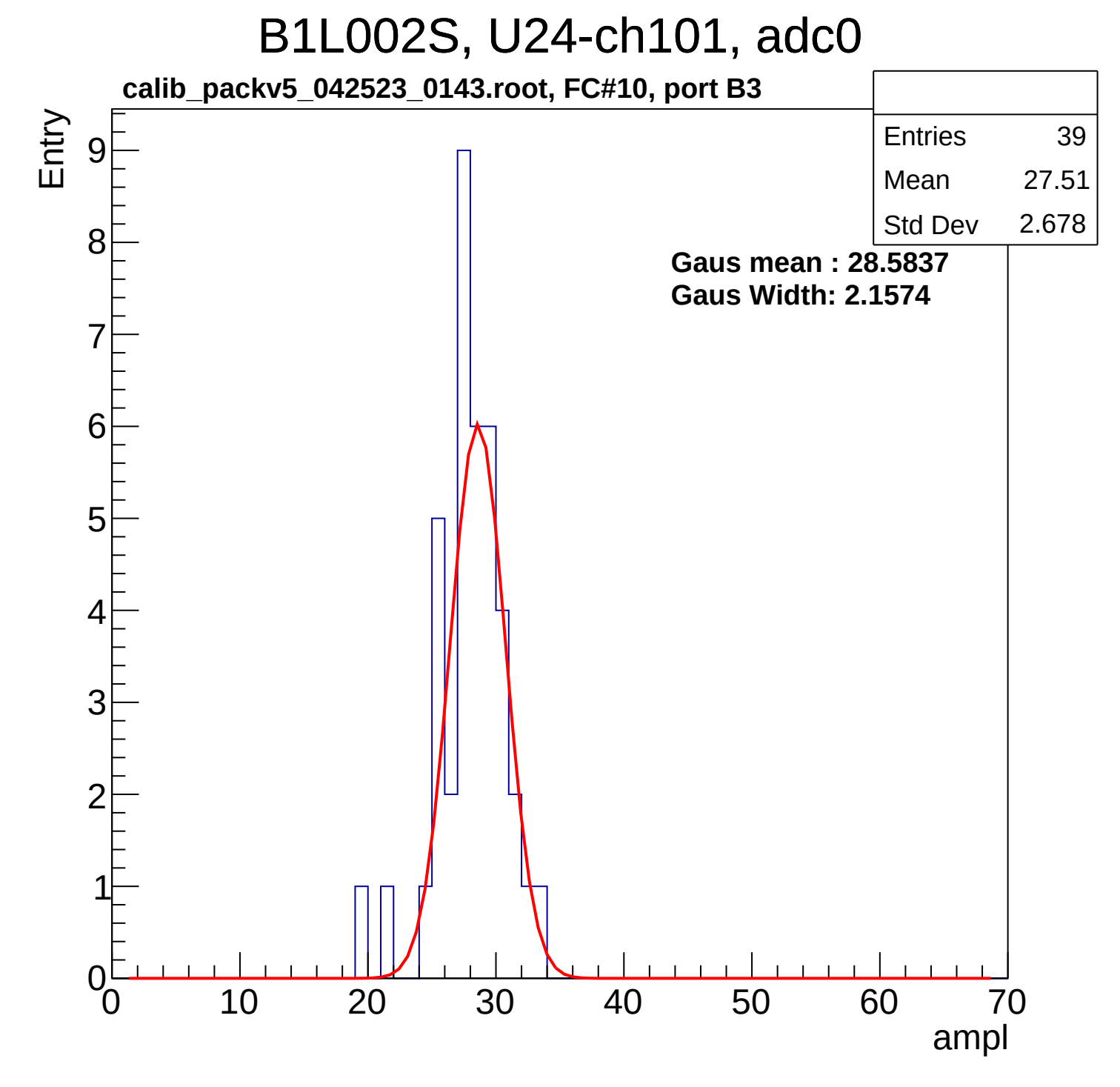
B1L002S, U24-ch101, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 27.51
Std Dev 2.678
Gaus mean : 28.5837
Gaus Width: 2.1574
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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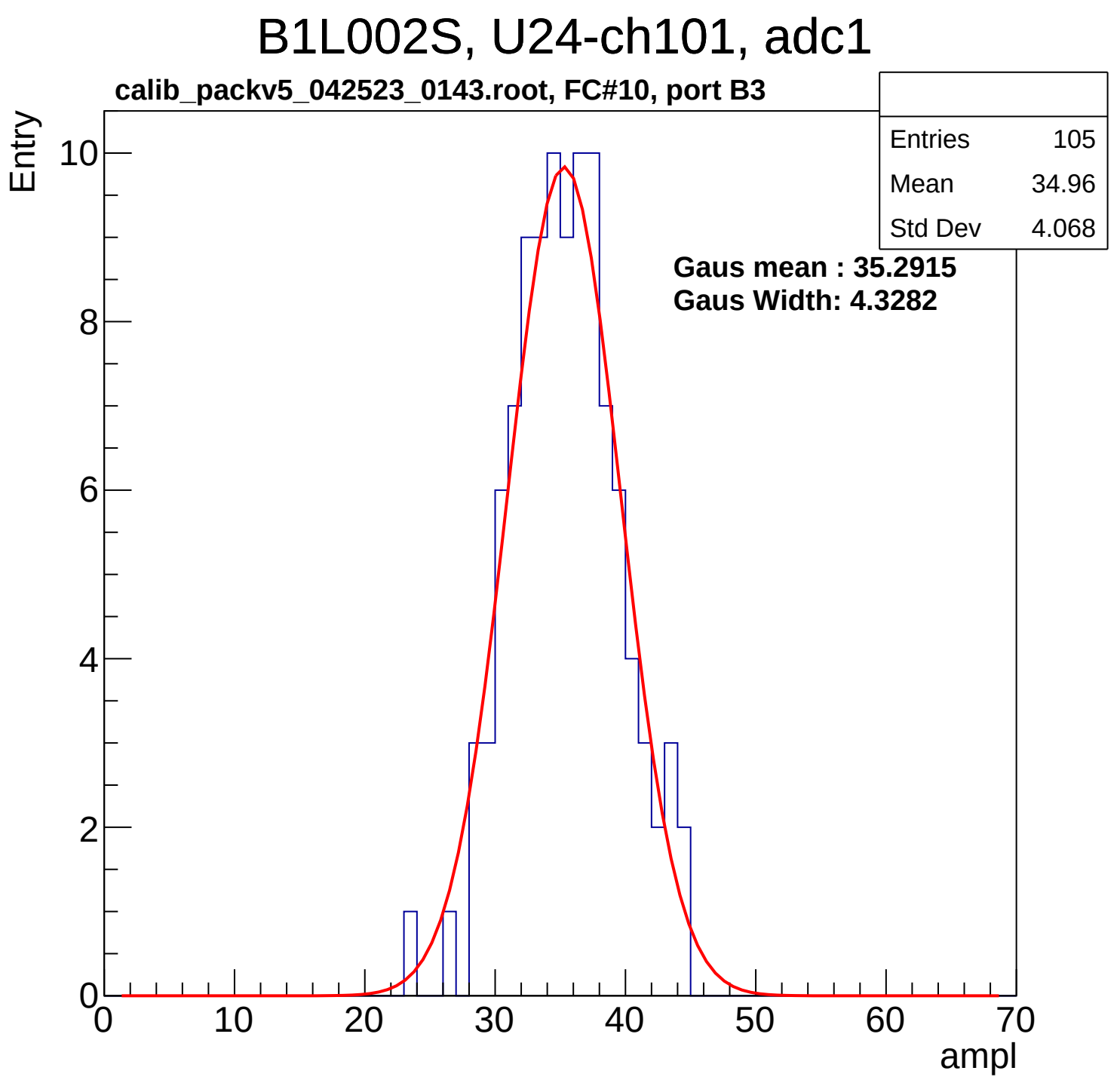
B1L002S, U24-ch101, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 105
Mean 34.96
Std Dev 4.068
Gaus mean : 35.2915
Gaus Width: 4.3282
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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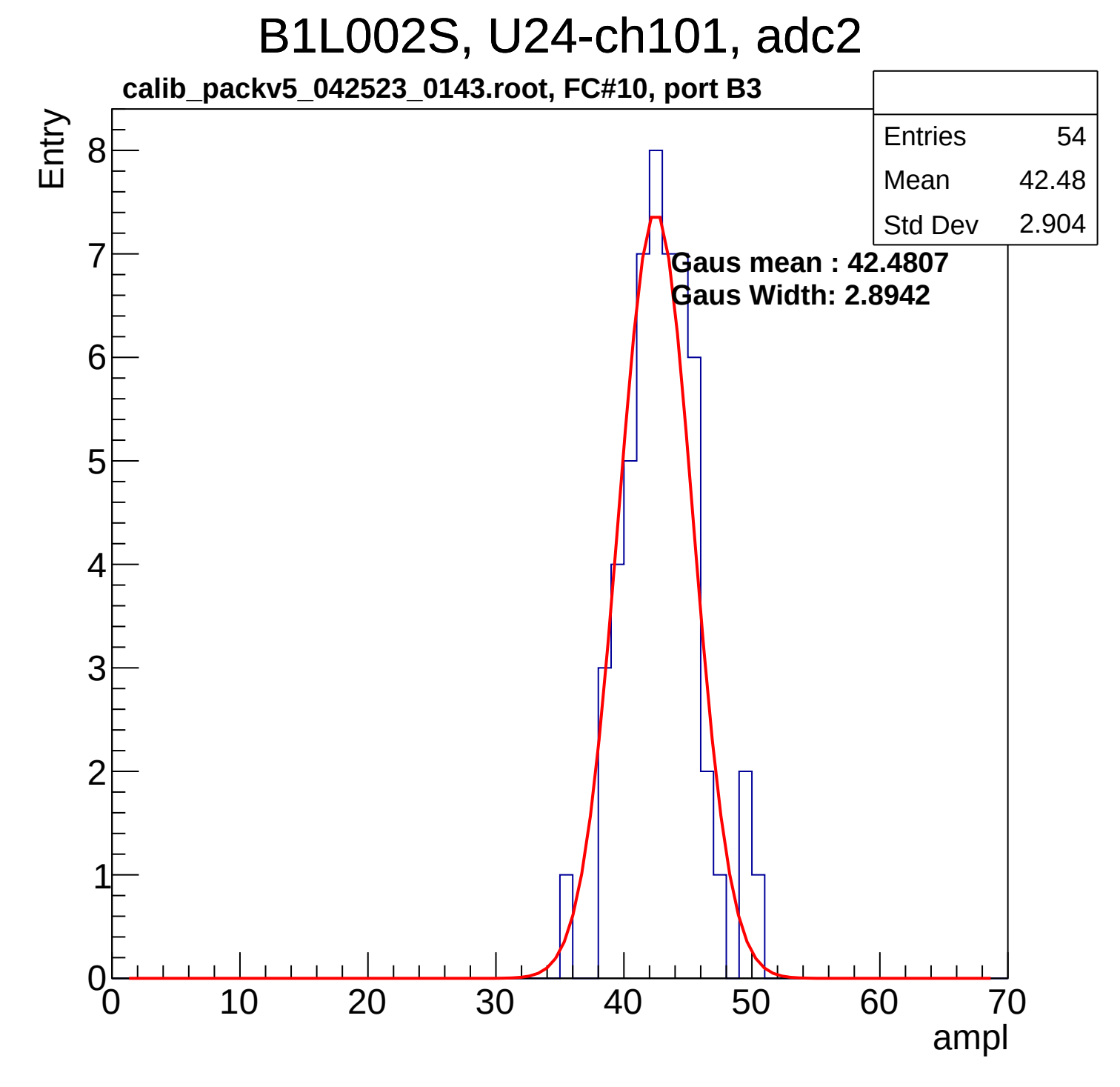
B1L002S, U24-ch101, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 42.48
Std Dev 2.904
Gaus mean : 42.4807
Gaus Width: 2.8942
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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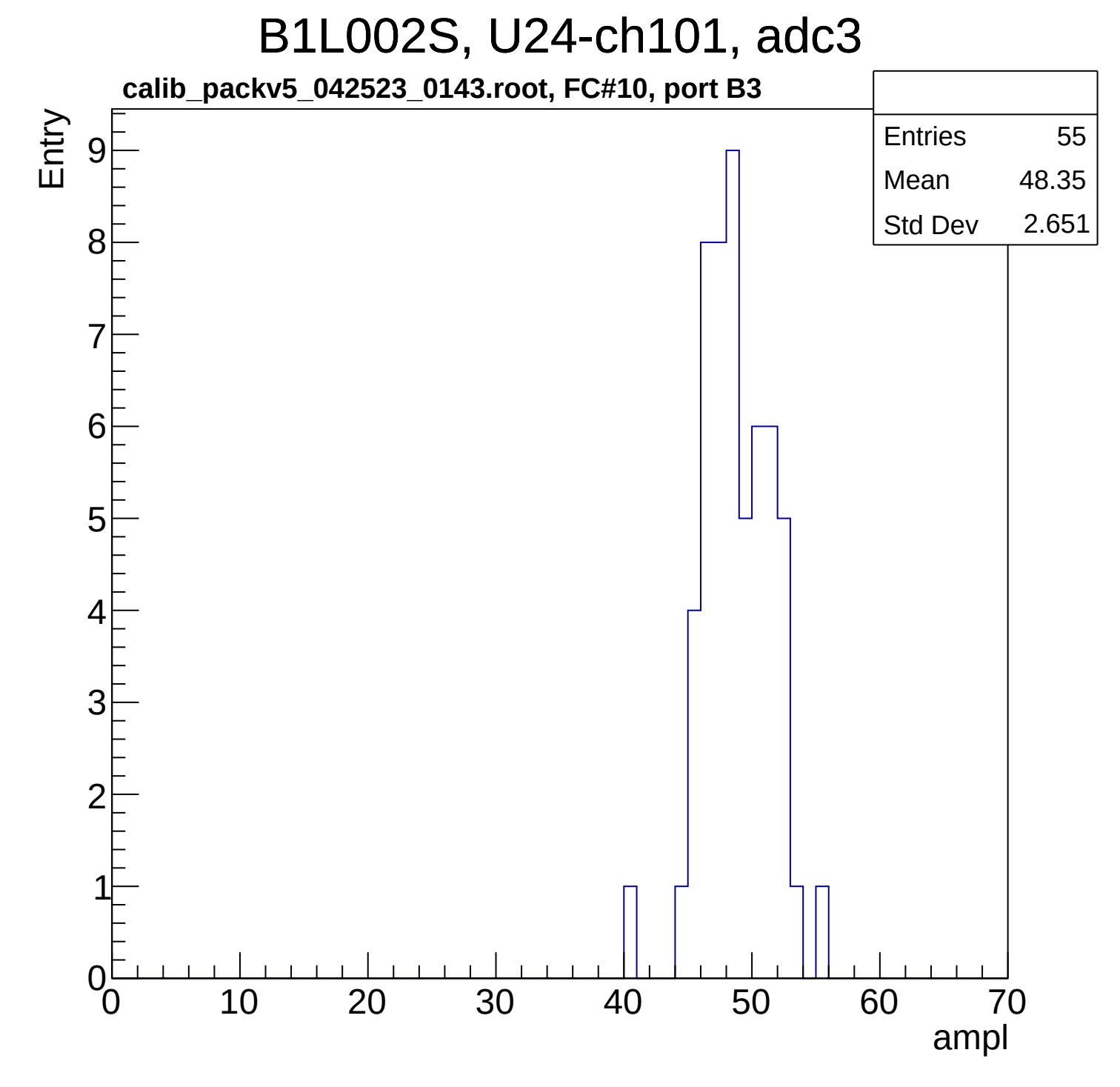
B1L002S, U24-ch101, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 48.35
Std Dev 2.651
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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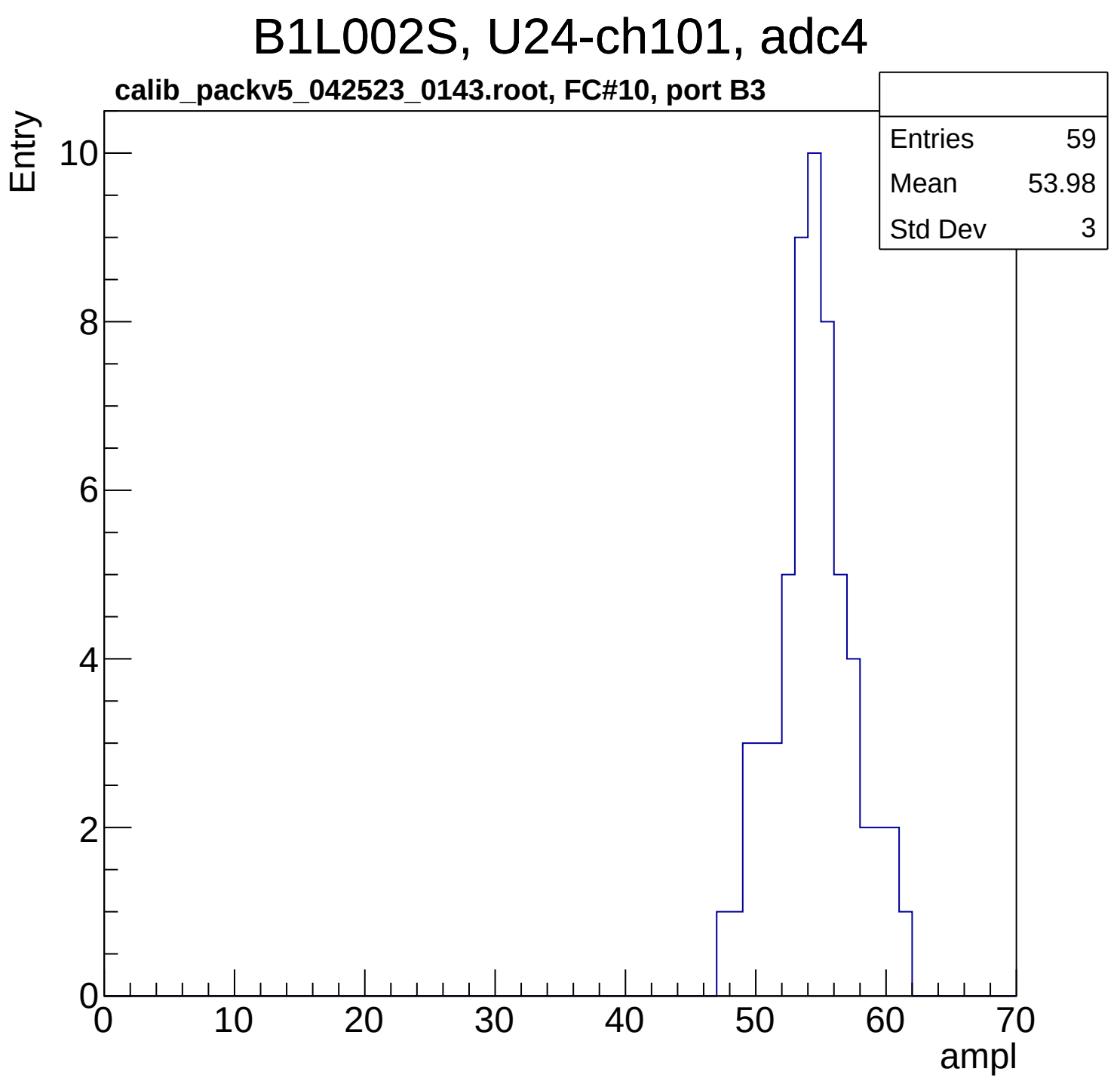
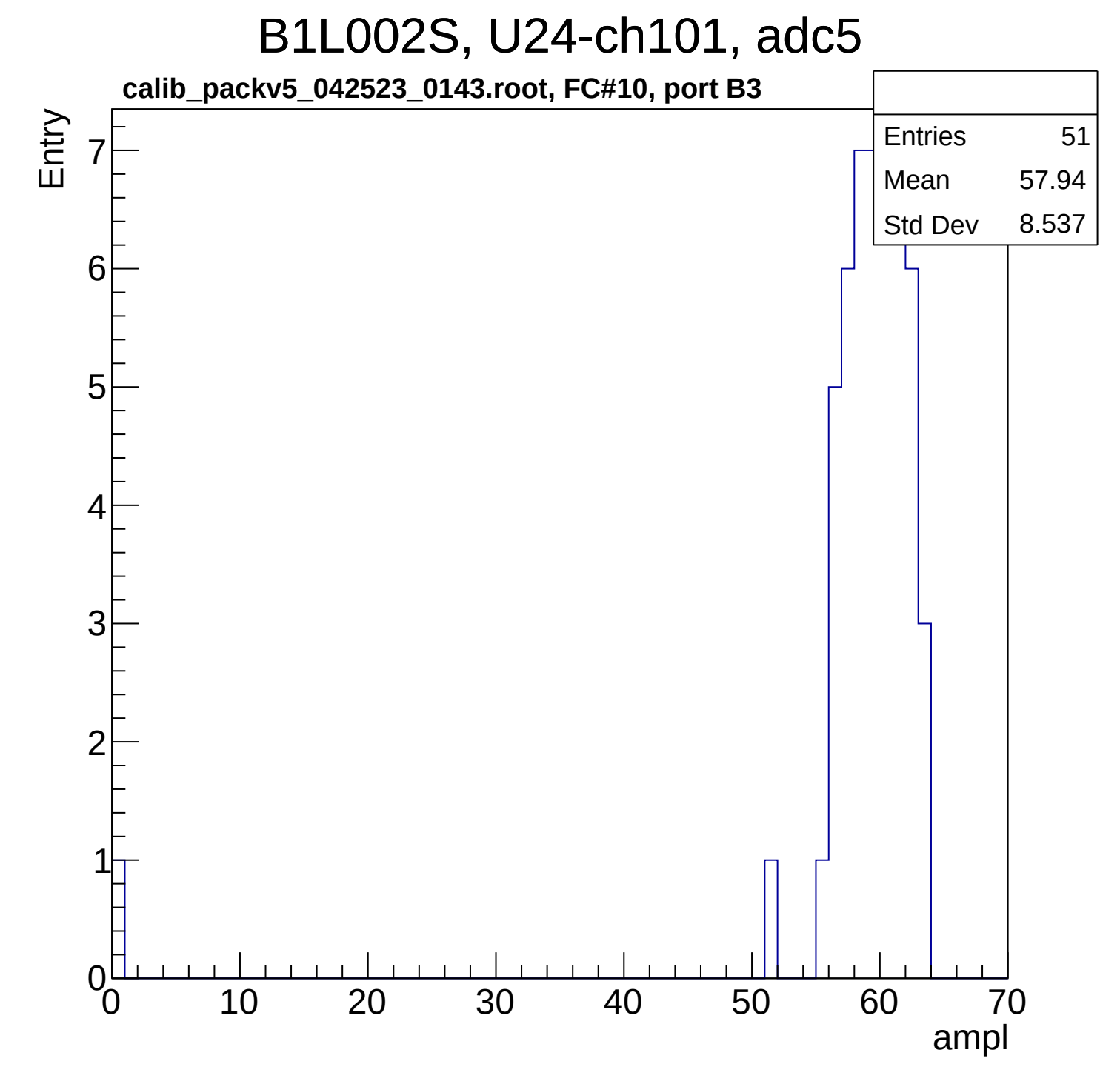
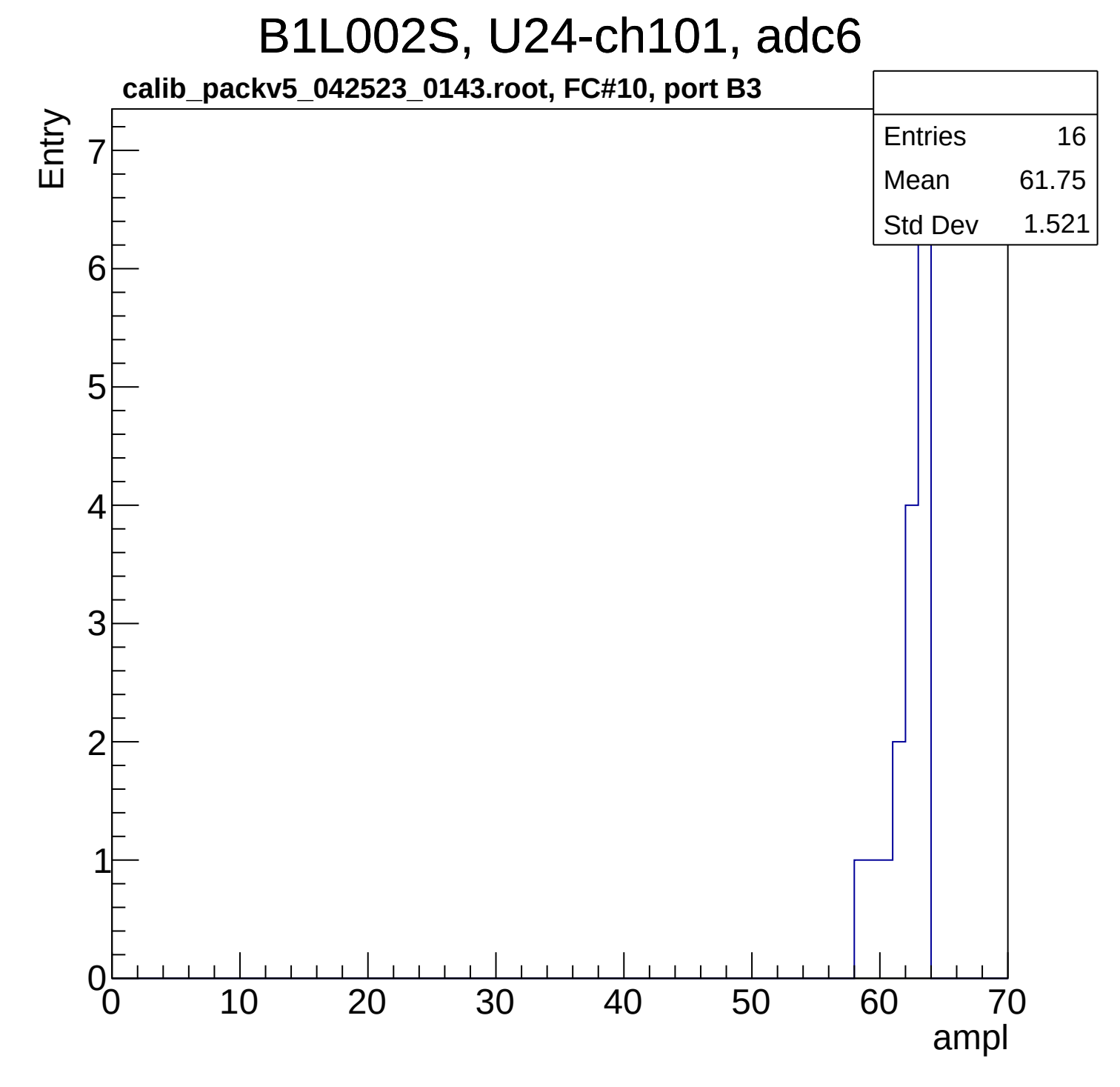
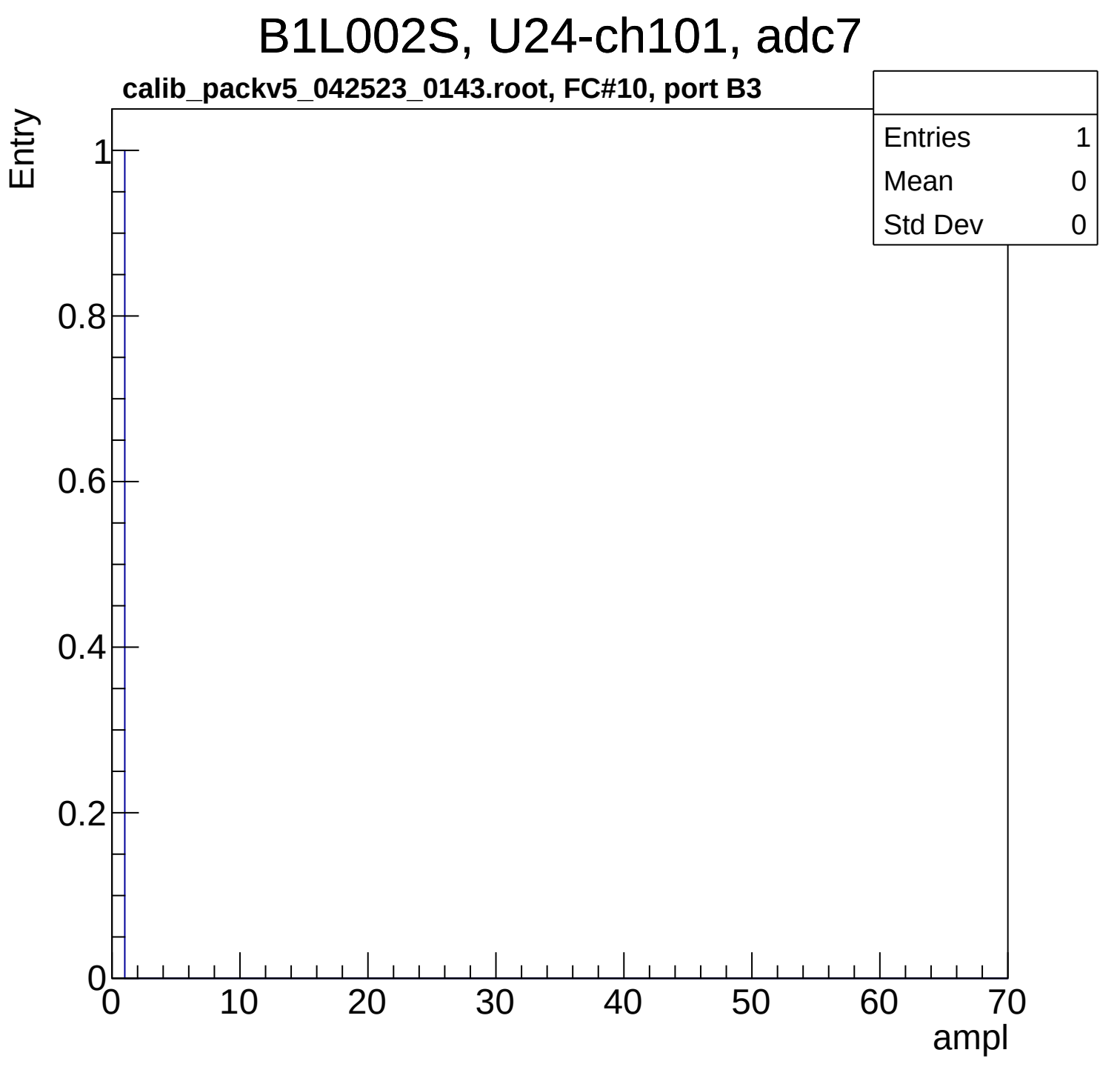
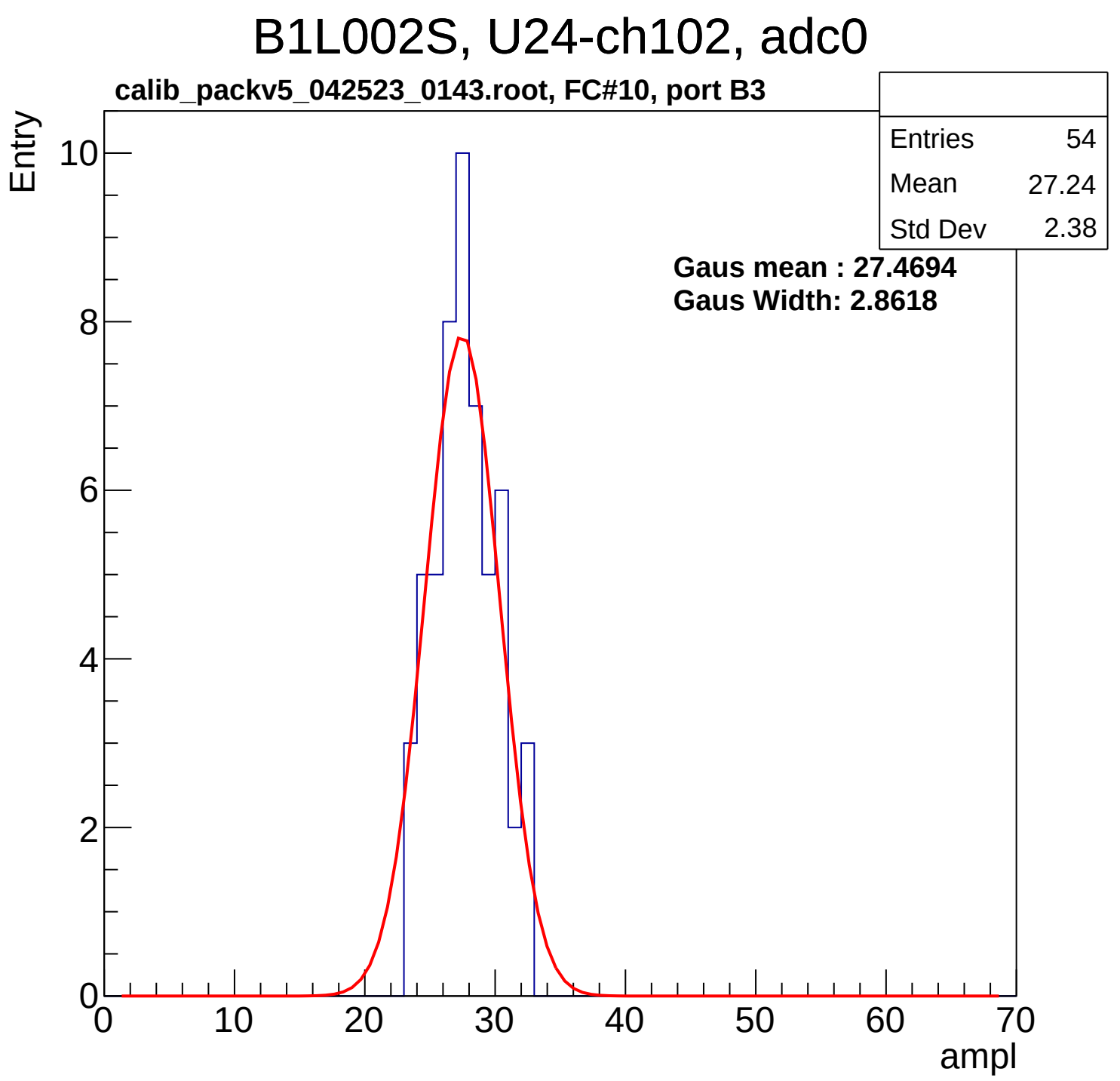
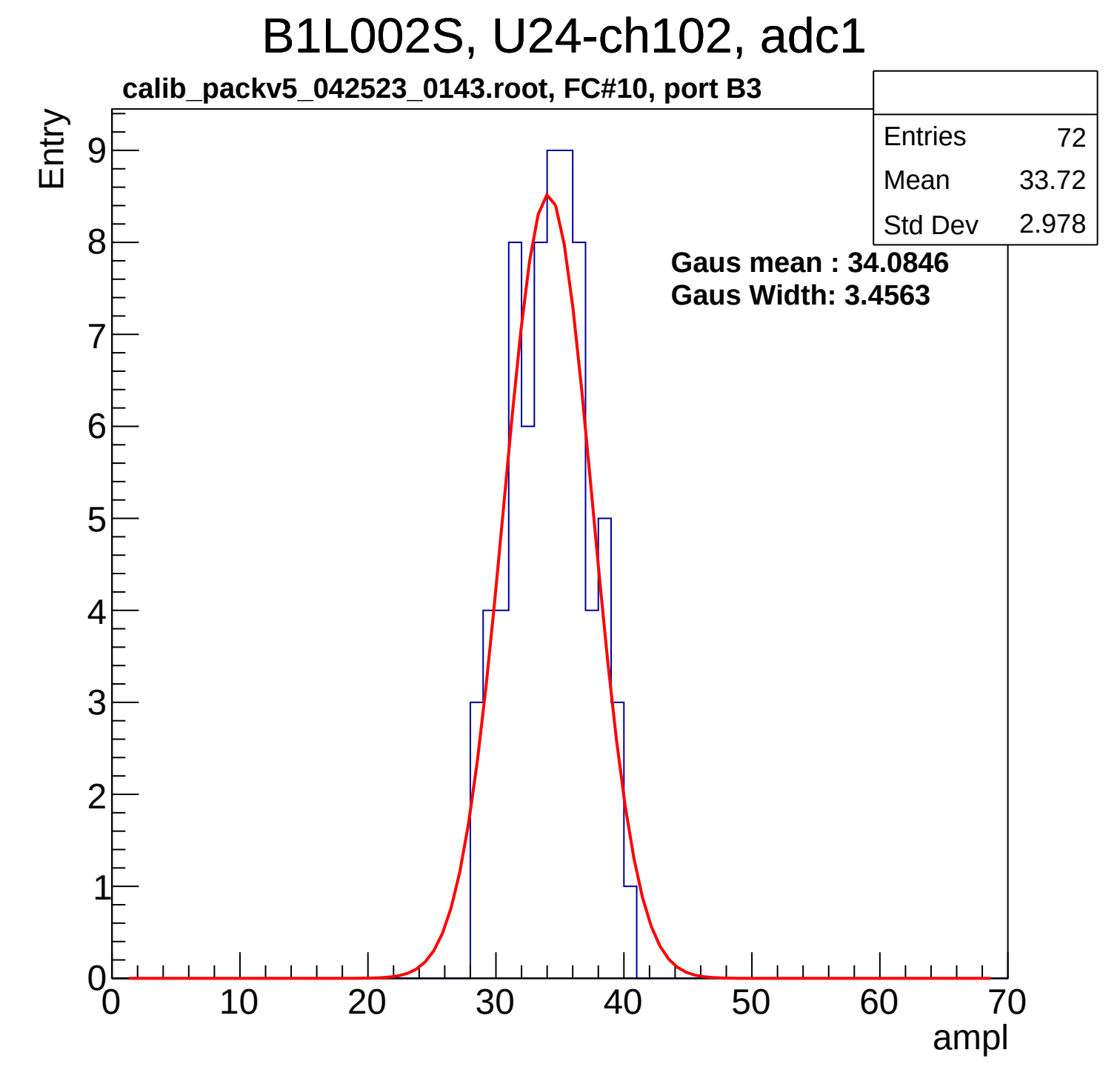

B1L002S, U24-ch101, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 53.98
Std Dev 3
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

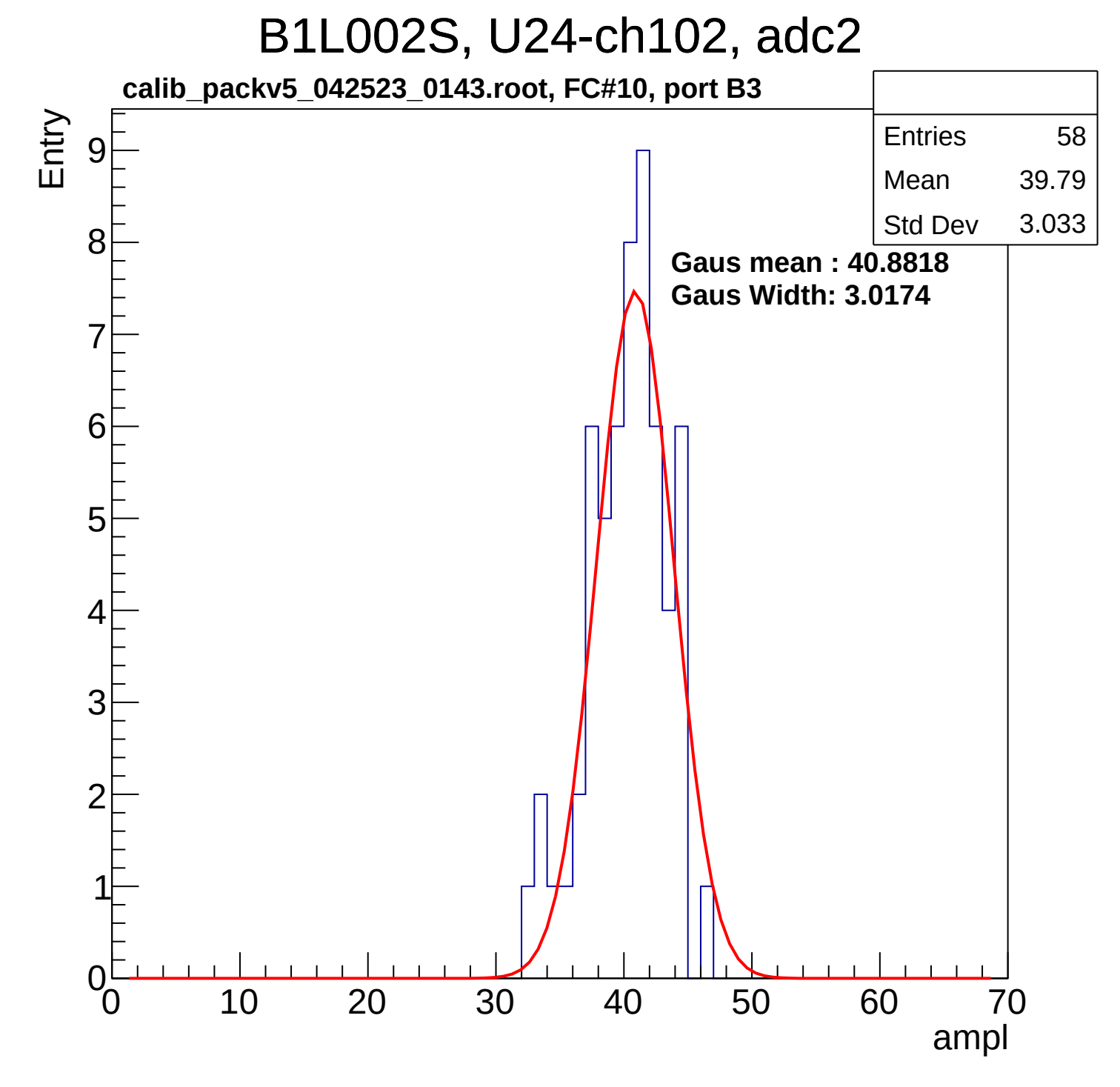

B1L002S, U24-ch101, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 57.94
Std Dev 8.537
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

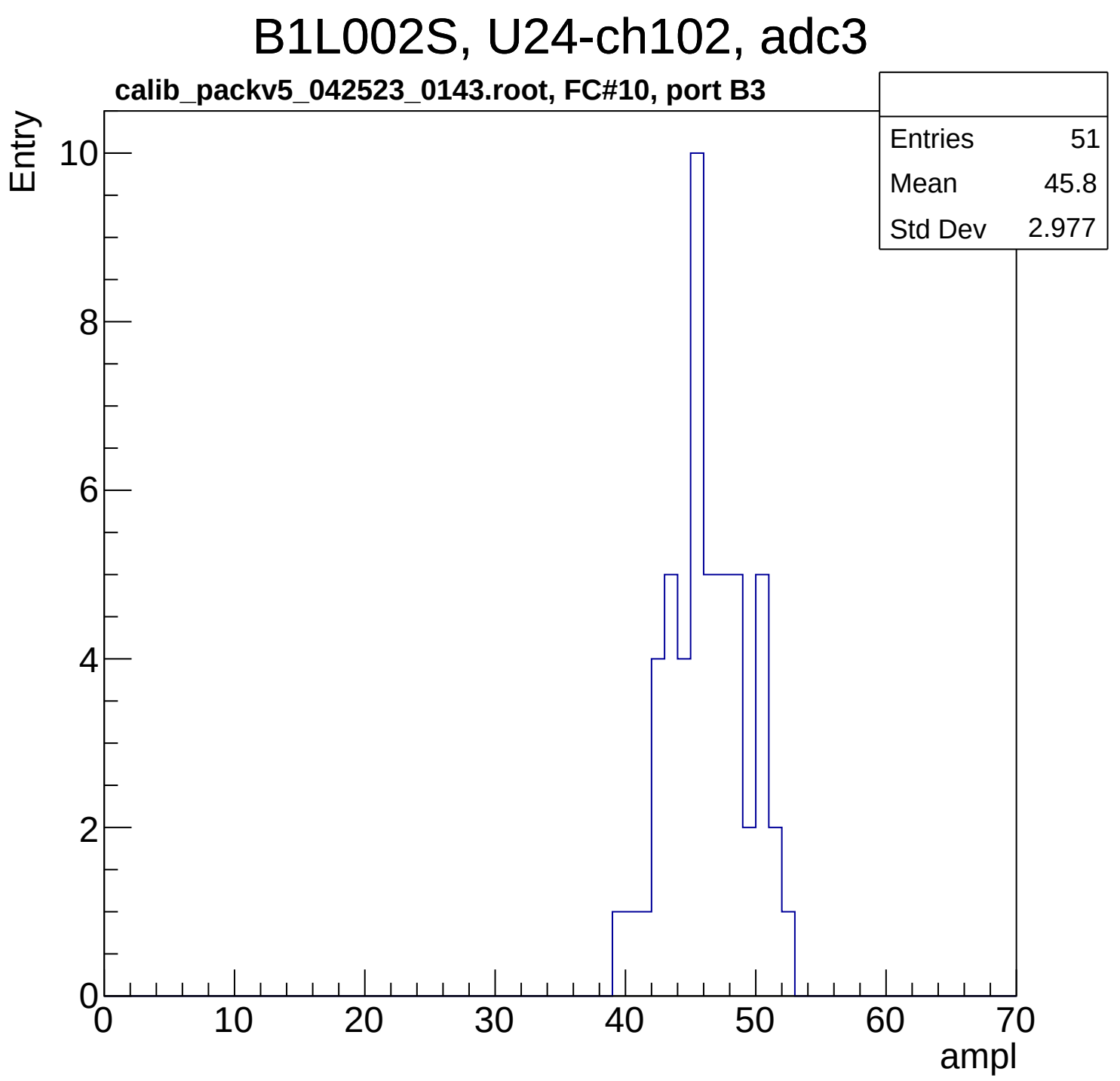

B1L002S, U24-ch101, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 61.75
Std Dev 1.521
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl

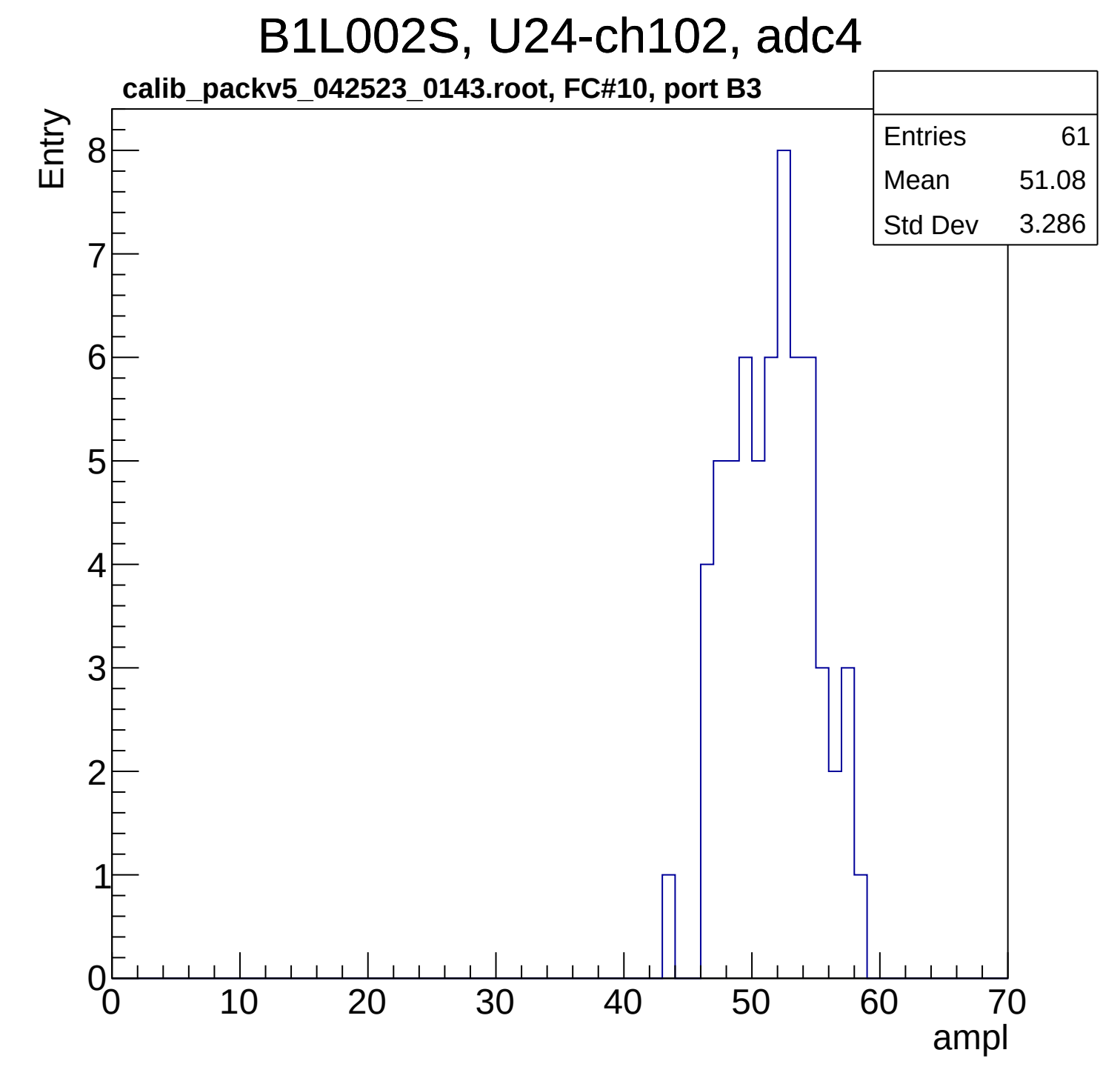

B1L002S, U24-ch101, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch102, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 27.24
Std Dev 2.38
Gaus mean : 27.4694
Gaus Width: 2.8618
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch102, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 33.72
Std Dev 2.978
Gaus mean : 34.0846
Gaus Width: 3.4563
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch102, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 39.79
Std Dev 3.033
Gaus mean : 40.8818
Gaus Width: 3.0174
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch102, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 45.8
Std Dev 2.977
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch102, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 51.08
Std Dev 3.286
Entry
ampl
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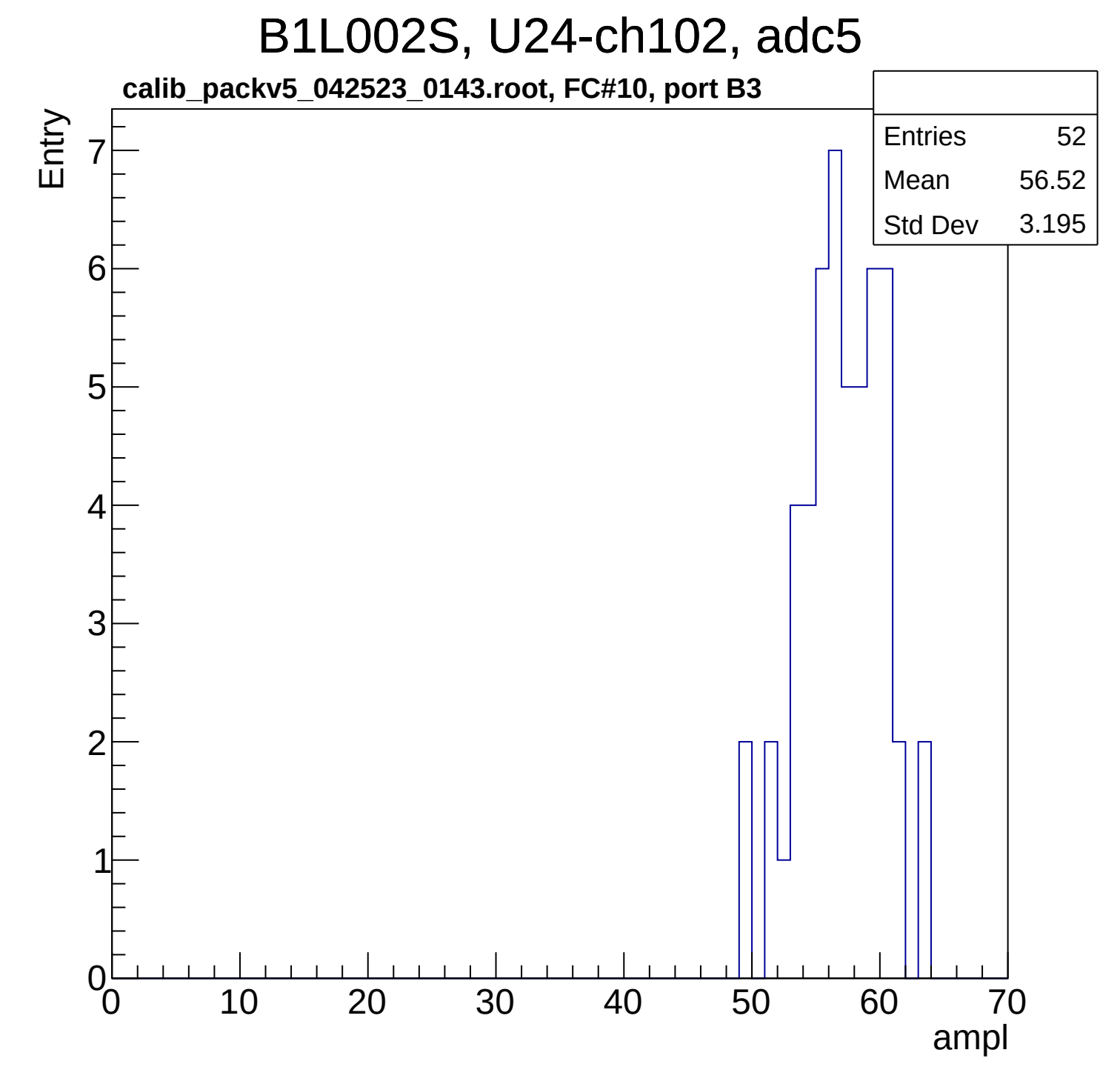
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch102, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 56.52
Std Dev 3.195
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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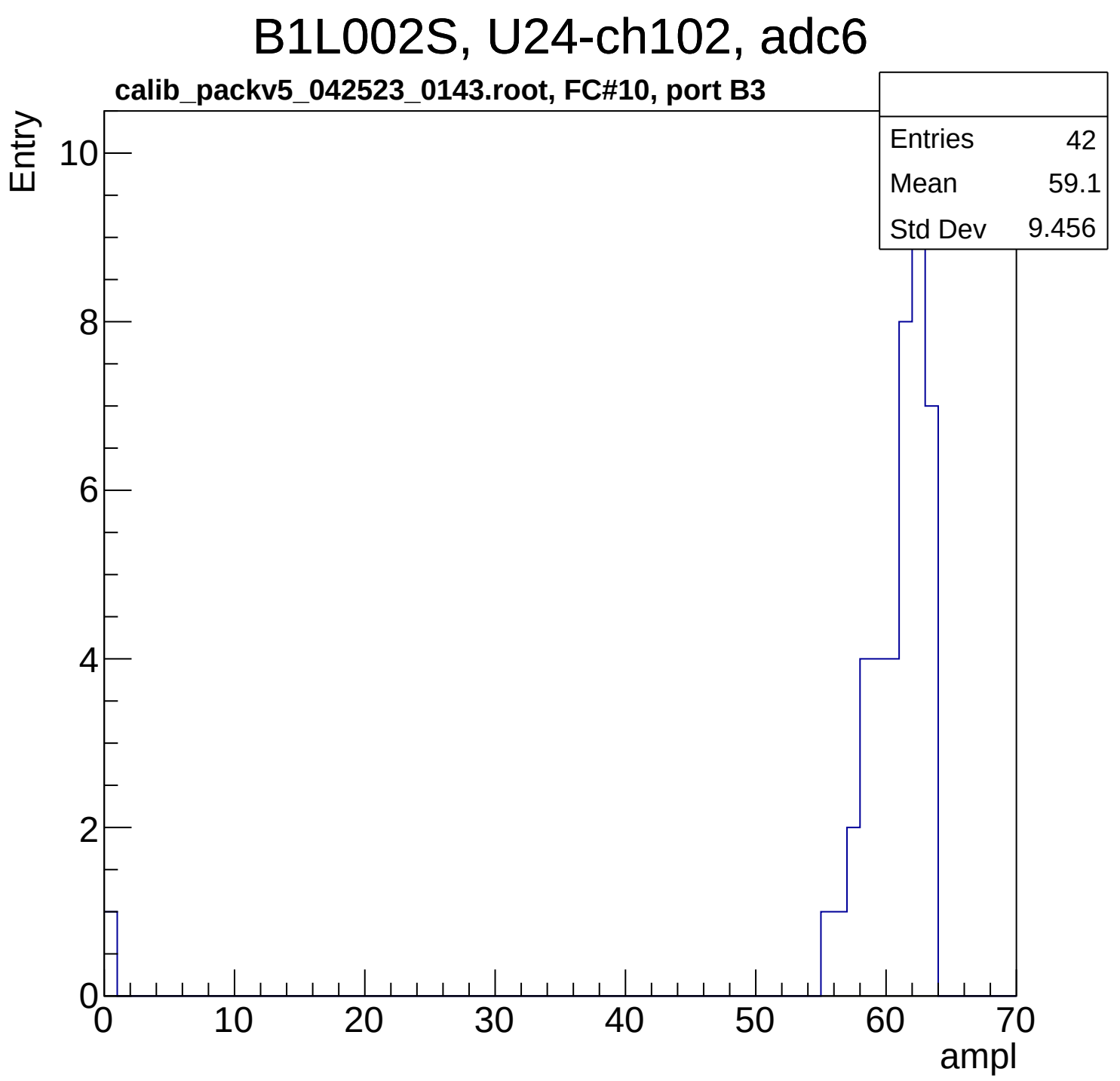
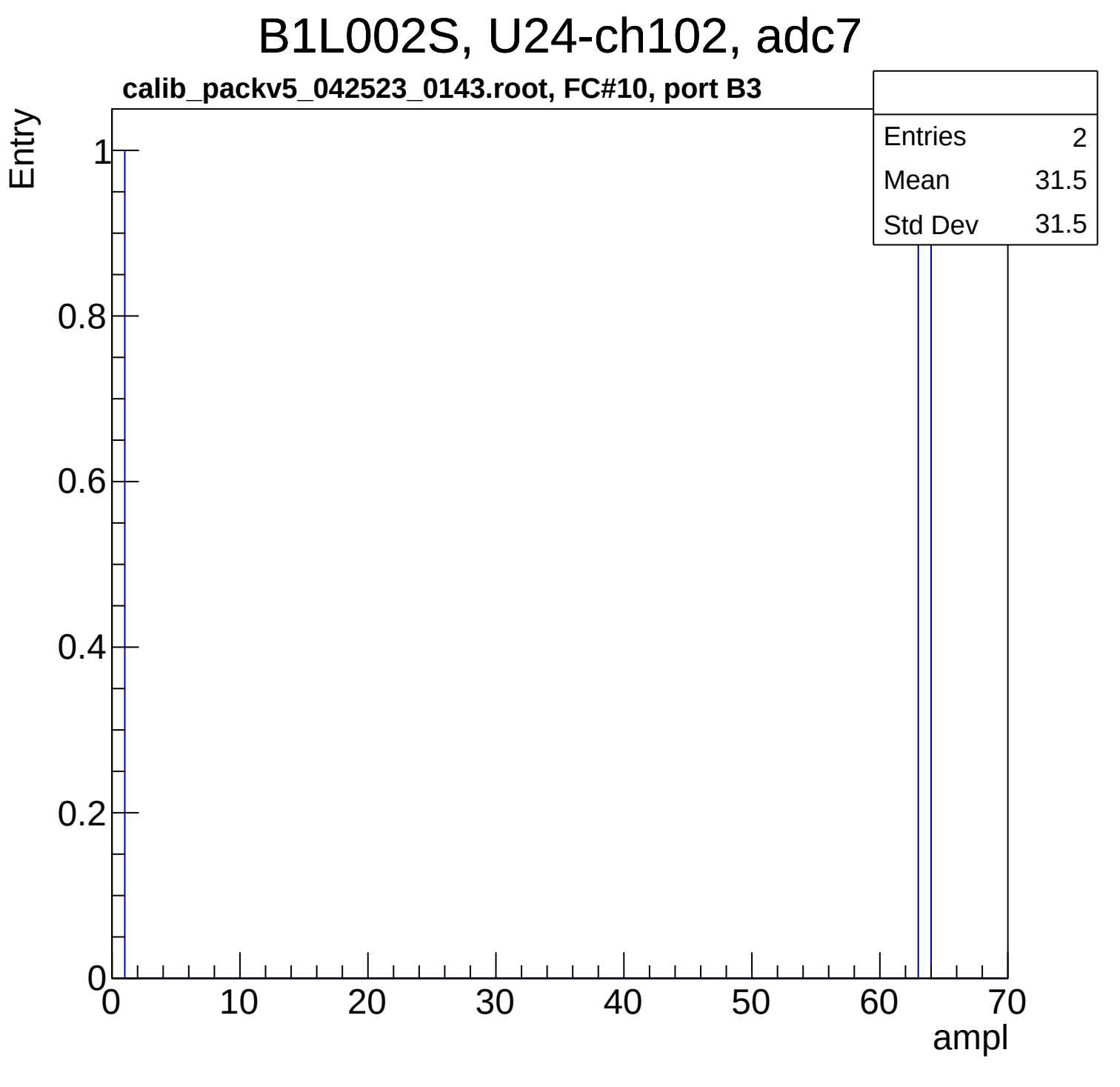
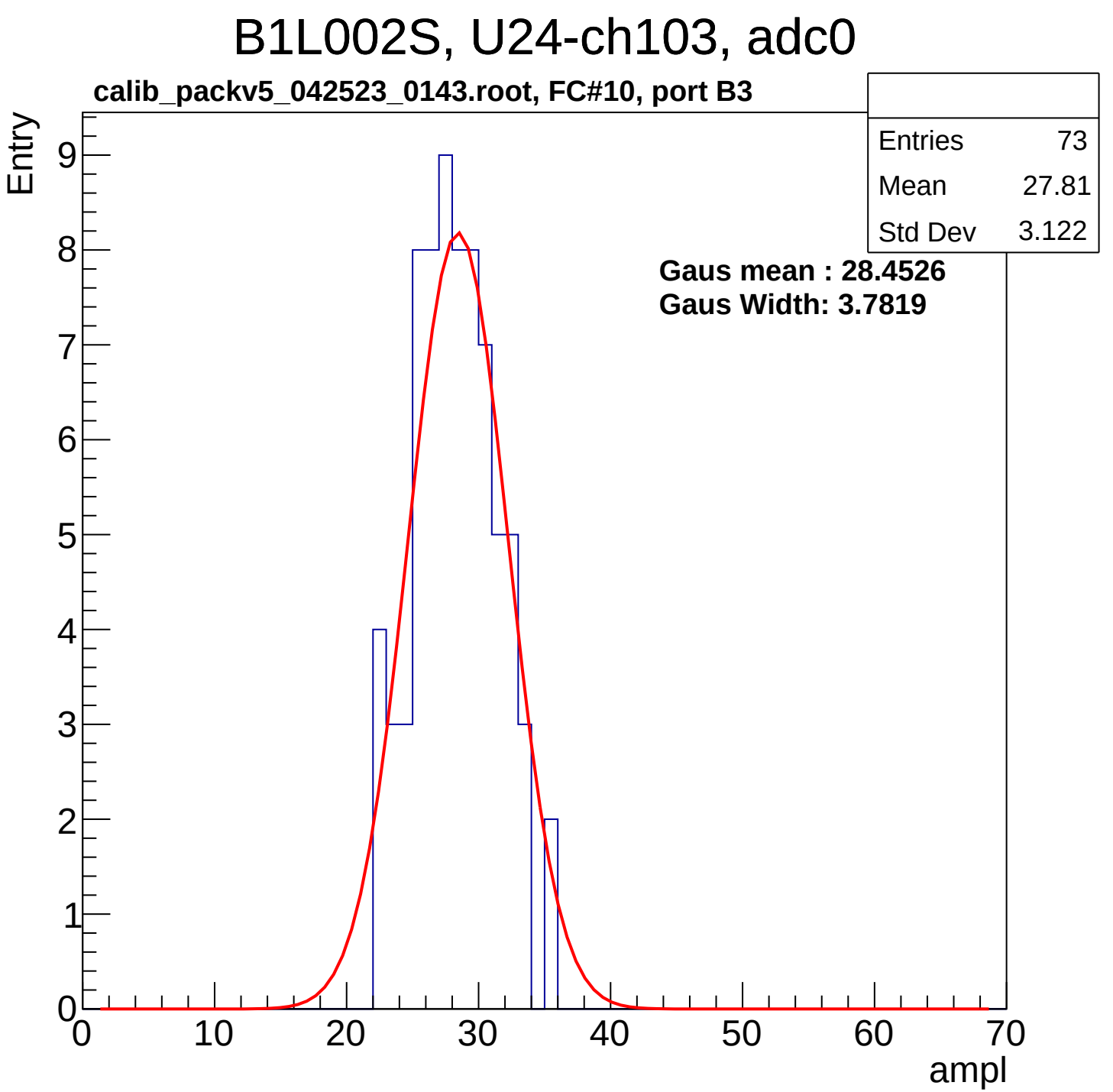

B1L002S, U24-ch102, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 59.1
Std Dev 9.456
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch102, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch103, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 27.81
Std Dev 3.122
Gaus mean : 28.4526
Gaus Width: 3.7819
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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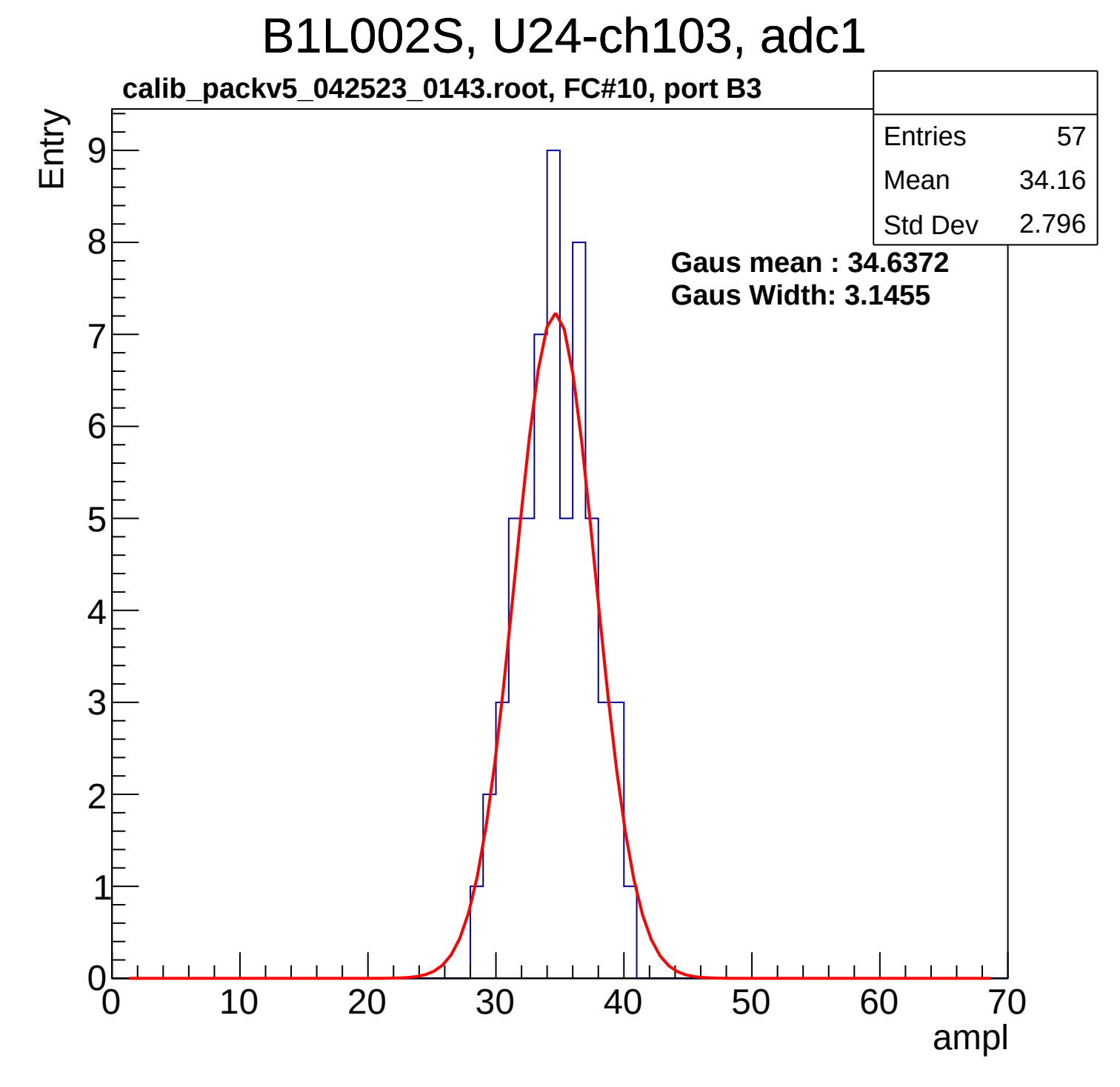
20
30
40
50
60
70


B1L002S, U24-ch103, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 34.16
Std Dev 2.796
Gaus mean : 34.6372
Gaus Width: 3.1455
Entry
ampl
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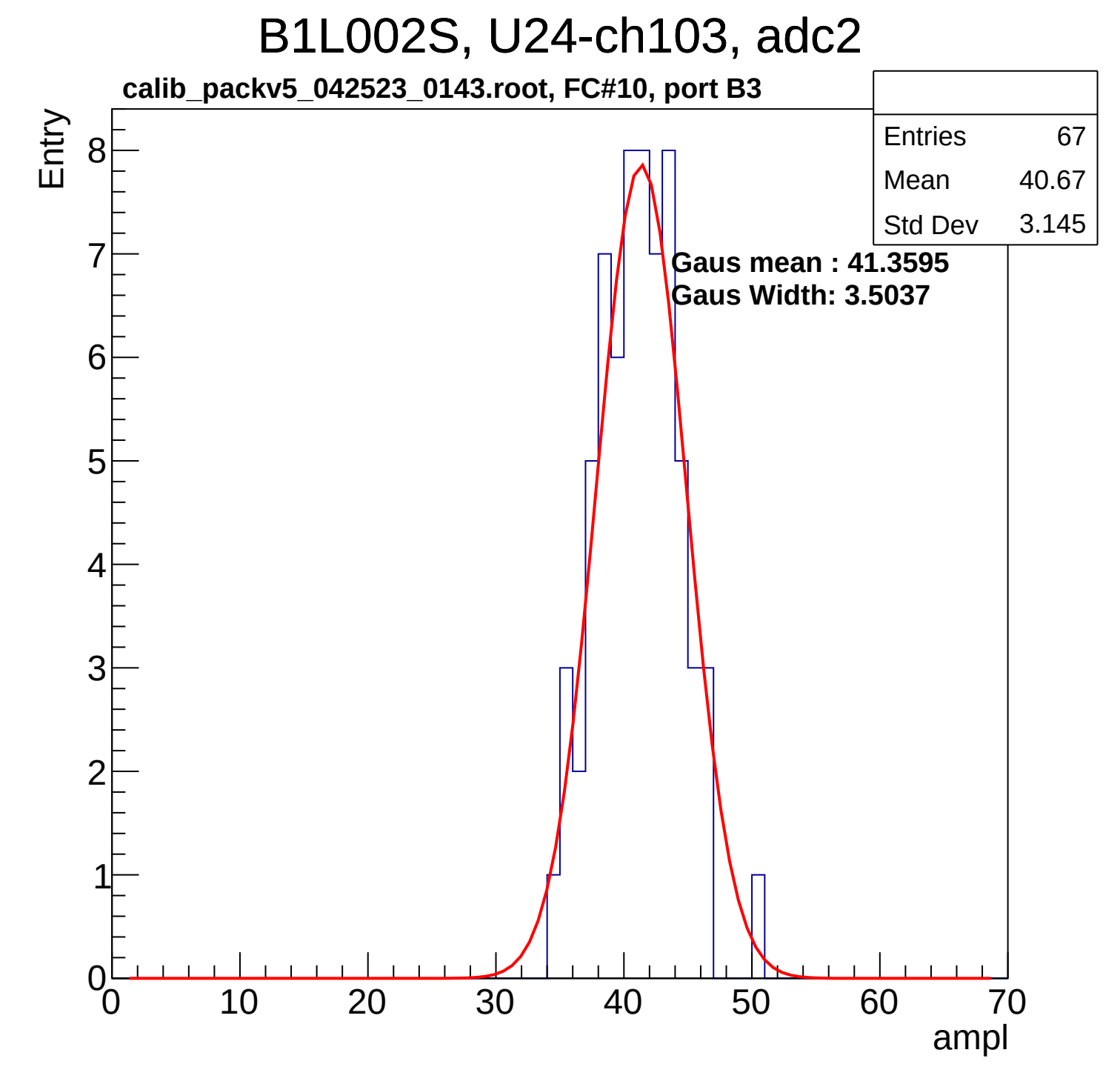
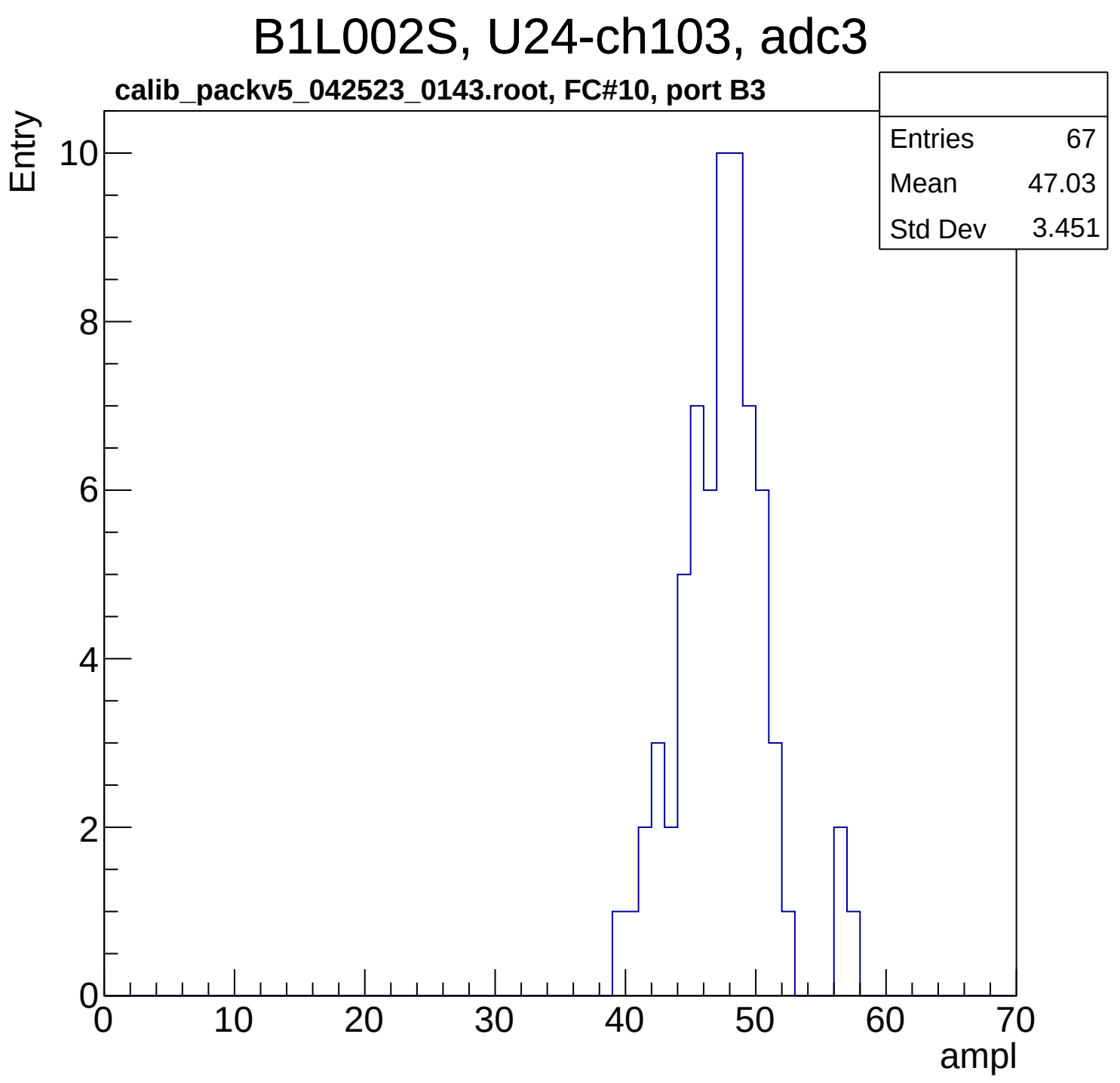
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch103, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 40.67
Std Dev 3.145
Gaus mean : 41.3595
Gaus Width: 3.5037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch103, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 47.03
Std Dev 3.451
Entry
ampl
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2
4
6
8
10
0
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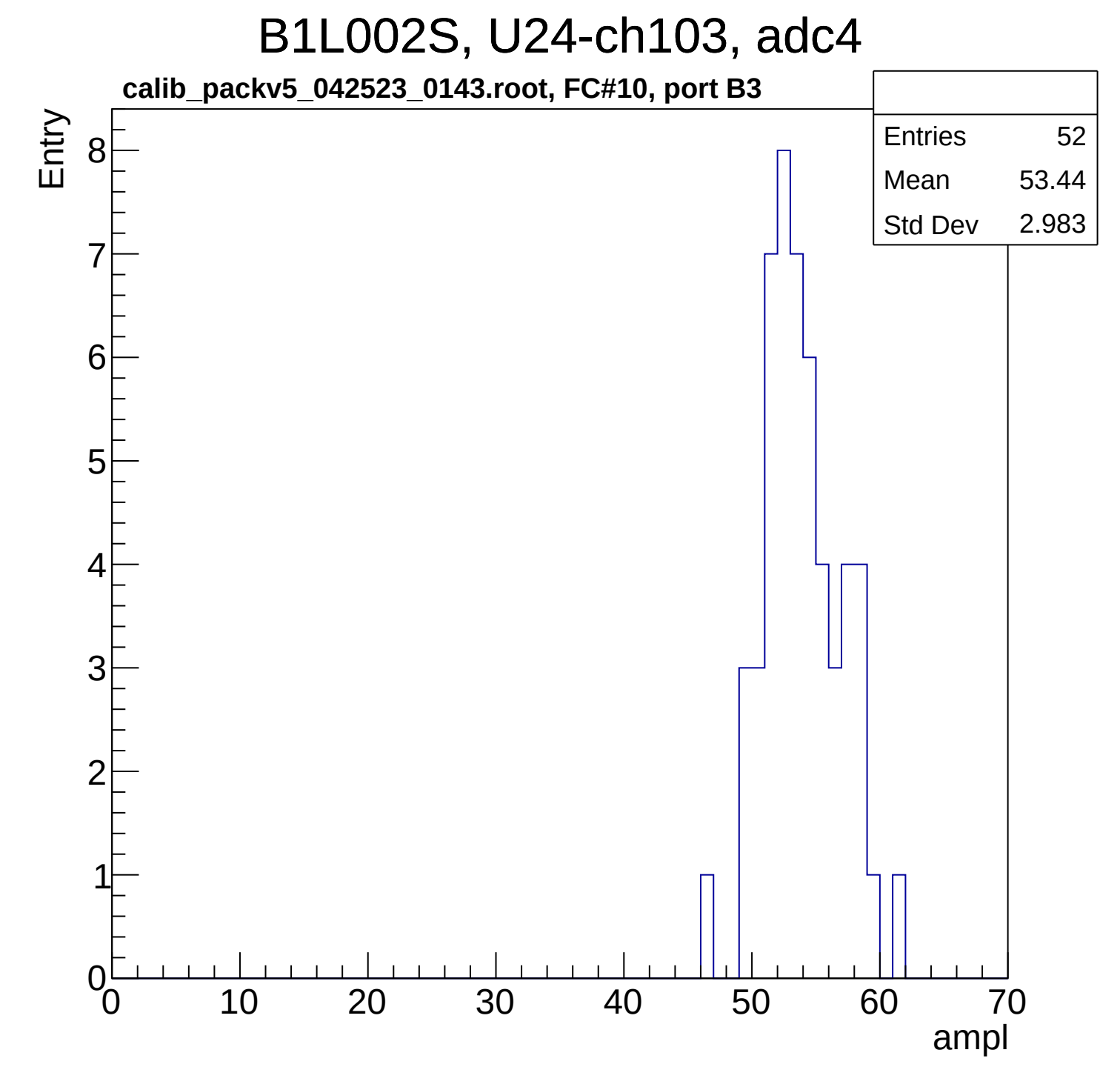
20
30
40
50
60
70


B1L002S, U24-ch103, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.44
Std Dev 2.983
Entry
ampl
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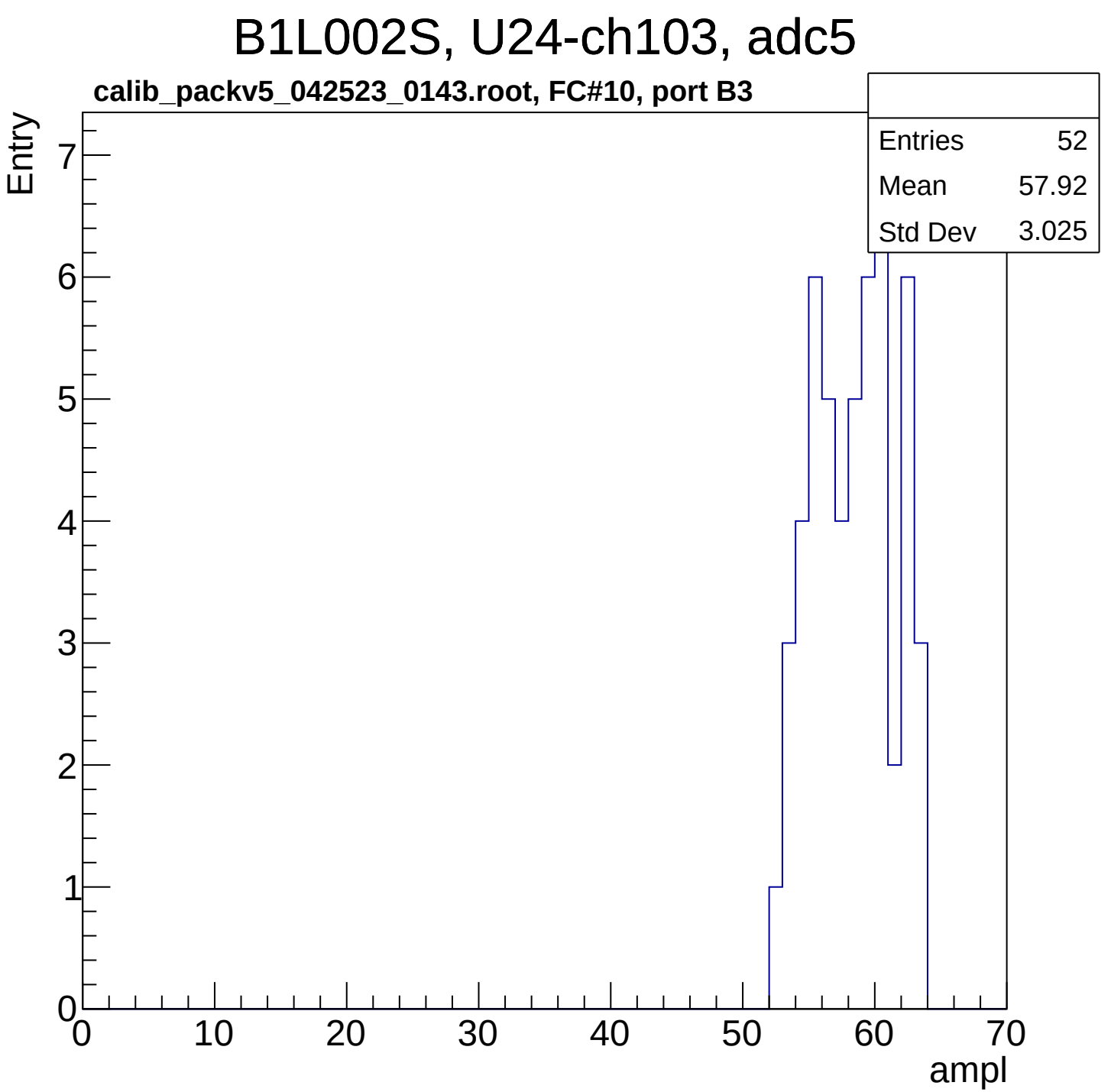
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch103, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 57.92
Std Dev 3.025
Entry
ampl
0
1
2
3
4
5
6
7
0
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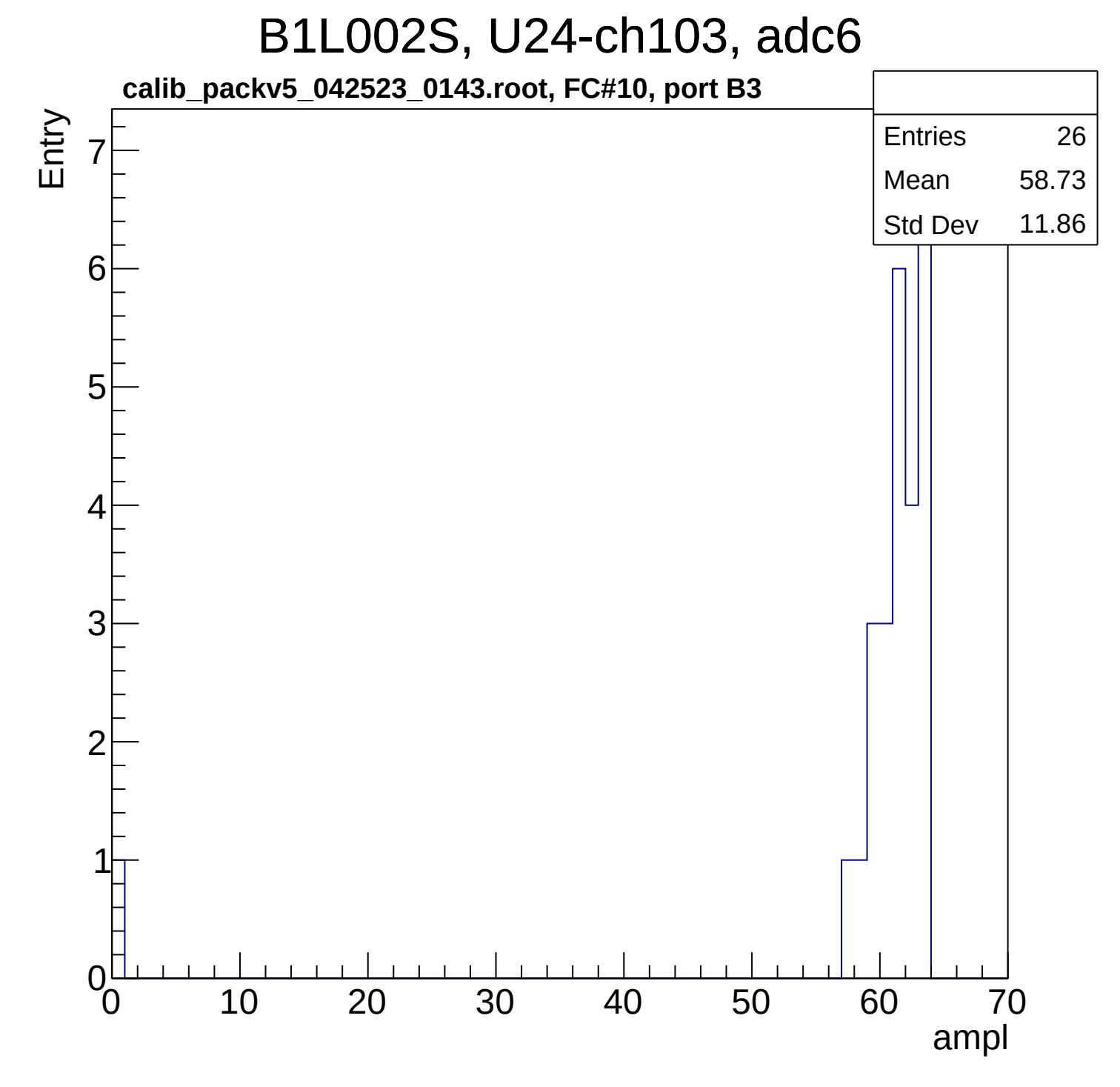
20
30
40
50
60
70


B1L002S, U24-ch103, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 26
Mean 58.73
Std Dev 11.86
Entry
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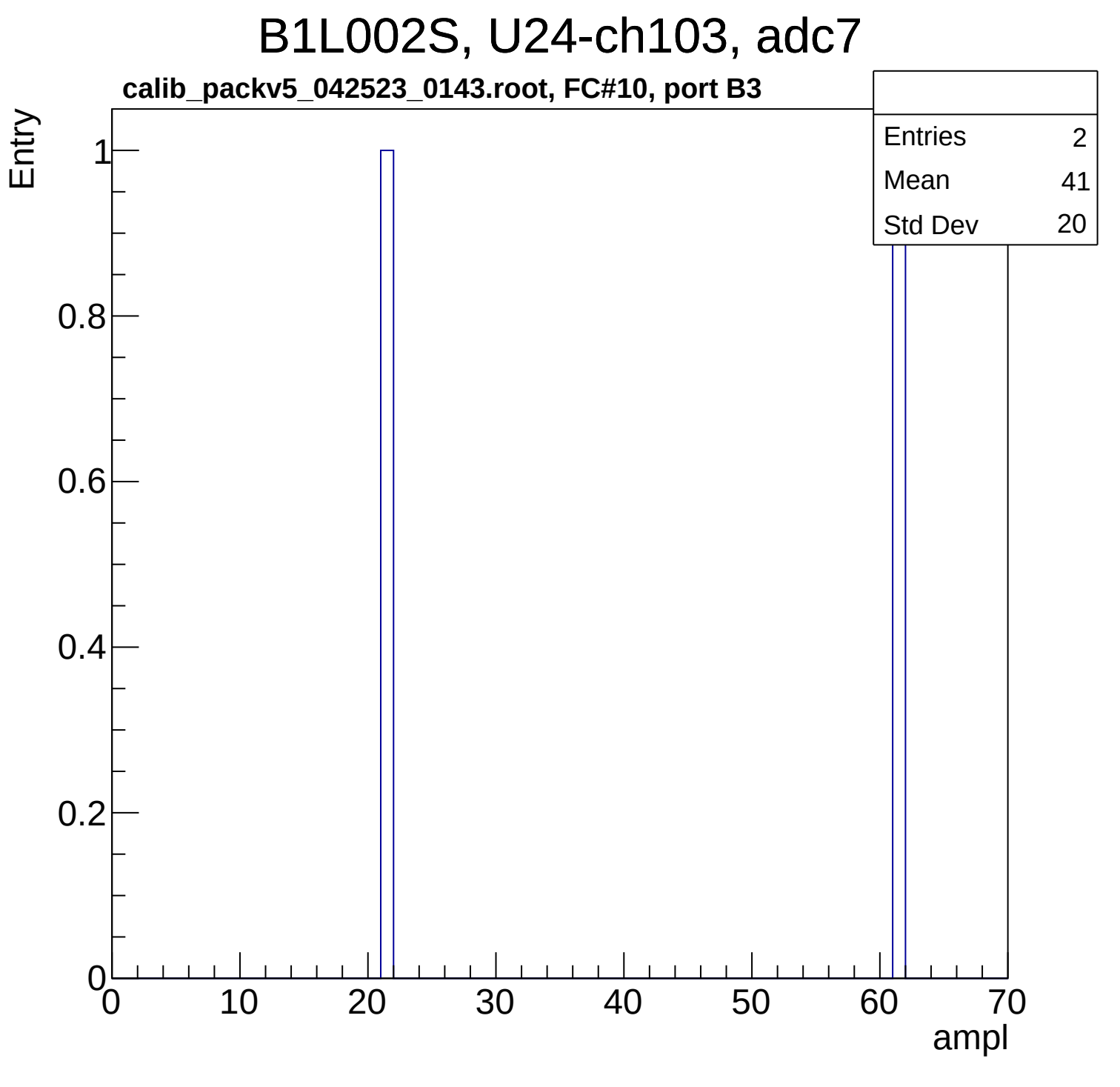
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch103, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 41
Std Dev 20
Entry
ampl
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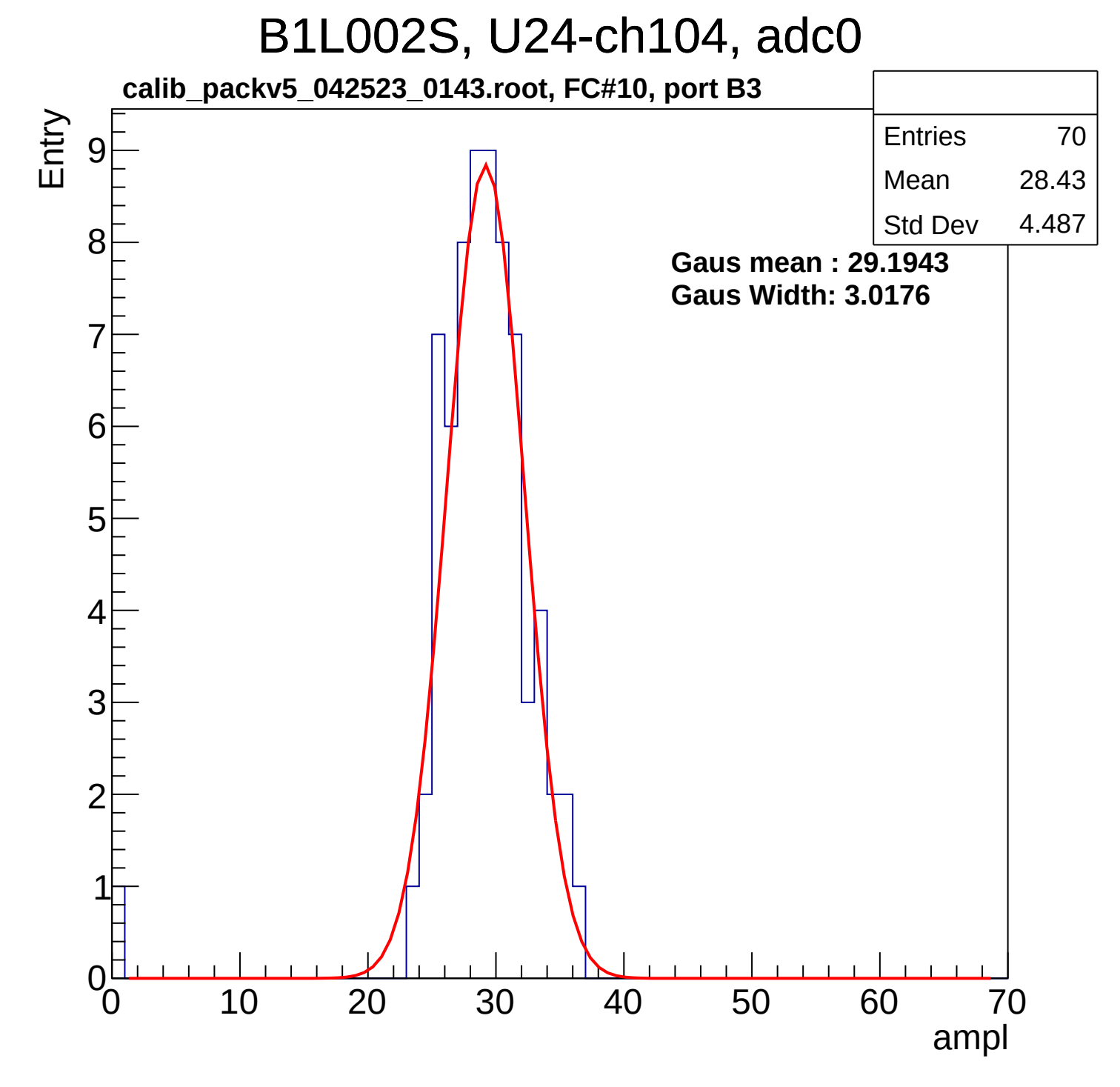
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L002S, U24-ch104, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 28.43
Std Dev 4.487
Gaus mean : 29.1943
Gaus Width: 3.0176
Entry
ampl
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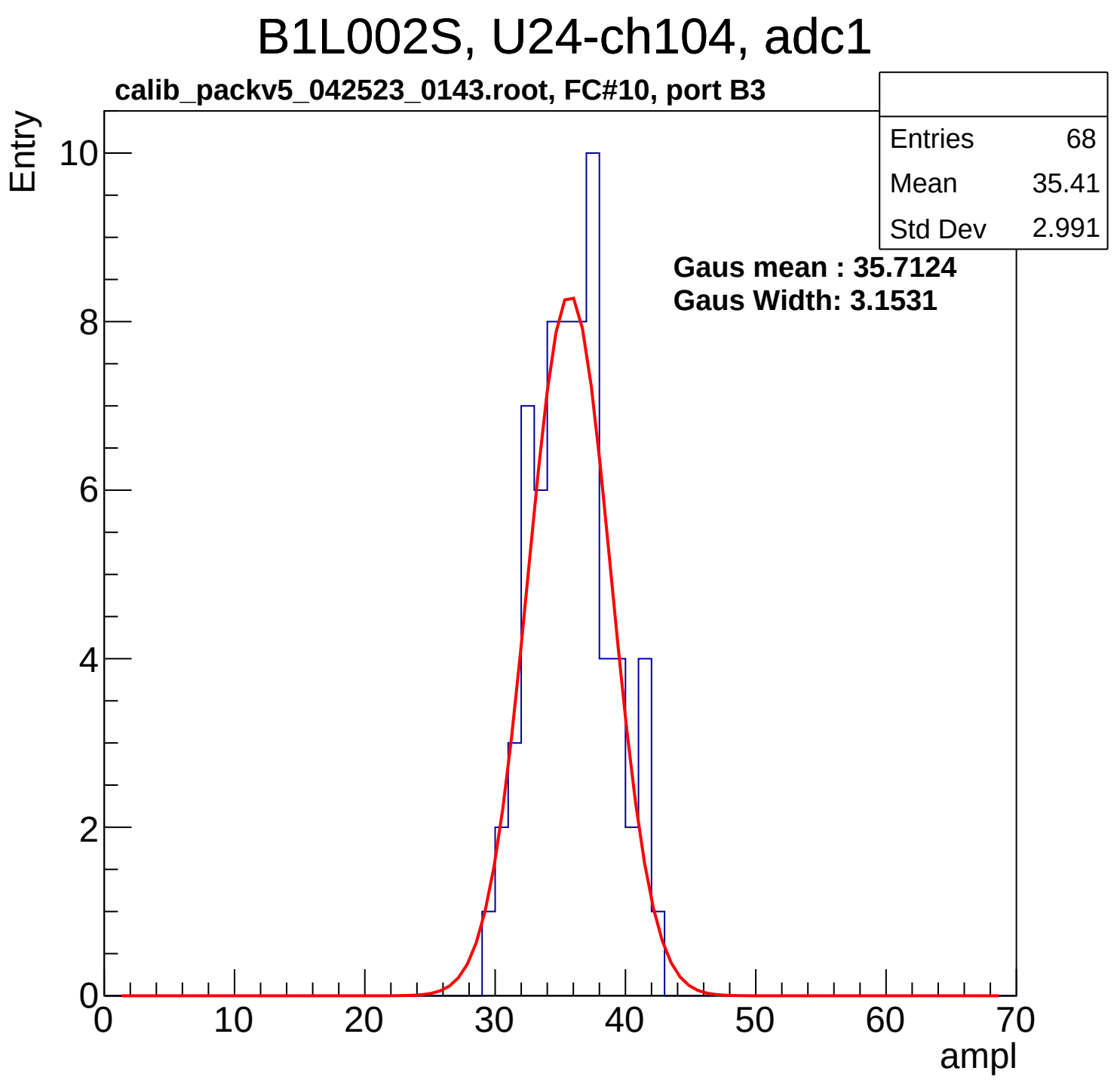
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch104, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 35.41
Std Dev 2.991
Gaus mean : 35.7124
Gaus Width: 3.1531
Entry
ampl
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4
6
8
10
0
10
20
30
40
50
60
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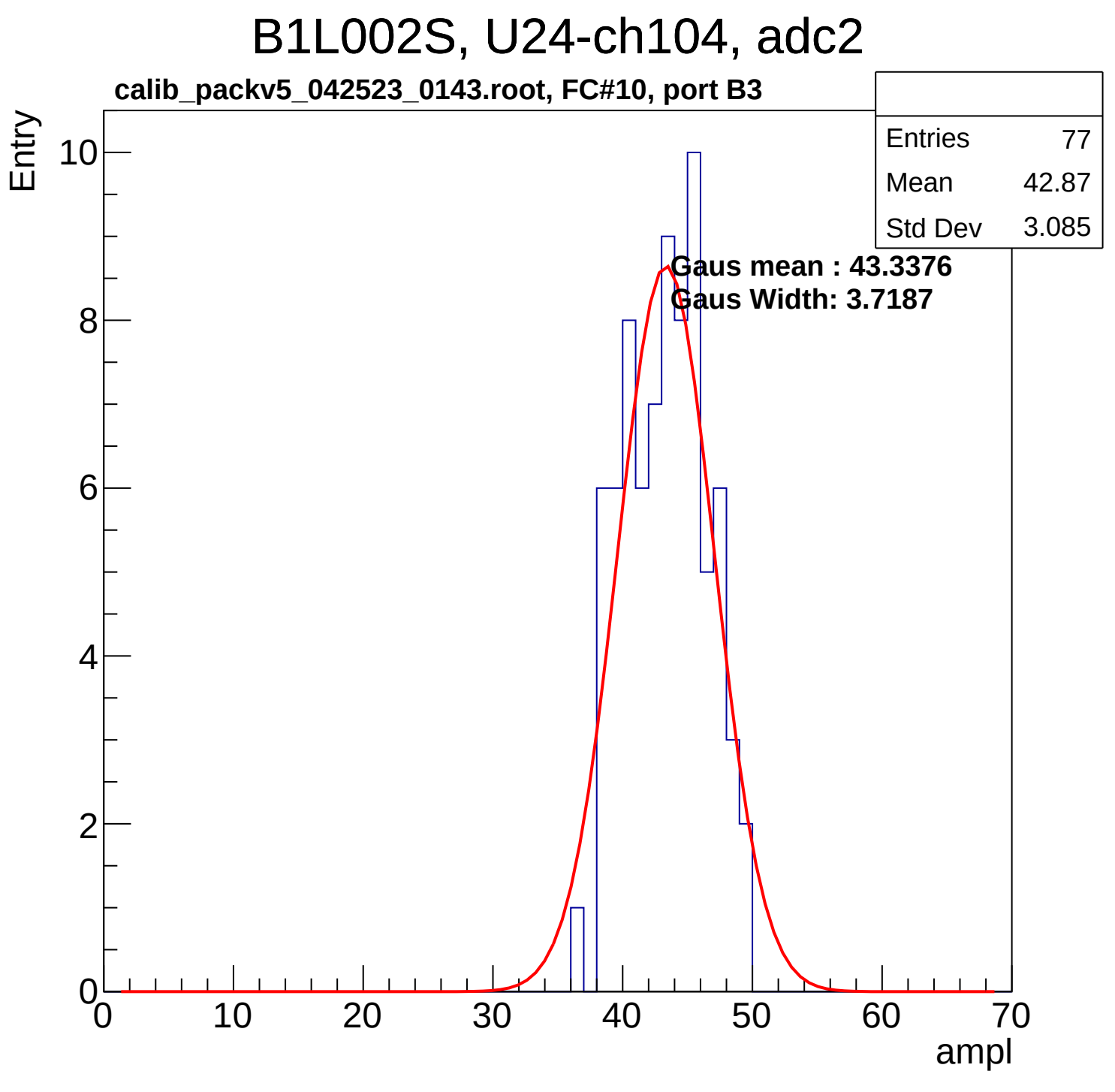
B1L002S, U24-ch104, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 42.87
Std Dev 3.085
Gaus mean : 43.3376
Gaus Width: 3.7187
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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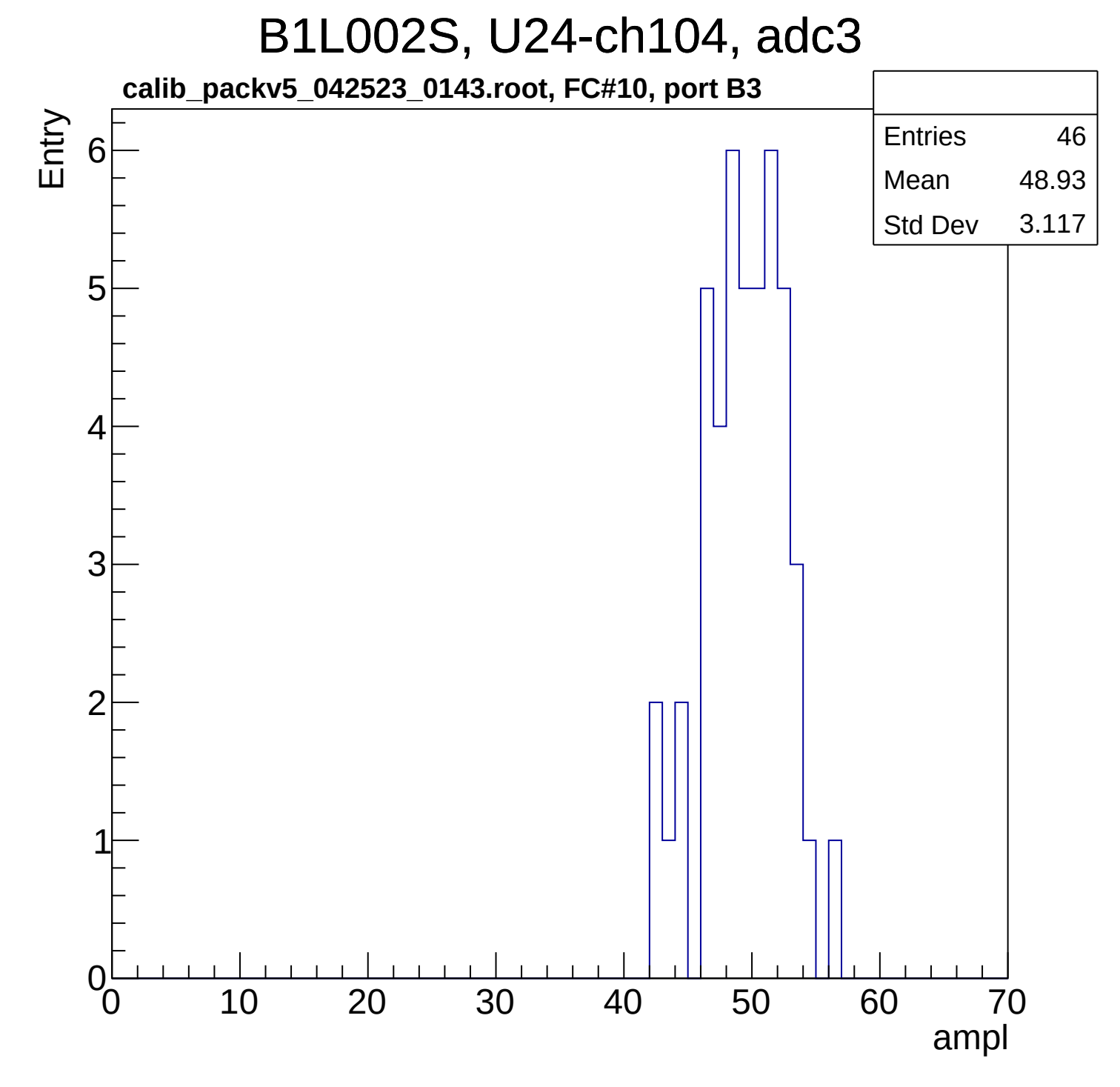
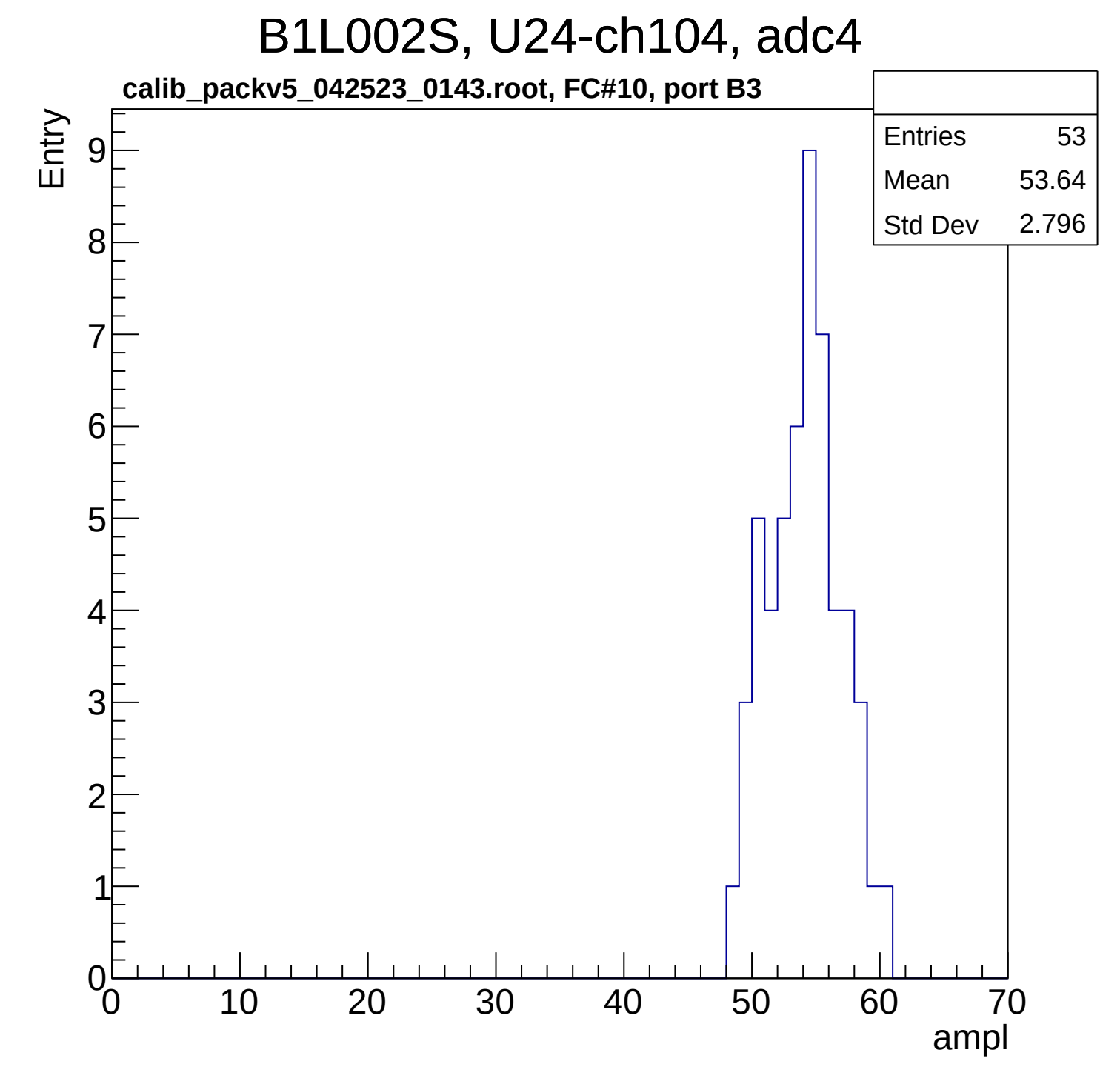

B1L002S, U24-ch104, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 48.93
Std Dev 3.117
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch104, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 53.64
Std Dev 2.796
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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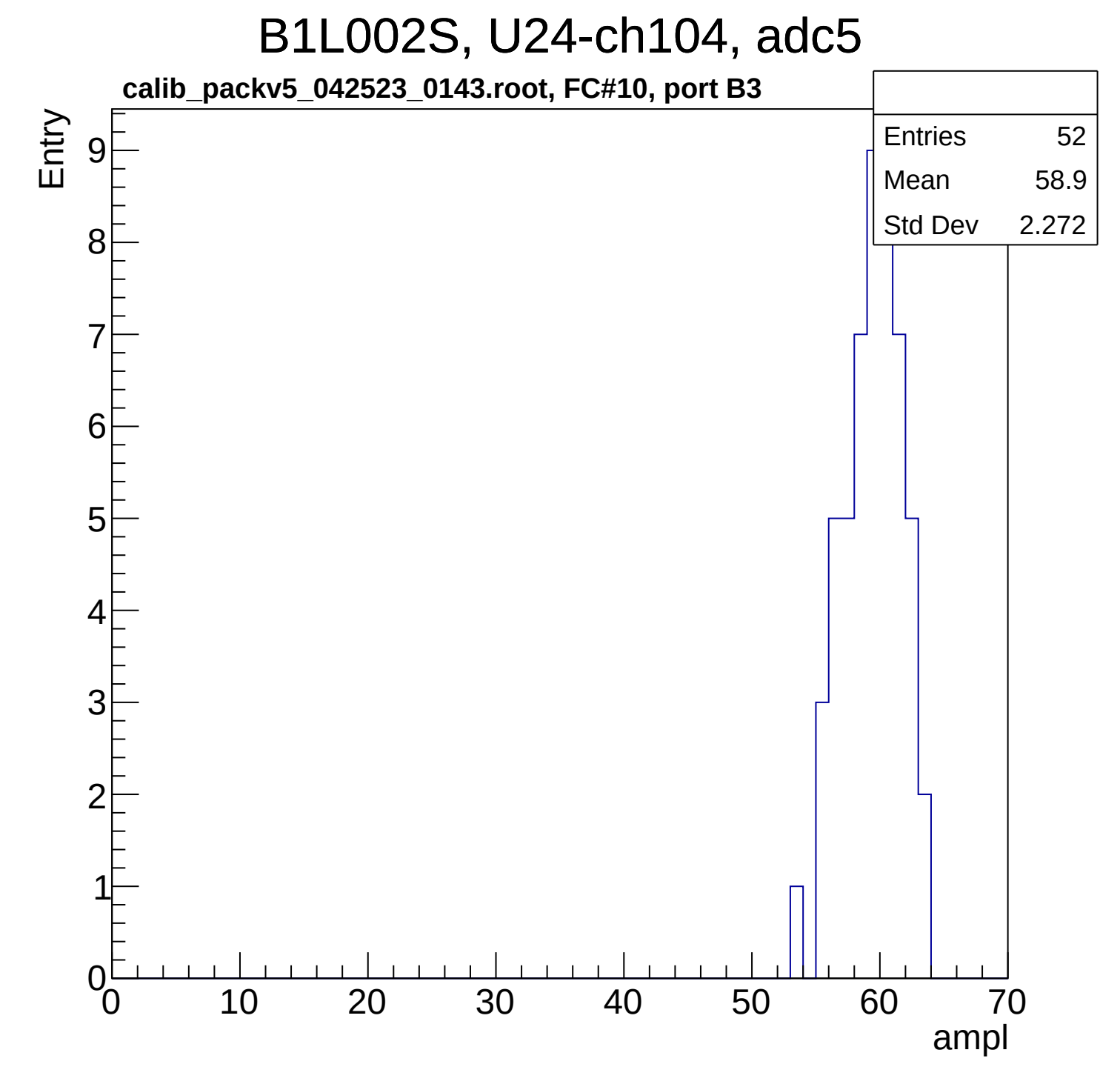
B1L002S, U24-ch104, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 58.9
Std Dev 2.272
Entry
ampl
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3
4
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6
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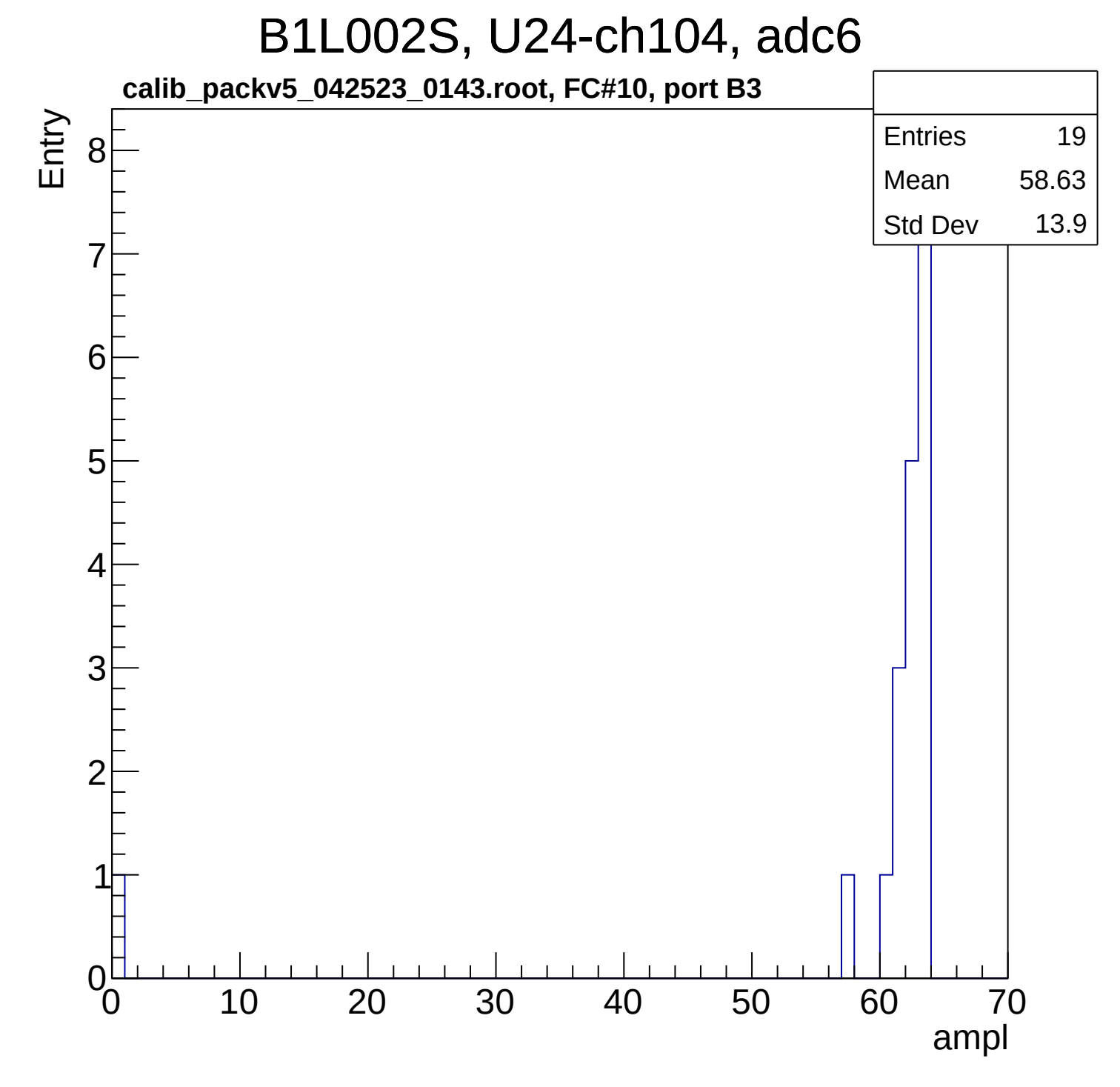
B1L002S, U24-ch104, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 58.63
Std Dev 13.9
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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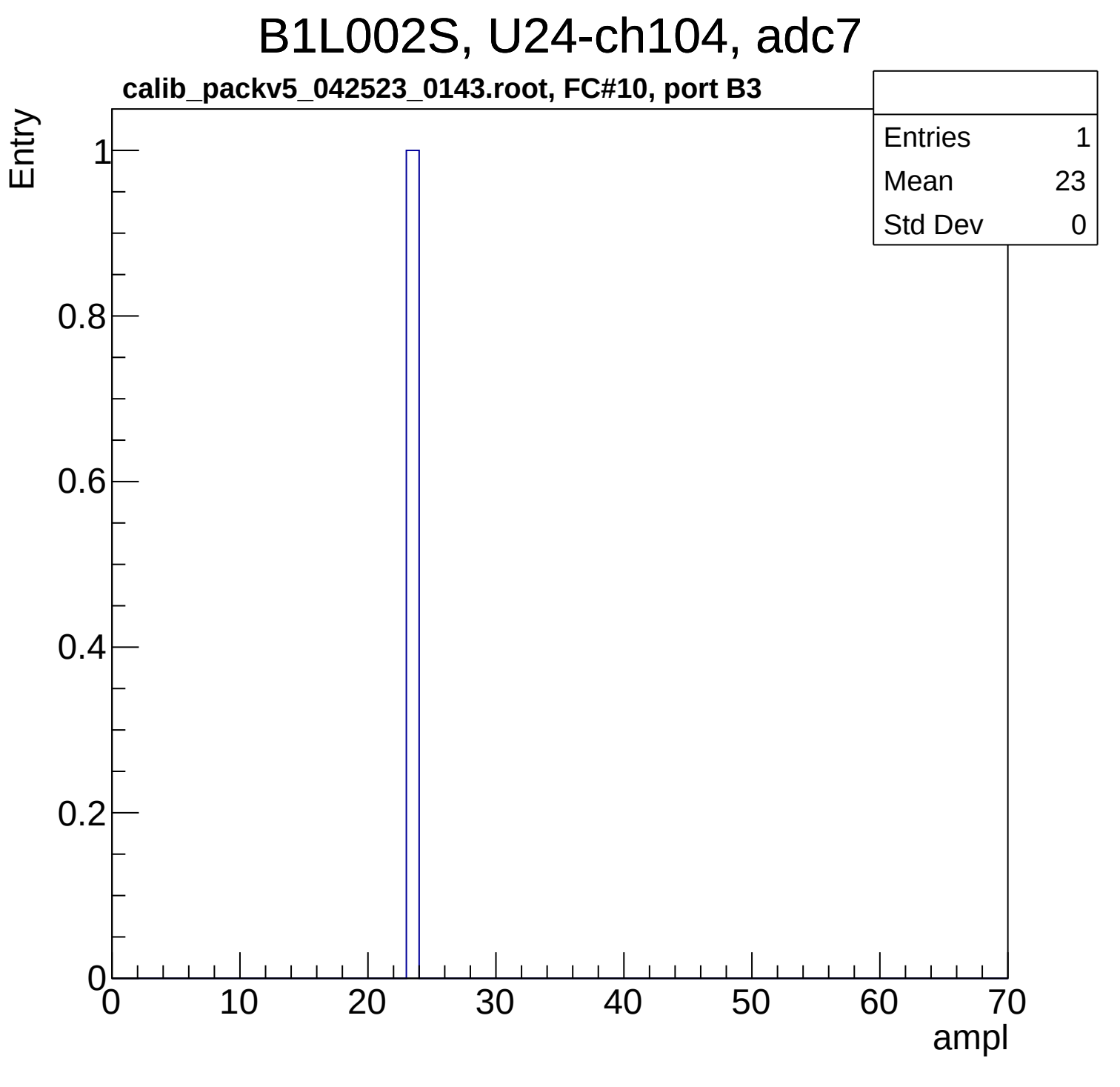
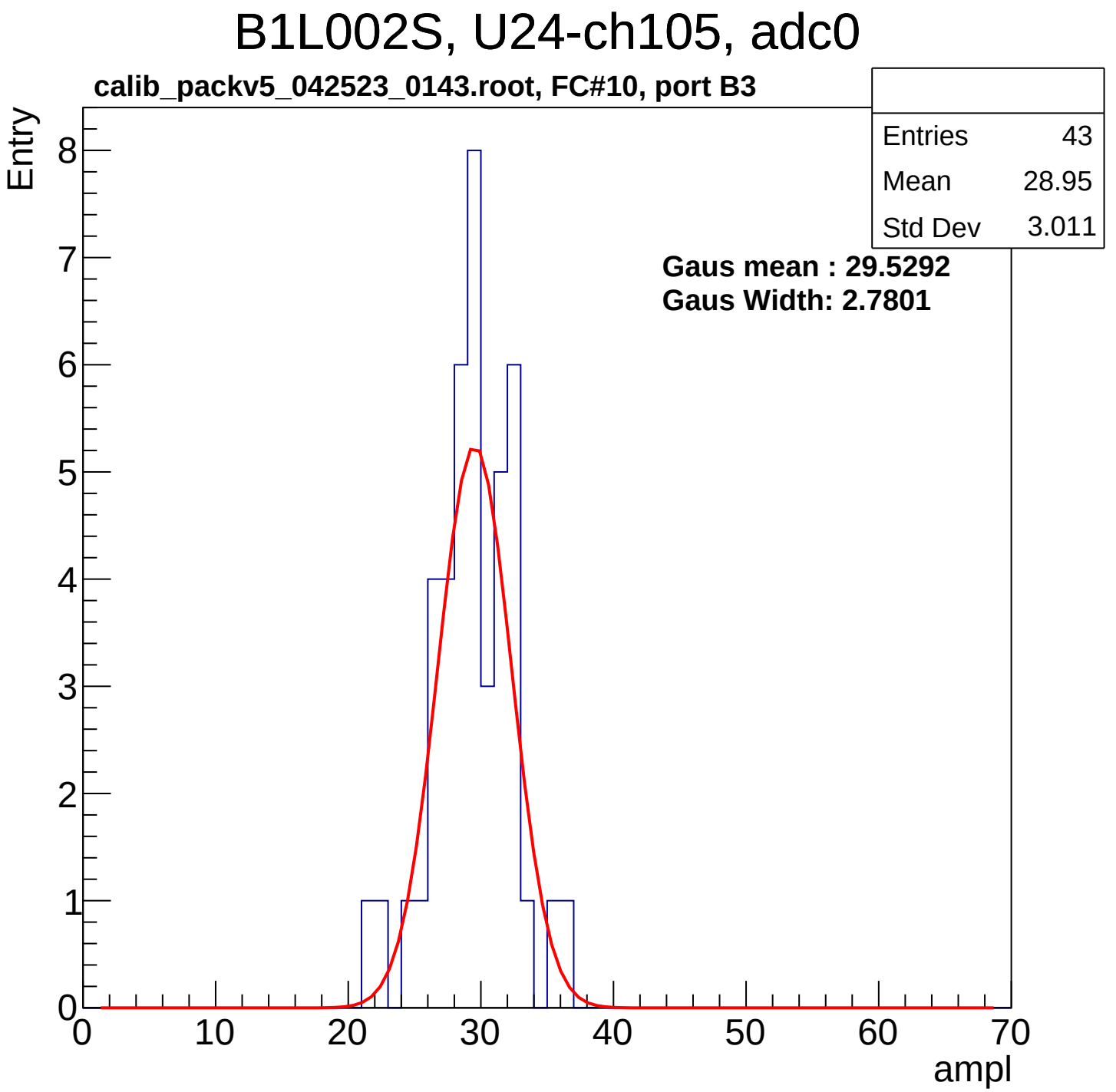

B1L002S, U24-ch104, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch105, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 28.95
Std Dev 3.011
Gaus mean : 29.5292
Gaus Width: 2.7801
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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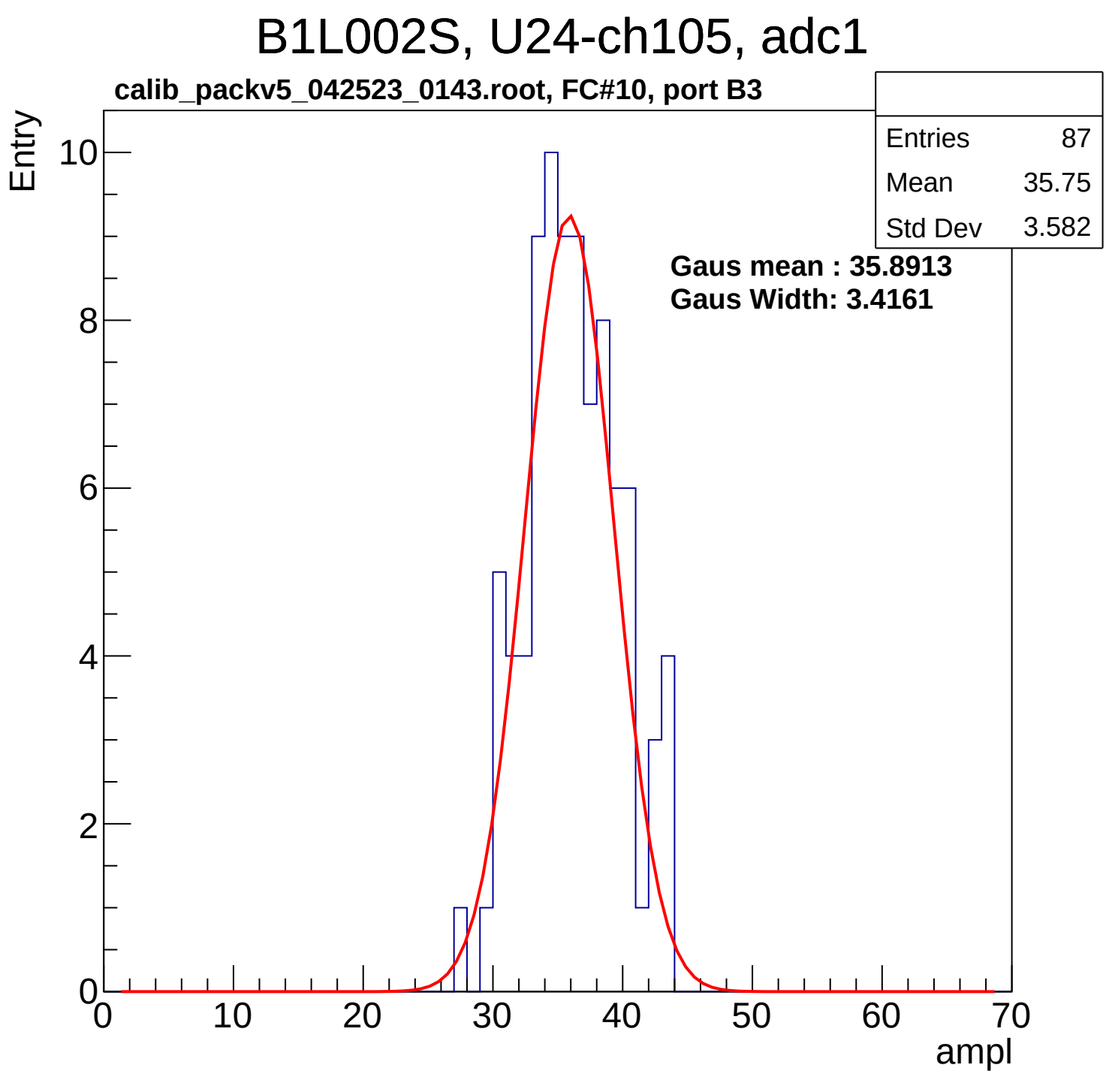
B1L002S, U24-ch105, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 87
Mean 35.75
Std Dev 3.582
Gaus mean : 35.8913
Gaus Width: 3.4161
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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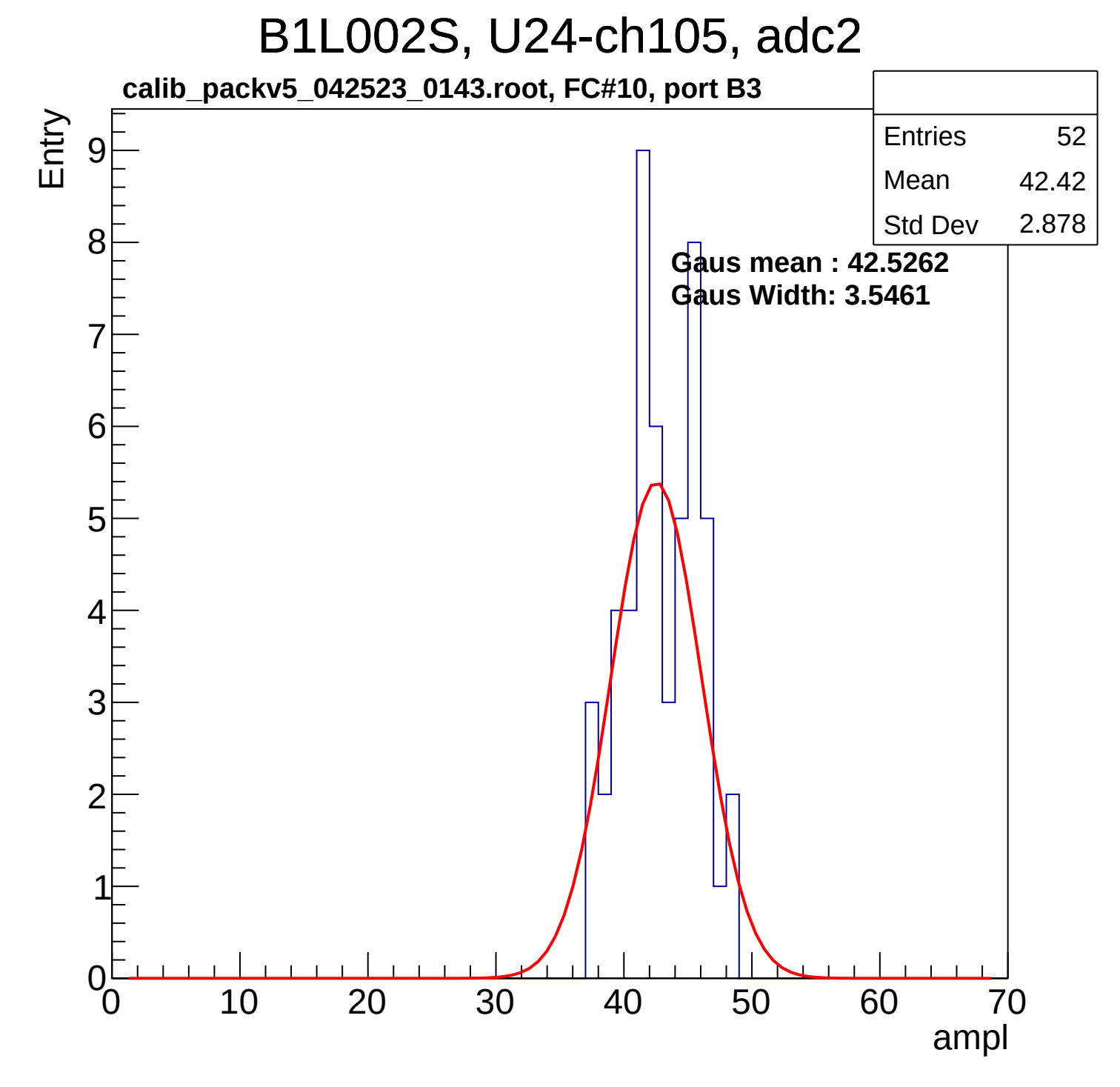
B1L002S, U24-ch105, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 42.42
Std Dev 2.878
Gaus mean : 42.5262
Gaus Width: 3.5461
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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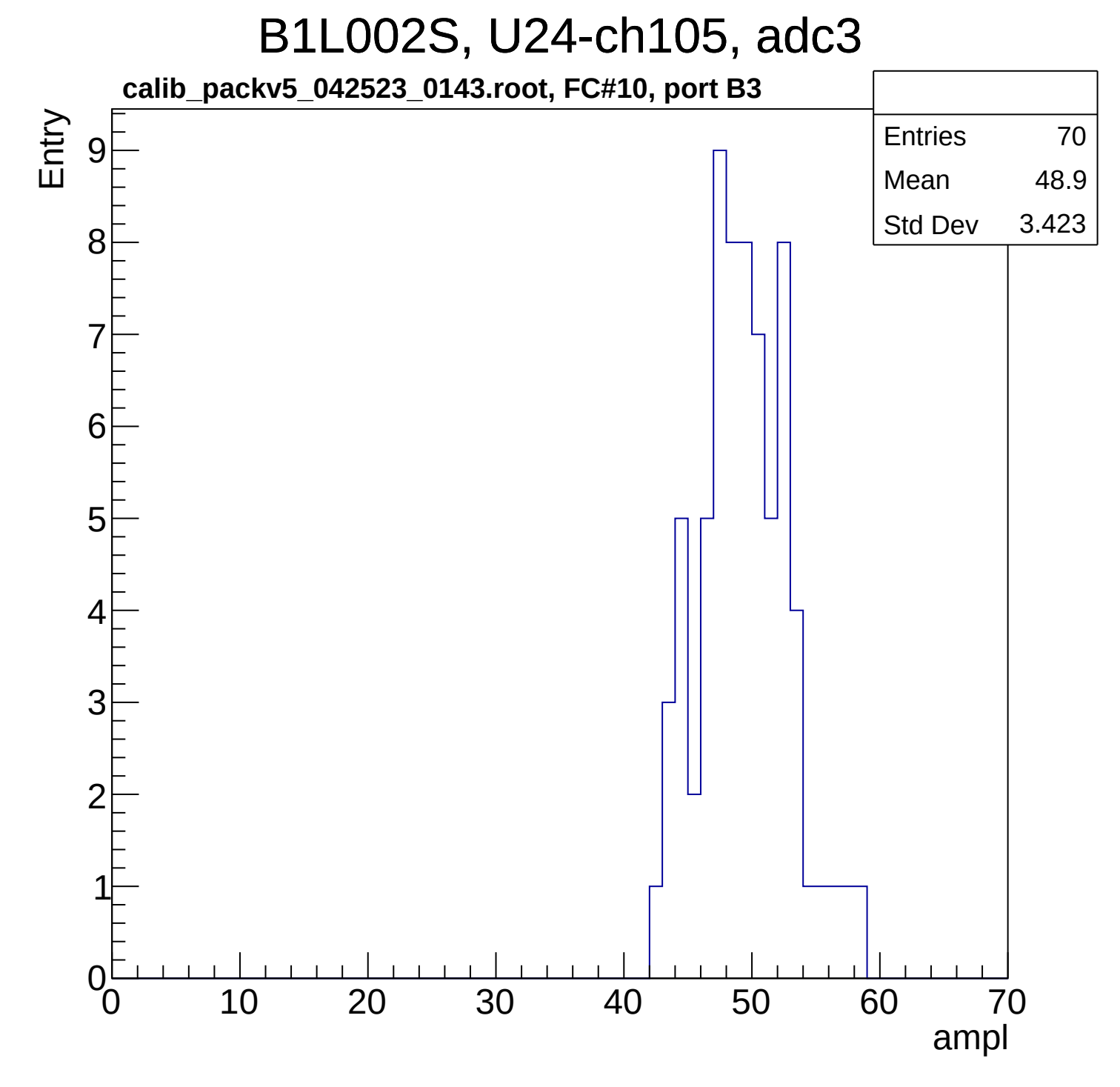
B1L002S, U24-ch105, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 48.9
Std Dev 3.423
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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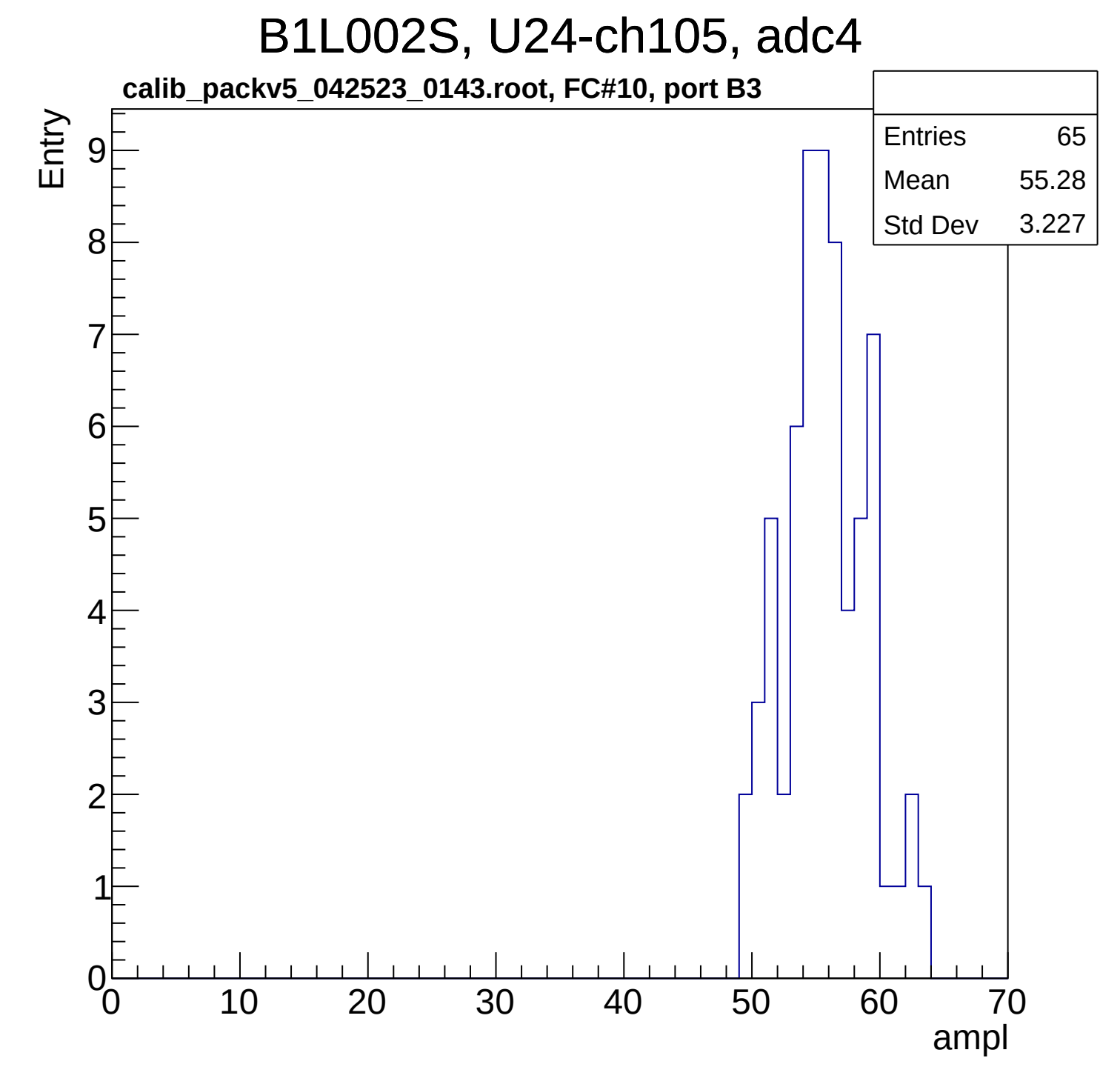
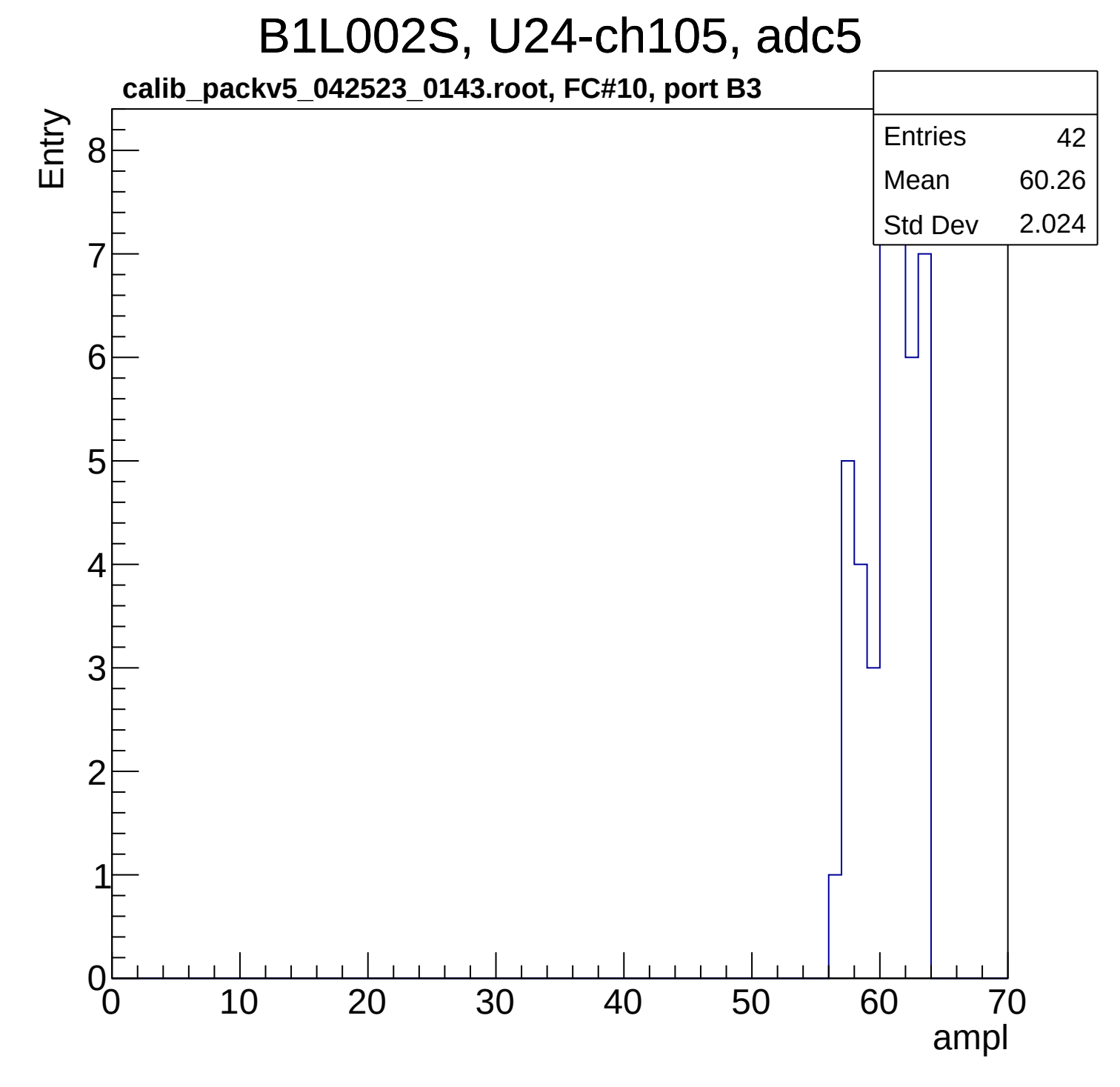
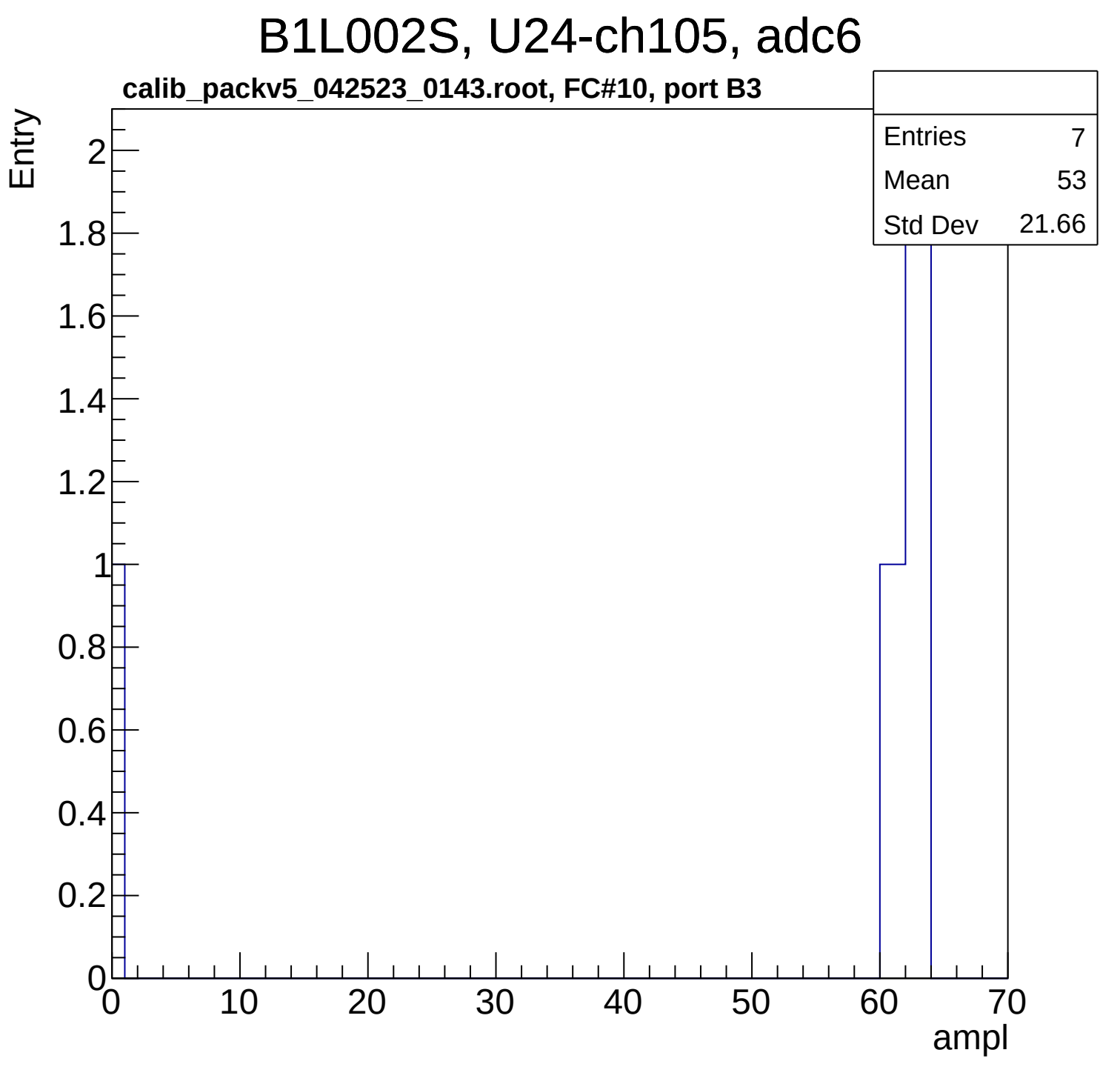

B1L002S, U24-ch105, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 55.28
Std Dev 3.227
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch105, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 60.26
Std Dev 2.024
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch105, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 7
Mean 53
Std Dev 21.66
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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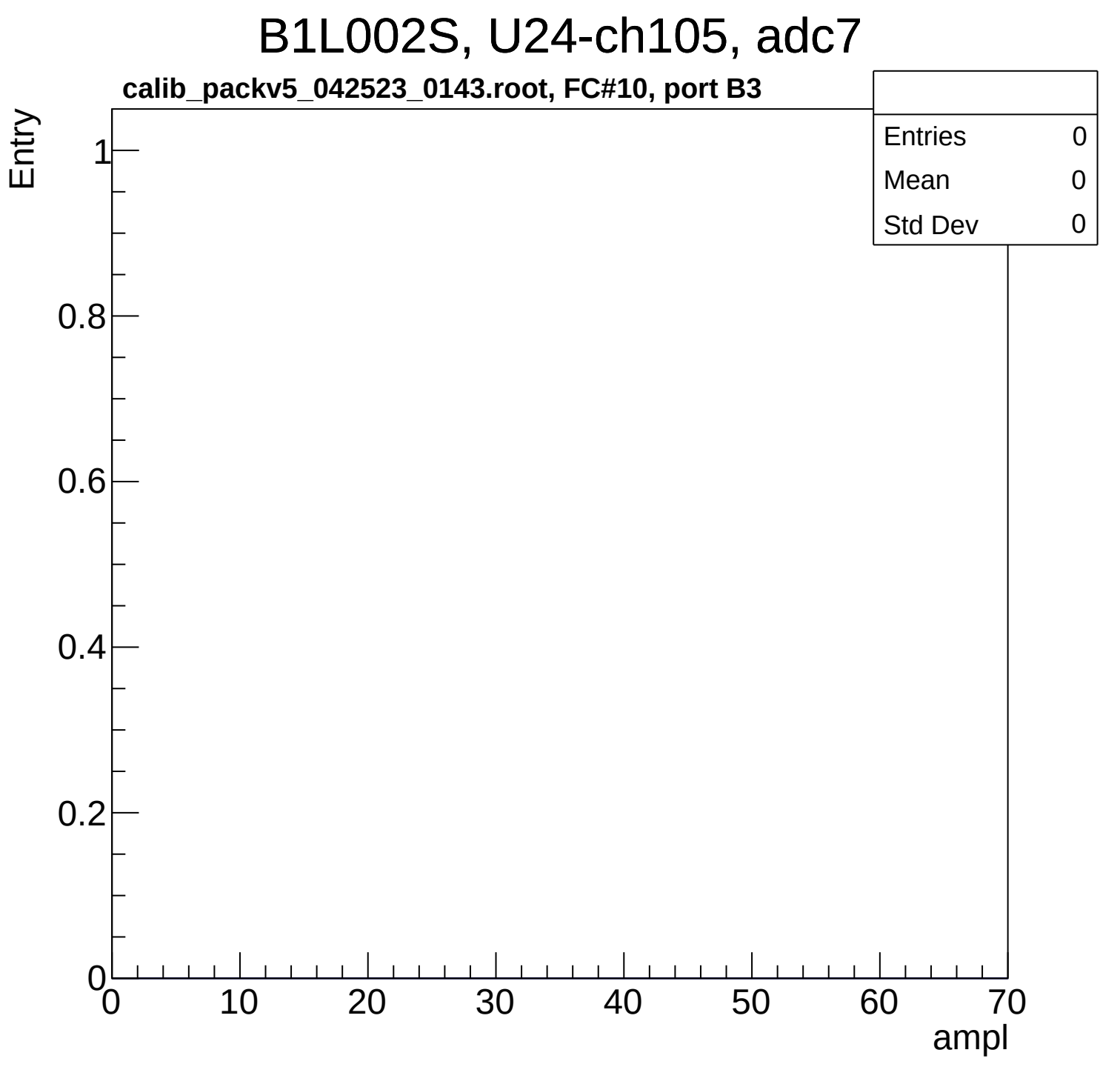
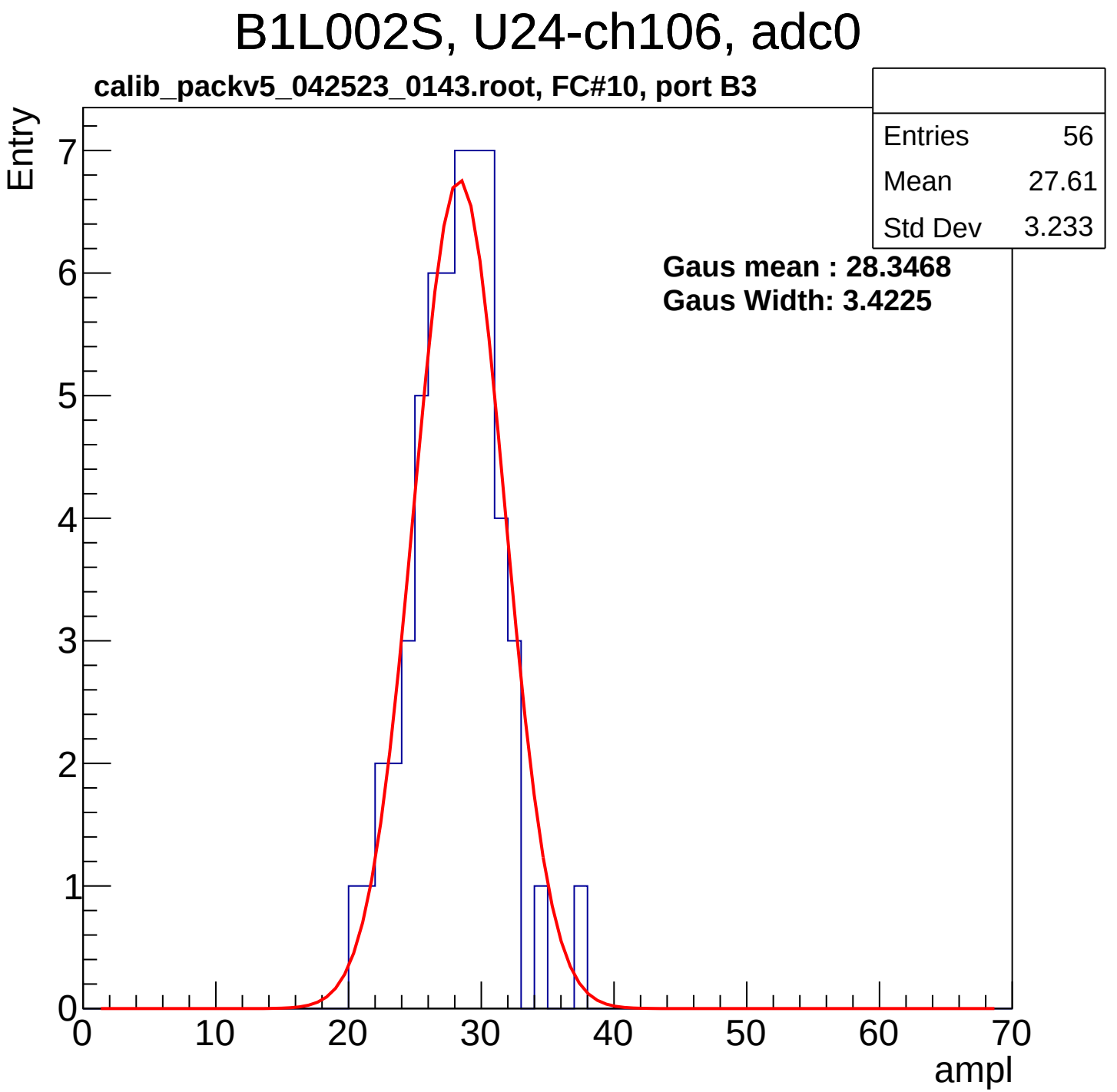
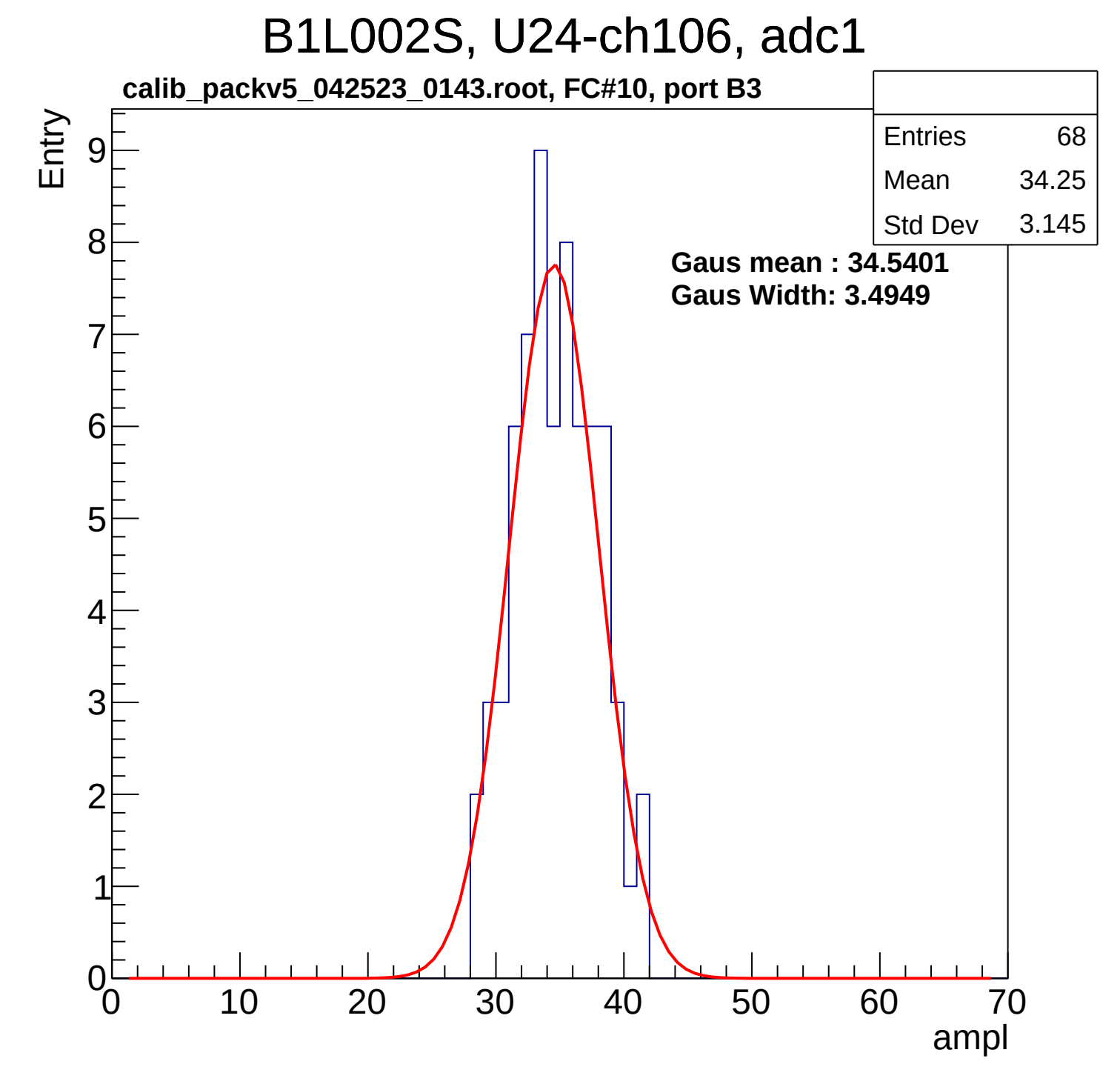

B1L002S, U24-ch105, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch106, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 27.61
Std Dev 3.233
Gaus mean : 28.3468
Gaus Width: 3.4225
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch106, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 34.25
Std Dev 3.145
Gaus mean : 34.5401
Gaus Width: 3.4949
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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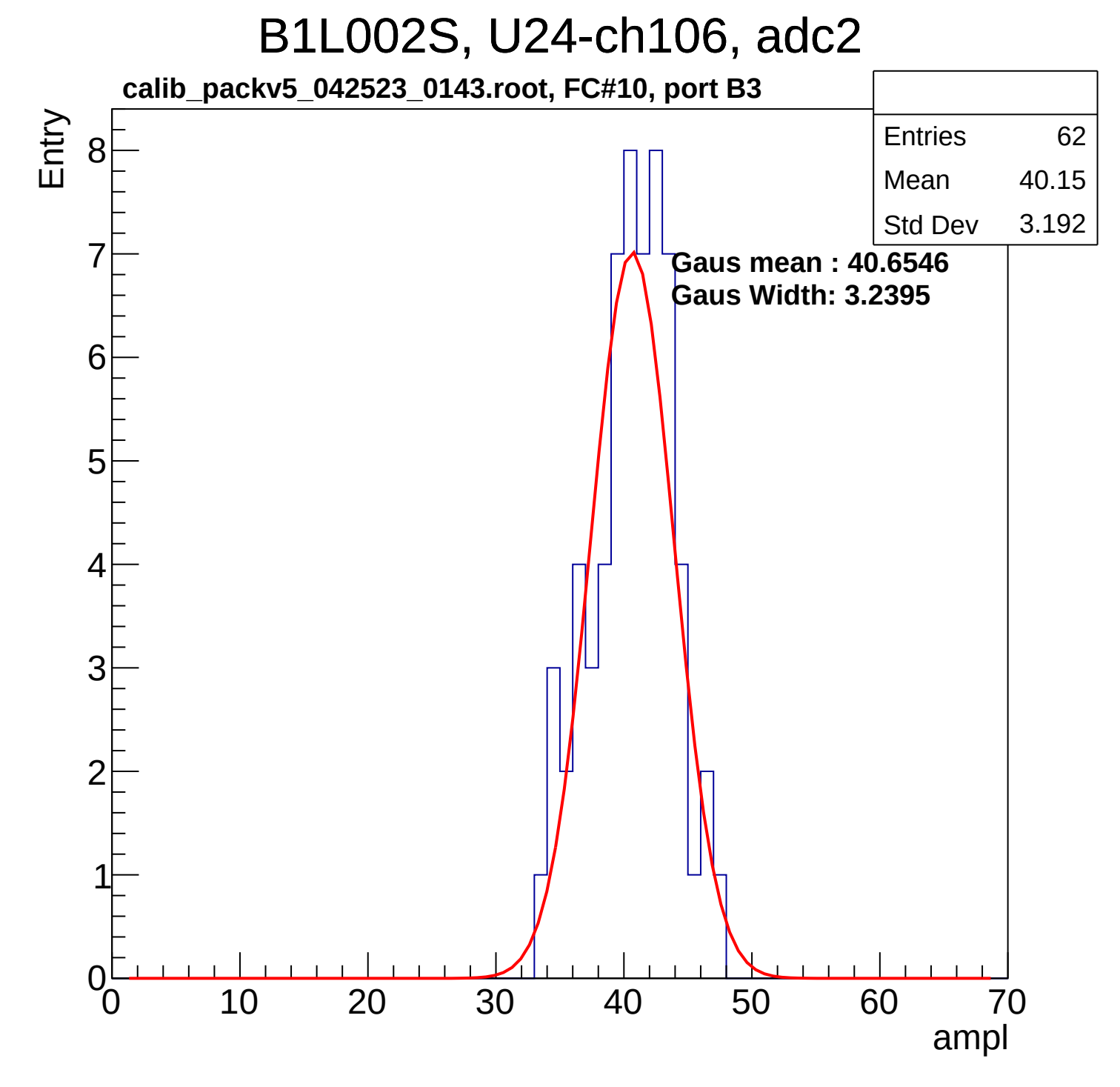
B1L002S, U24-ch106, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 40.15
Std Dev 3.192
Gaus mean : 40.6546
Gaus Width: 3.2395
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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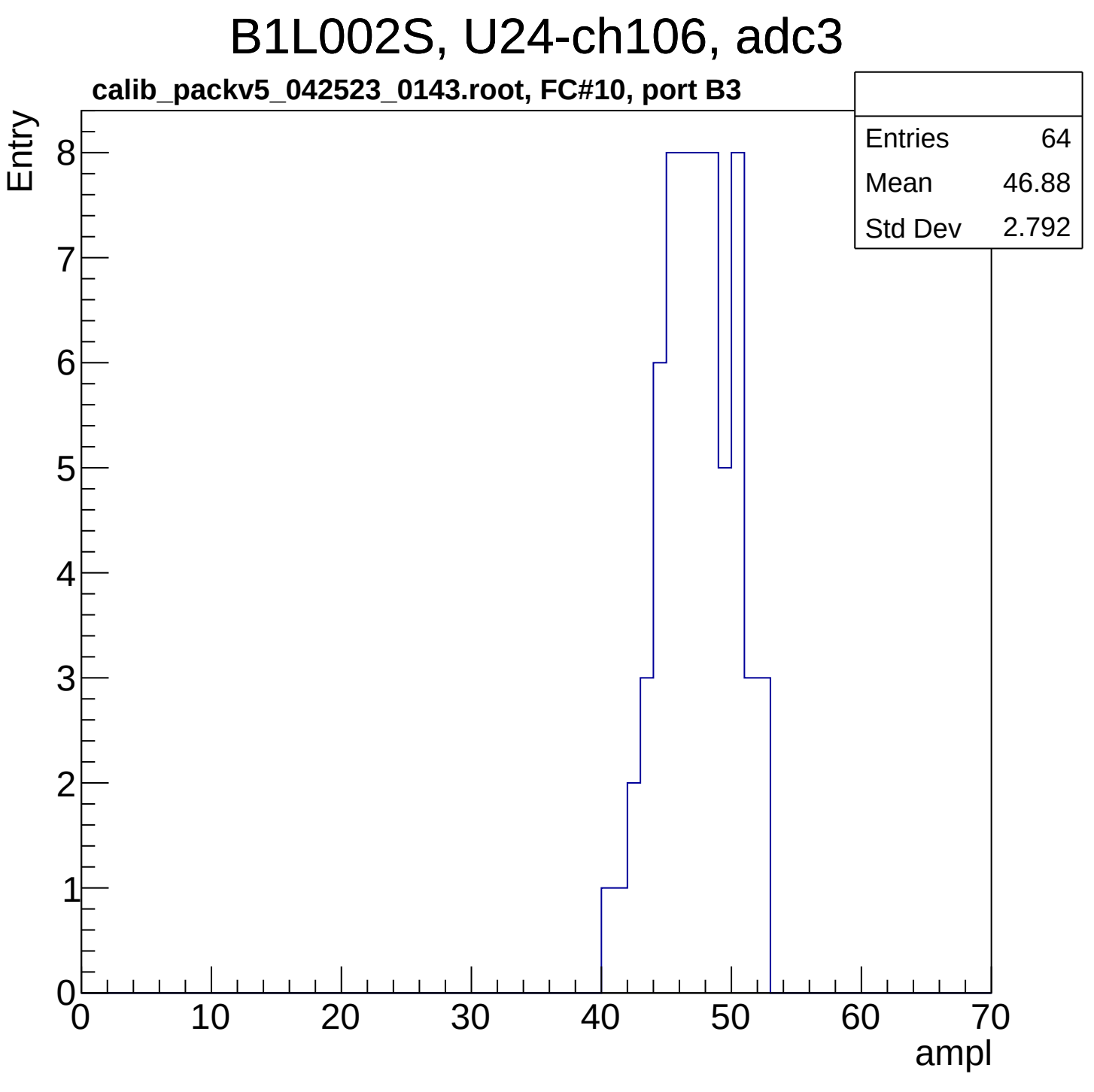
B1L002S, U24-ch106, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 46.88
Std Dev 2.792
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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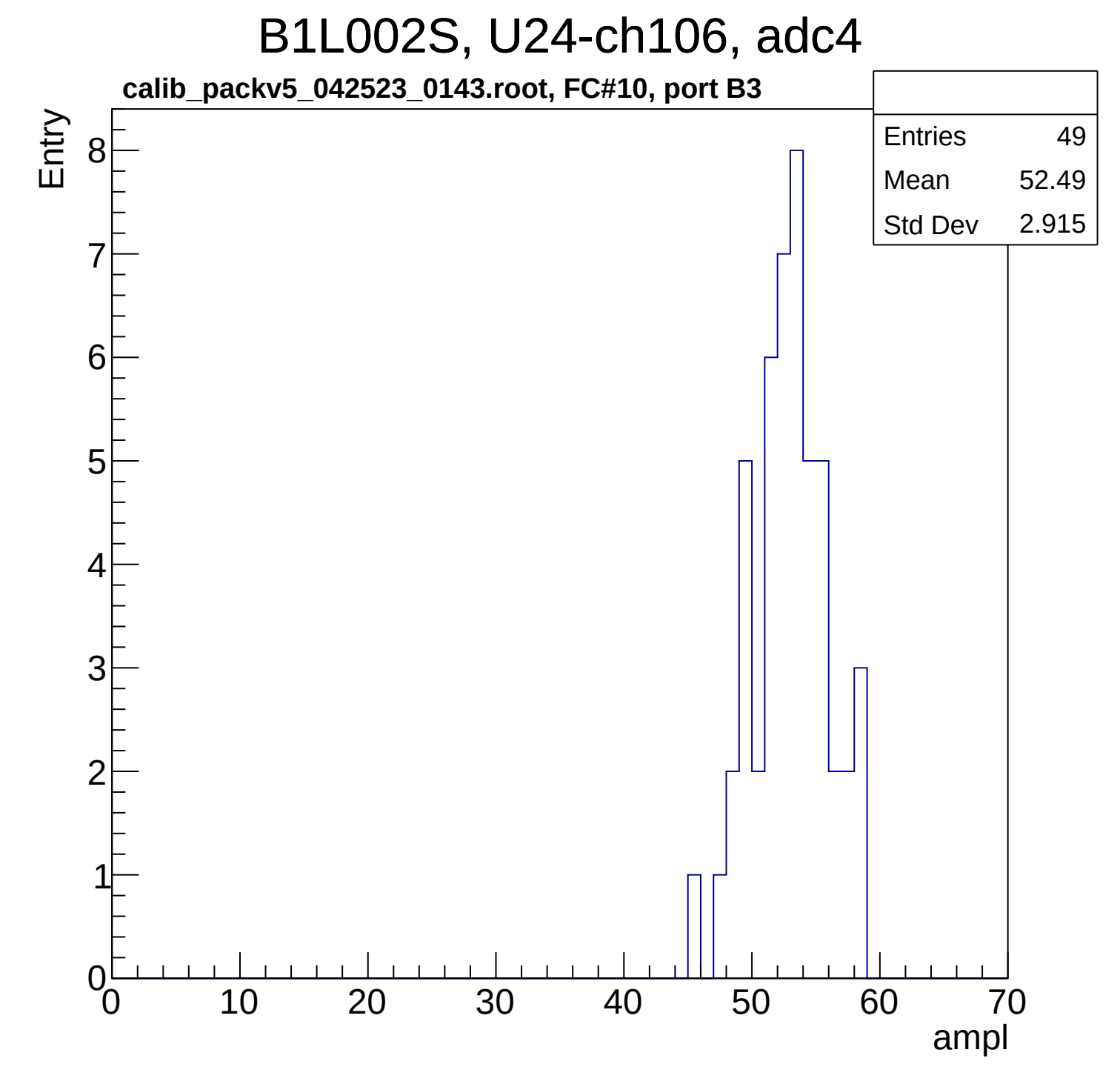
B1L002S, U24-ch106, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 52.49
Std Dev 2.915
Entry
ampl
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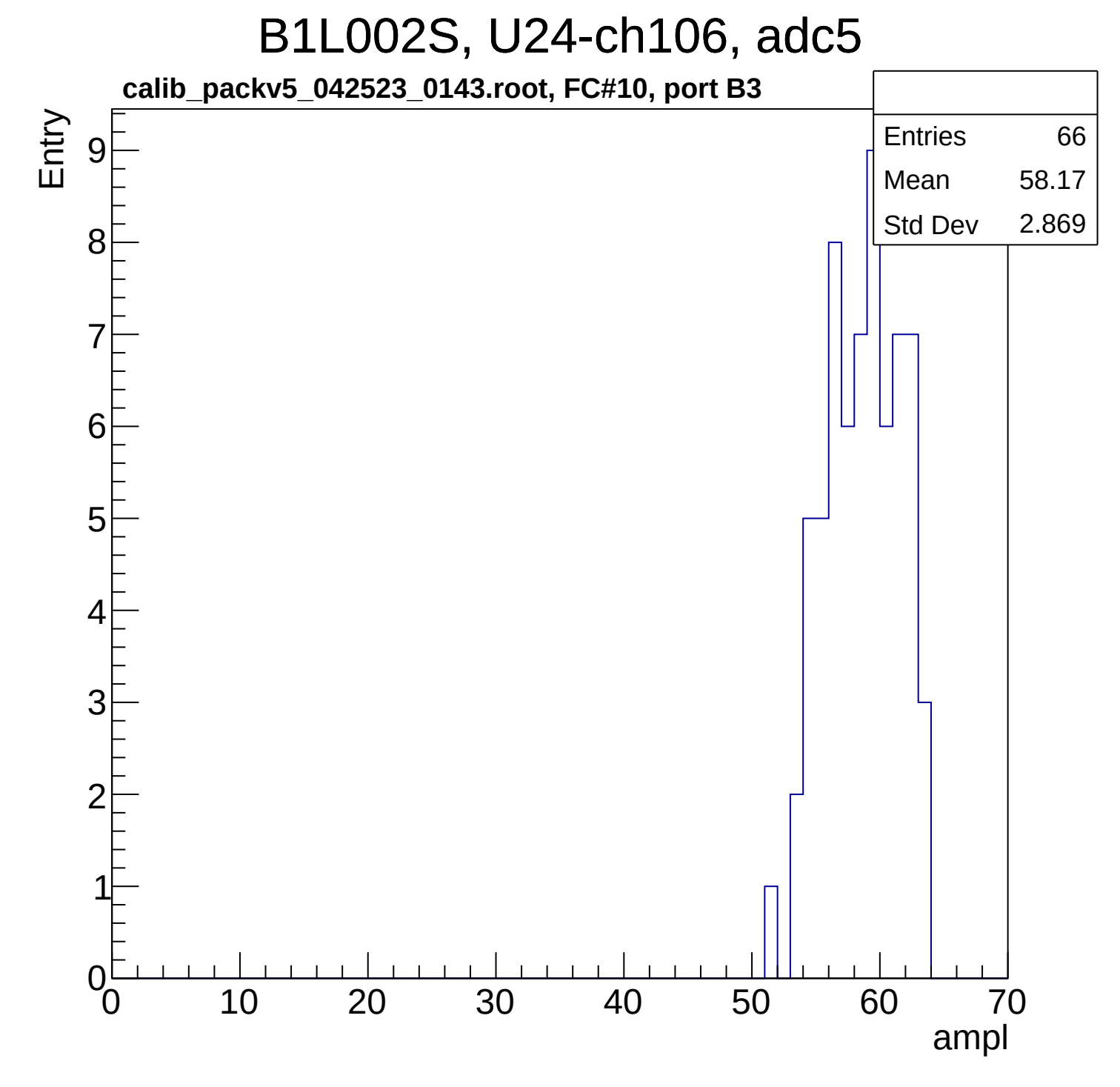
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch106, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.17
Std Dev 2.869
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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60
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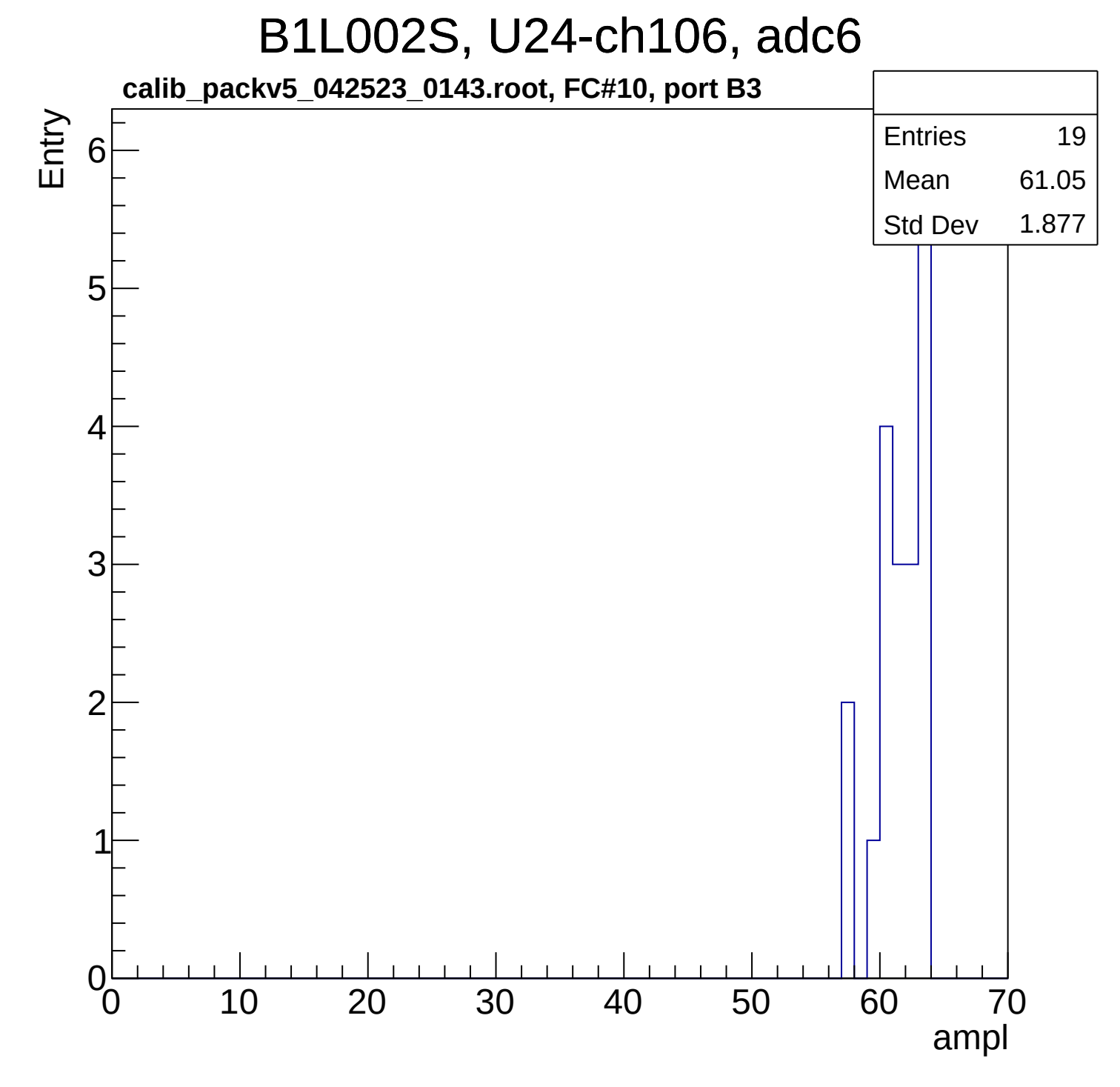
B1L002S, U24-ch106, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 19
Mean 61.05
Std Dev 1.877
Entry
ampl
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1
2
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0
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20
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40
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60
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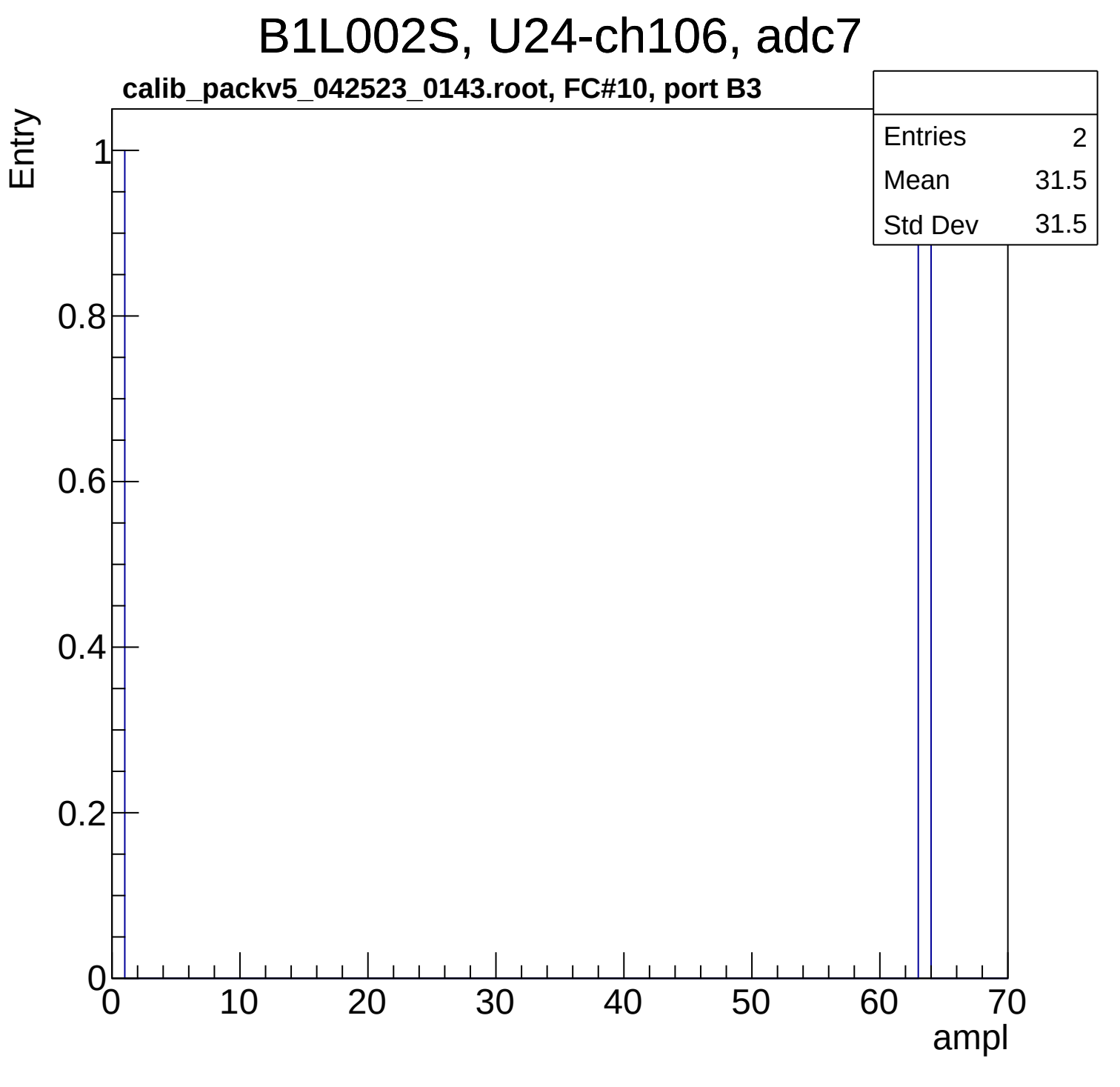
B1L002S, U24-ch106, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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0.6
0.4
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0
0
10
20
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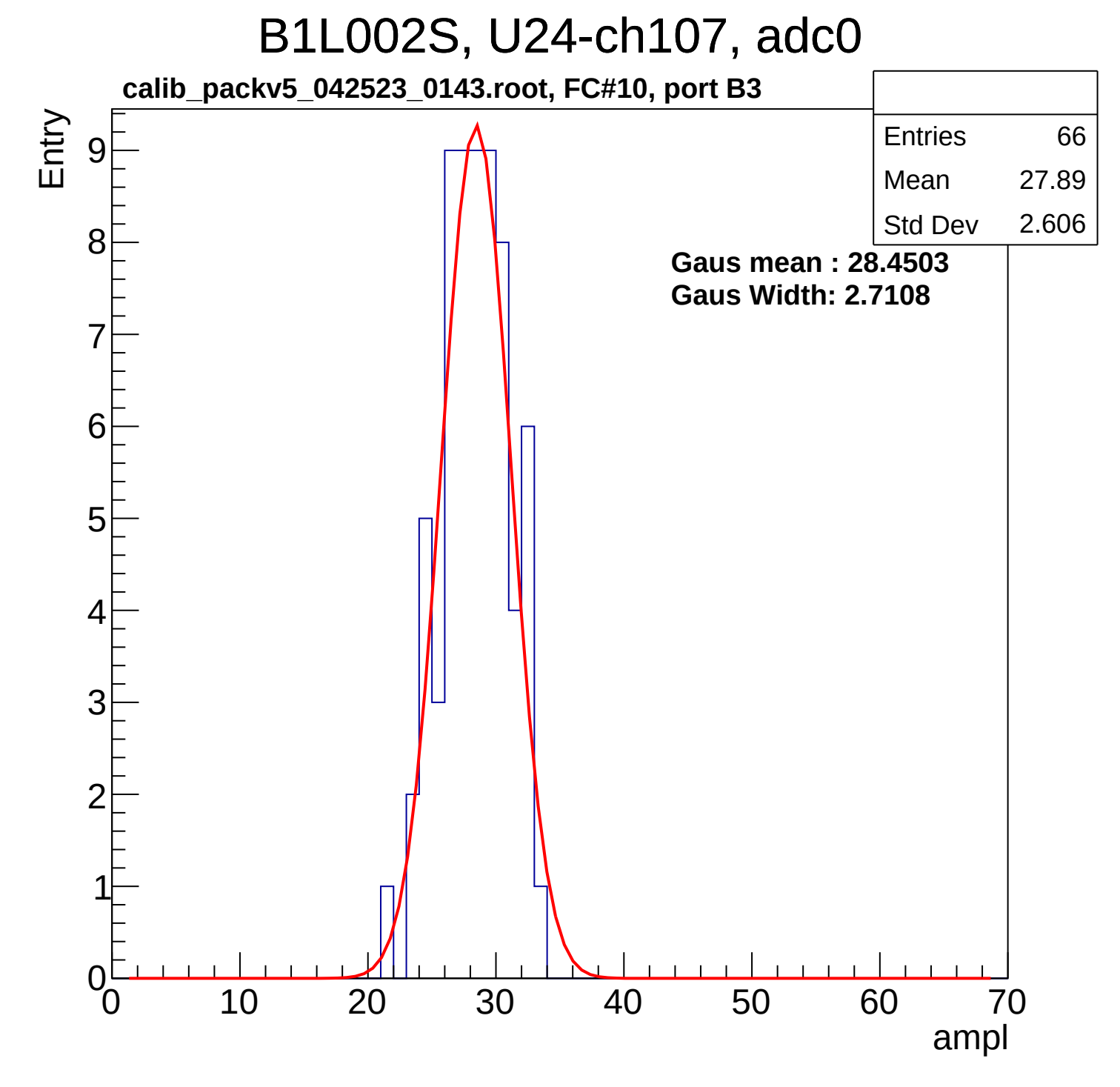
50
60
70
ampl


B1L002S, U24-ch107, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 27.89
Std Dev 2.606
Gaus mean : 28.4503
Gaus Width: 2.7108
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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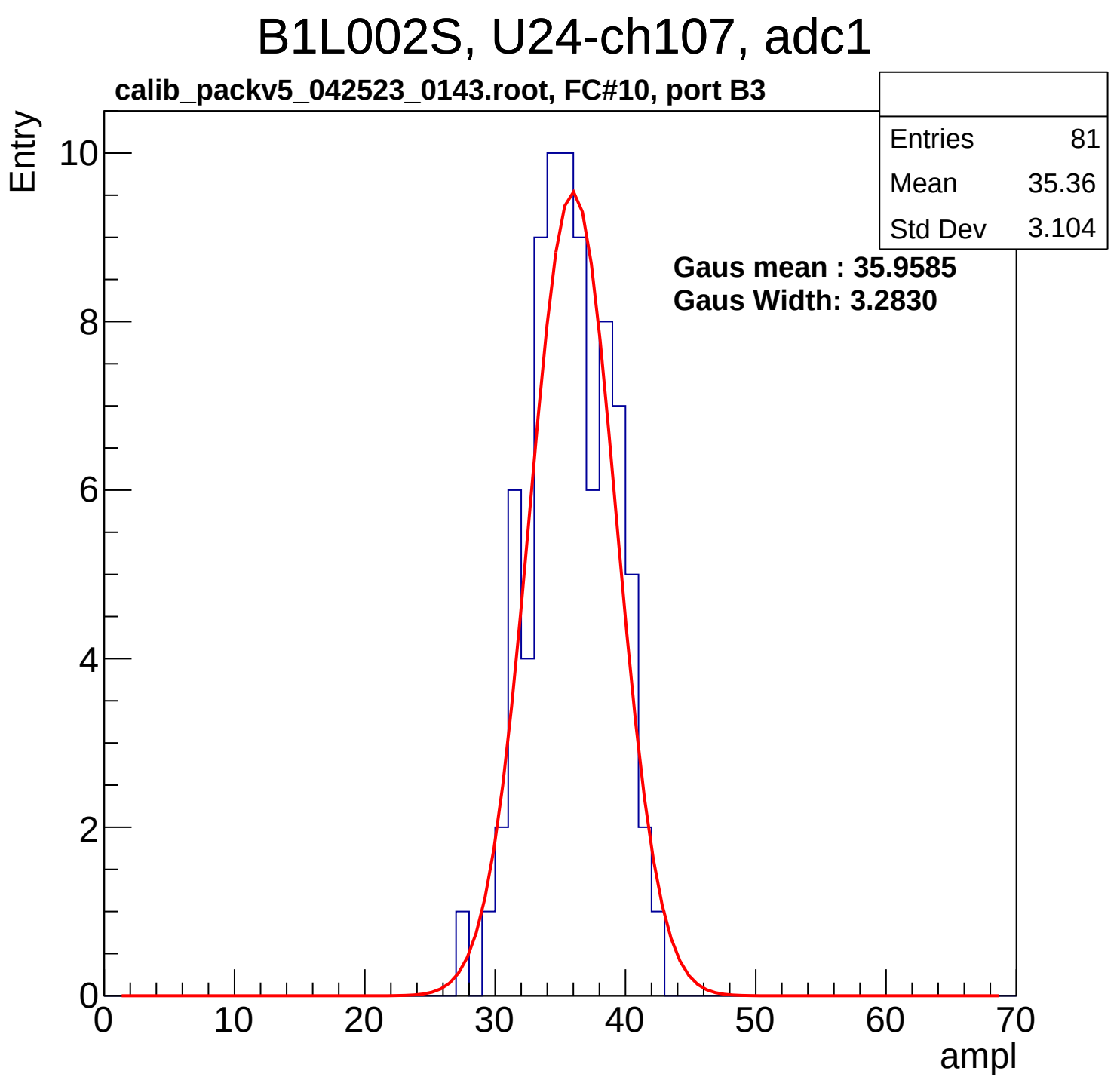
B1L002S, U24-ch107, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 81
Mean 35.36
Std Dev 3.104
Gaus mean : 35.9585
Gaus Width: 3.2830
Entry
ampl
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2
4
6
8
10
0
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20
30
40
50
60
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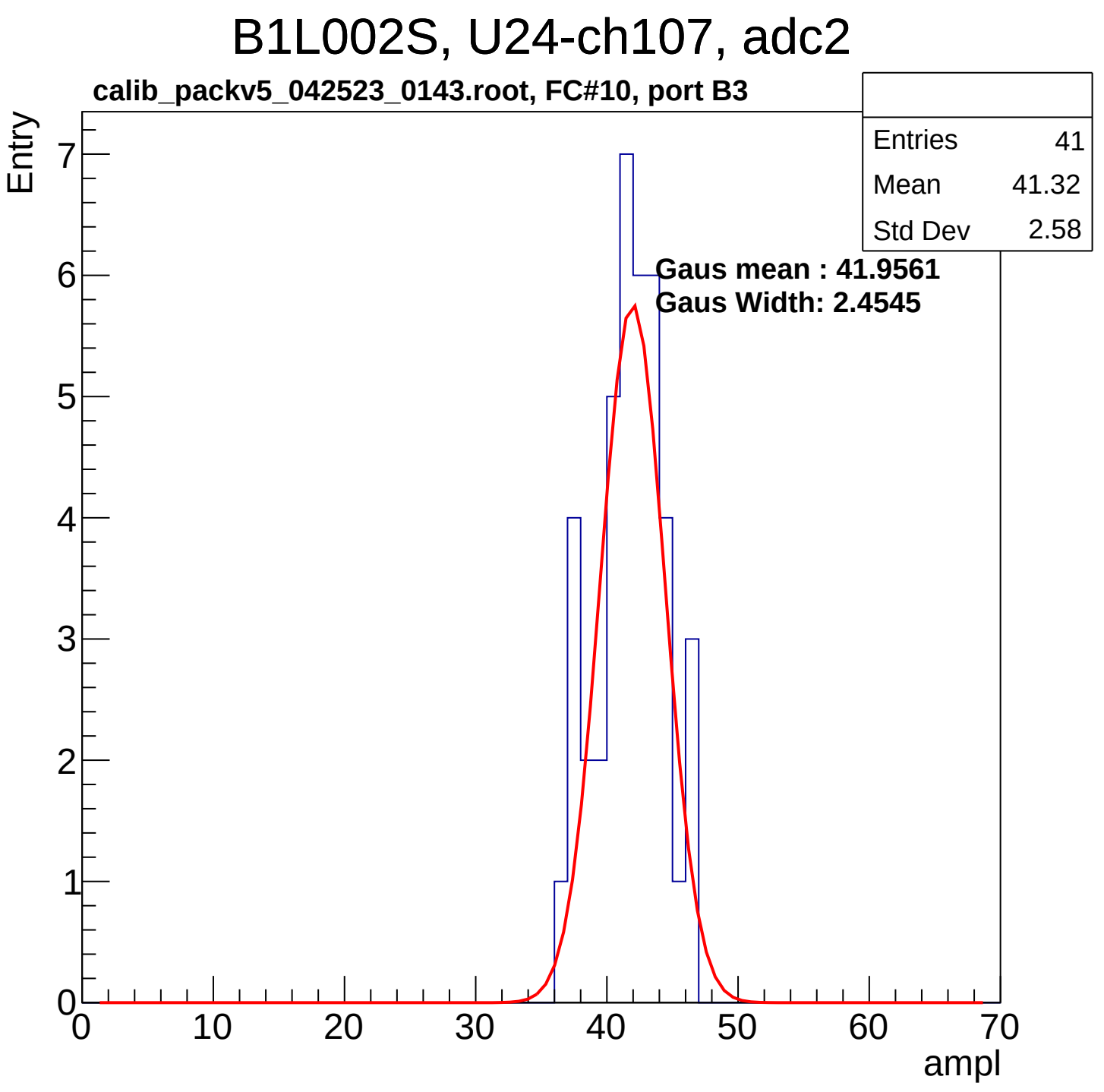
B1L002S, U24-ch107, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 41
Mean 41.32
Std Dev 2.58
Gaus mean : 41.9561
Gaus Width: 2.4545
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
30
40
50
60
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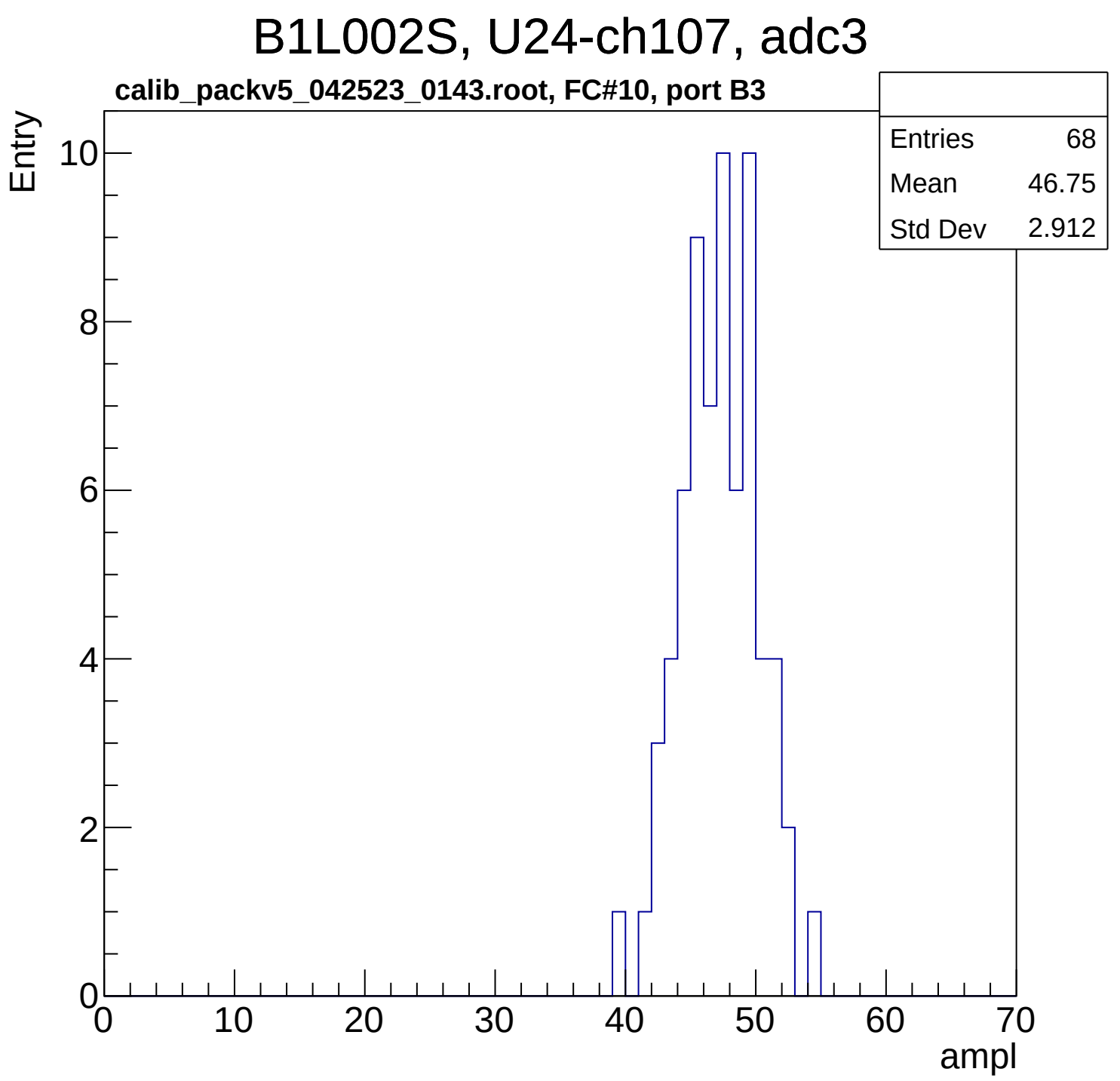
B1L002S, U24-ch107, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 46.75
Std Dev 2.912
Entry
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2
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6
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10
0
10
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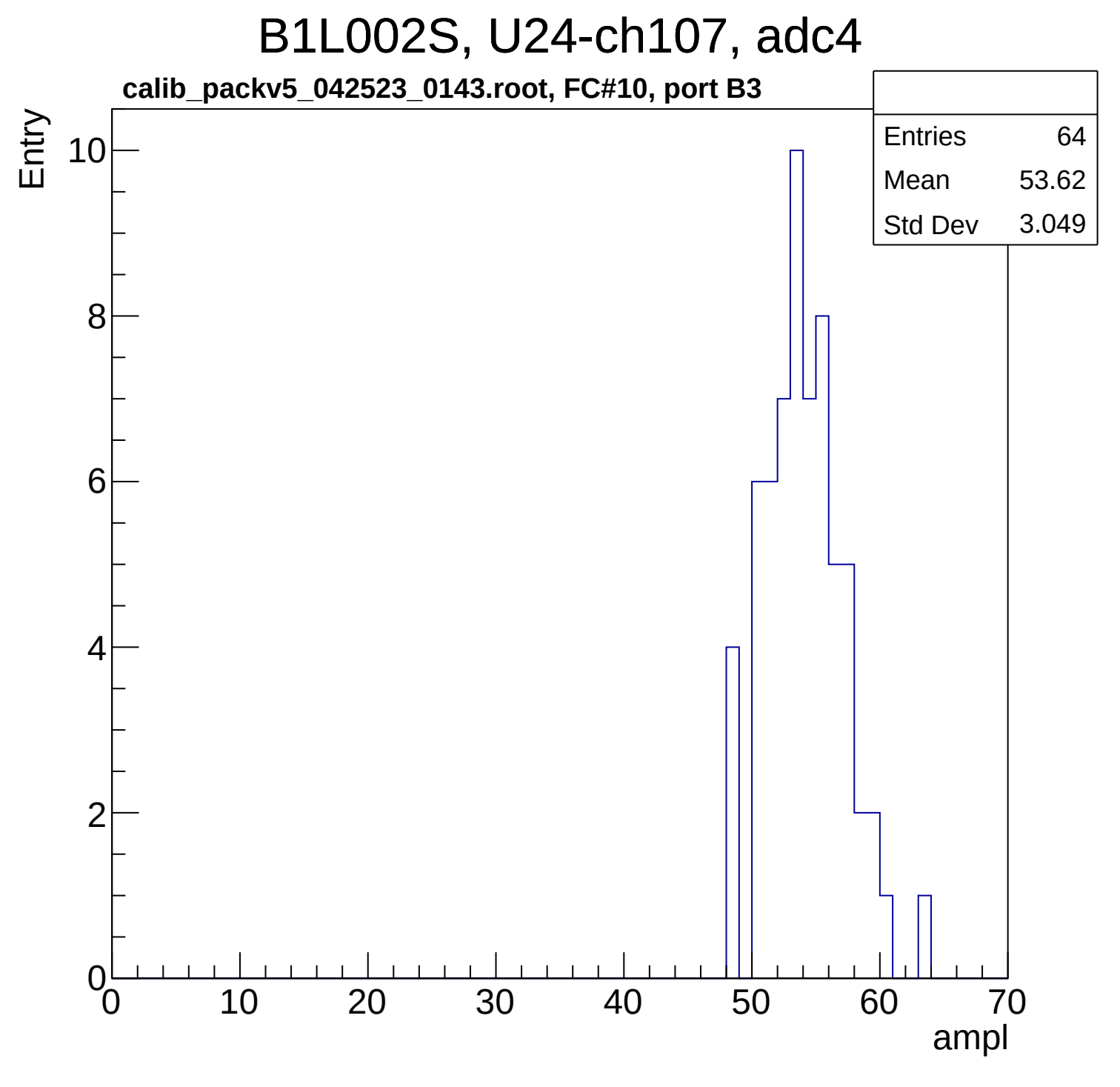
30
40
50
60
70
ampl


B1L002S, U24-ch107, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.62
Std Dev 3.049
Entry
0
2
4
6
8
10
0
10
20
30
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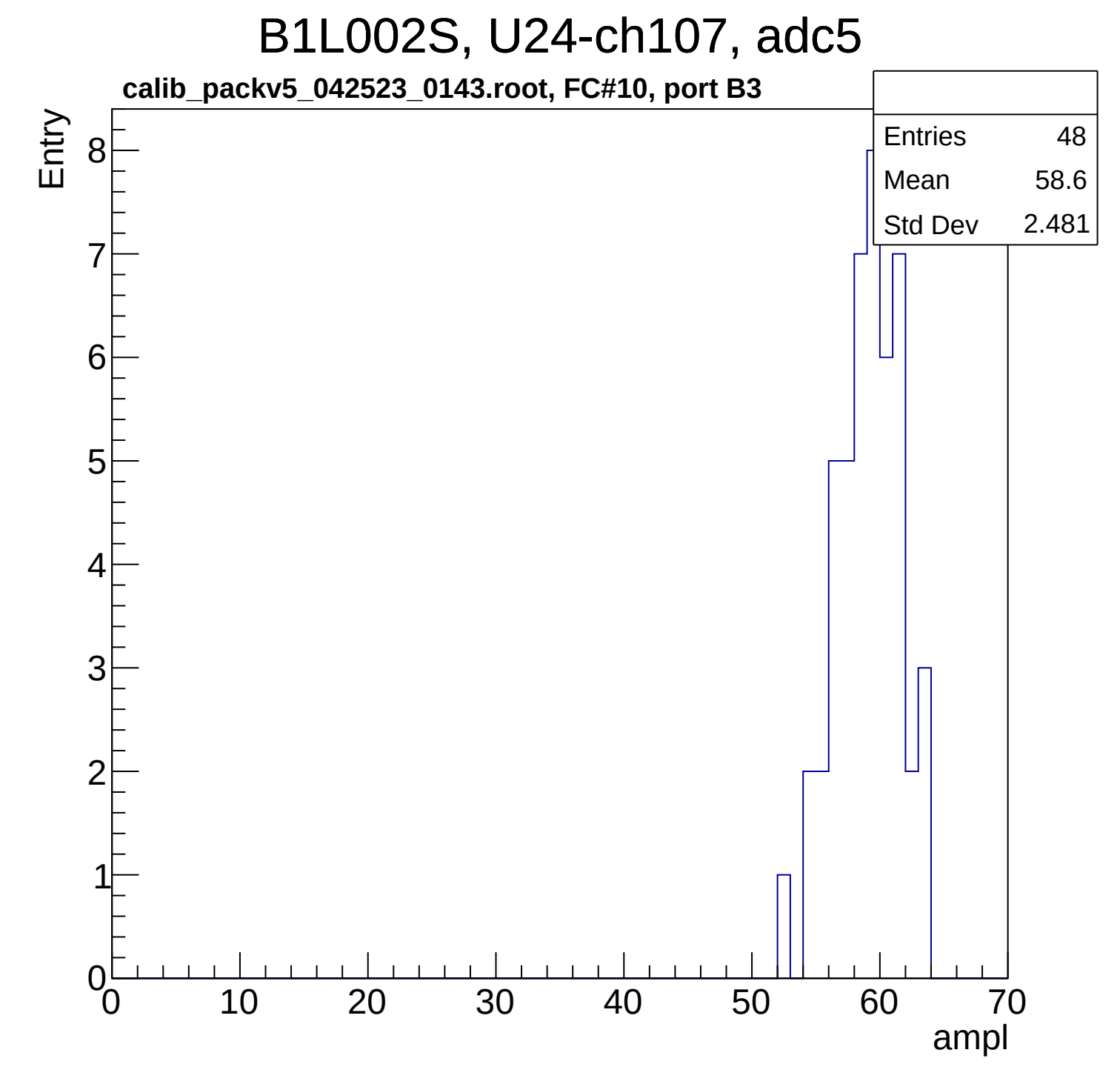
50
60
70
ampl


B1L002S, U24-ch107, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 58.6
Std Dev 2.481
Entry
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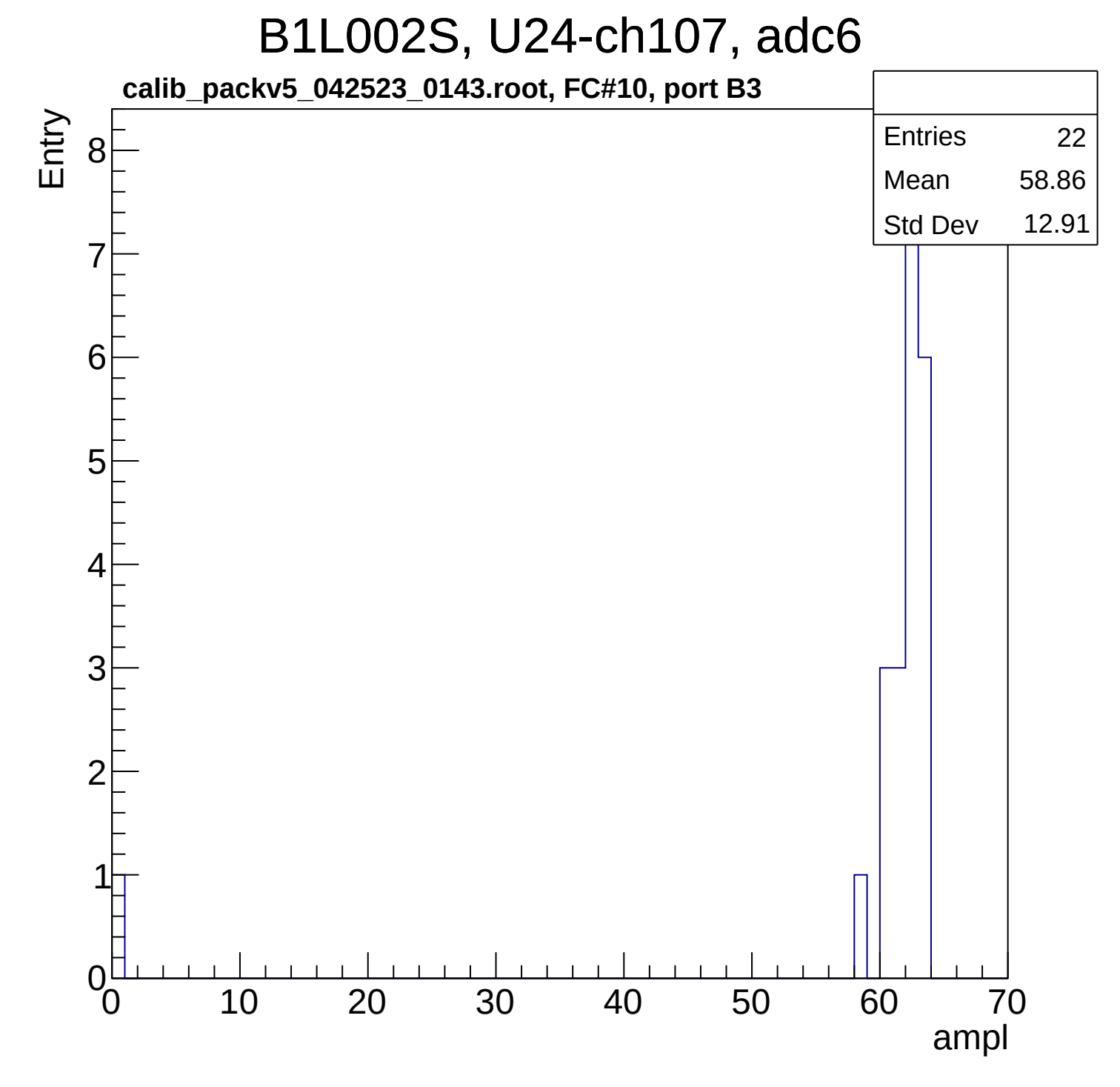
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch107, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.86
Std Dev 12.91
Entry
ampl
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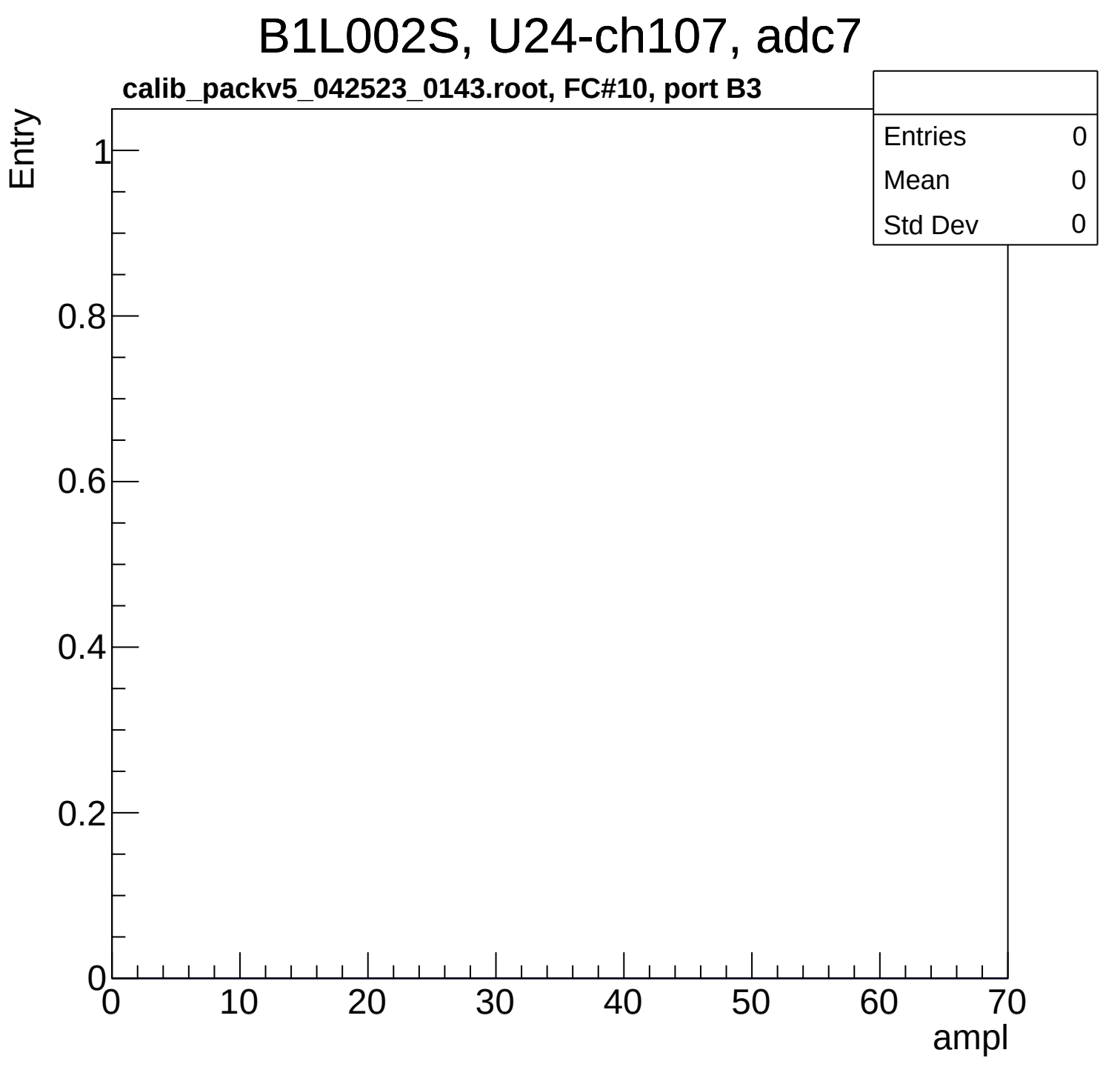
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch107, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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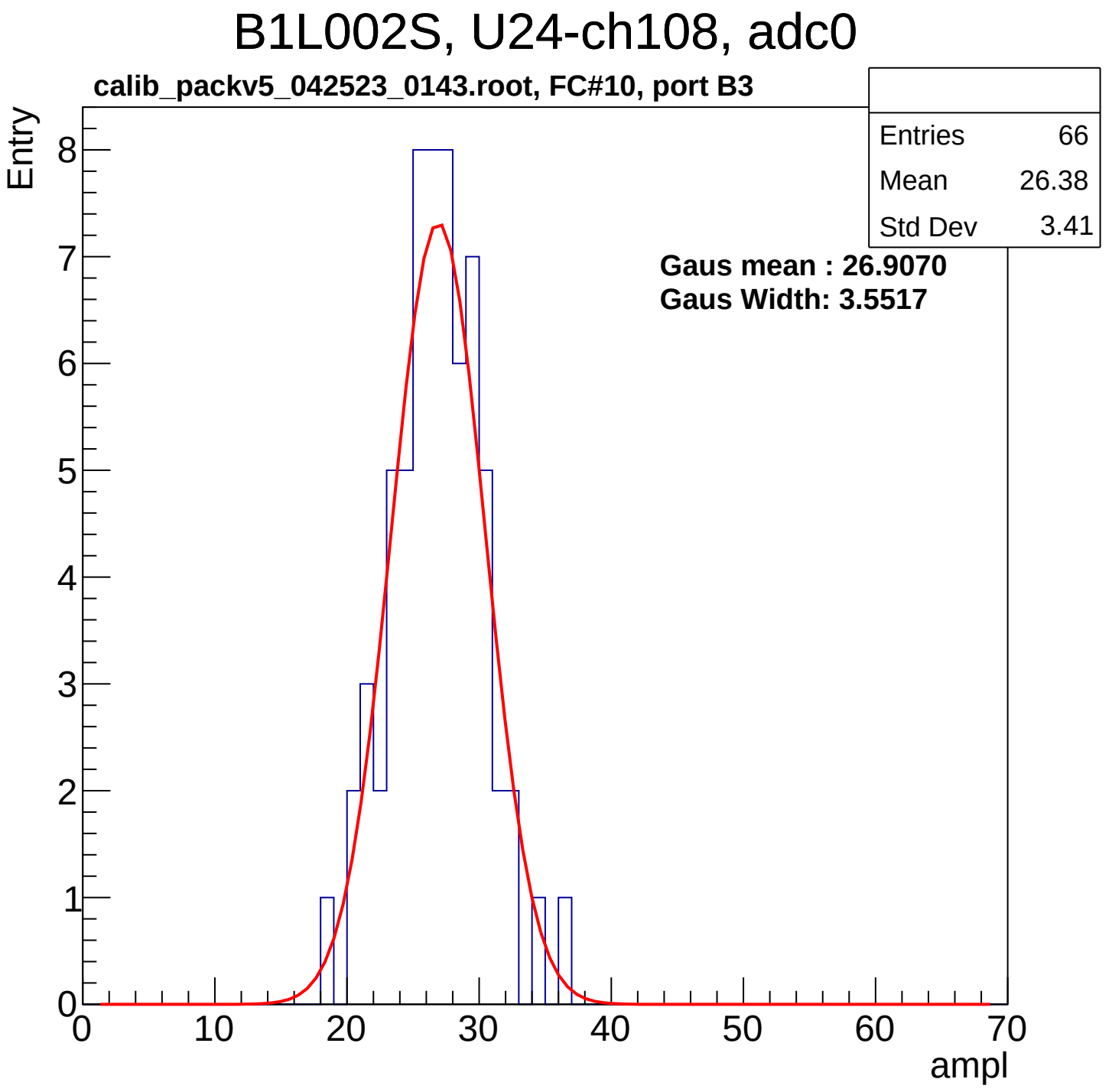
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch108, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 26.38
Std Dev 3.41
Gaus mean : 26.9070
Gaus Width: 3.5517
Entry
ampl
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2
3
4
5
6
7
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0
10
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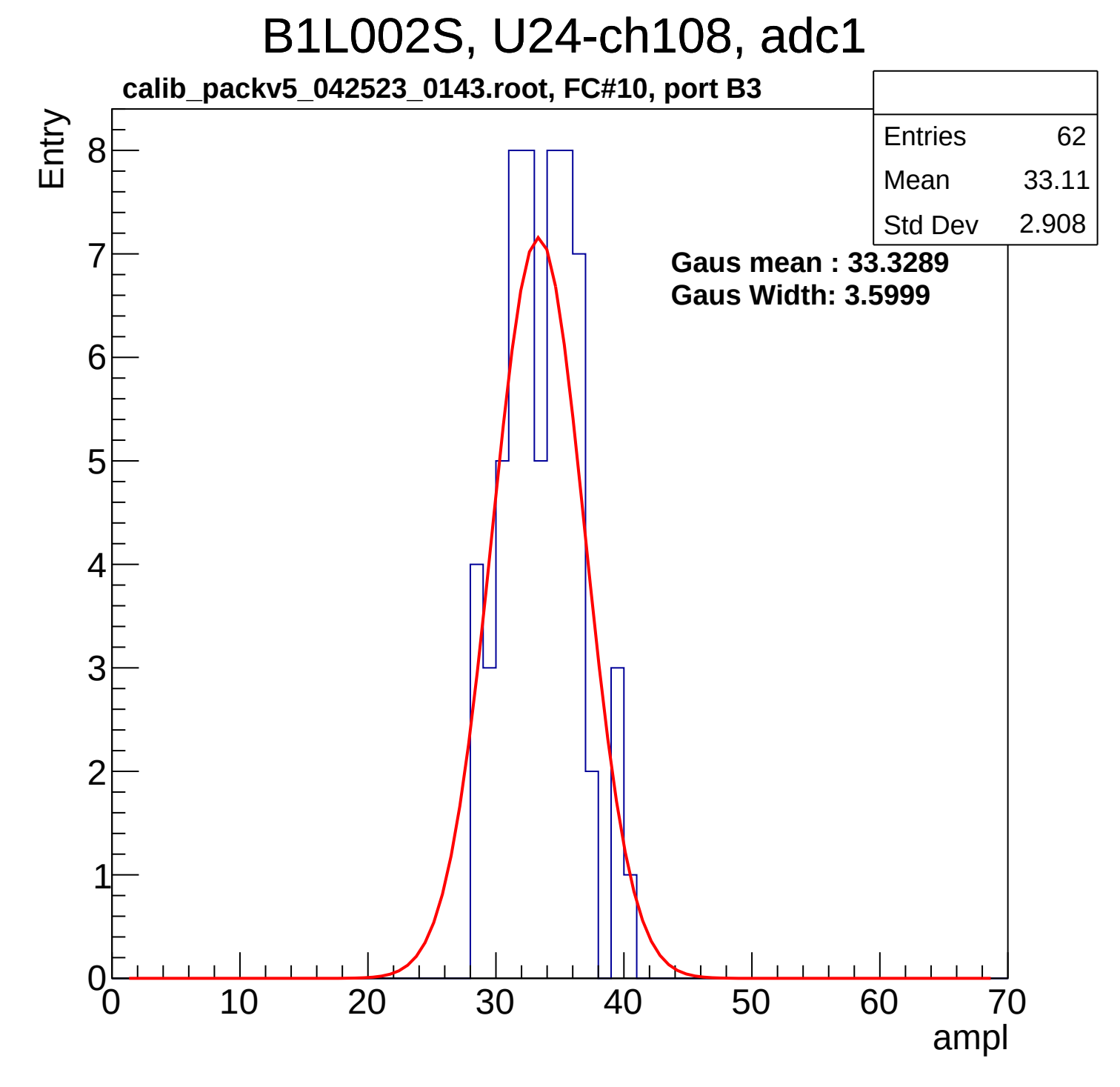
B1L002S, U24-ch108, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 33.11
Std Dev 2.908
Gaus mean : 33.3289
Gaus Width: 3.5999
Entry
ampl
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2
3
4
5
6
7
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0
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20
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40
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60
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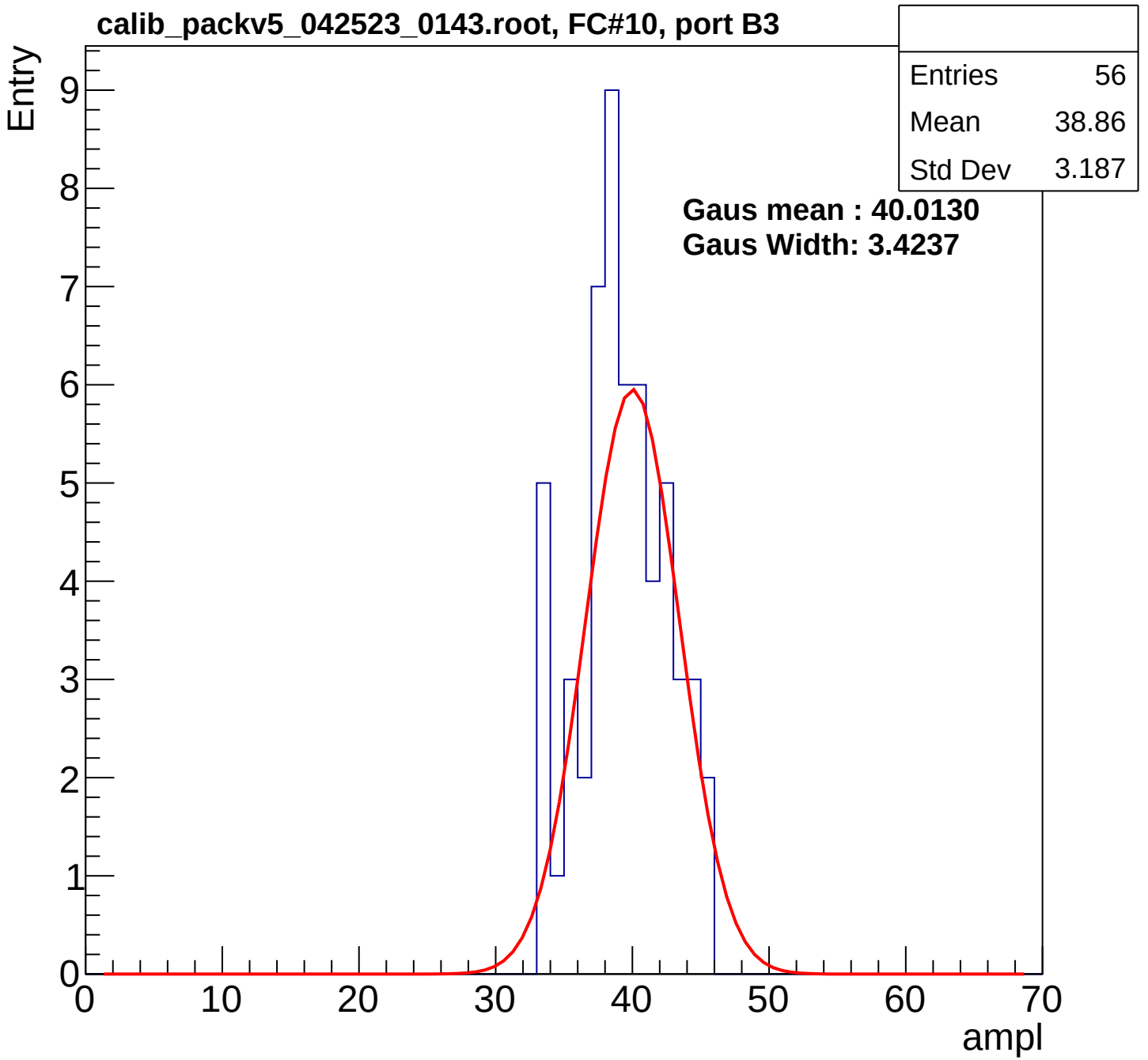
B1L002S, U24-ch108, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 38.86
Std Dev 3.187
Gaus mean : 40.0130
Gaus Width: 3.4237
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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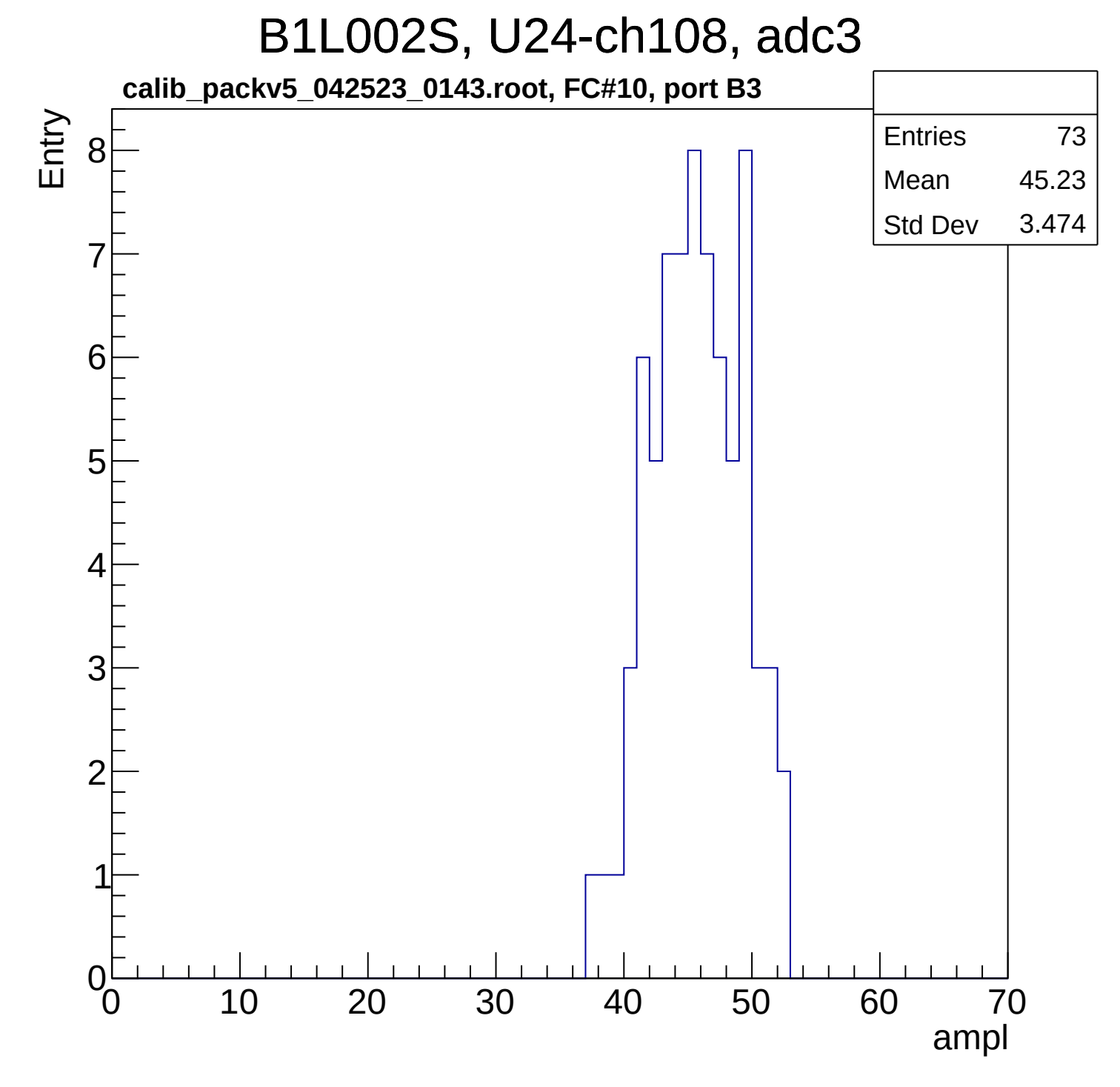
B1L002S, U24-ch108, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 45.23
Std Dev 3.474
Entry
ampl
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2
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4
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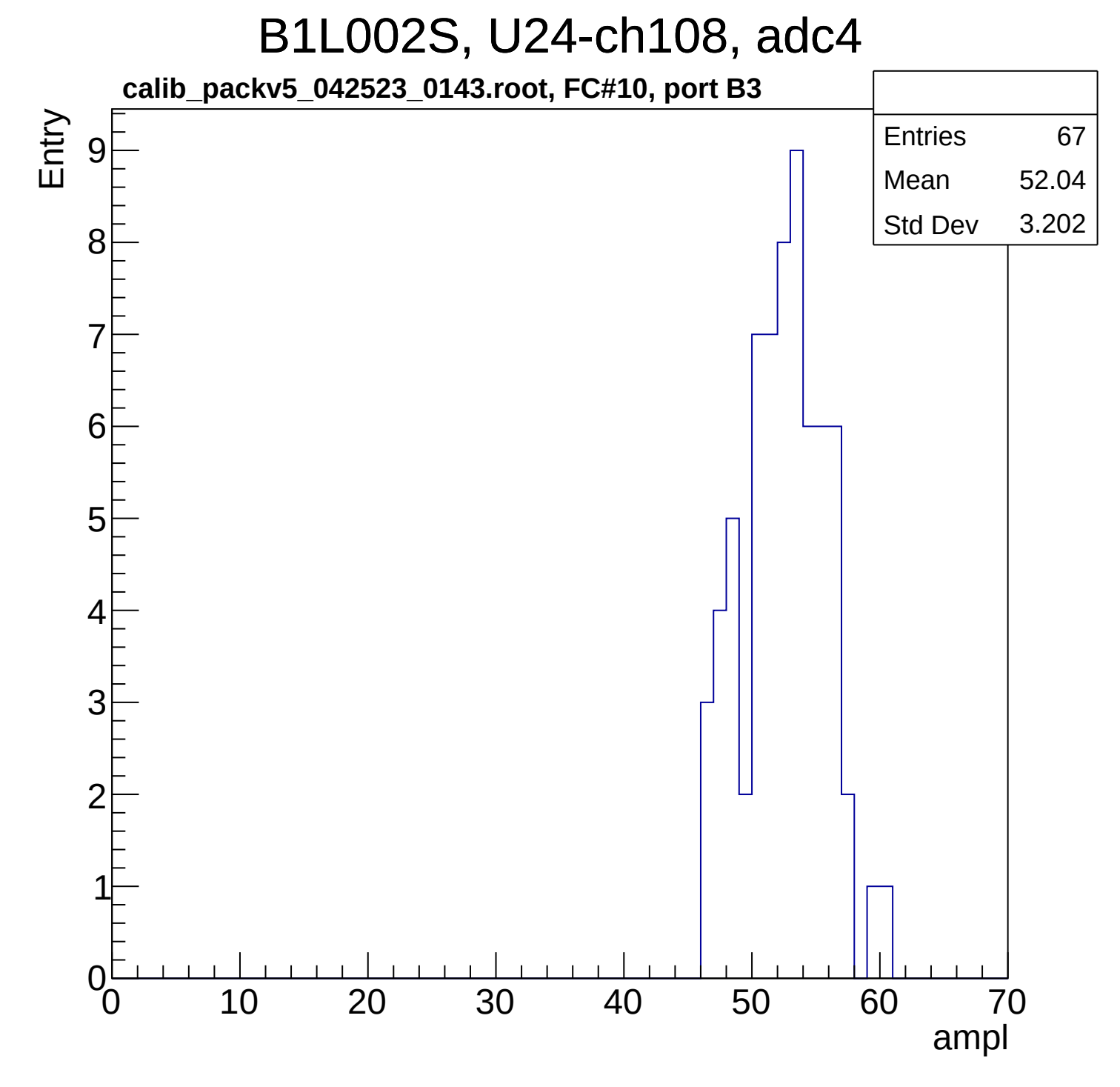
B1L002S, U24-ch108, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 52.04
Std Dev 3.202
Entry
ampl
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3
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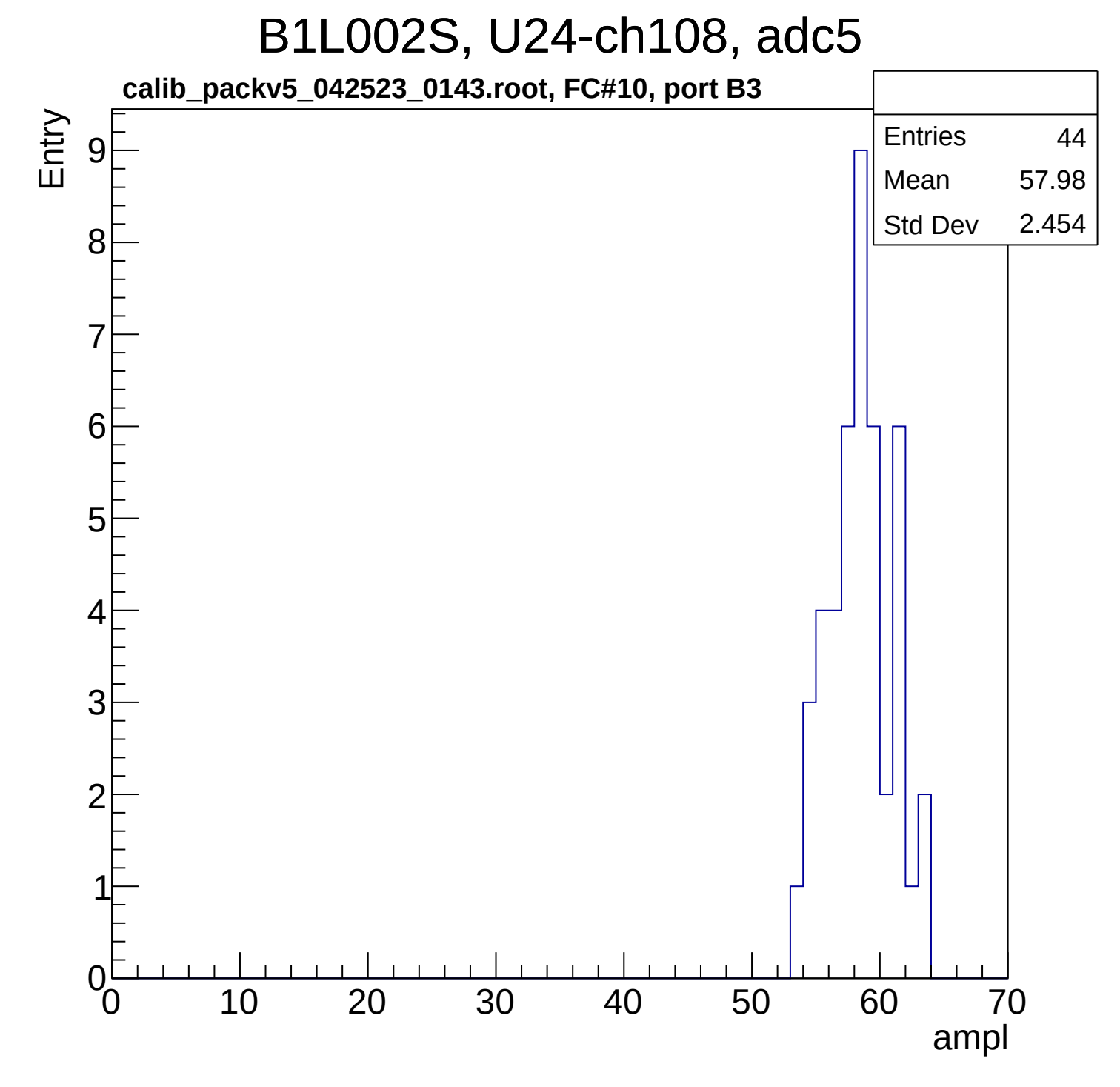
B1L002S, U24-ch108, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 57.98
Std Dev 2.454
Entry
ampl
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5
6
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0
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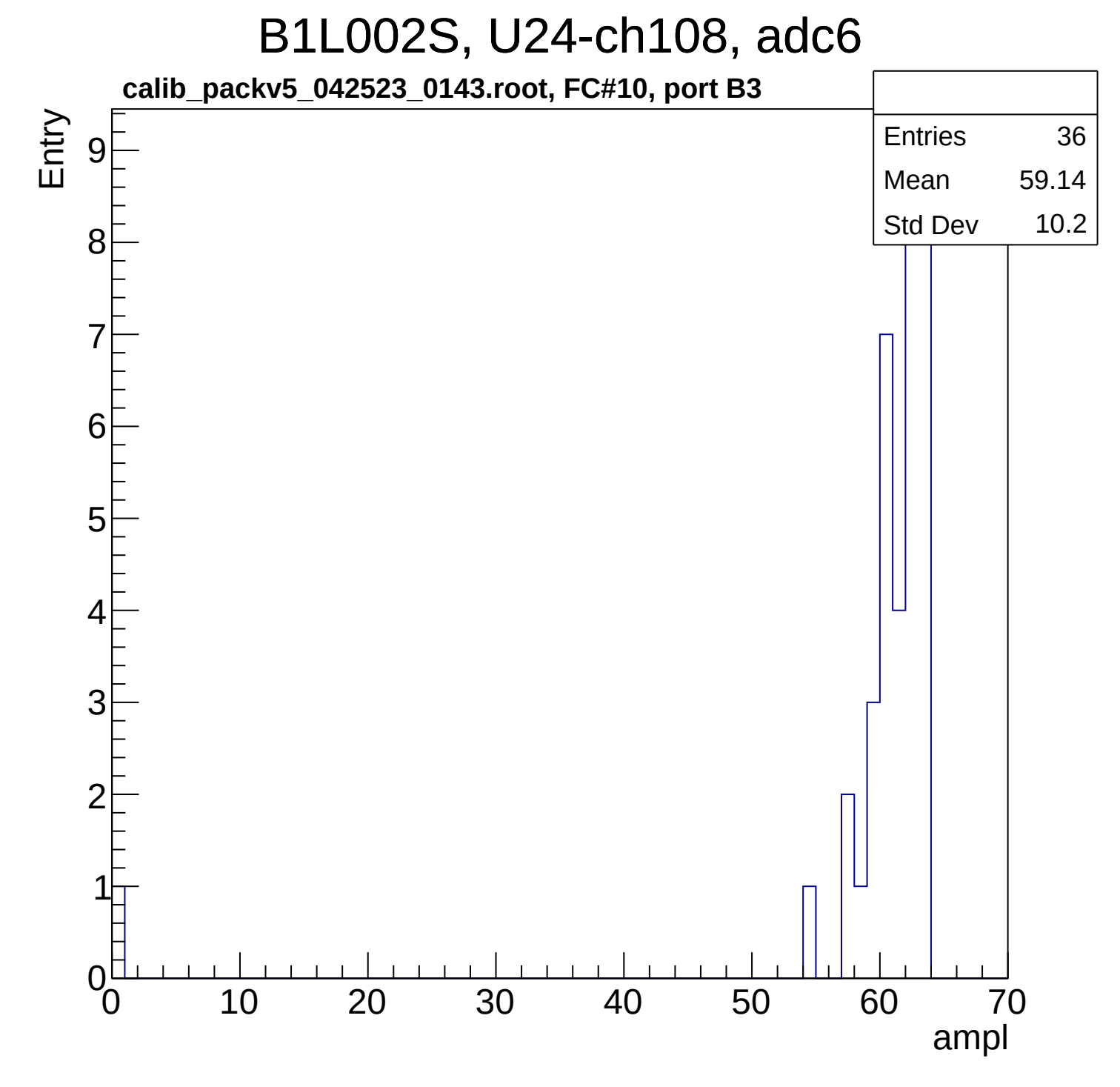
B1L002S, U24-ch108, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 36
Mean 59.14
Std Dev 10.2
Entry
ampl
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2
3
4
5
6
7
8
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0
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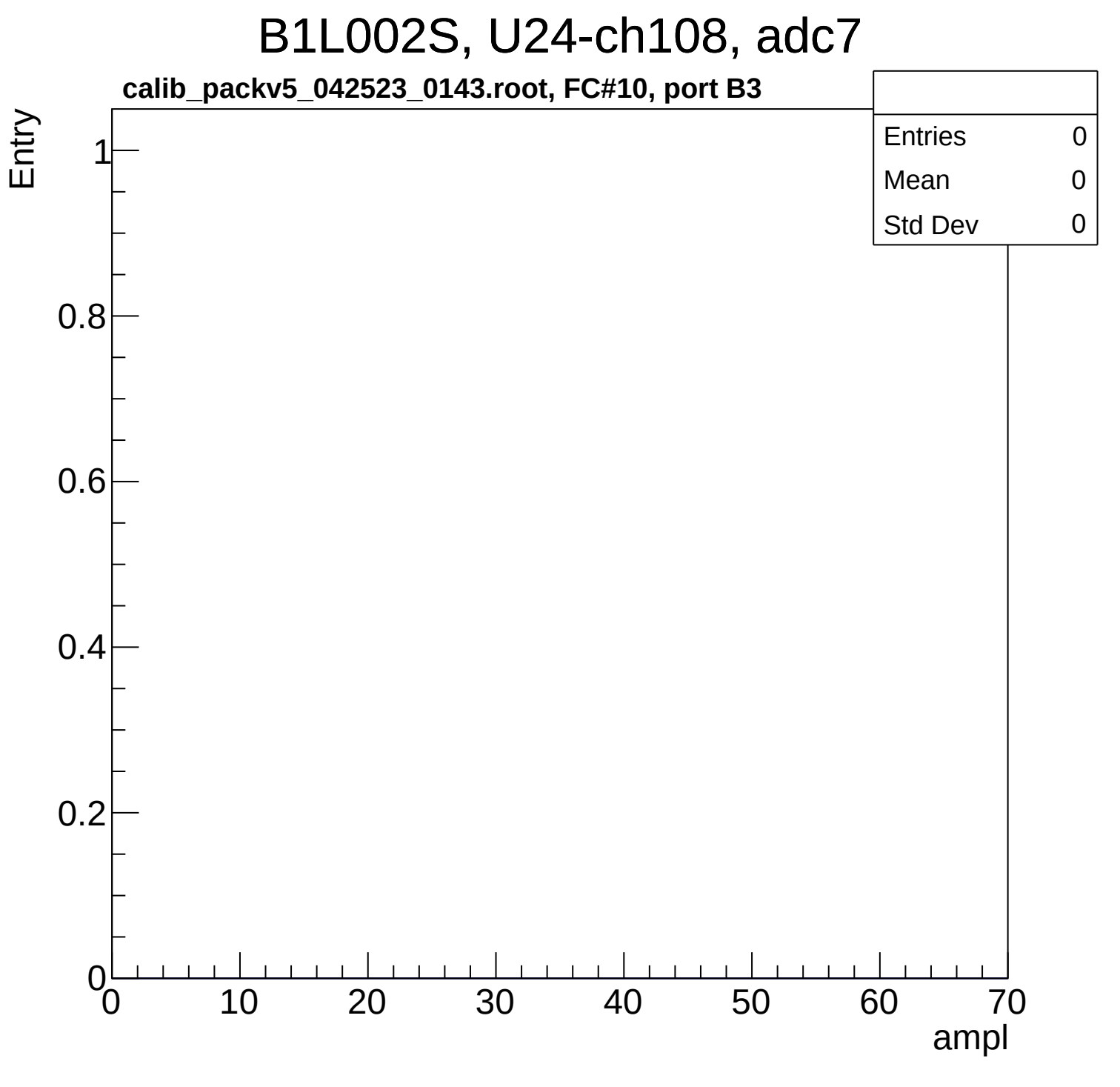
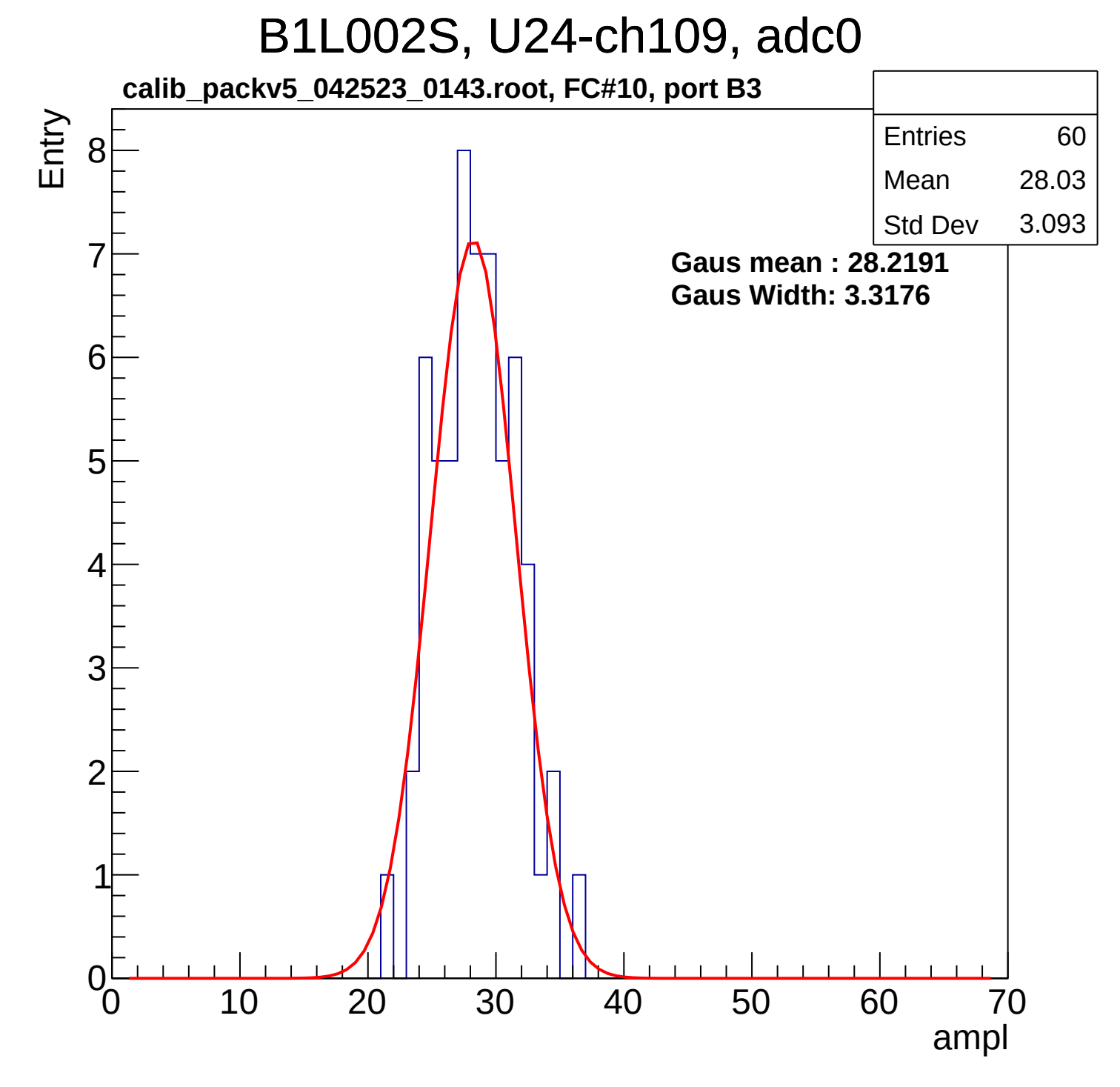

B1L002S, U24-ch108, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch109, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 28.03
Std Dev 3.093
Gaus mean : 28.2191
Gaus Width: 3.3176
Entry
ampl
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1
2
3
4
5
6
7
8
0
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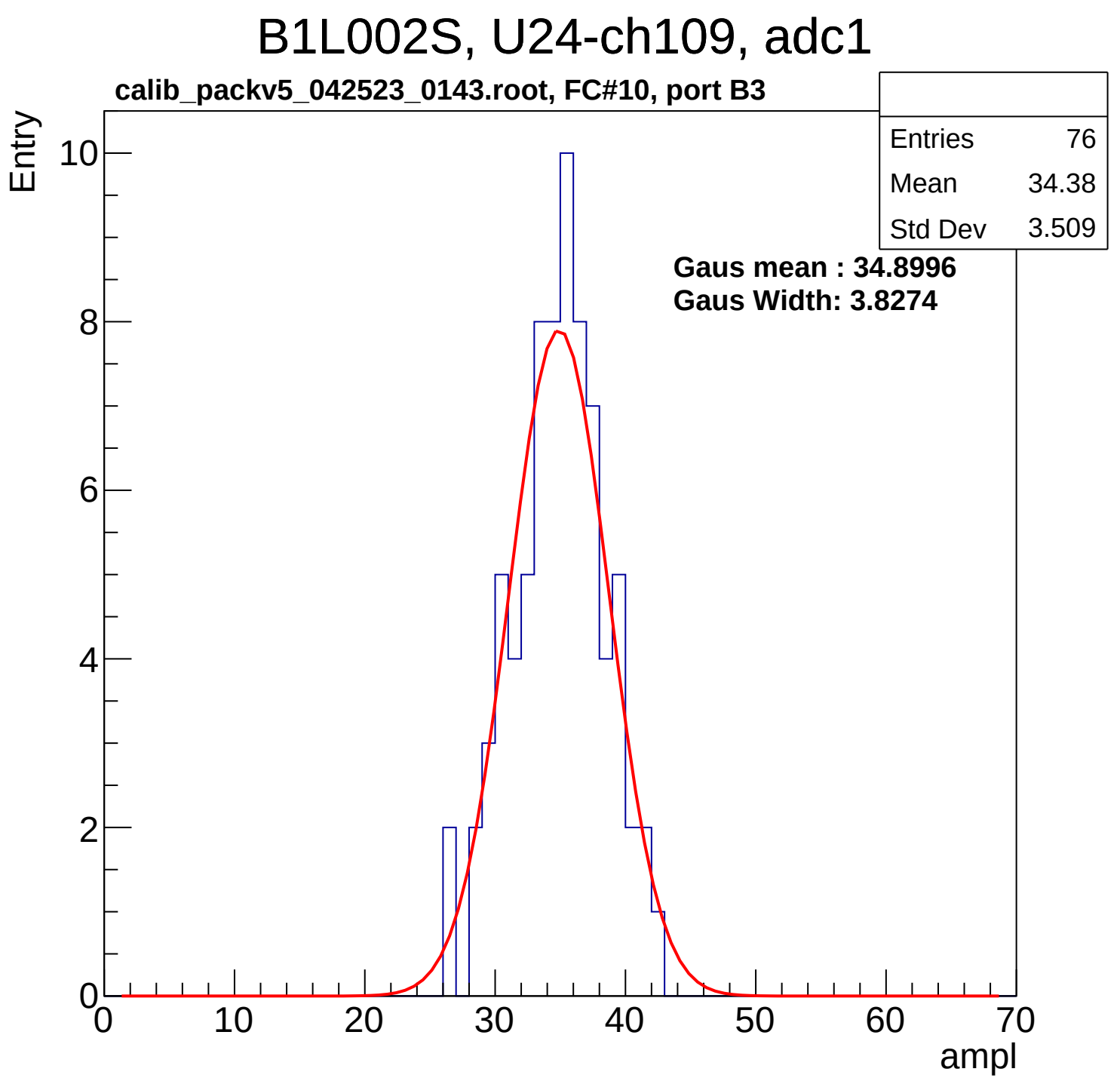
B1L002S, U24-ch109, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 34.38
Std Dev 3.509
Gaus mean : 34.8996
Gaus Width: 3.8274
Entry
ampl
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2
4
6
8
10
0
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20
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40
50
60
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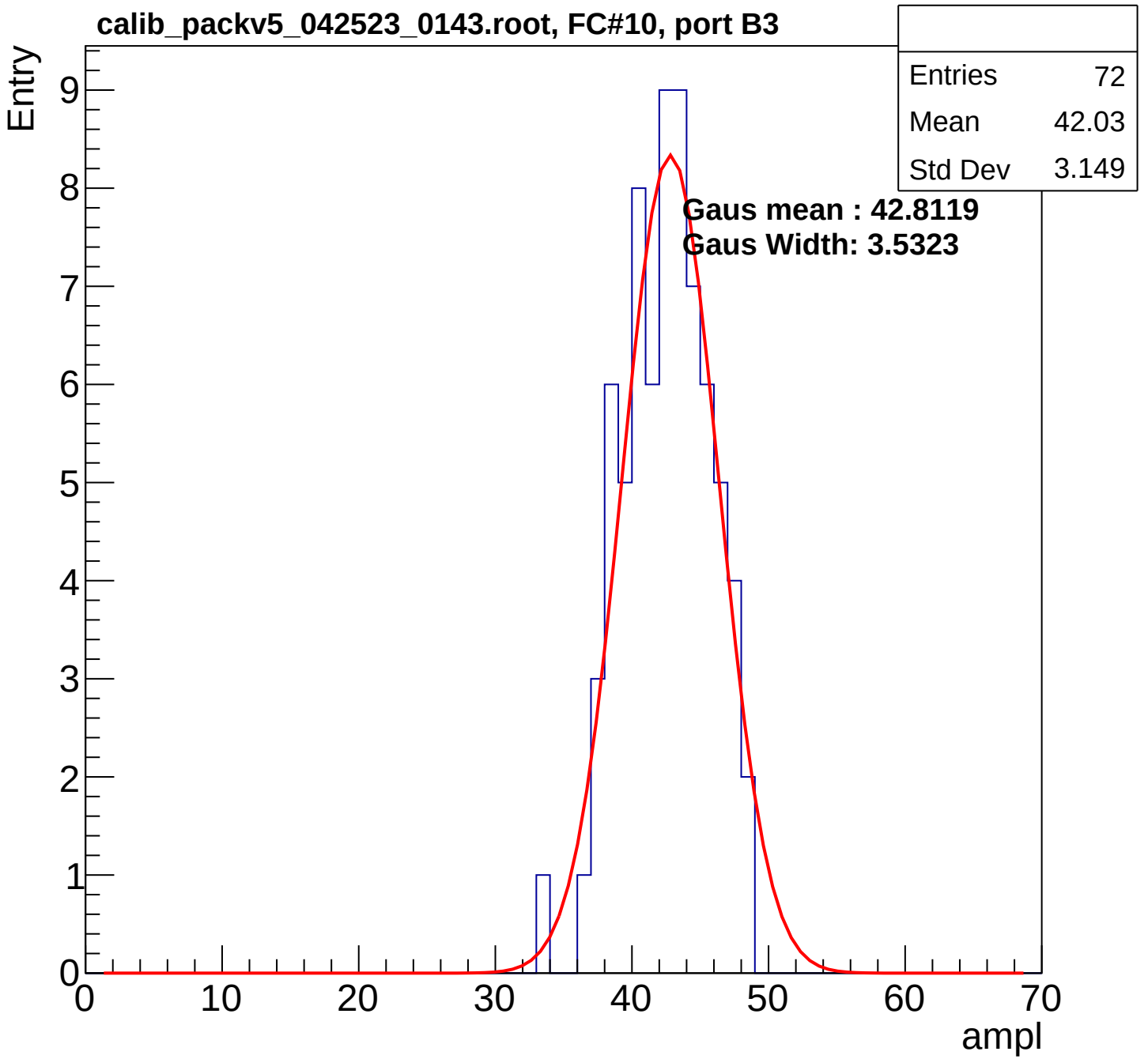
B1L002S, U24-ch109, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 42.03
Std Dev 3.149
Gaus mean : 42.8119
Gaus Width: 3.5323
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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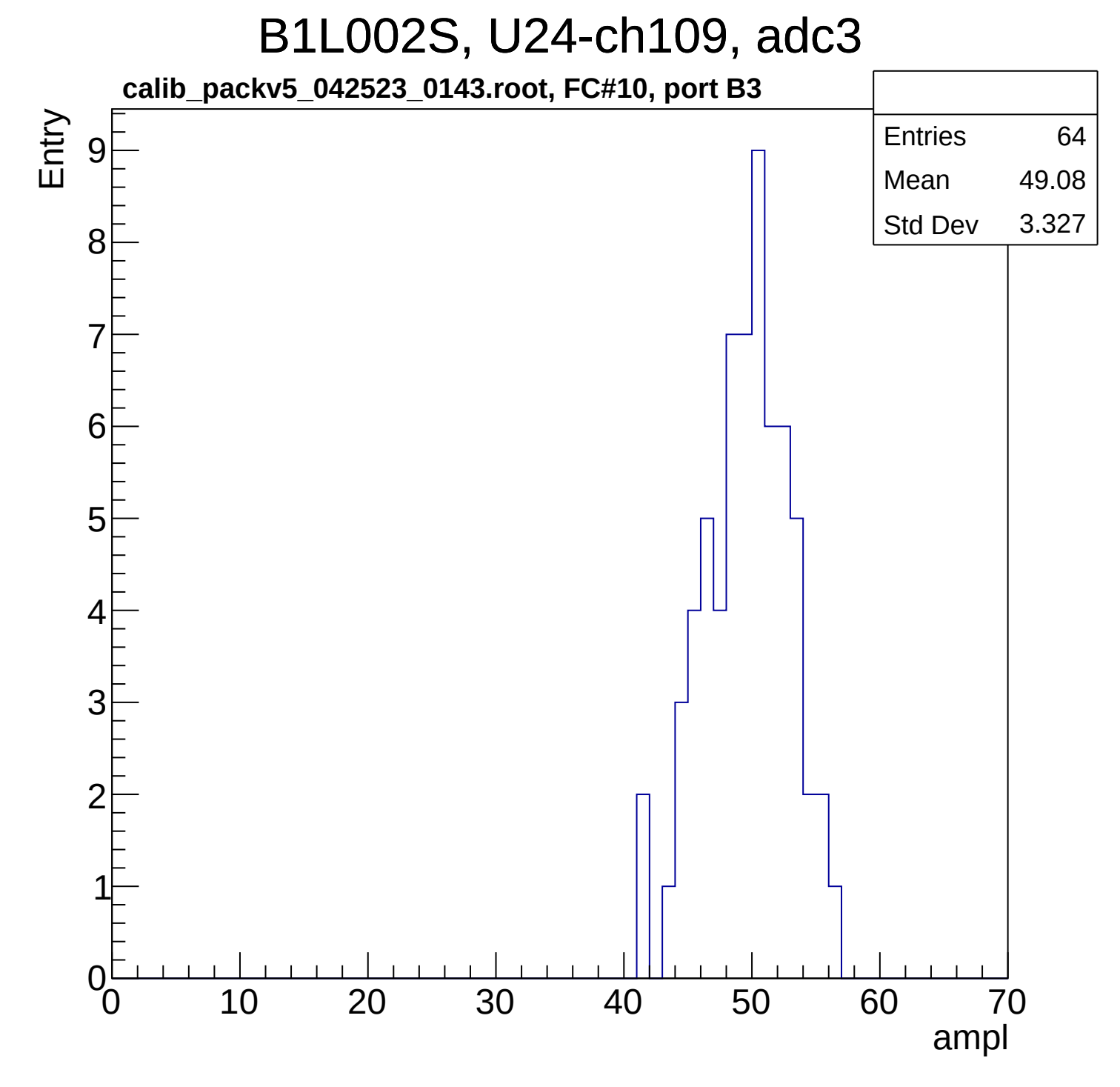
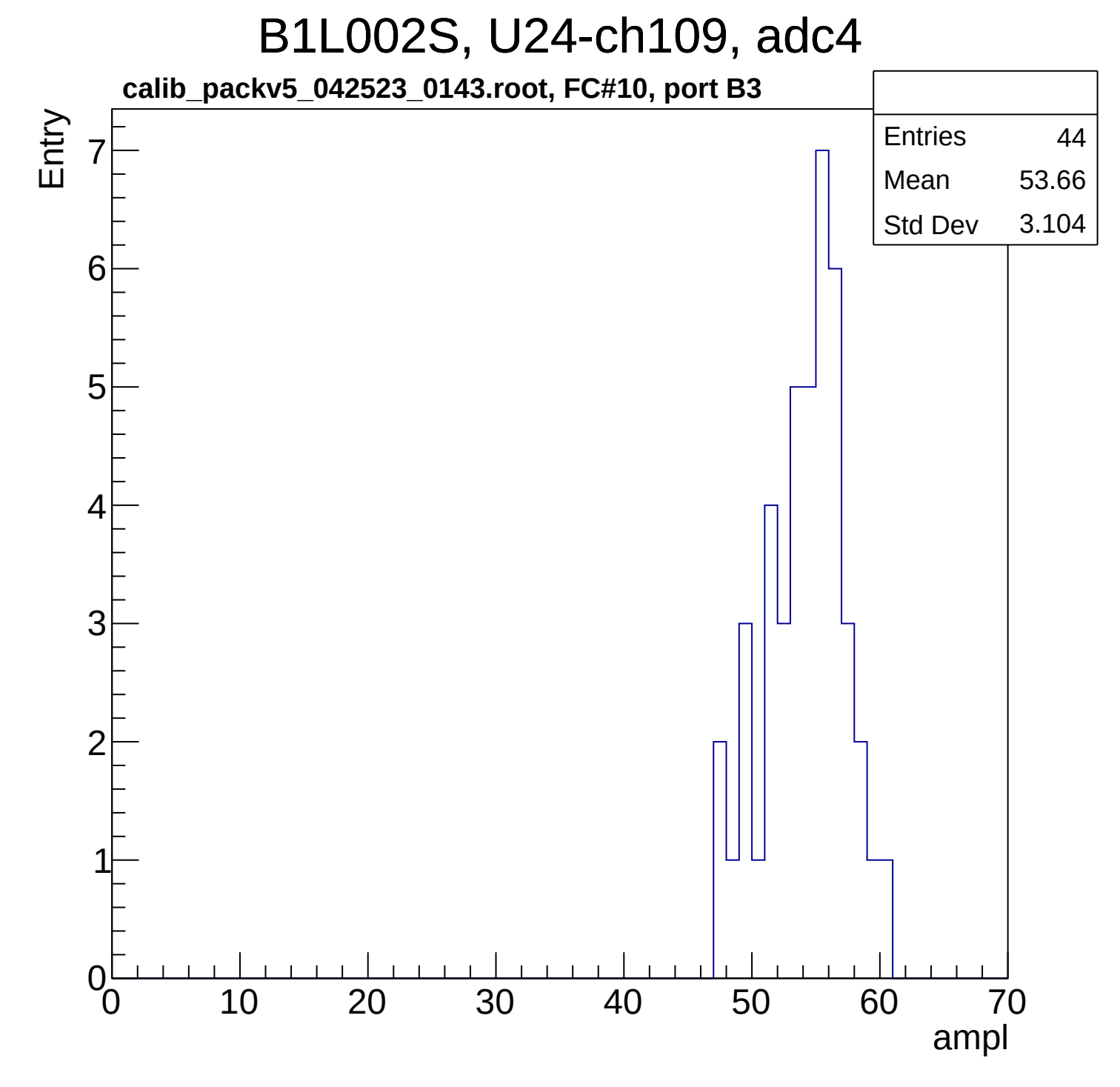
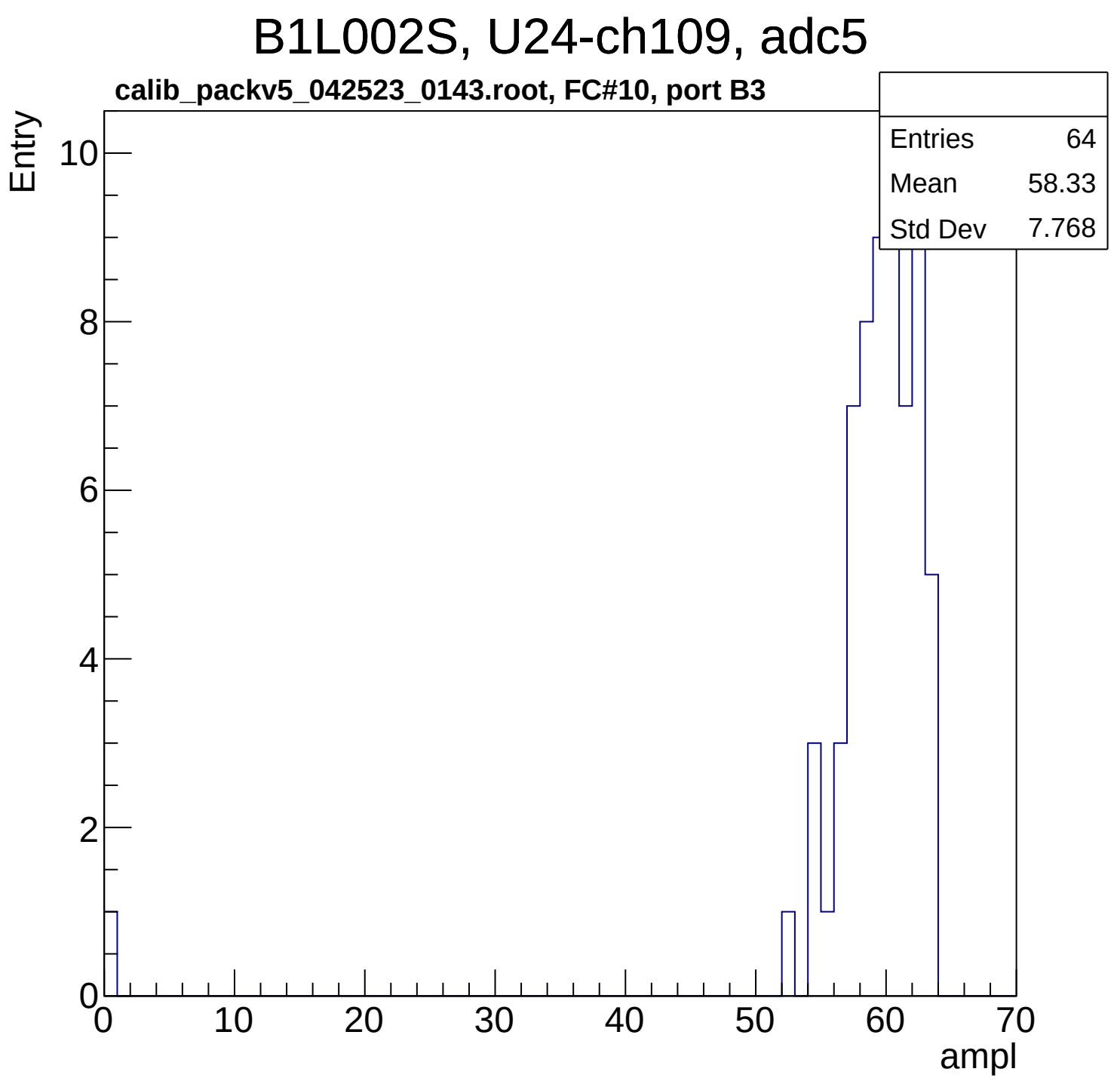
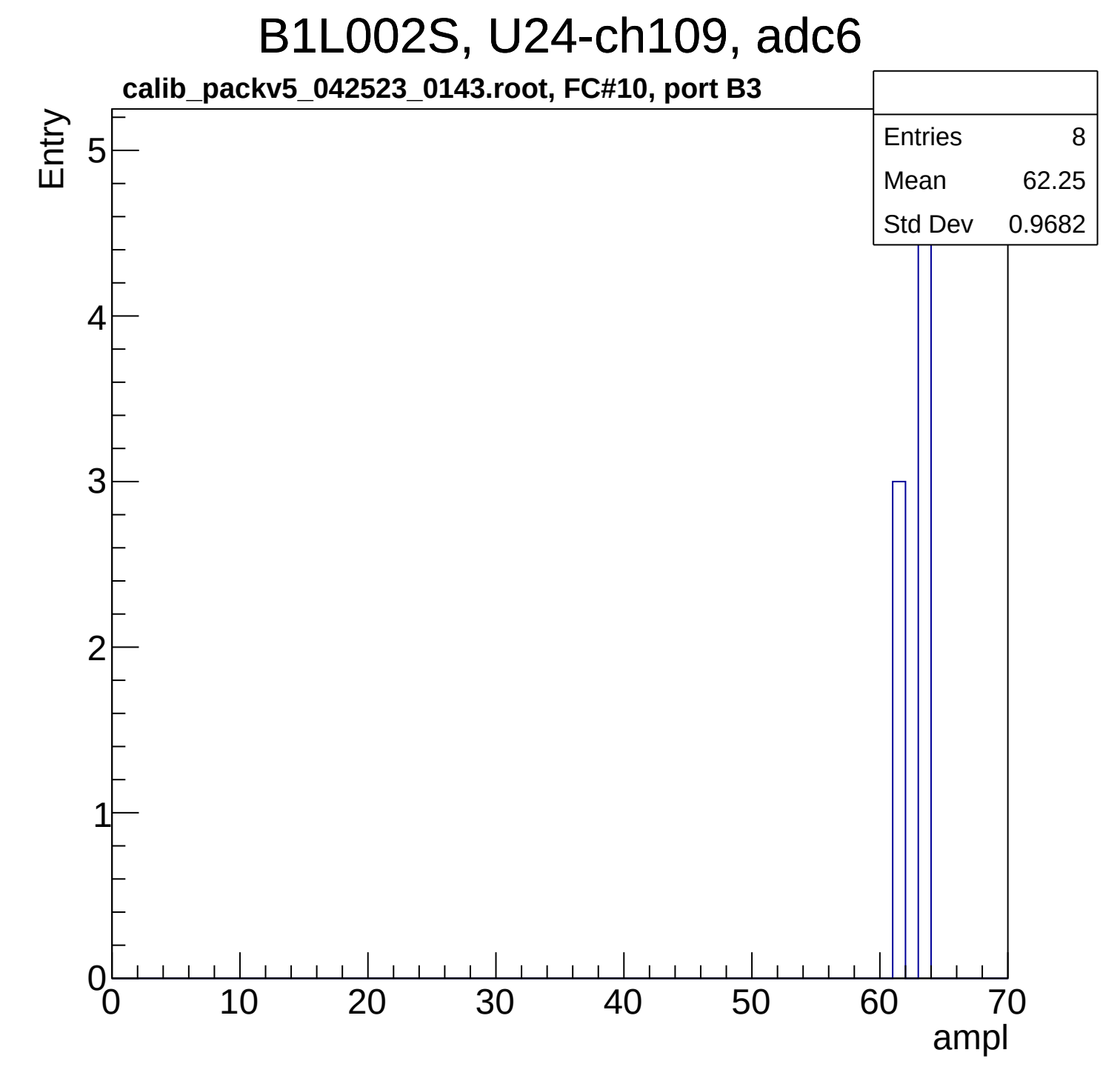
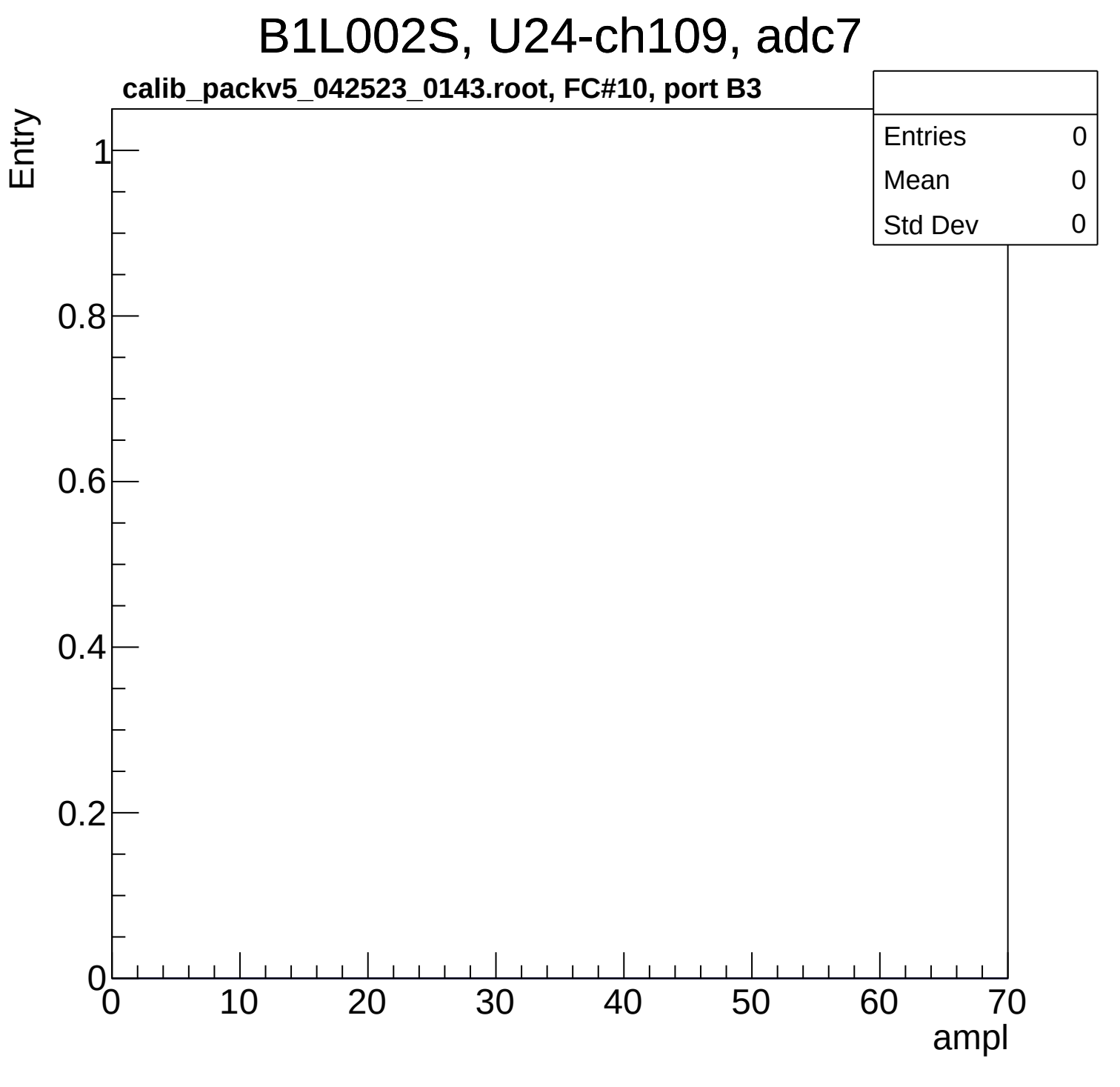
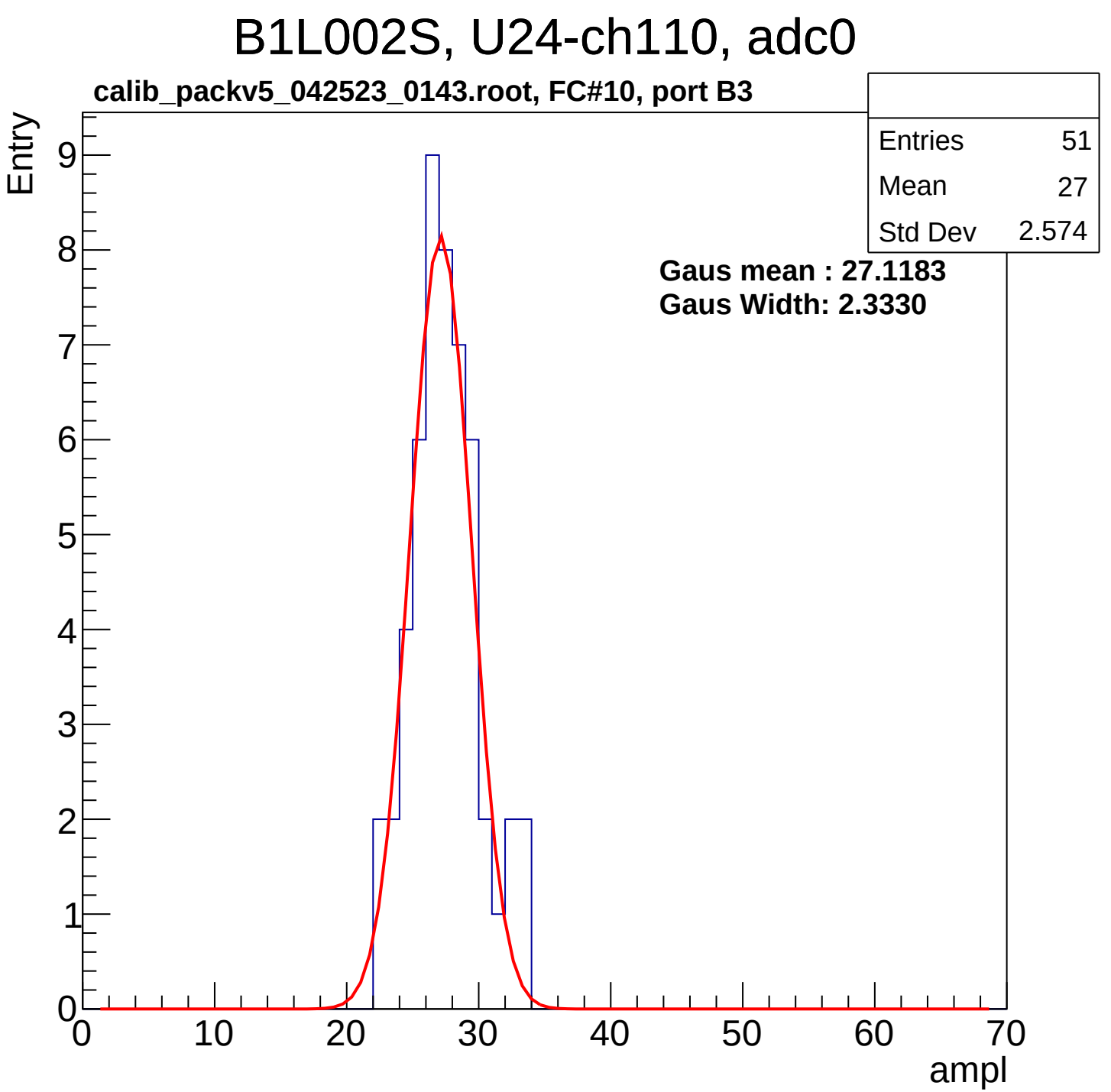

B1L002S, U24-ch109, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 49.08
Std Dev 3.327
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

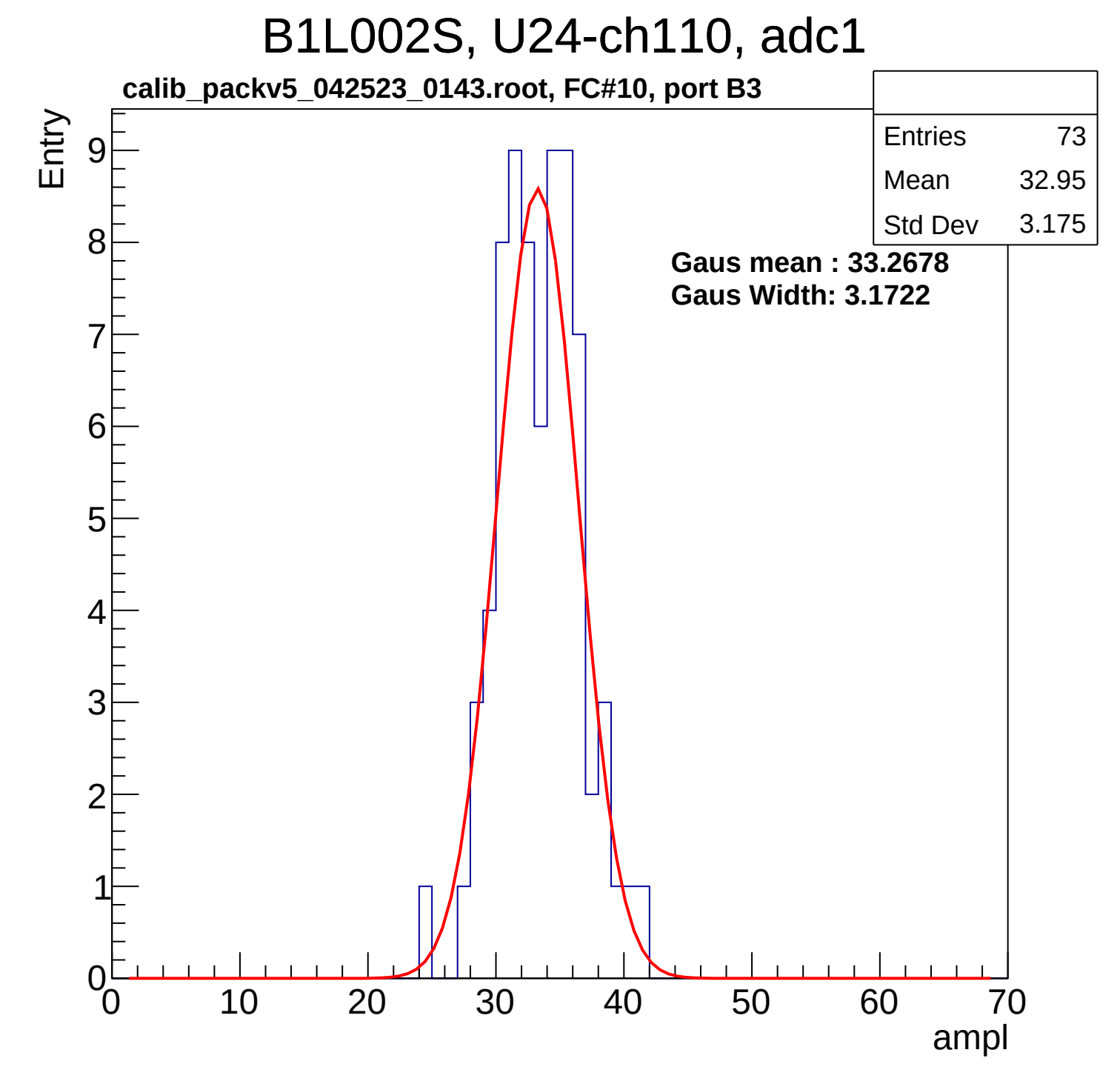

B1L002S, U24-ch109, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 44
Mean 53.66
Std Dev 3.104
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

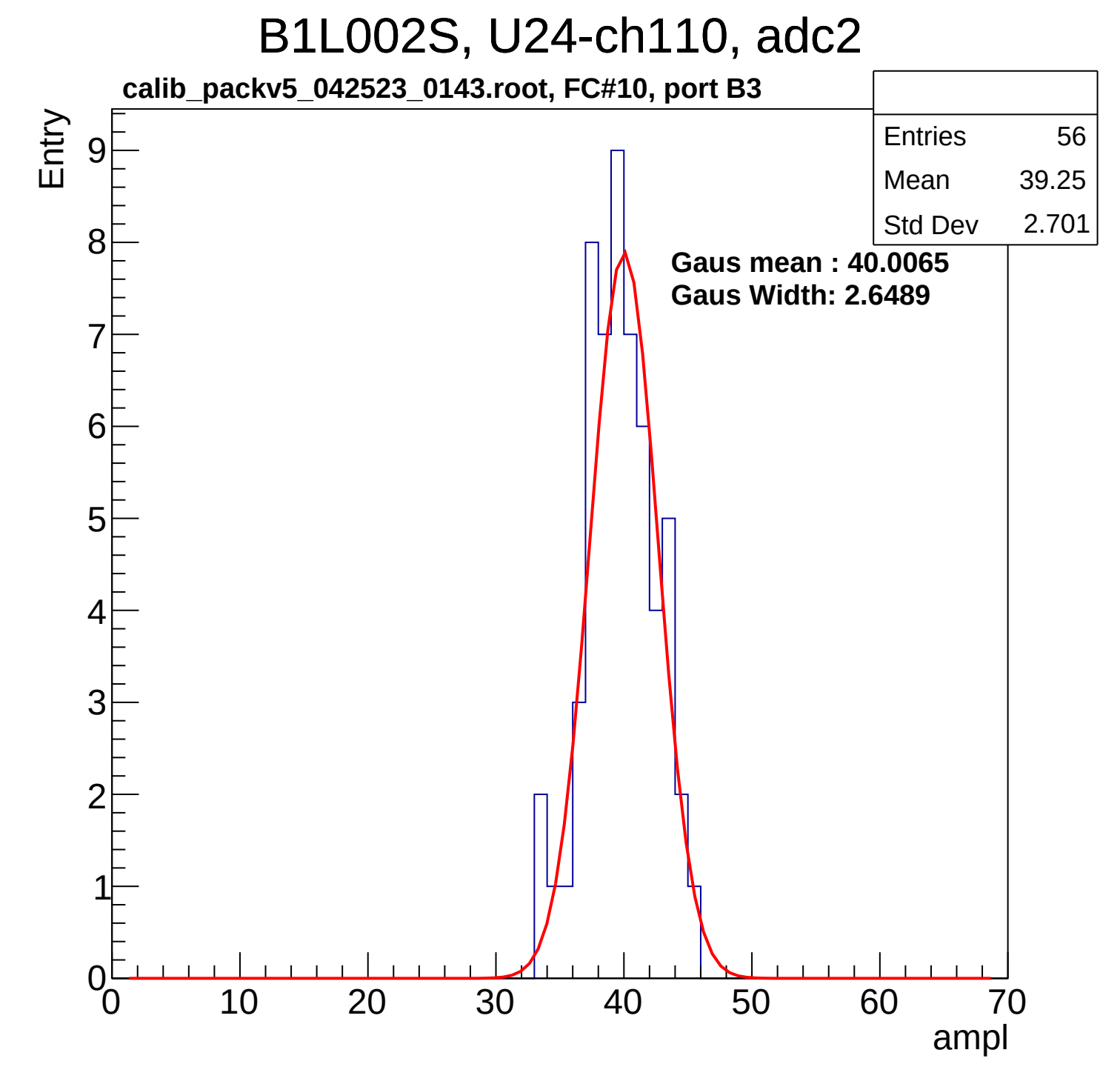

B1L002S, U24-ch109, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 58.33
Std Dev 7.768
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

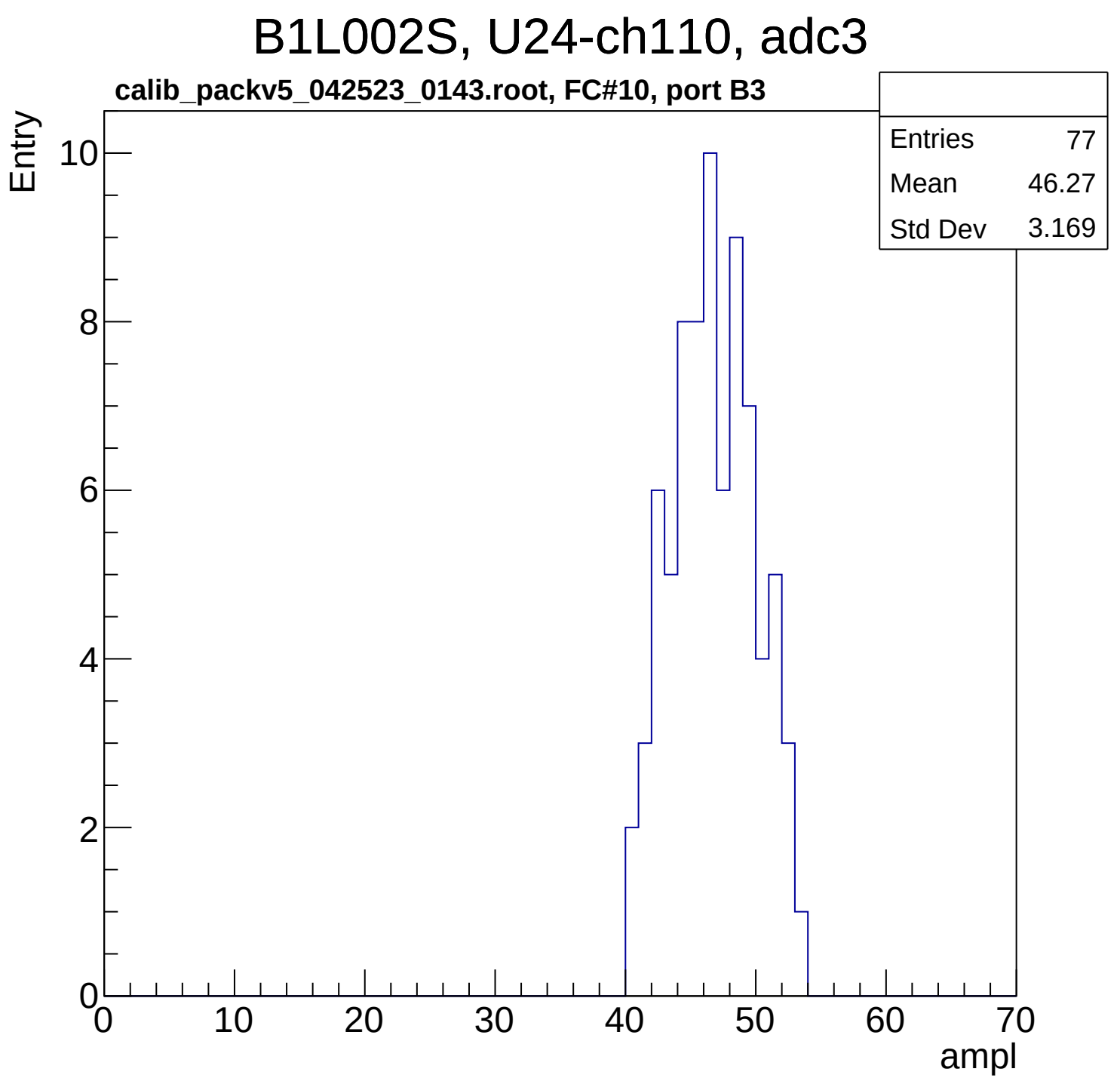

B1L002S, U24-ch109, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 8
Mean 62.25
Std Dev 0.9682
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70

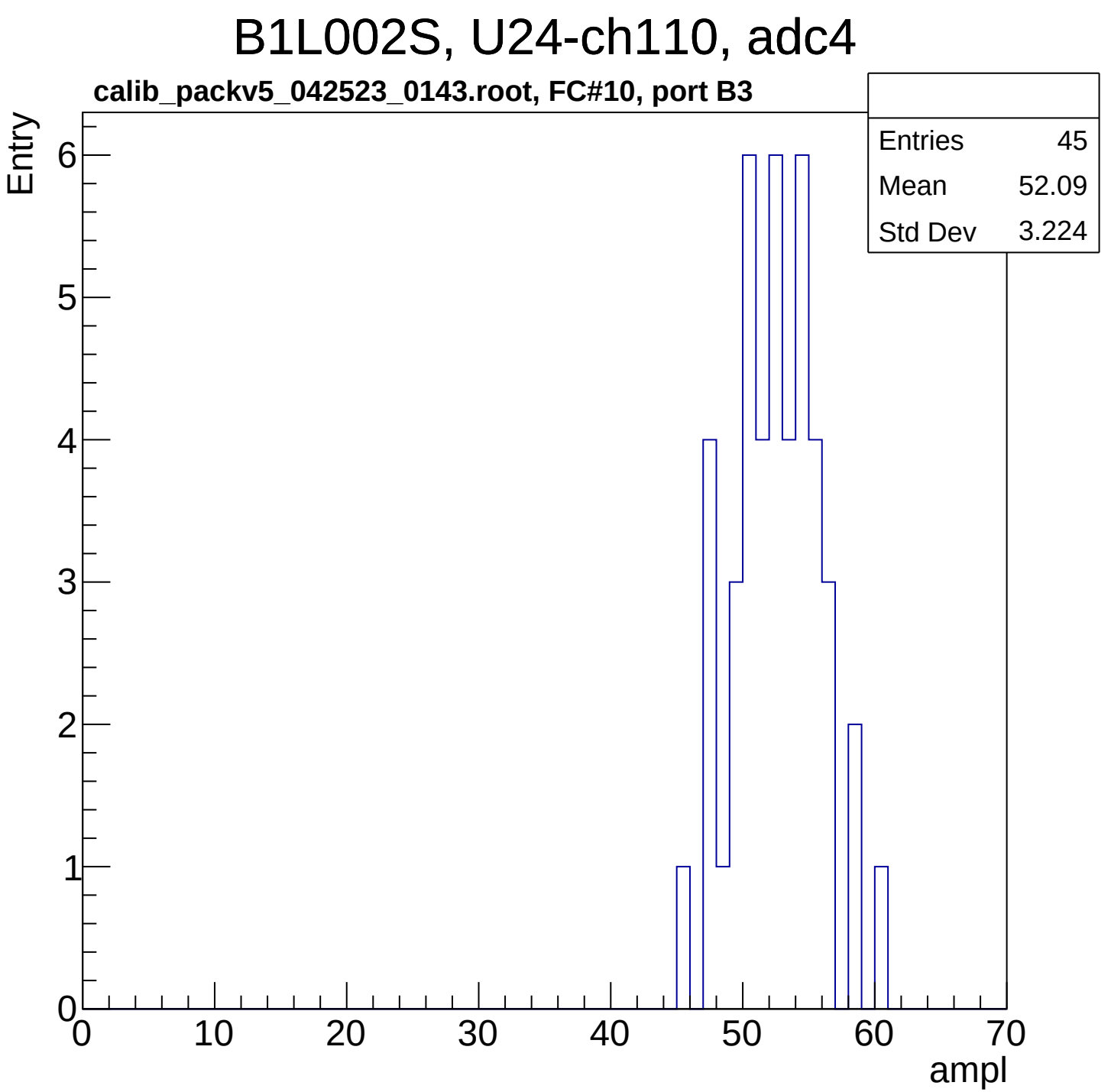

B1L002S, U24-ch109, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

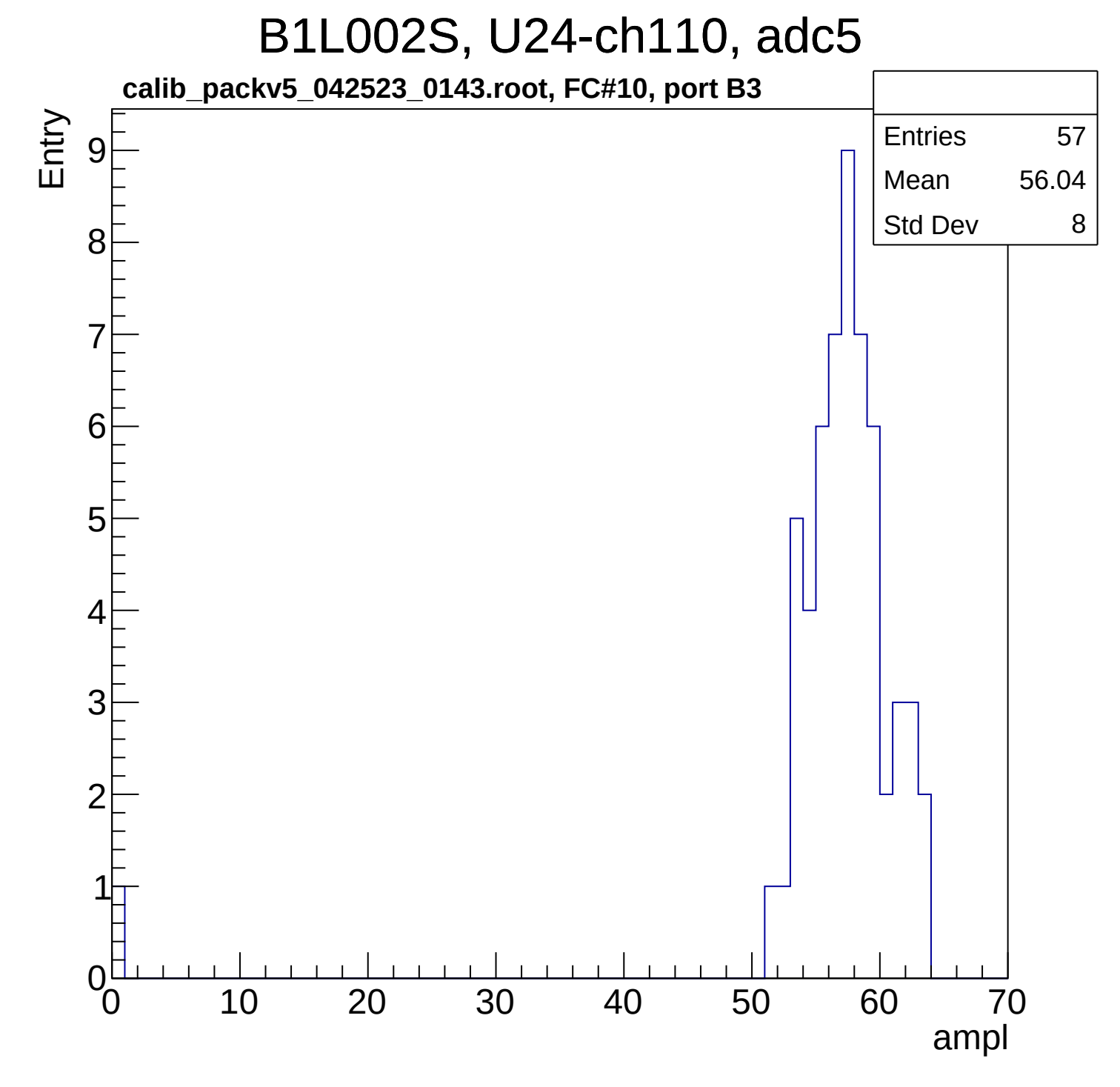

B1L002S, U24-ch110, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 27
Std Dev 2.574
Gaus mean : 27.1183
Gaus Width: 2.3330
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch110, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 32.95
Std Dev 3.175
Gaus mean : 33.2678
Gaus Width: 3.1722
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch110, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 39.25
Std Dev 2.701
Gaus mean : 40.0065
Gaus Width: 2.6489
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch110, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 46.27
Std Dev 3.169
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch110, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 52.09
Std Dev 3.224
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L002S, U24-ch110, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 57
Mean 56.04
Std Dev 8
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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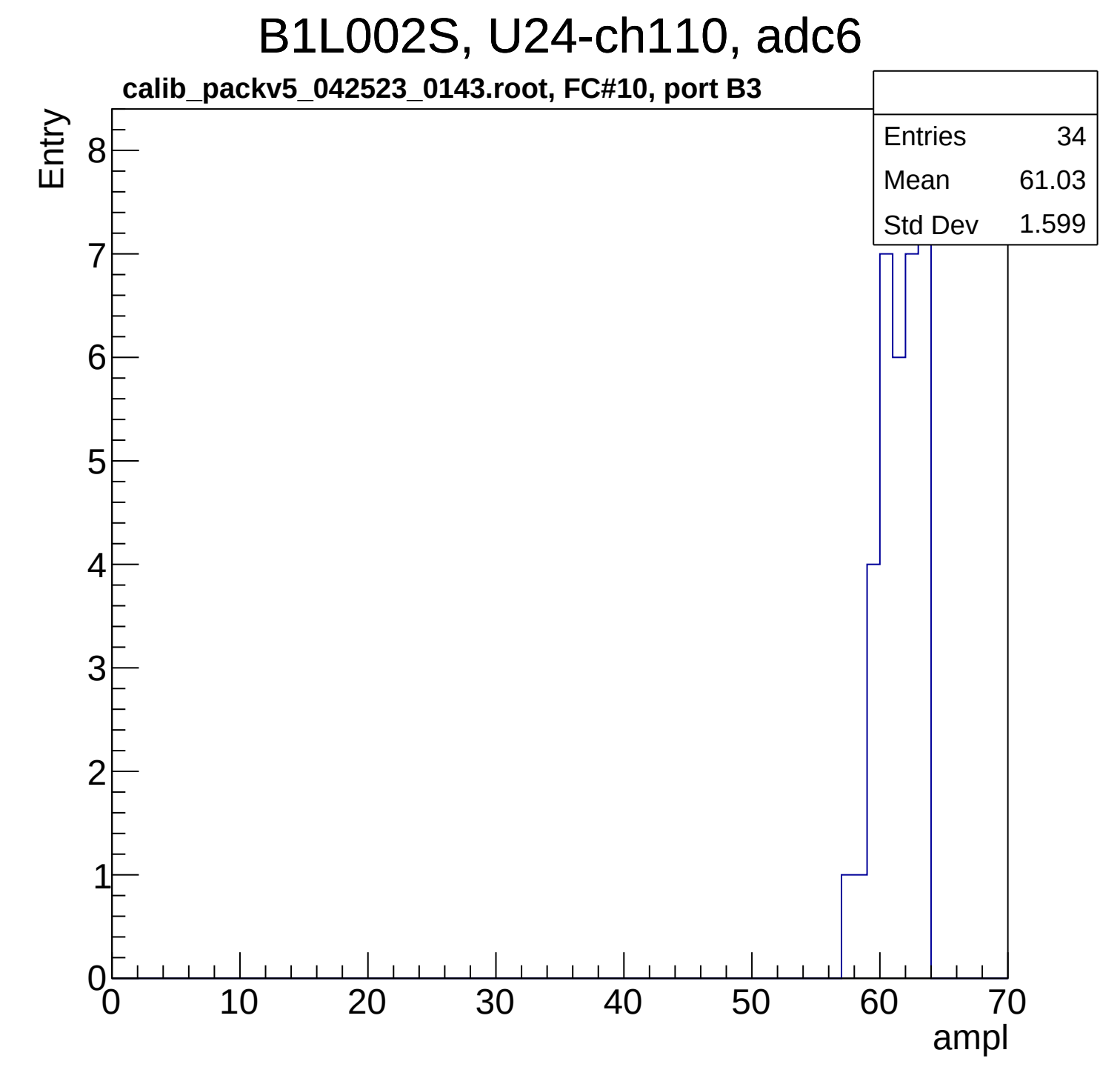
B1L002S, U24-ch110, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 34
Mean 61.03
Std Dev 1.599
Entry
ampl
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1
2
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4
5
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7
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0
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20
30
40
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60
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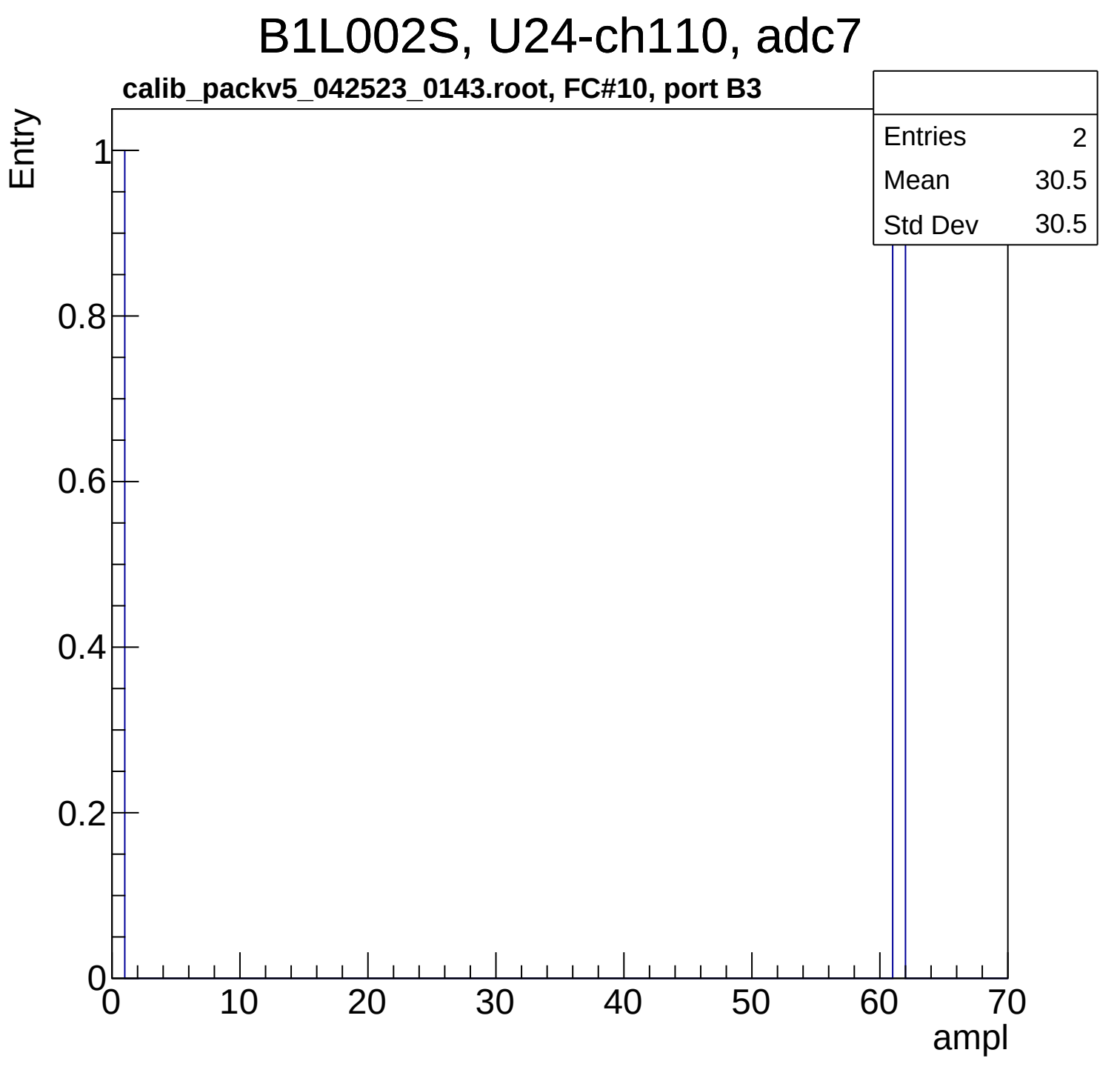
B1L002S, U24-ch110, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 30.5
Std Dev 30.5
Entry
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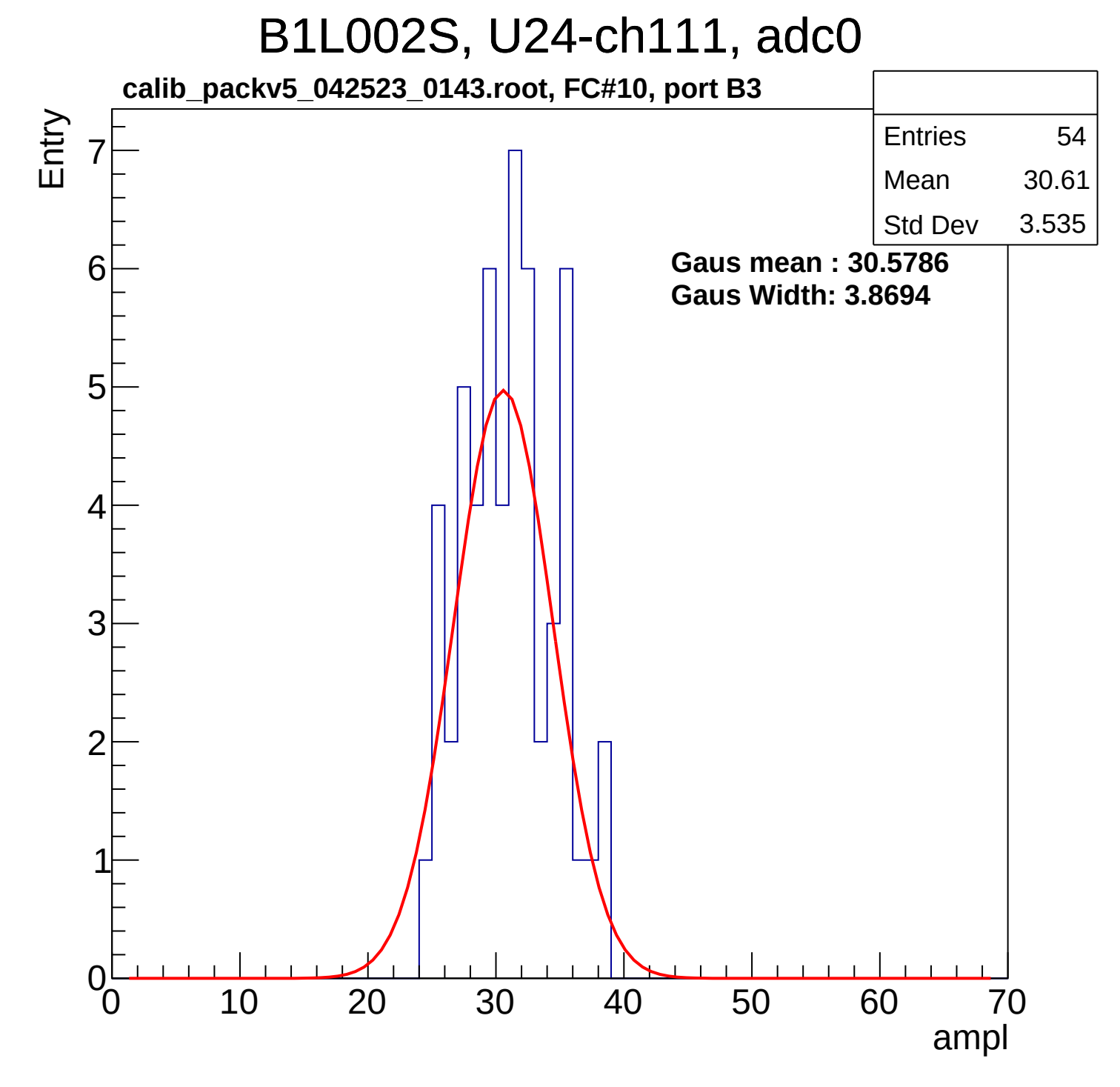
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch111, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 30.61
Std Dev 3.535
Gaus mean : 30.5786
Gaus Width: 3.8694
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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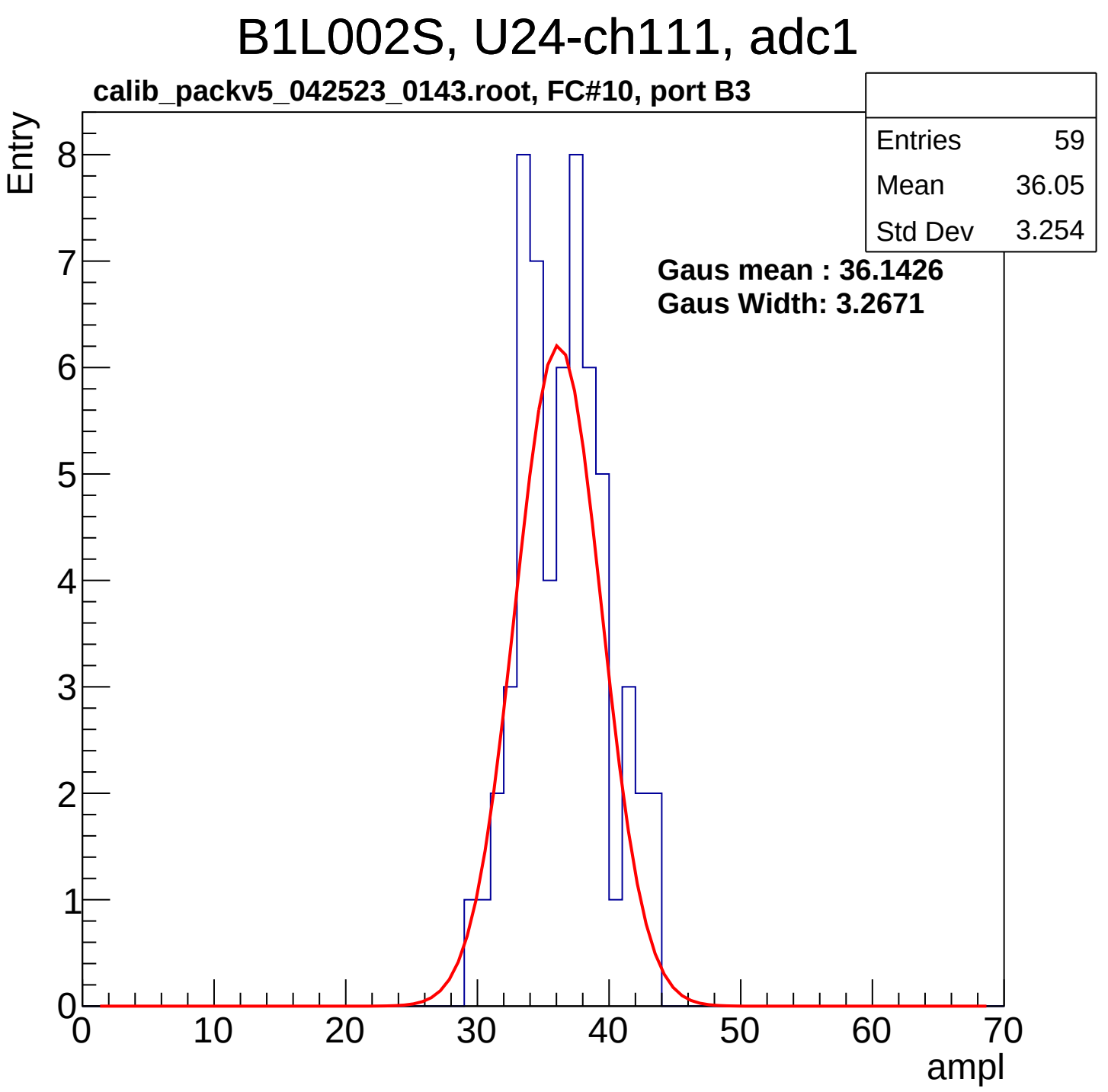
B1L002S, U24-ch111, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 59
Mean 36.05
Std Dev 3.254
Gaus mean : 36.1426
Gaus Width: 3.2671
Entry
ampl
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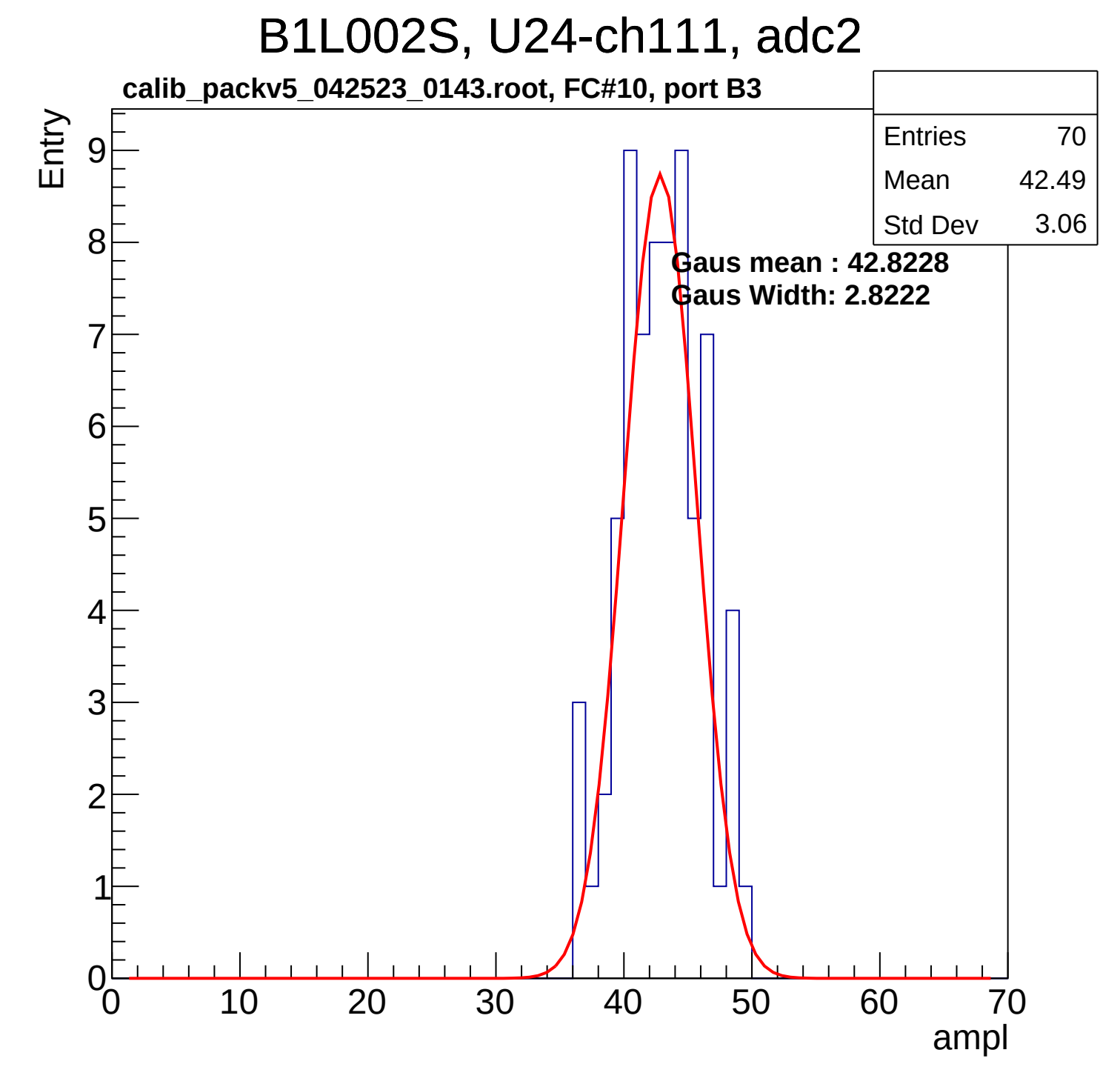
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch111, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 42.49
Std Dev 3.06
Gaus mean : 42.8228
Gaus Width: 2.8222
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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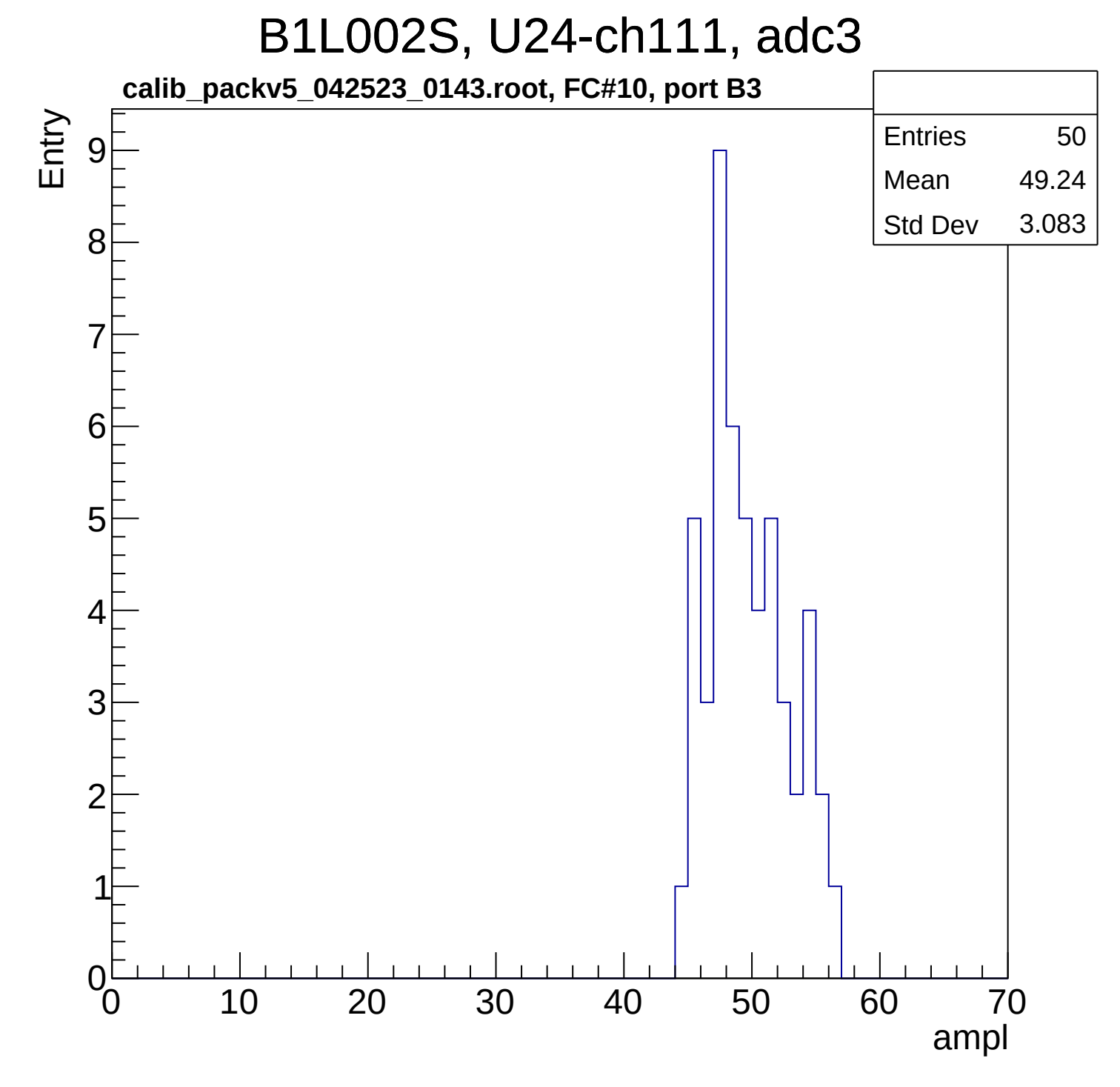
B1L002S, U24-ch111, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 49.24
Std Dev 3.083
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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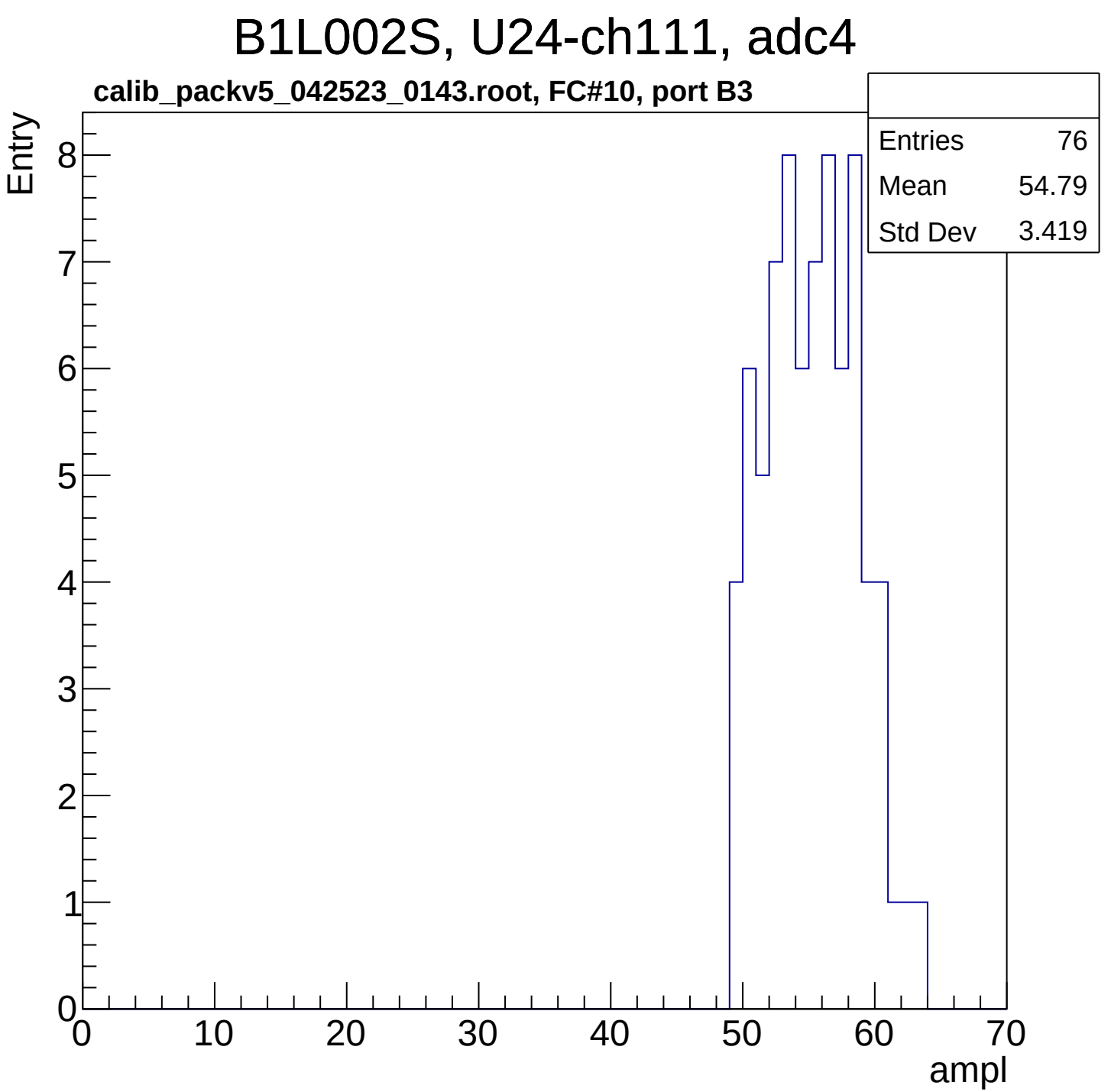
B1L002S, U24-ch111, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 76
Mean 54.79
Std Dev 3.419
Entry
ampl
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2
3
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5
6
7
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0
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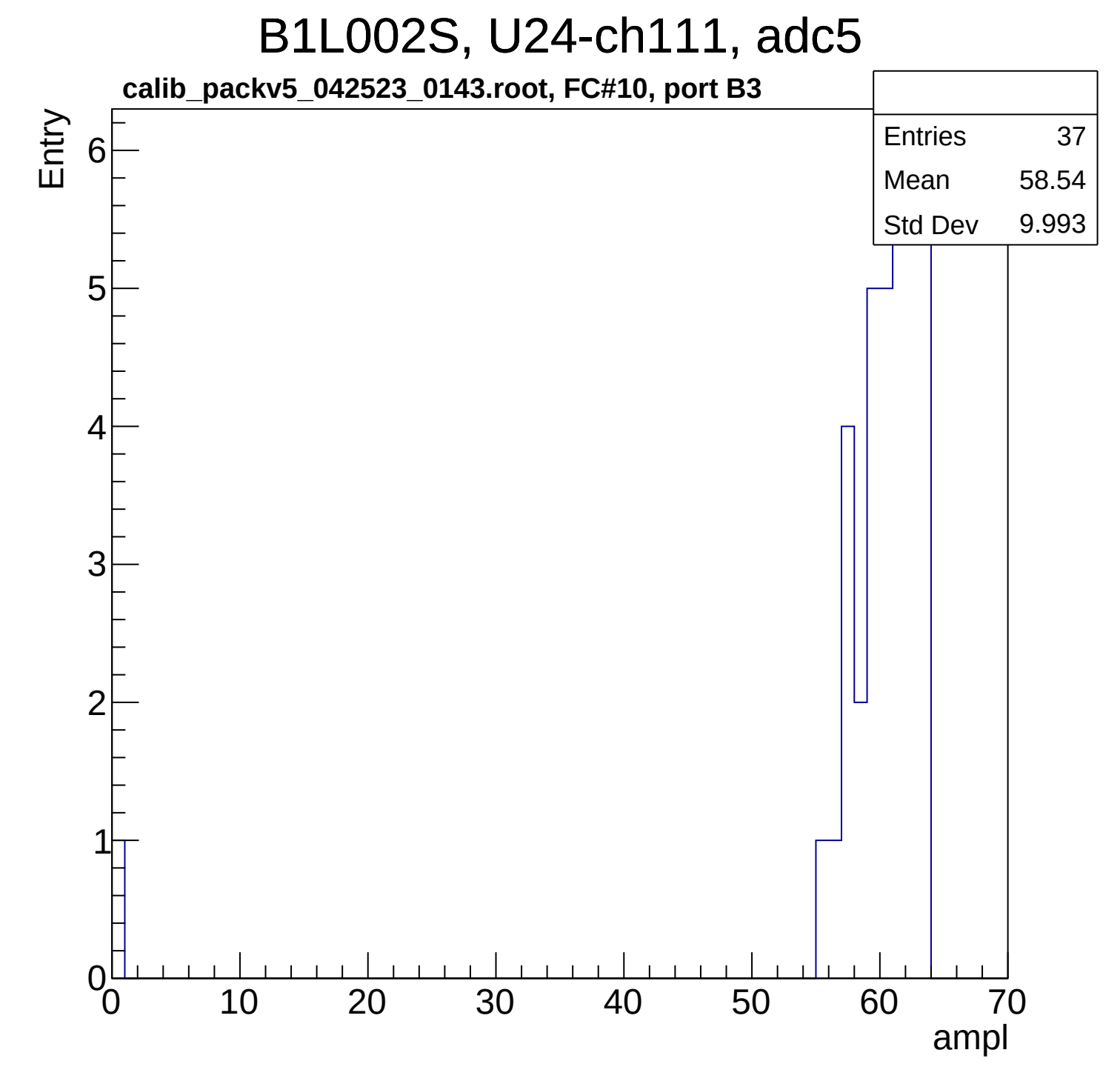
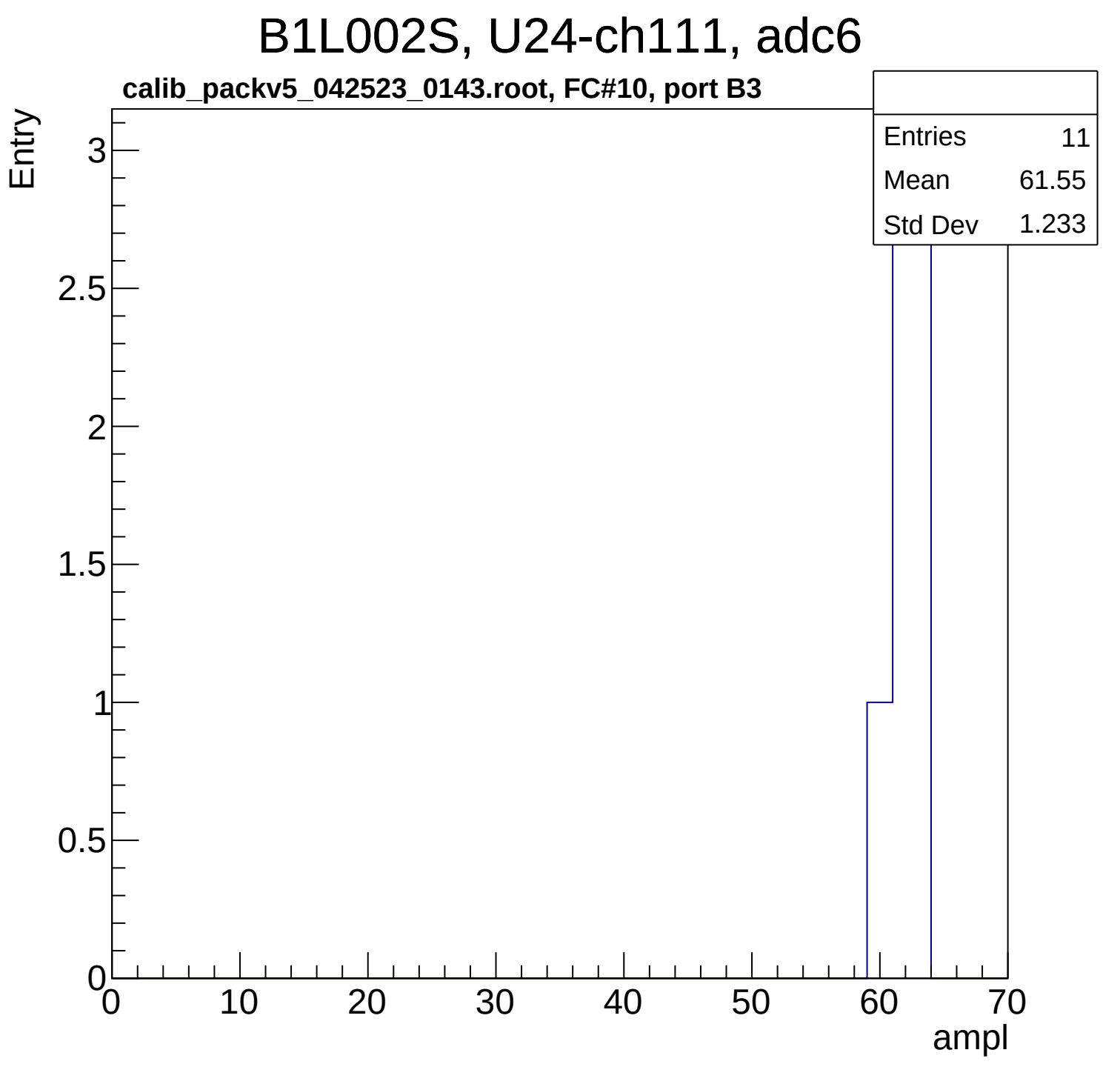

B1L002S, U24-ch111, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 58.54
Std Dev 9.993
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch111, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 11
Mean 61.55
Std Dev 1.233
Entry
ampl
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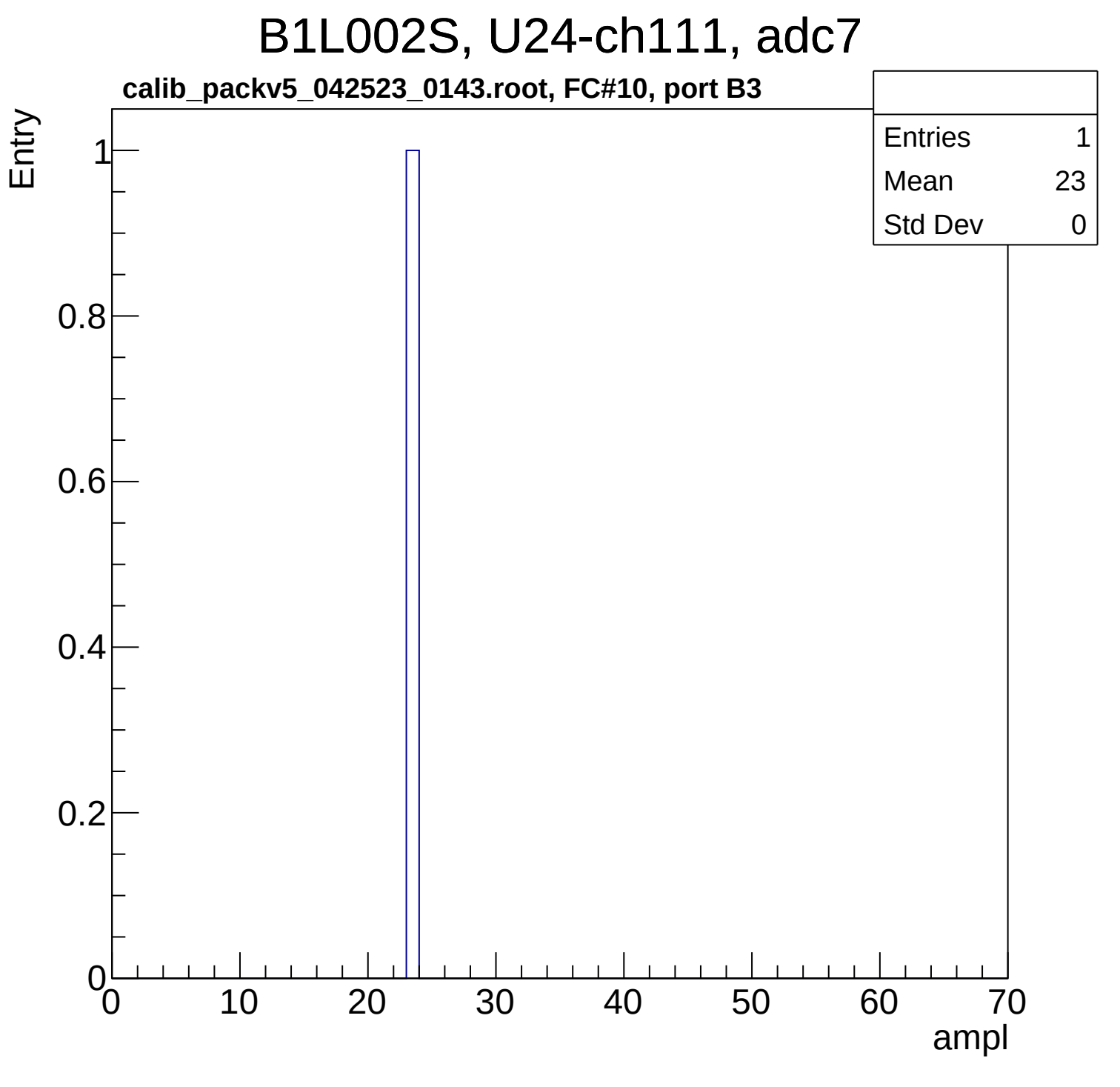
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch111, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 23
Std Dev 0
Entry
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0.6
0.4
0.2
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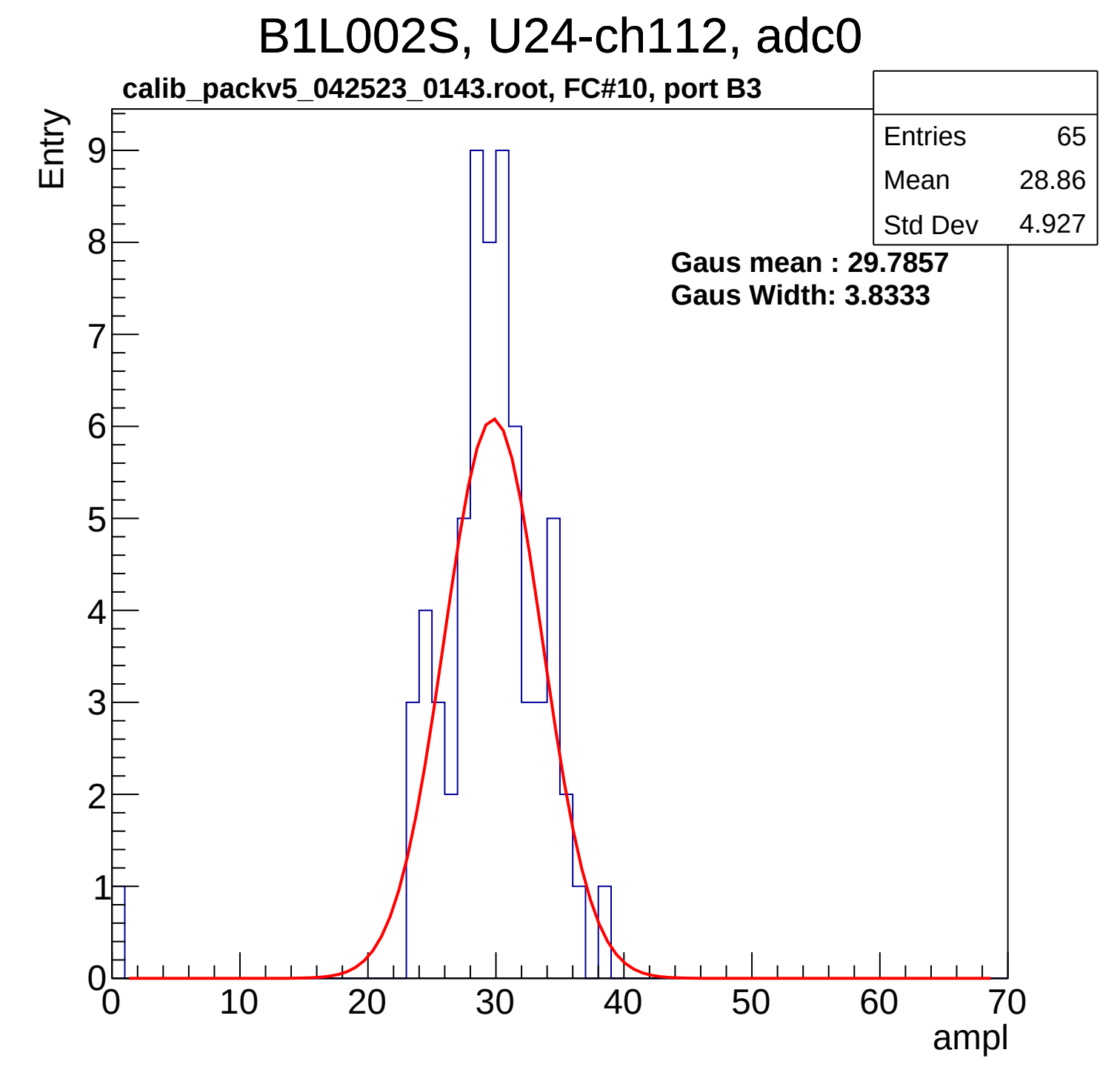
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch112, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 28.86
Std Dev 4.927
Gaus mean : 29.7857
Gaus Width: 3.8333
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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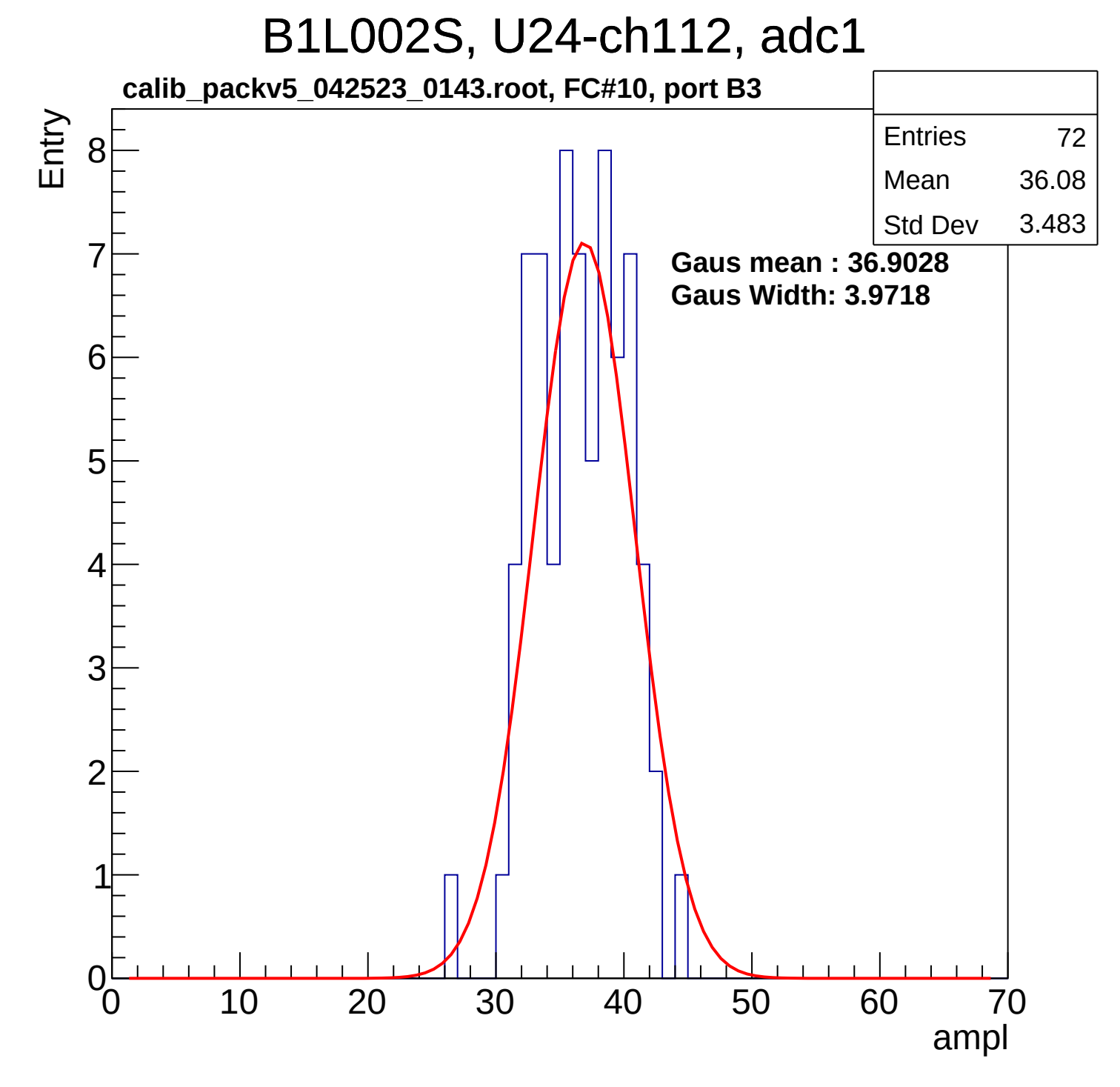
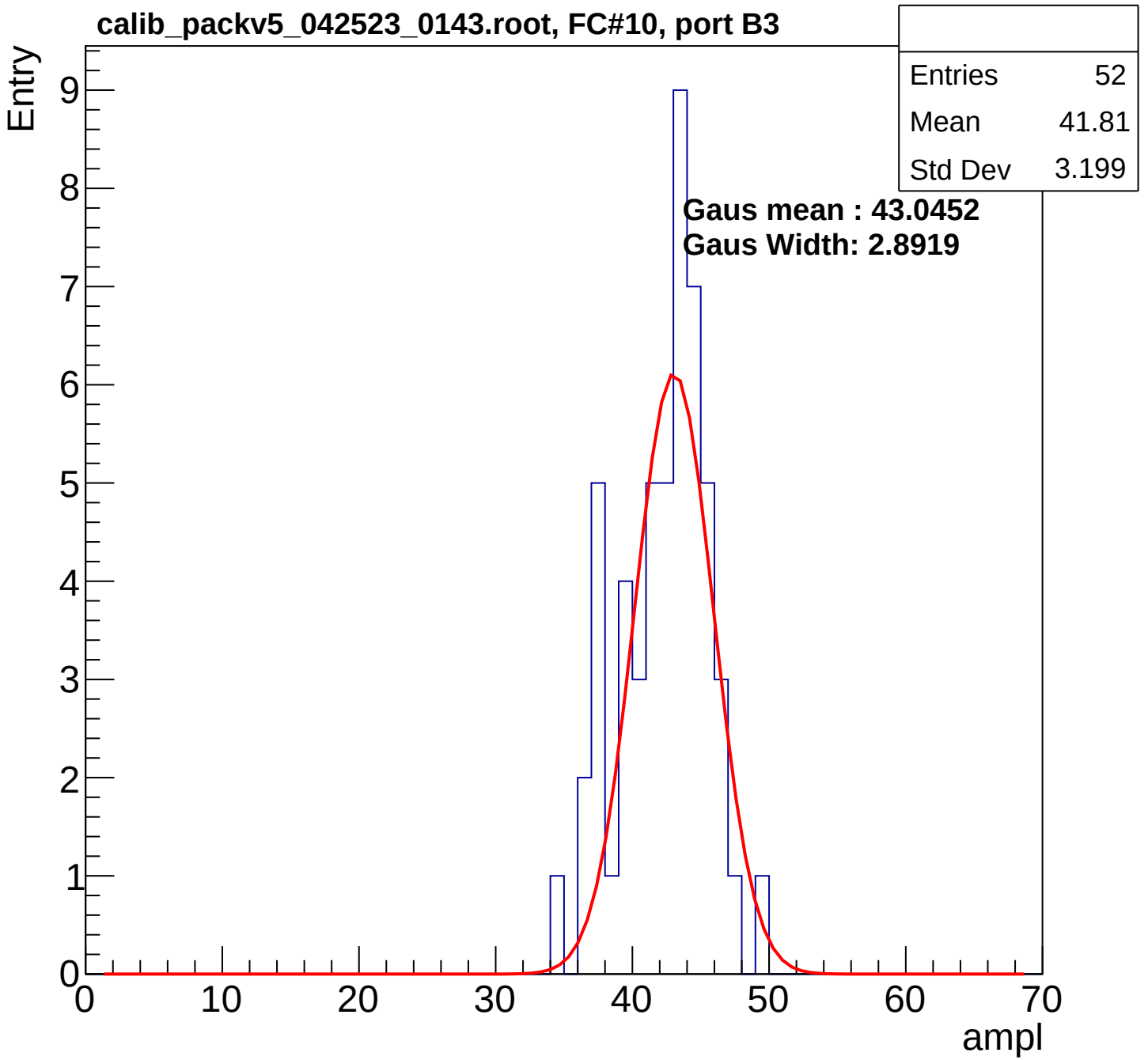

B1L002S, U24-ch112, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 36.08
Std Dev 3.483
Gaus mean : 36.9028
Gaus Width: 3.9718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch112, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 41.81
Std Dev 3.199
Gaus mean : 43.0452
Gaus Width: 2.8919
Entry
ampl
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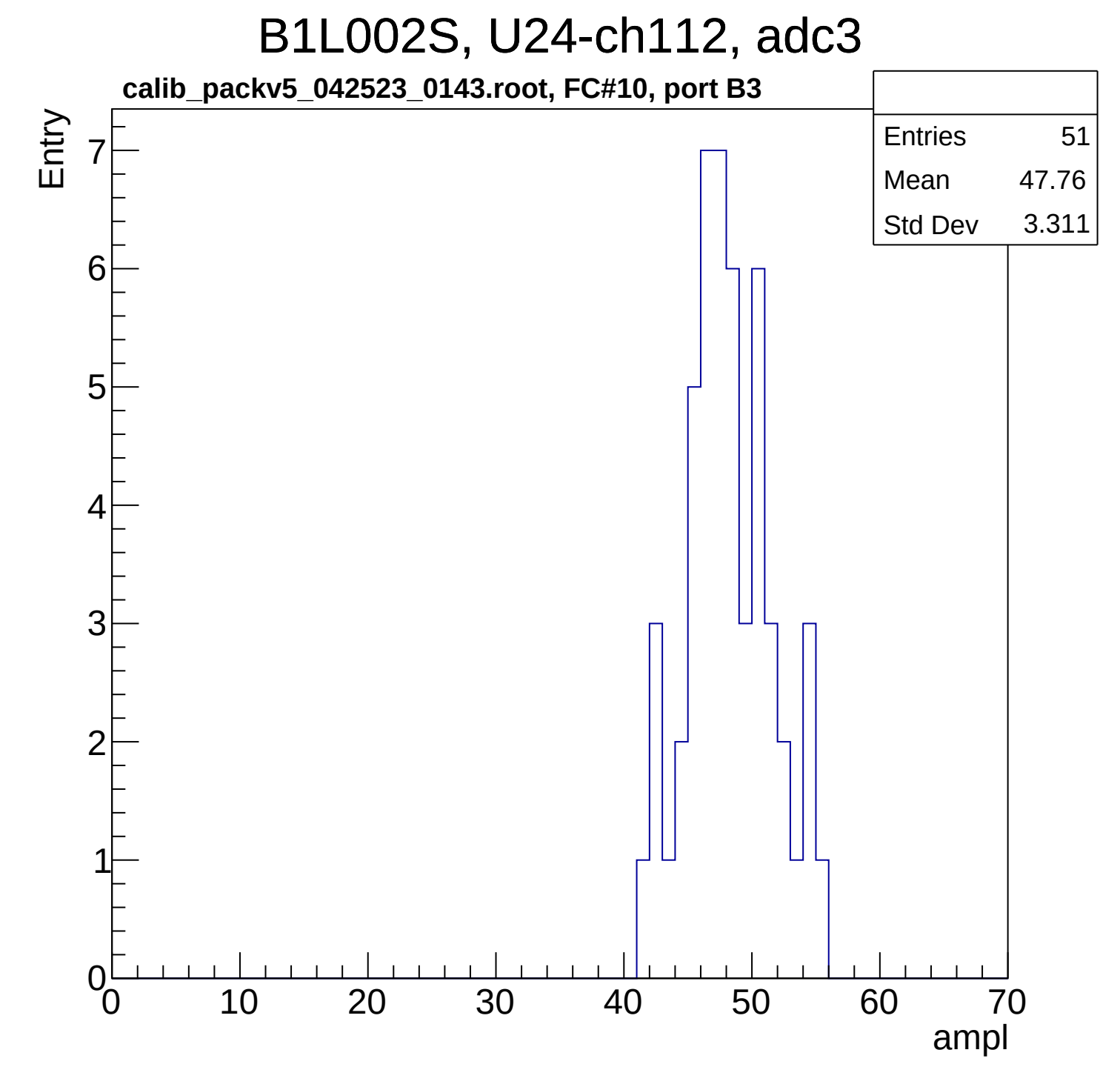
B1L002S, U24-ch112, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 47.76
Std Dev 3.311
Entry
ampl
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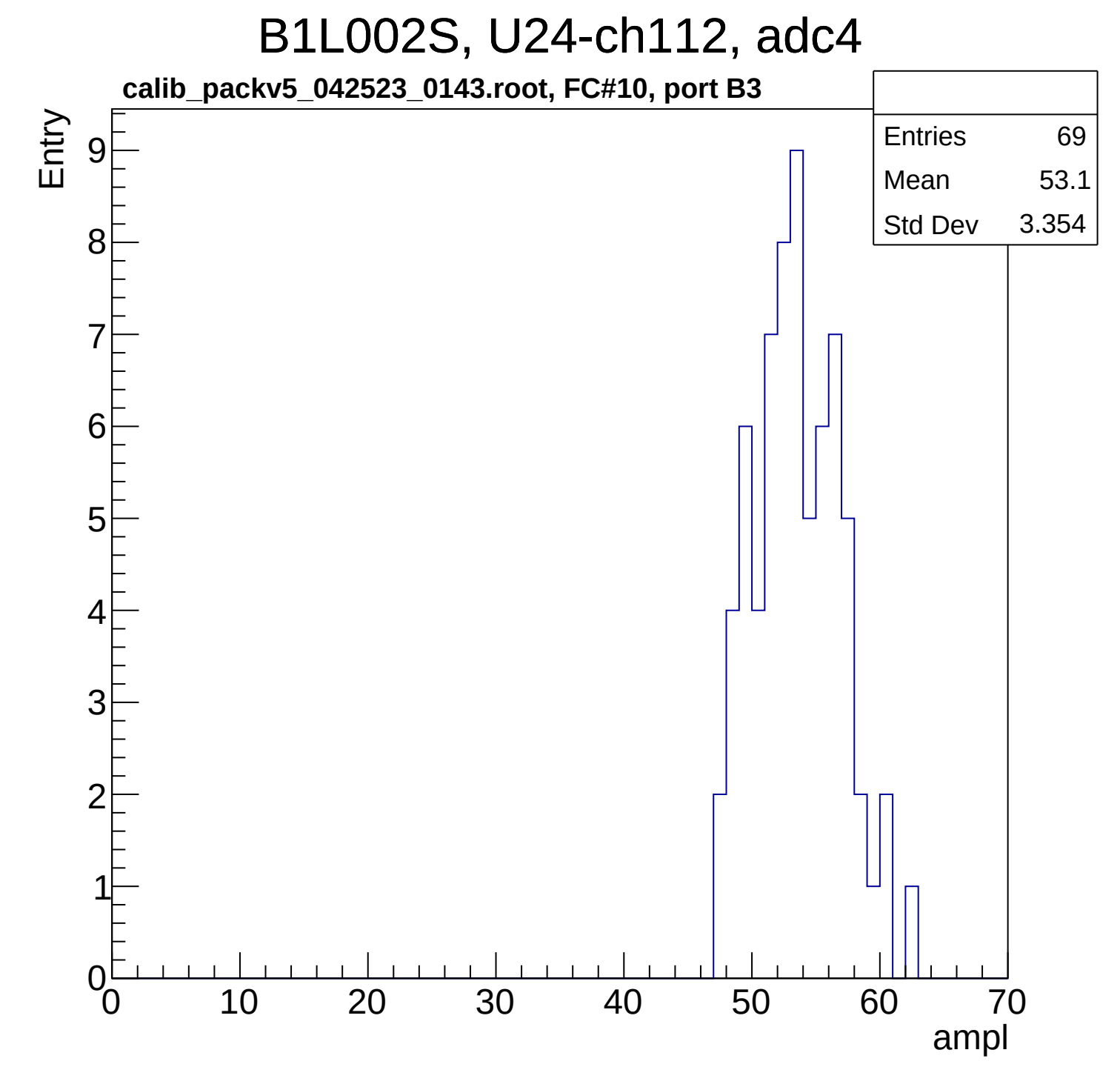
B1L002S, U24-ch112, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 53.1
Std Dev 3.354
Entry
ampl
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4
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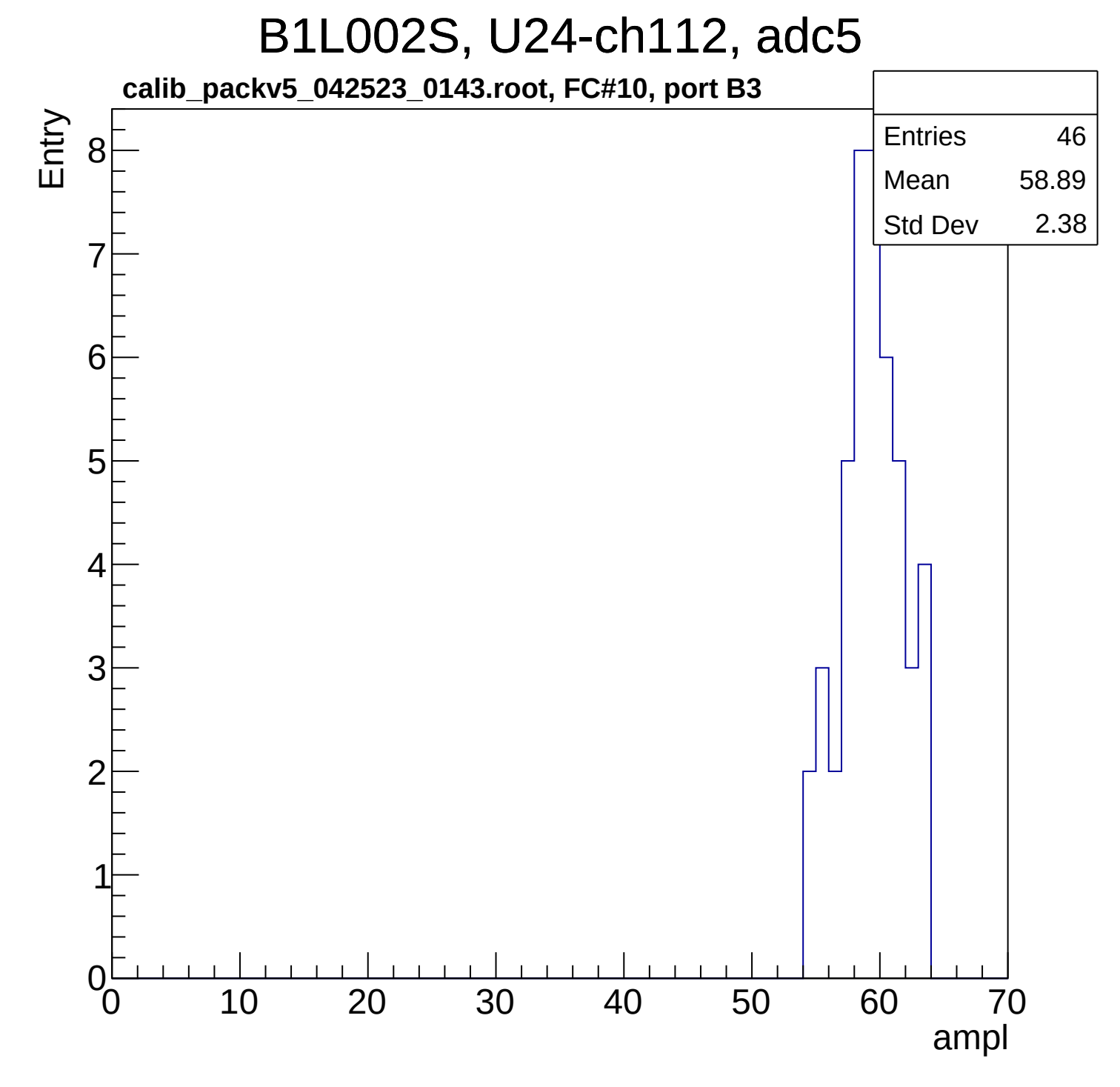
B1L002S, U24-ch112, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.89
Std Dev 2.38
Entry
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3
4
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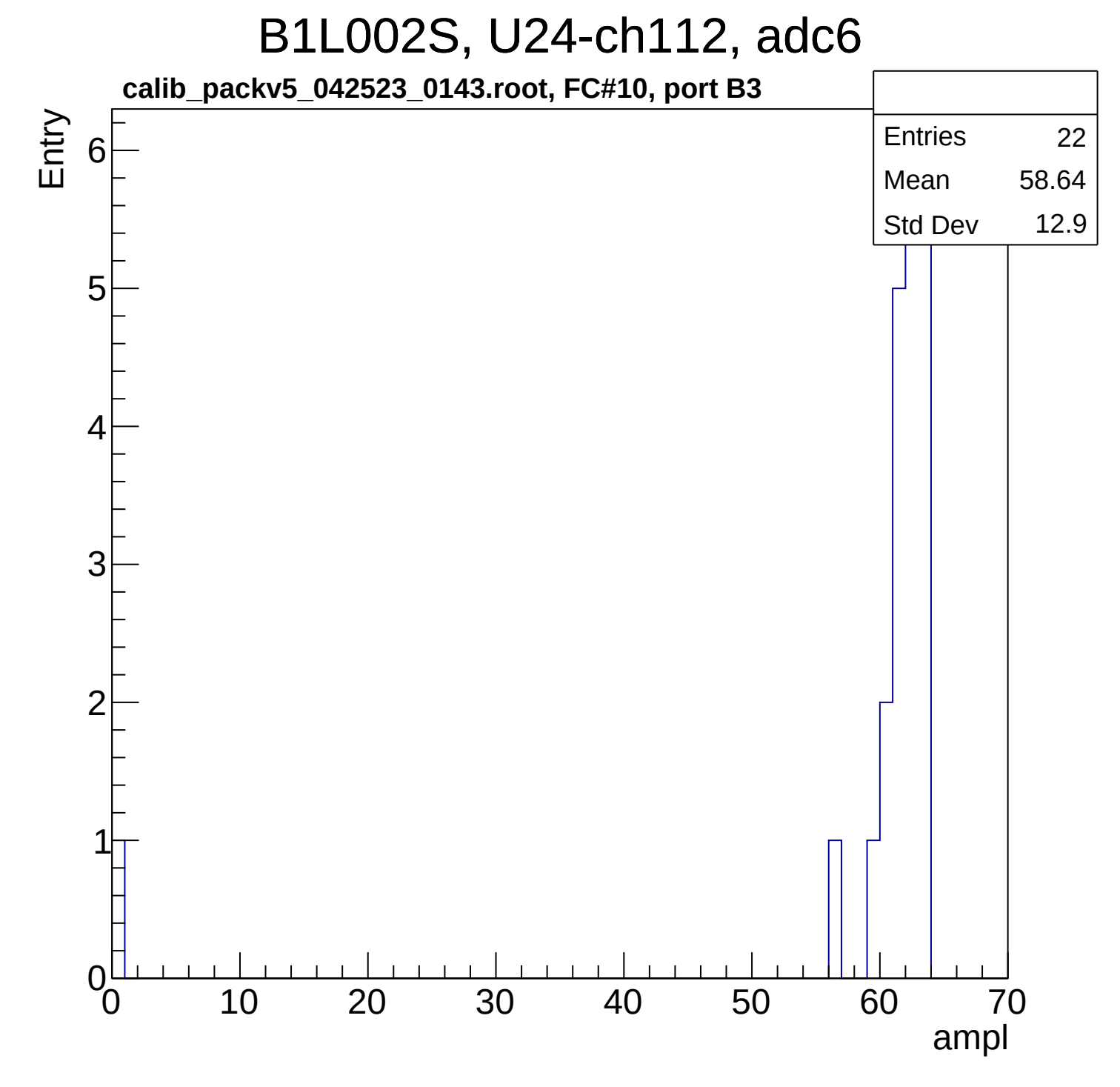
70
ampl


B1L002S, U24-ch112, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 22
Mean 58.64
Std Dev 12.9
Entry
0
1
2
3
4
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6
0
10
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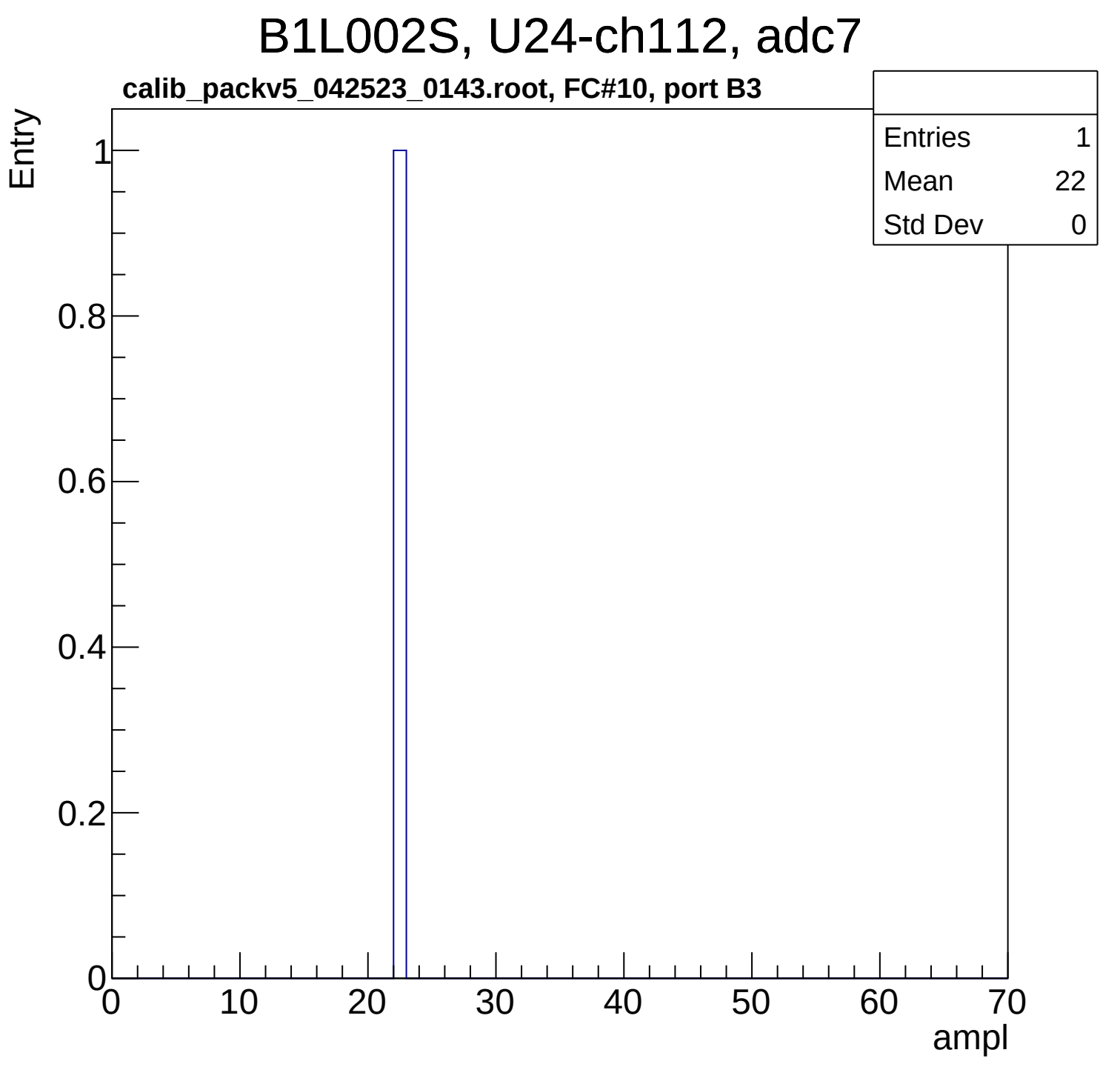
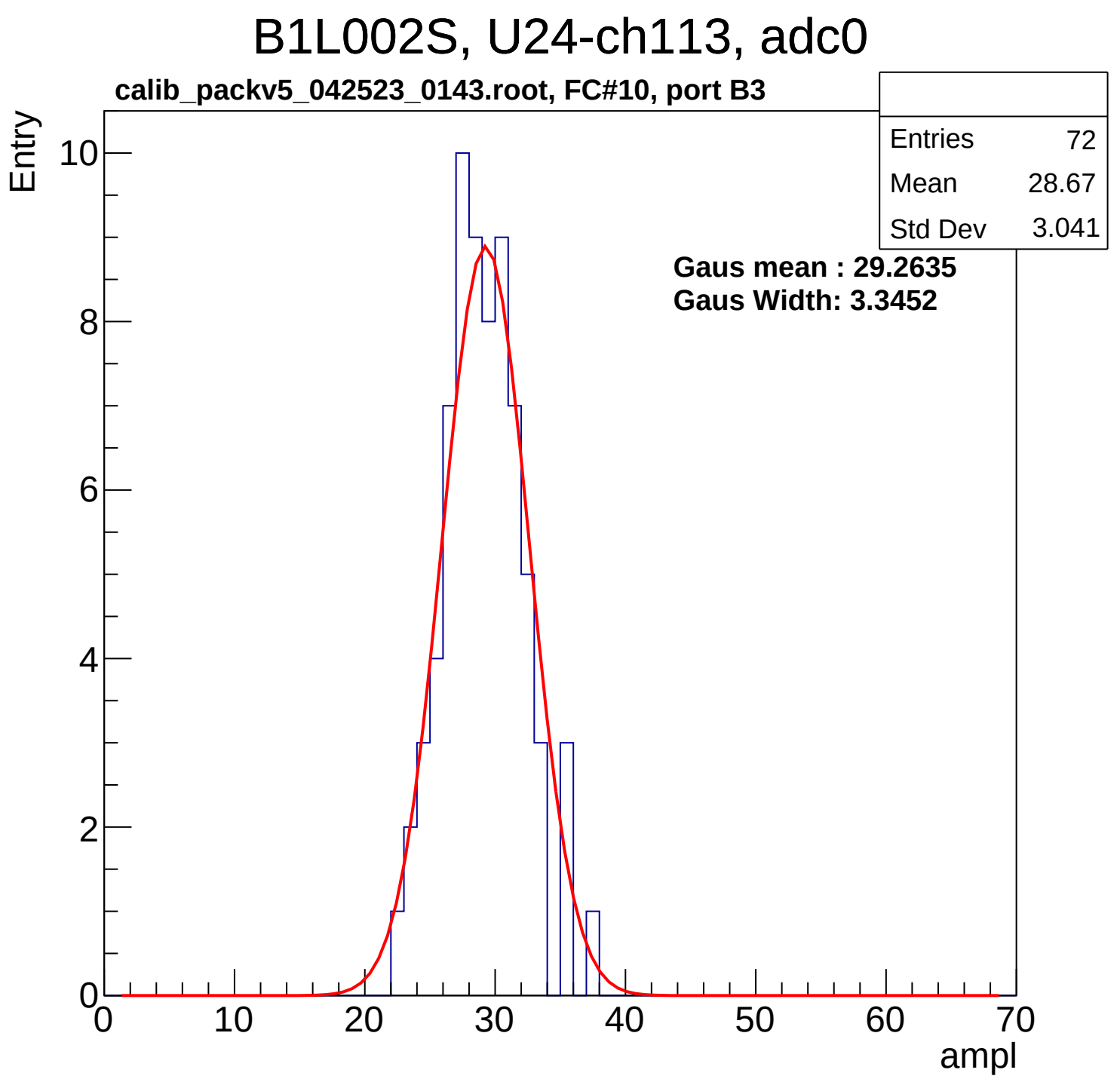
50
60
70
ampl


B1L002S, U24-ch112, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch113, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 28.67
Std Dev 3.041
Gaus mean : 29.2635
Gaus Width: 3.3452
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
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60
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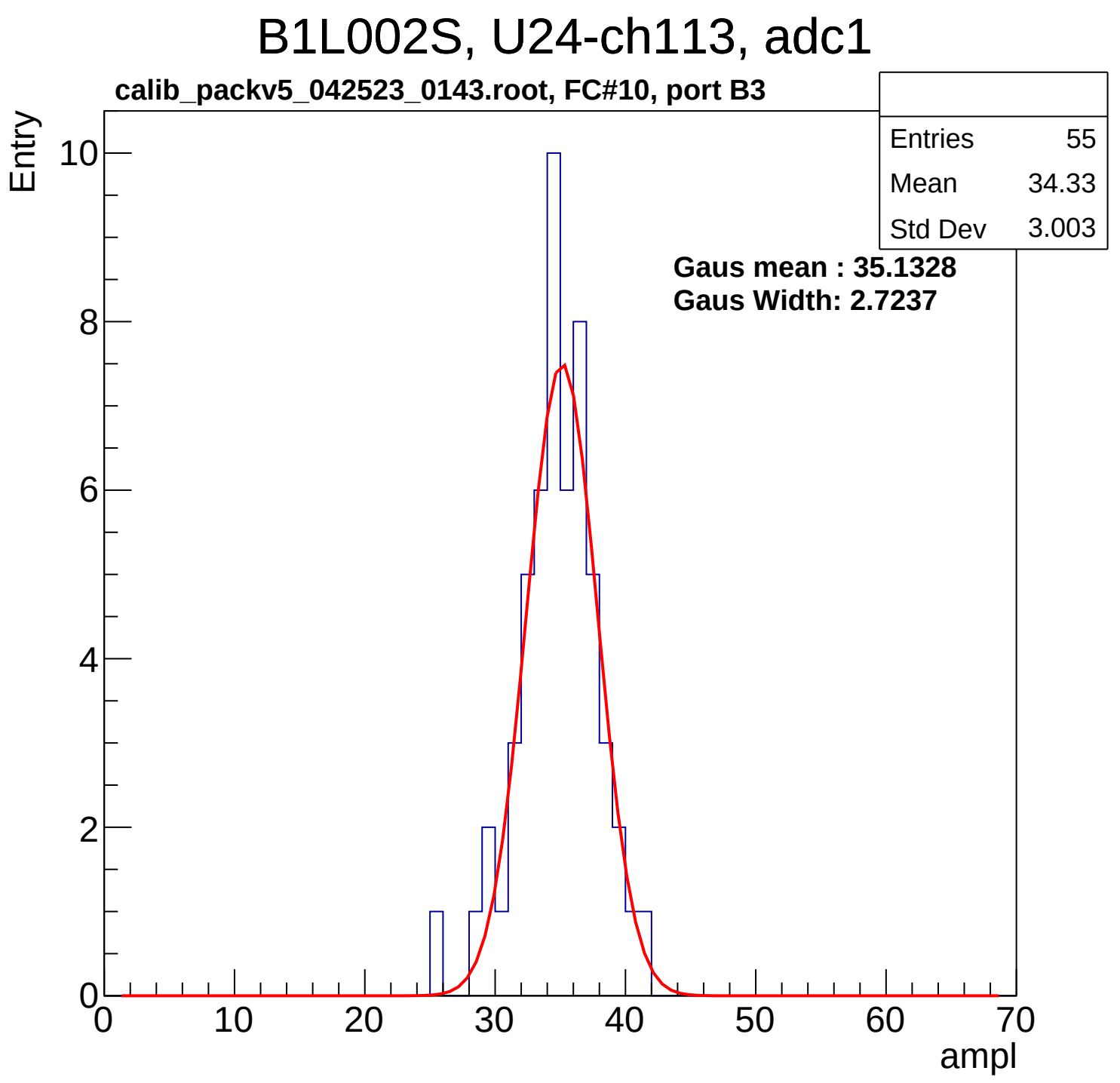
B1L002S, U24-ch113, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 34.33
Std Dev 3.003
Gaus mean : 35.1328
Gaus Width: 2.7237
Entry
ampl
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2
4
6
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0
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20
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40
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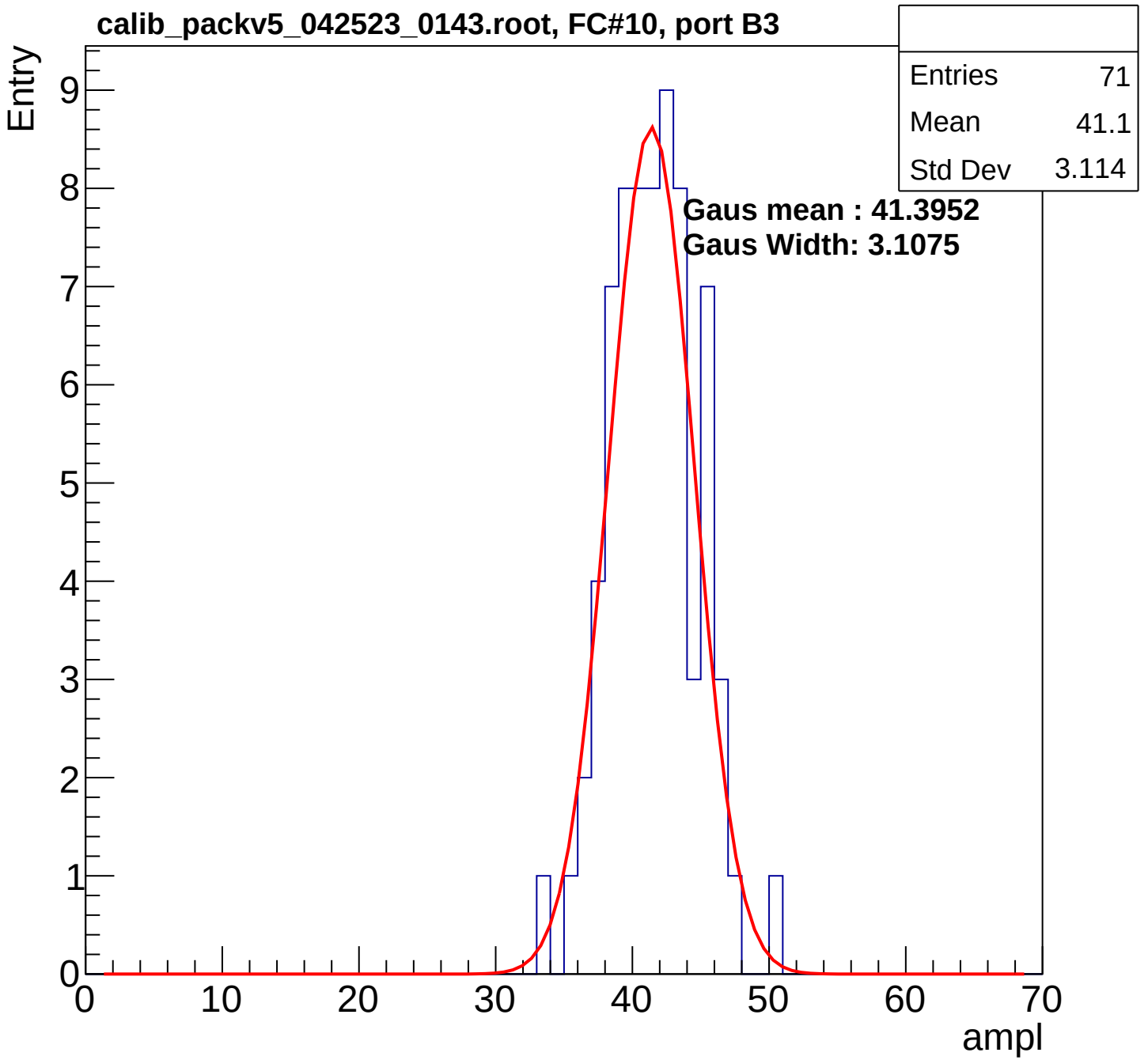
B1L002S, U24-ch113, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 41.1
Std Dev 3.114
Gaus mean : 41.3952
Gaus Width: 3.1075
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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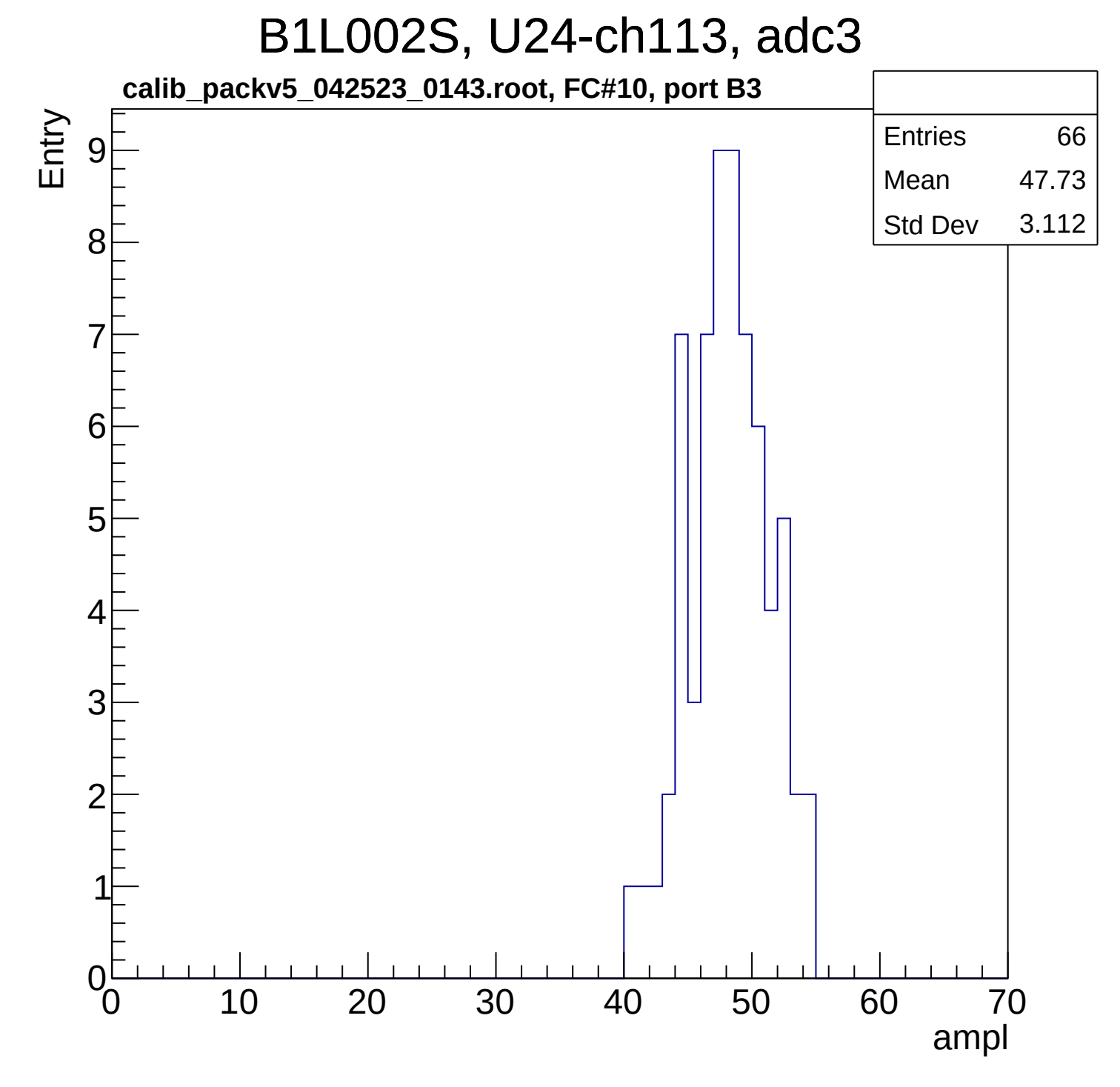
B1L002S, U24-ch113, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 47.73
Std Dev 3.112
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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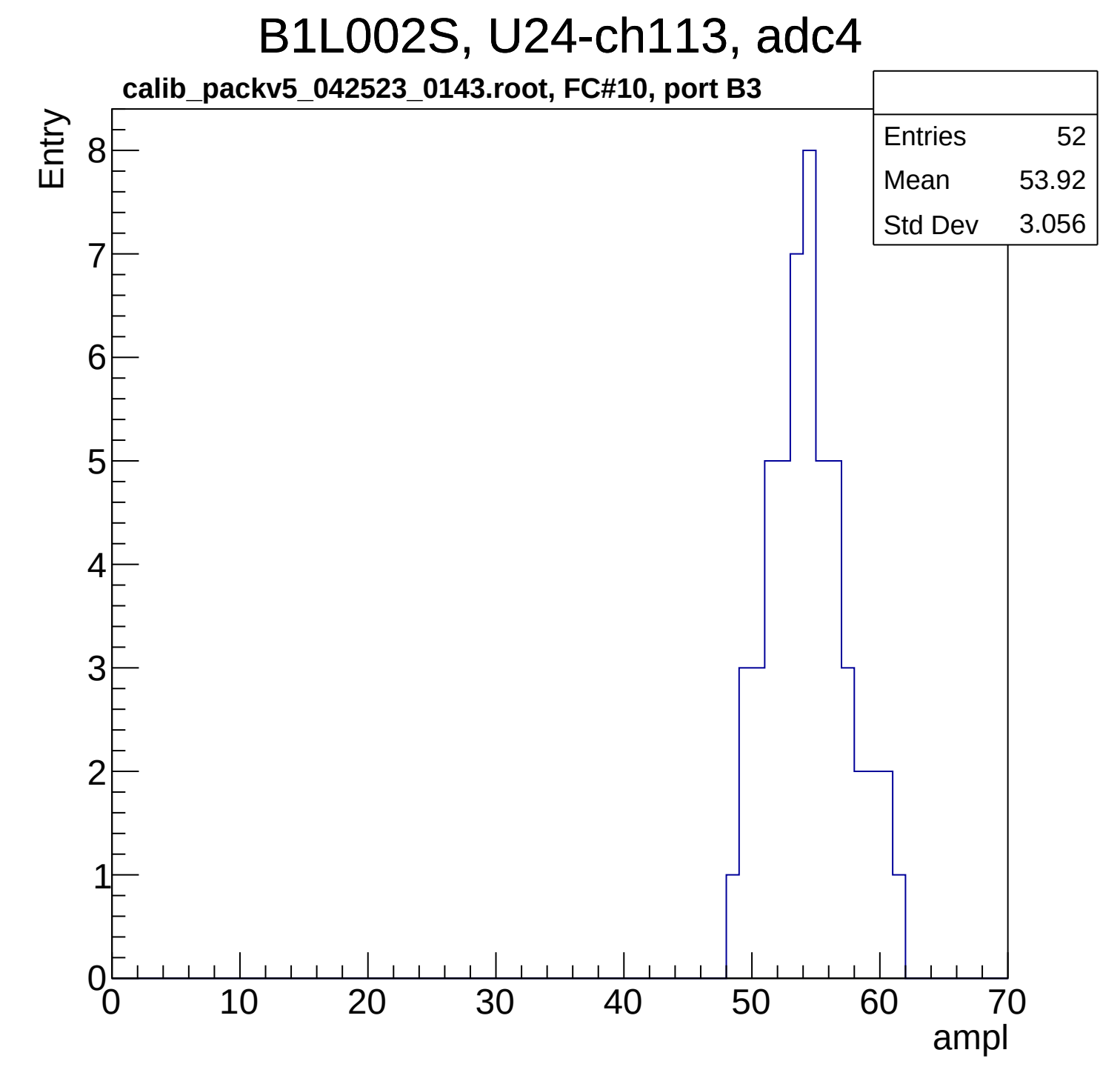
B1L002S, U24-ch113, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 53.92
Std Dev 3.056
Entry
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2
3
4
5
6
7
8
0
10
20
30
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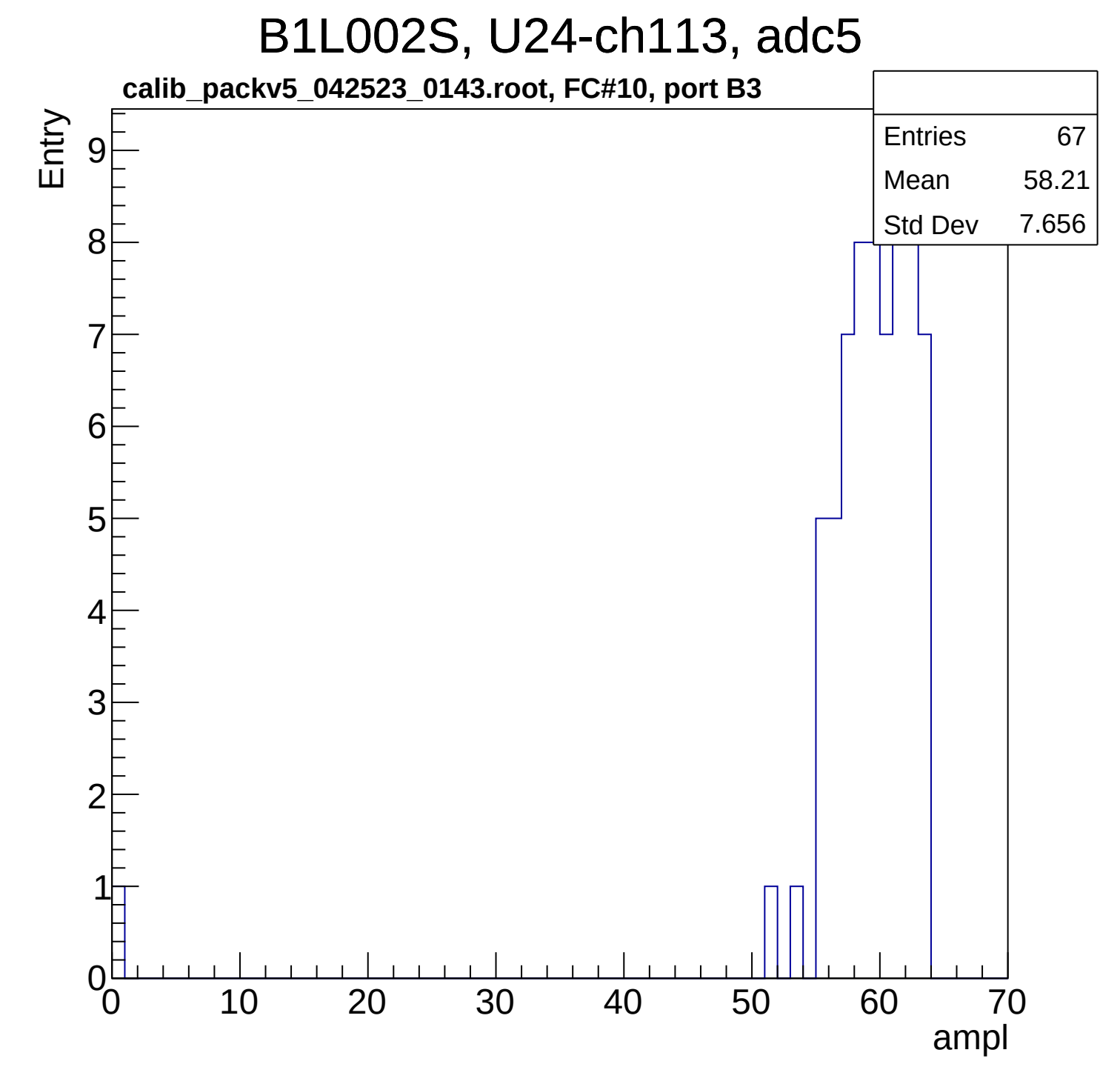
50
60
70
ampl


B1L002S, U24-ch113, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 67
Mean 58.21
Std Dev 7.656
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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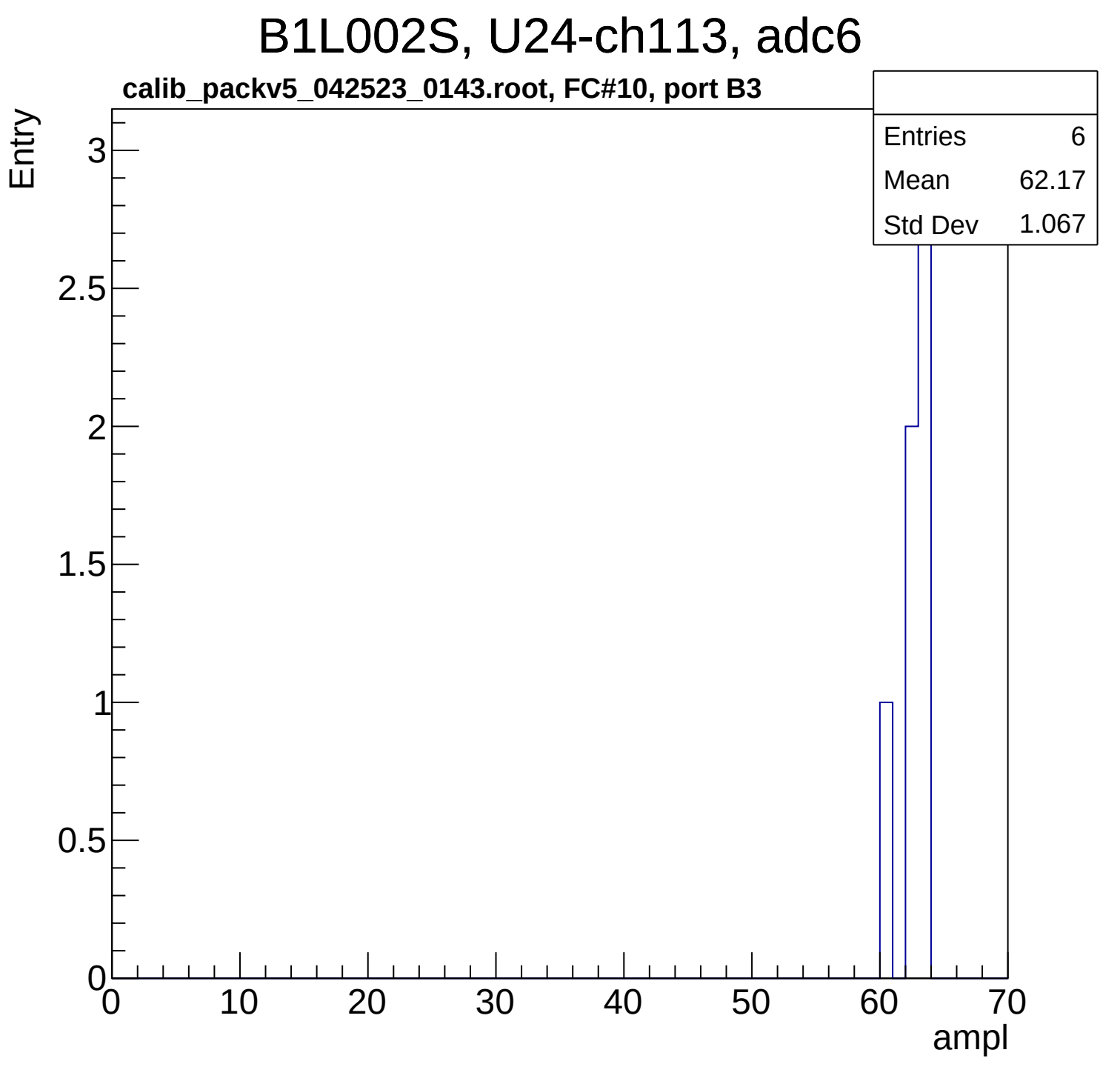
60
70


B1L002S, U24-ch113, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 6
Mean 62.17
Std Dev 1.067
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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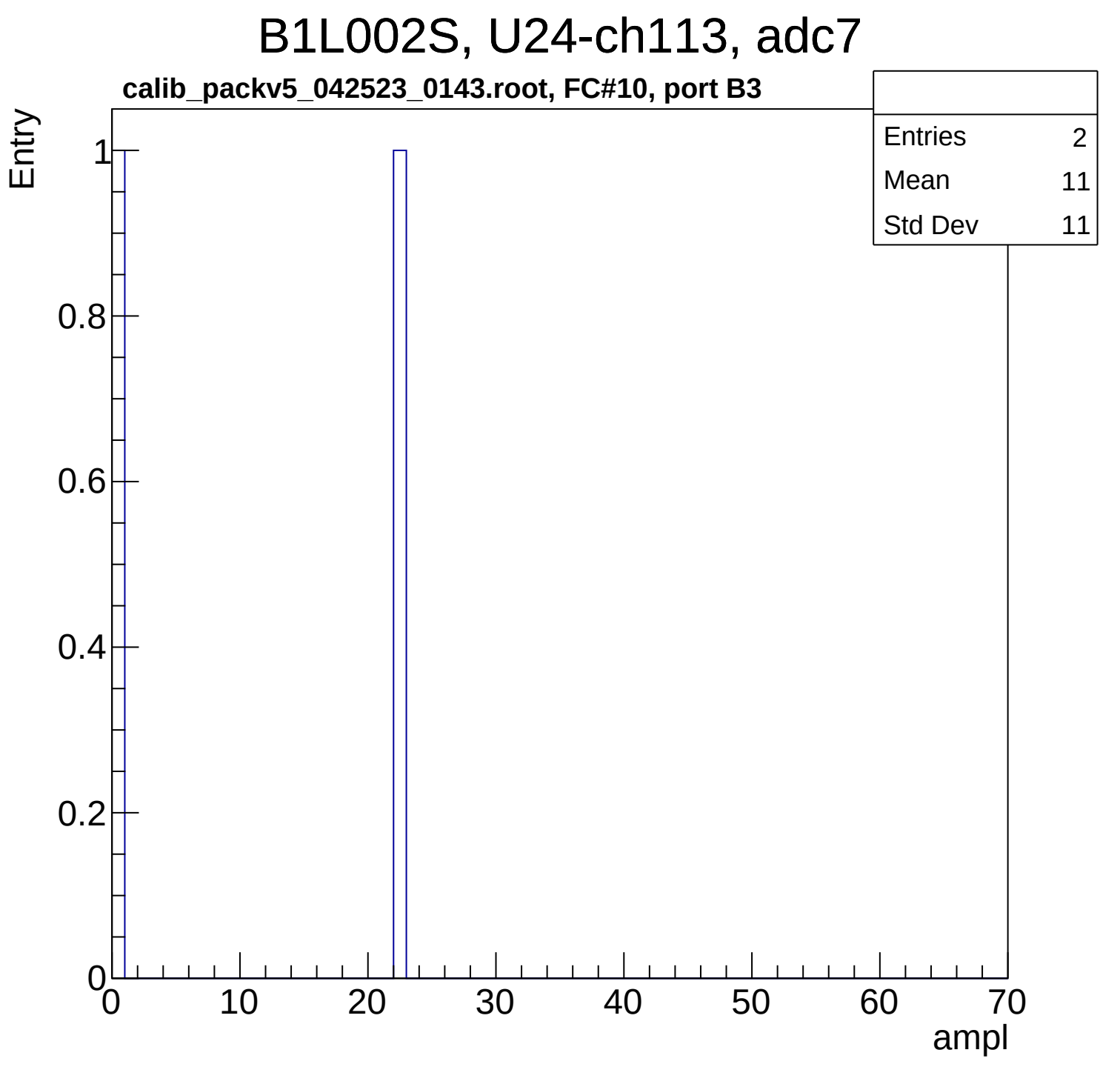
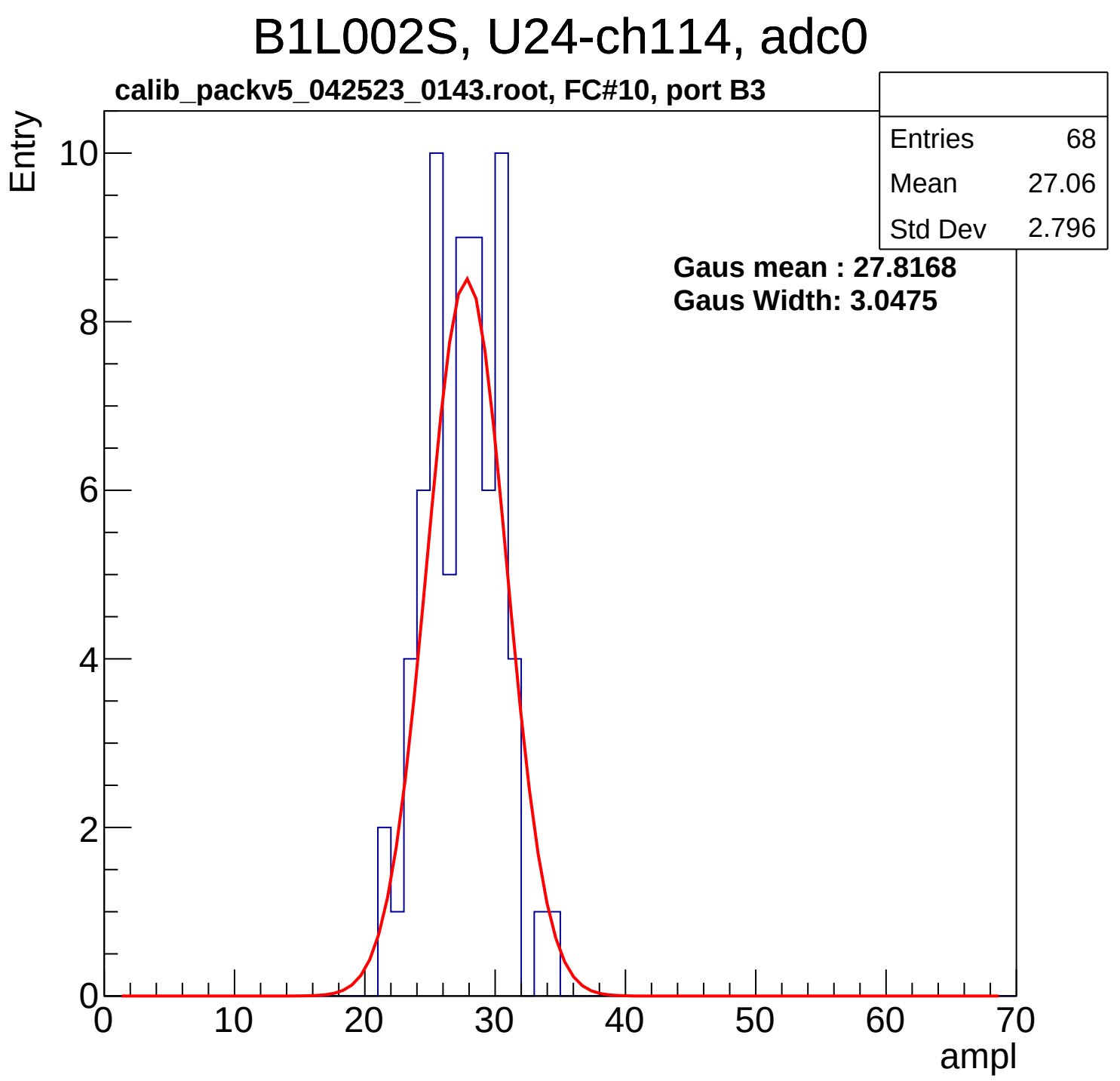
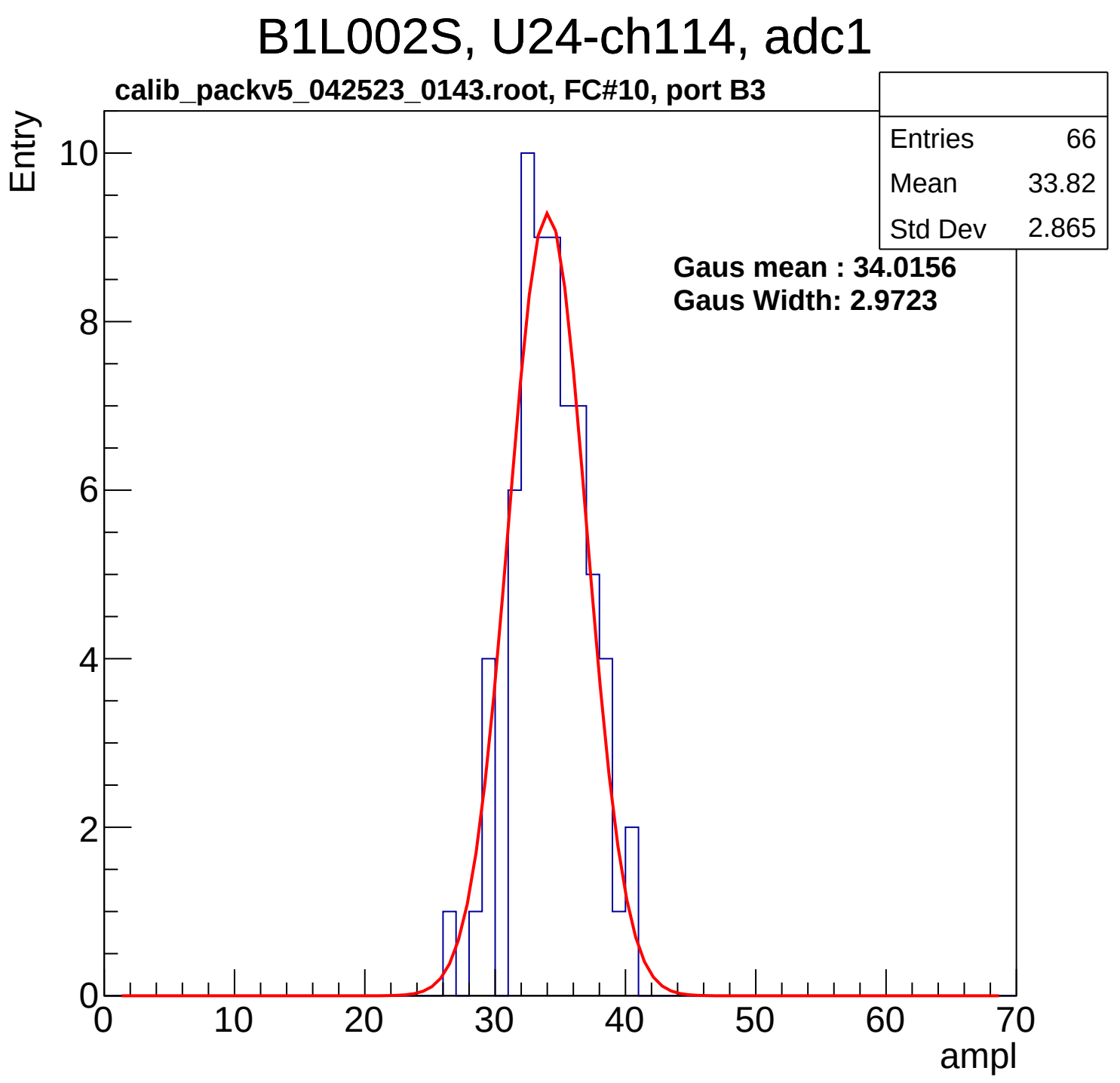
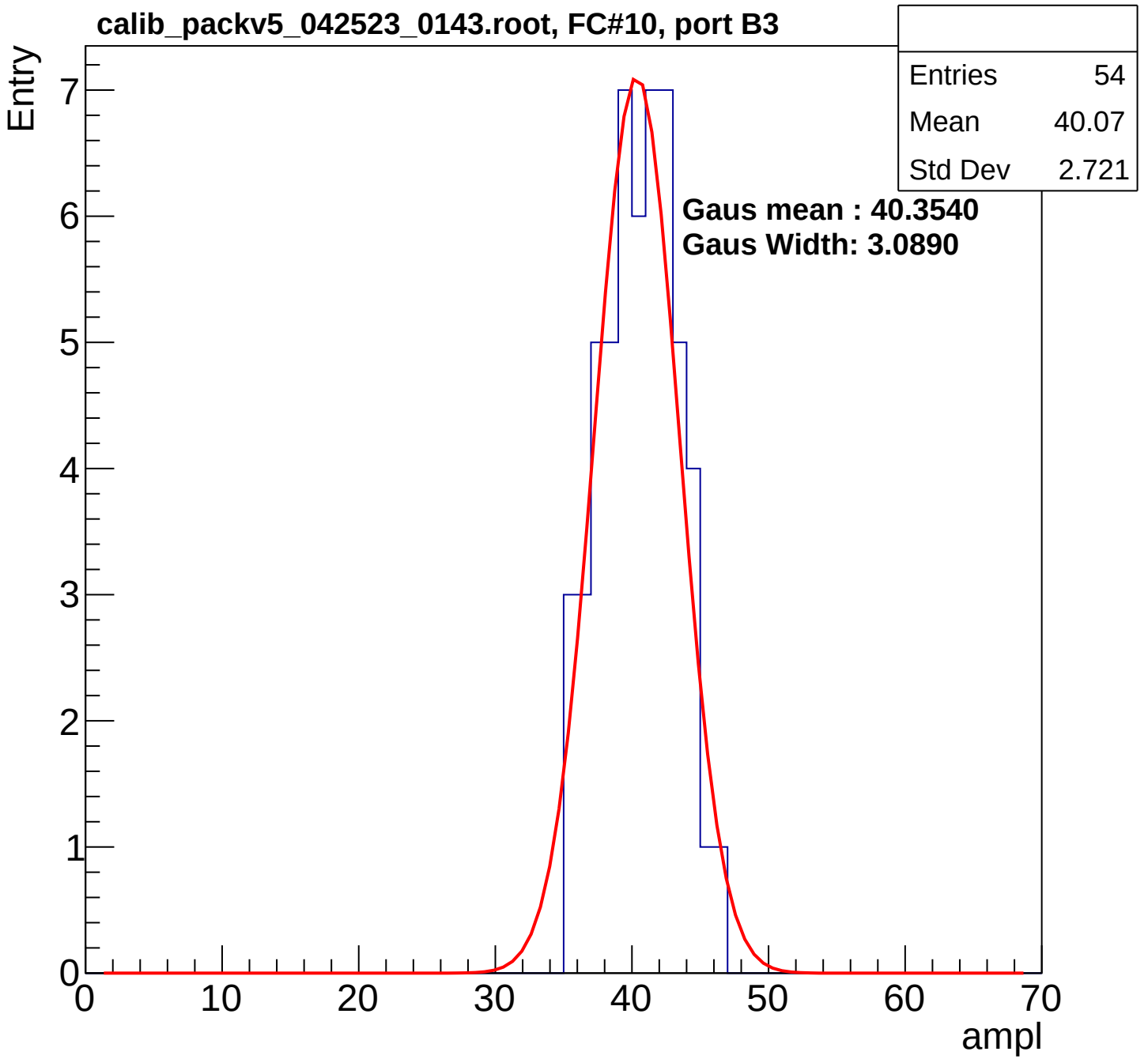

B1L002S, U24-ch113, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch114, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.06
Std Dev 2.796
Gaus mean : 27.8168
Gaus Width: 3.0475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch114, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 33.82
Std Dev 2.865
Gaus mean : 34.0156
Gaus Width: 2.9723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch114, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 40.07
Std Dev 2.721
Gaus mean : 40.3540
Gaus Width: 3.0890
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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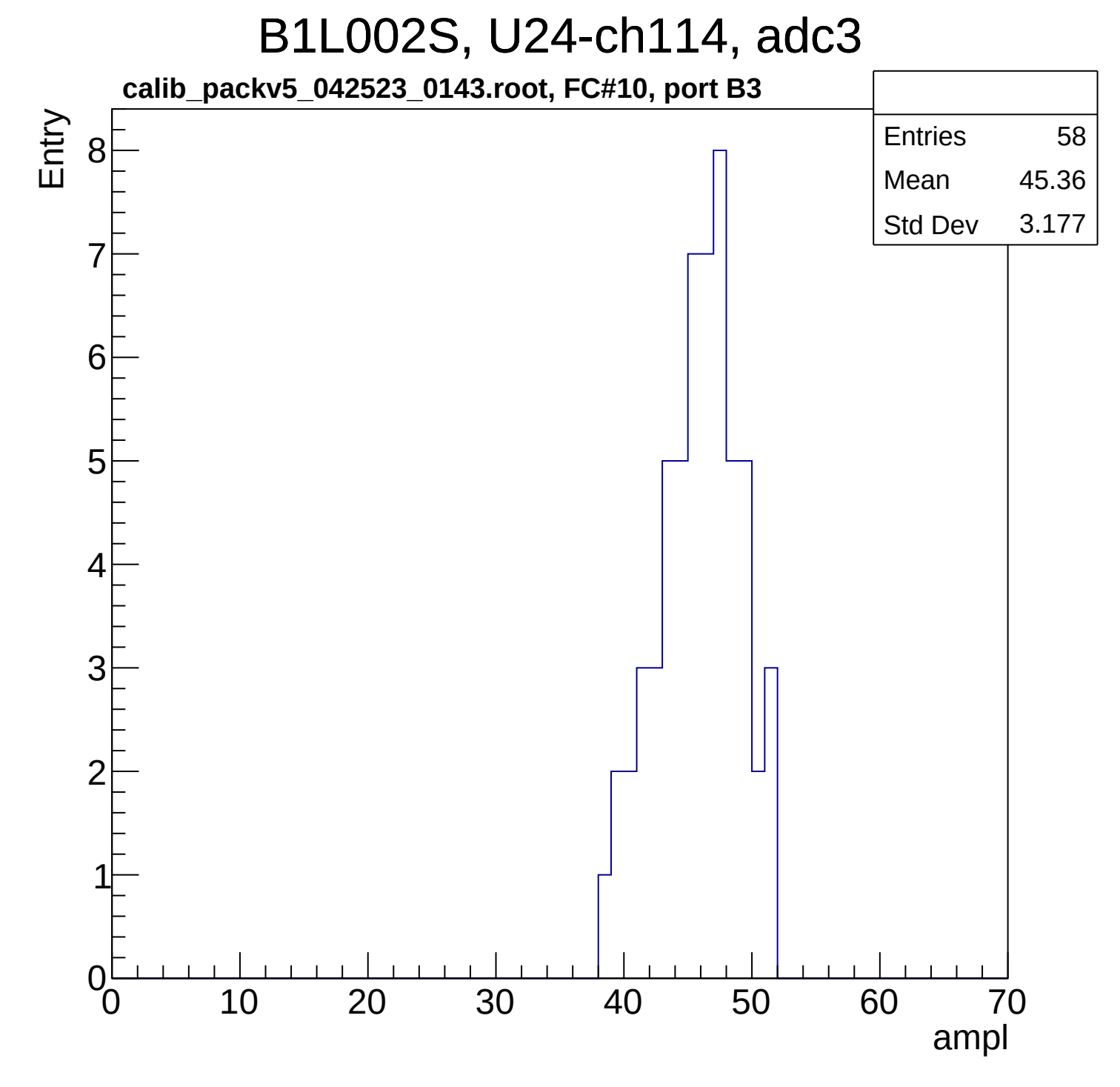
B1L002S, U24-ch114, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 45.36
Std Dev 3.177
Entry
ampl
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1
2
3
4
5
6
7
8
0
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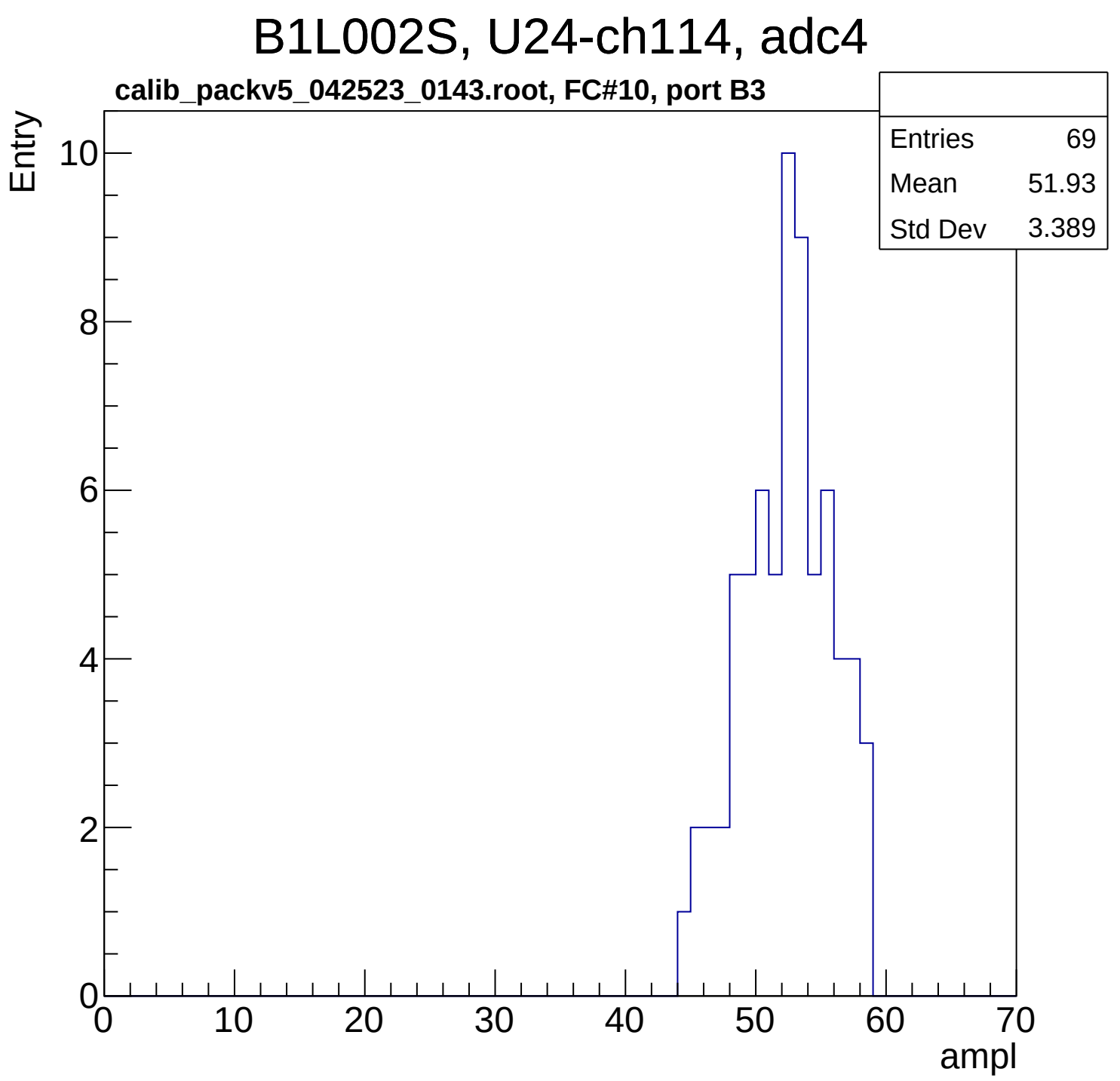
20
30
40
50
60
70


B1L002S, U24-ch114, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 51.93
Std Dev 3.389
Entry
0
2
4
6
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10
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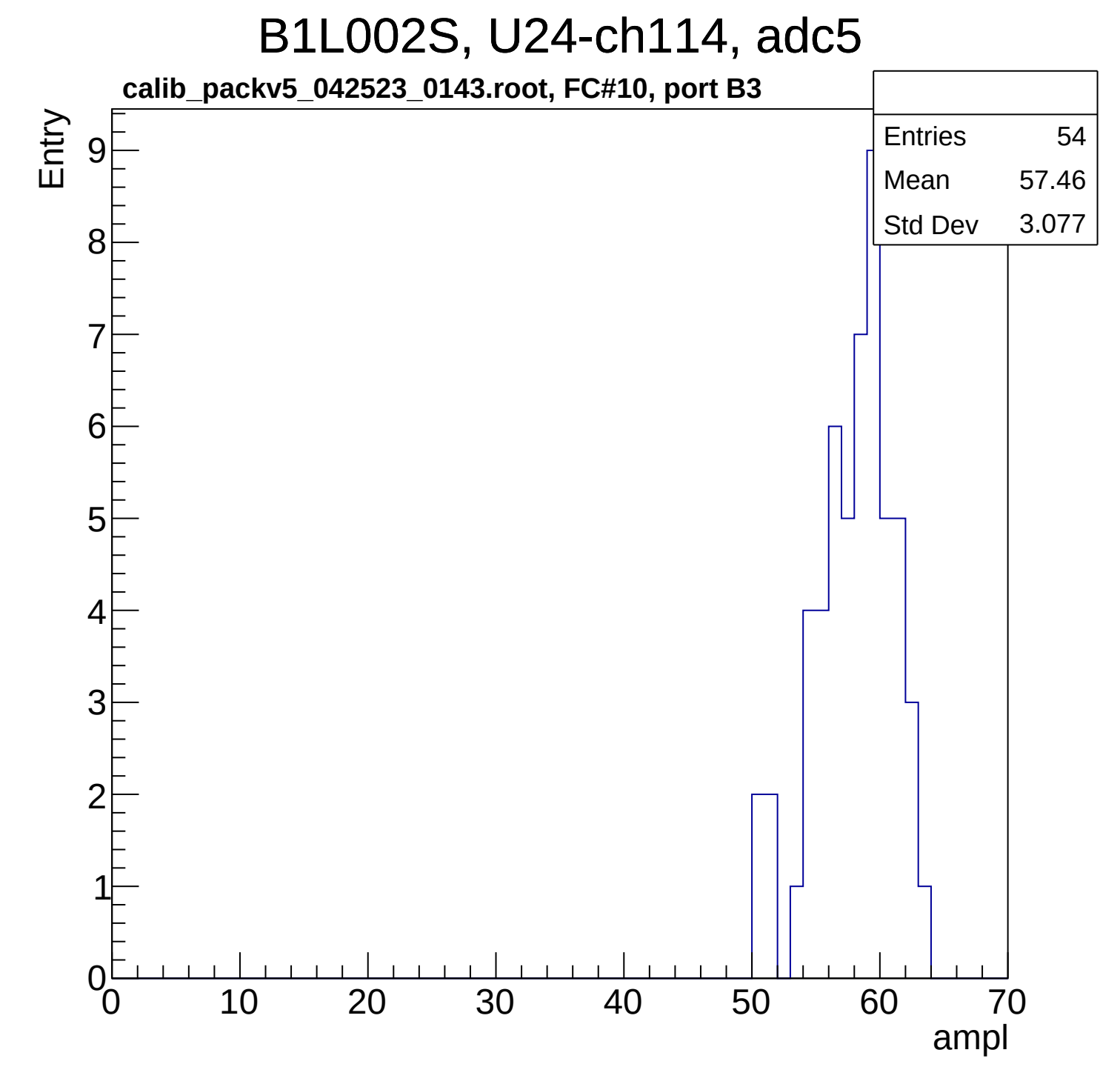
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch114, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 57.46
Std Dev 3.077
Entry
ampl
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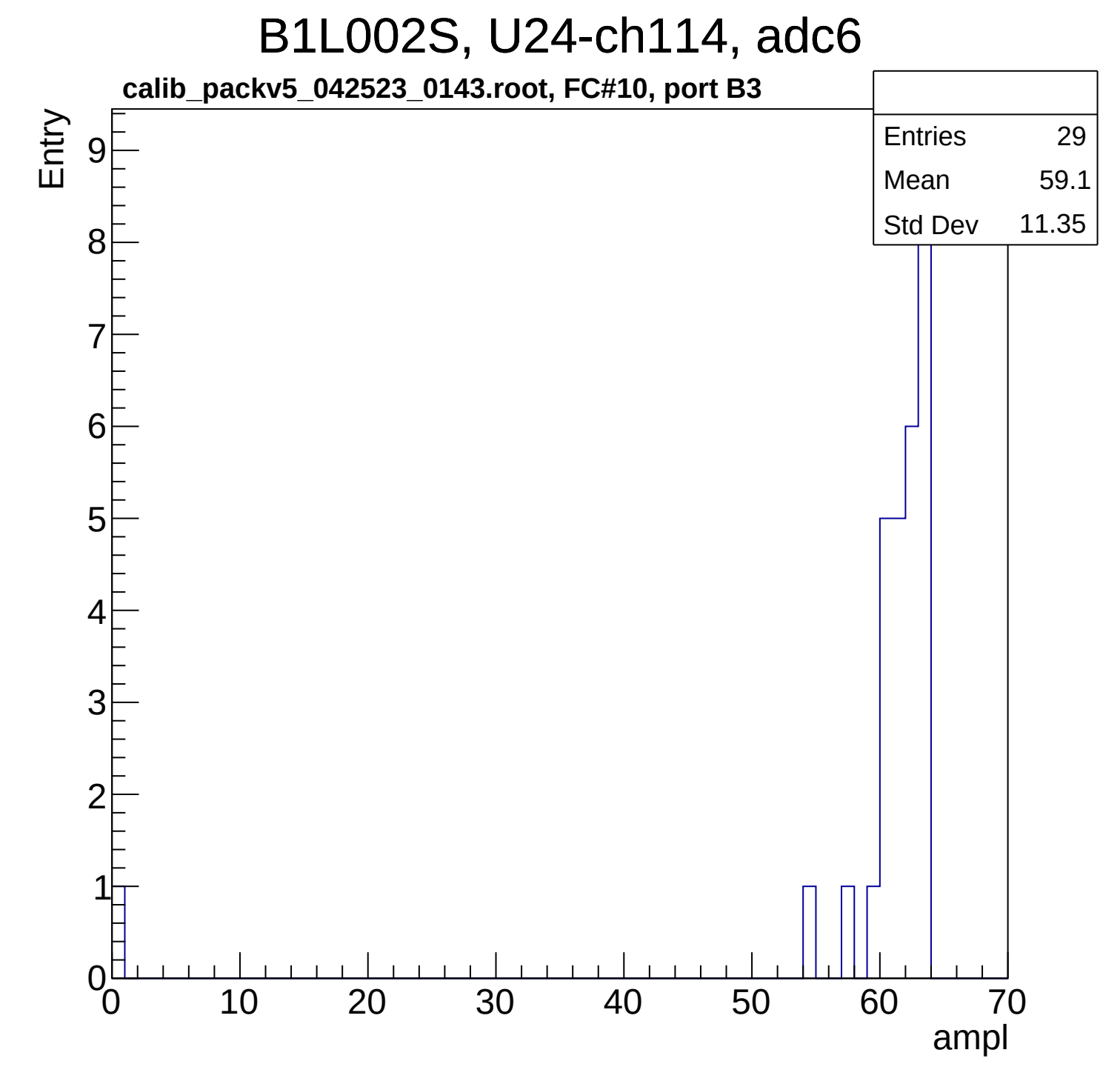
B1L002S, U24-ch114, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 29
Mean 59.1
Std Dev 11.35
Entry
ampl
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2
3
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60
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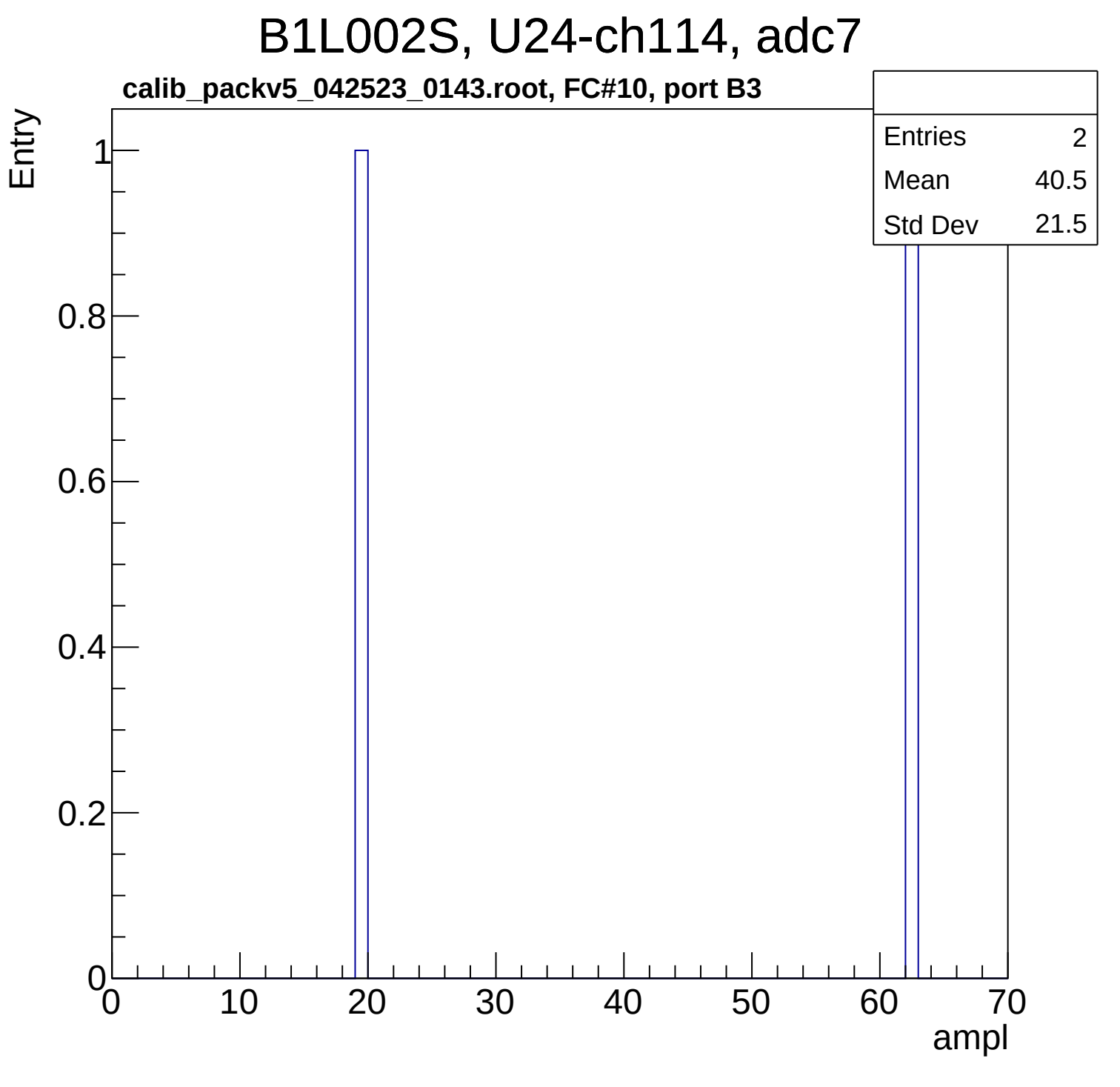
B1L002S, U24-ch114, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 40.5
Std Dev 21.5
Entry
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0.6
0.4
0.2
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10
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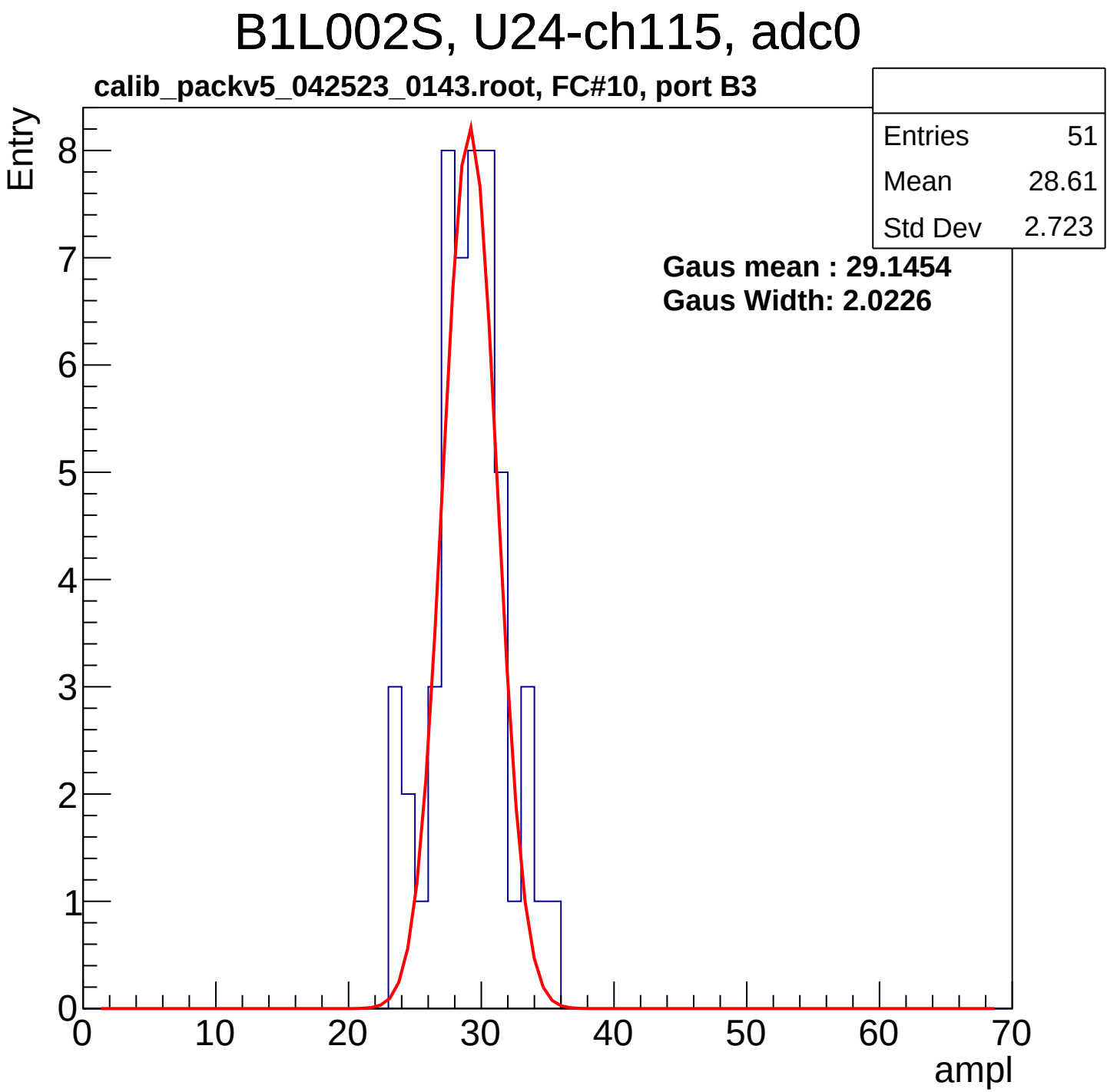
50
60
70
ampl


B1L002S, U24-ch115, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 28.61
Std Dev 2.723
Gaus mean : 29.1454
Gaus Width: 2.0226
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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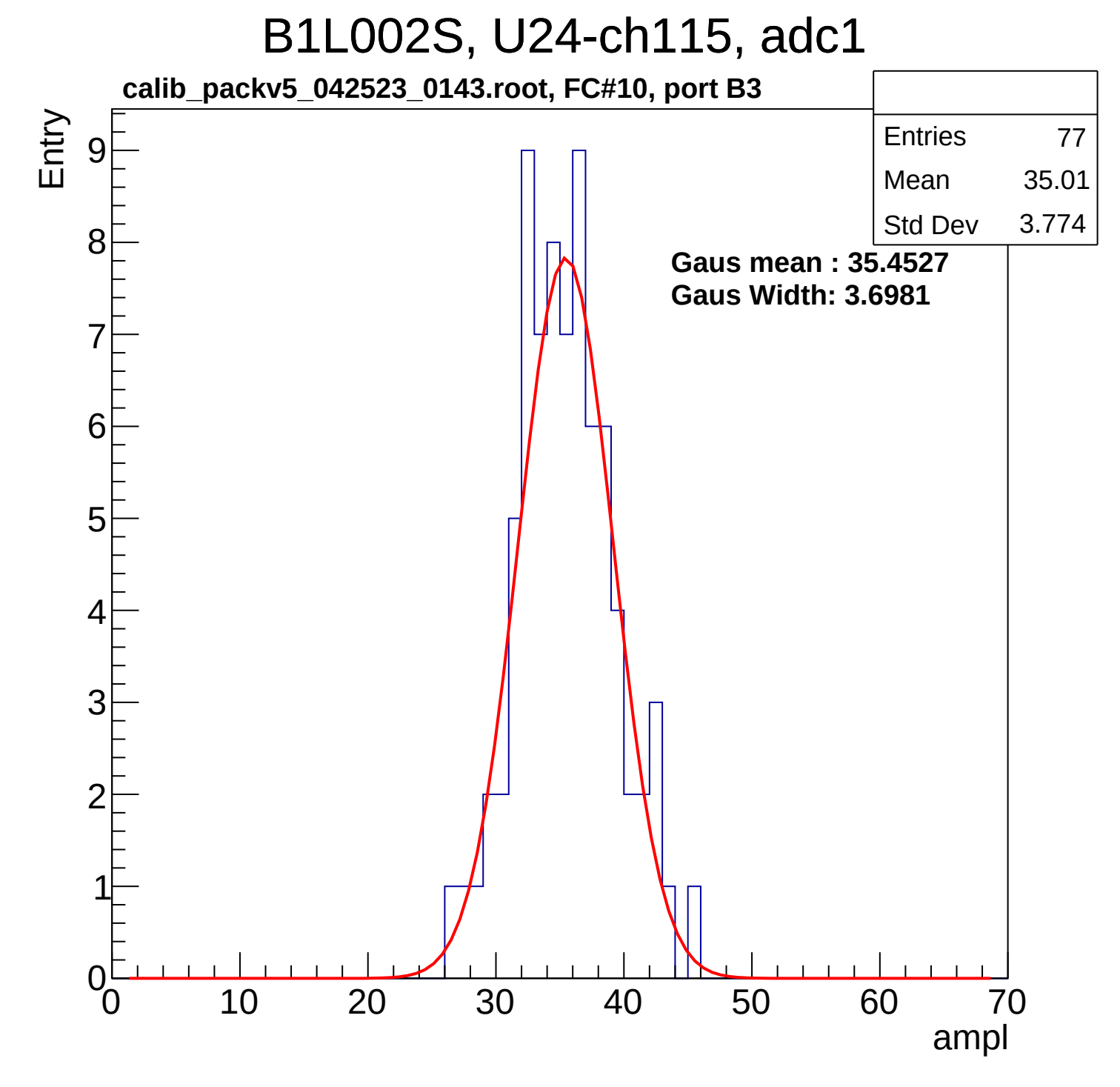
B1L002S, U24-ch115, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.01
Std Dev 3.774
Gaus mean : 35.4527
Gaus Width: 3.6981
Entry
ampl
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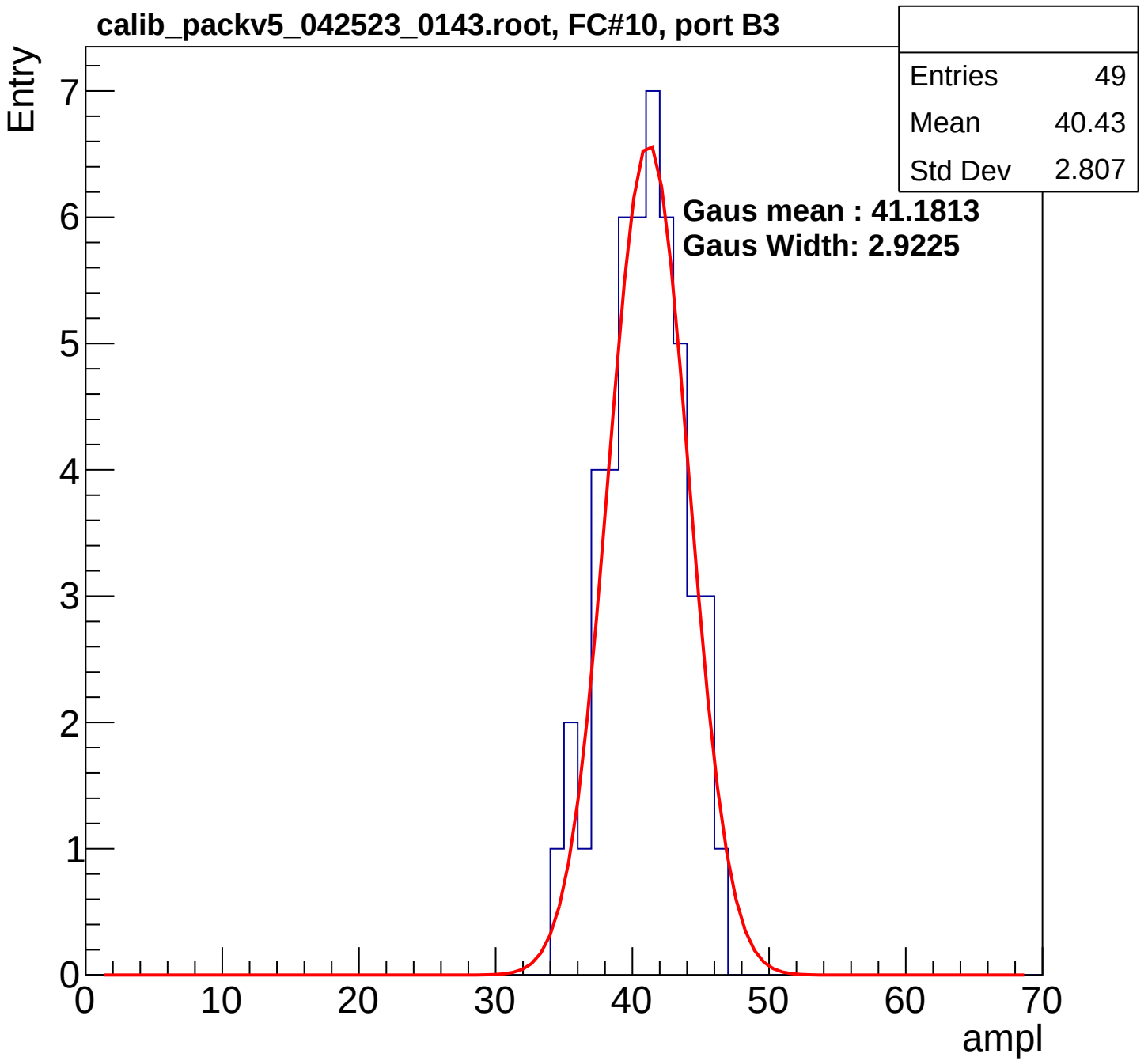
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U24-ch115, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 40.43
Std Dev 2.807
Gaus mean : 41.1813
Gaus Width: 2.9225
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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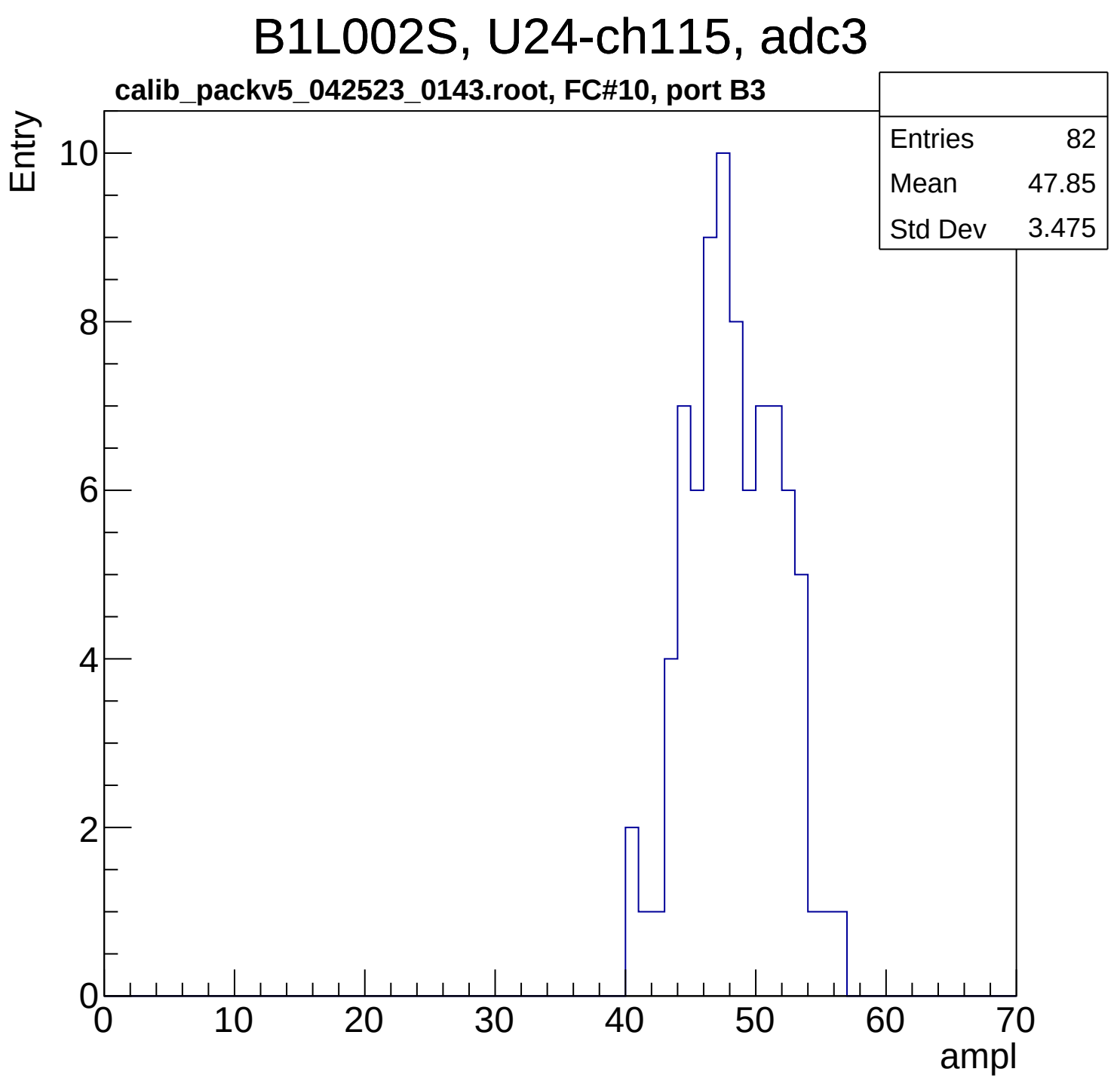
B1L002S, U24-ch115, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 82
Mean 47.85
Std Dev 3.475
Entry
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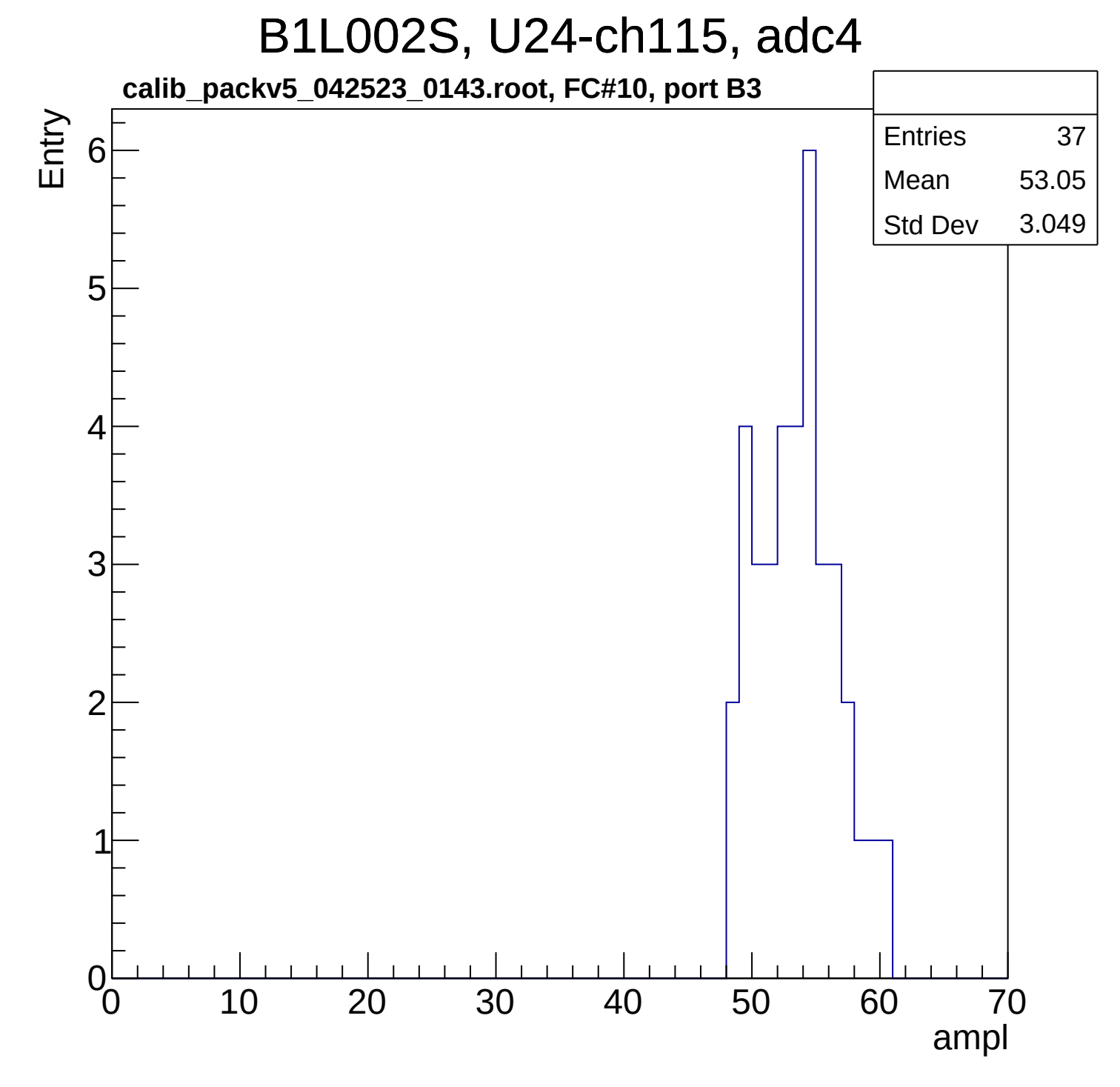
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch115, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 37
Mean 53.05
Std Dev 3.049
Entry
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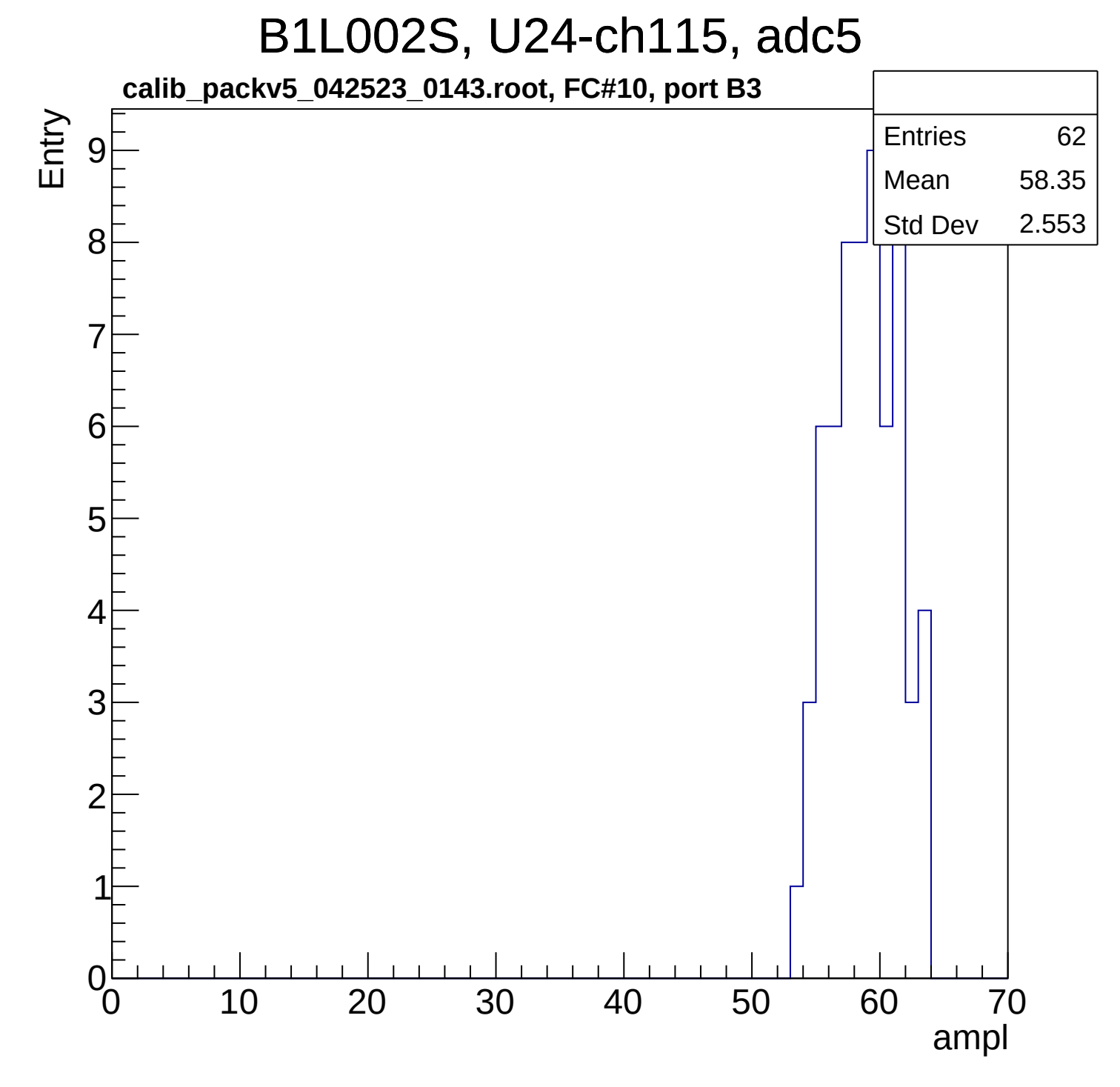
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch115, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 58.35
Std Dev 2.553
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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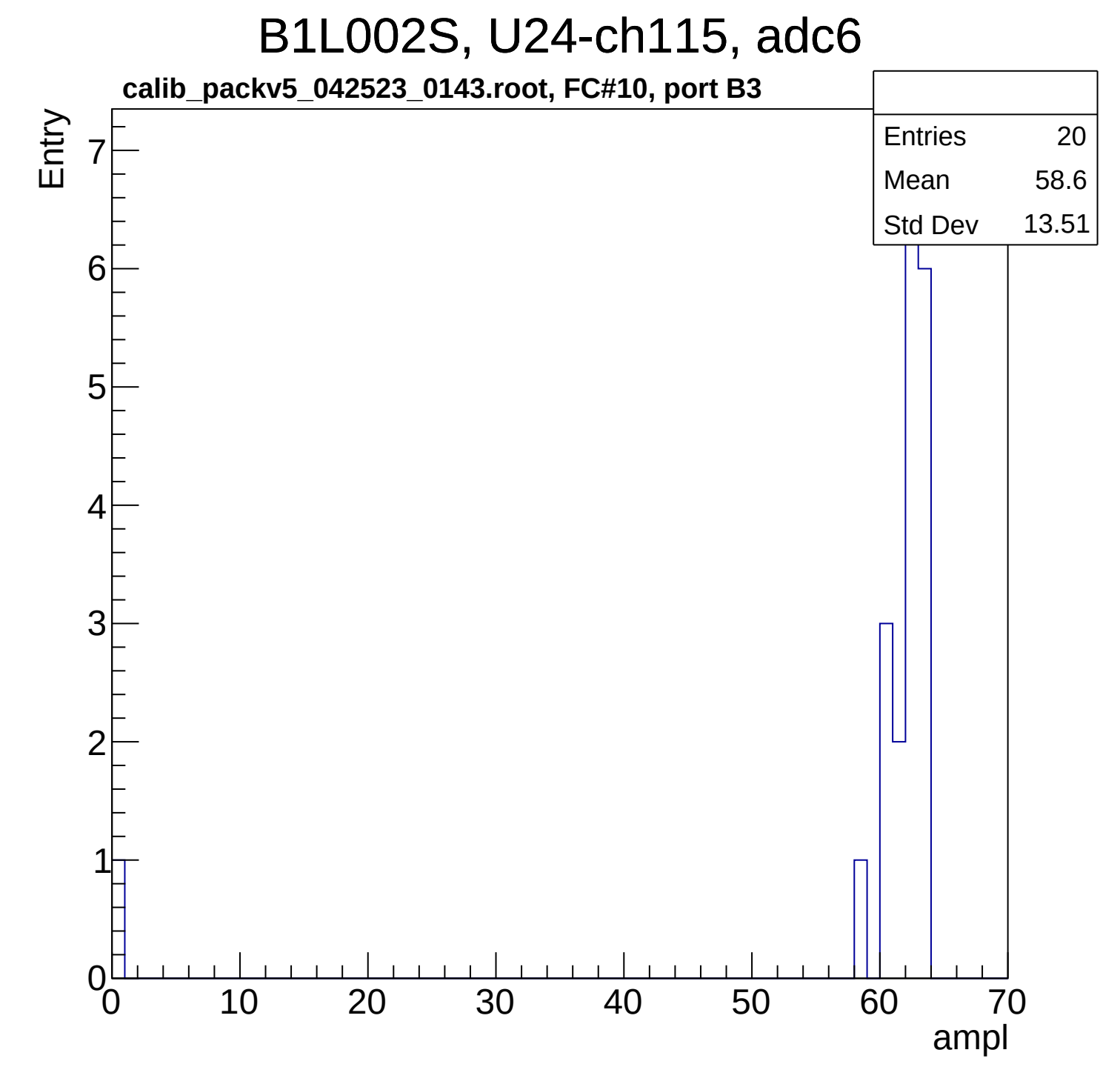
B1L002S, U24-ch115, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 20
Mean 58.6
Std Dev 13.51
Entry
ampl
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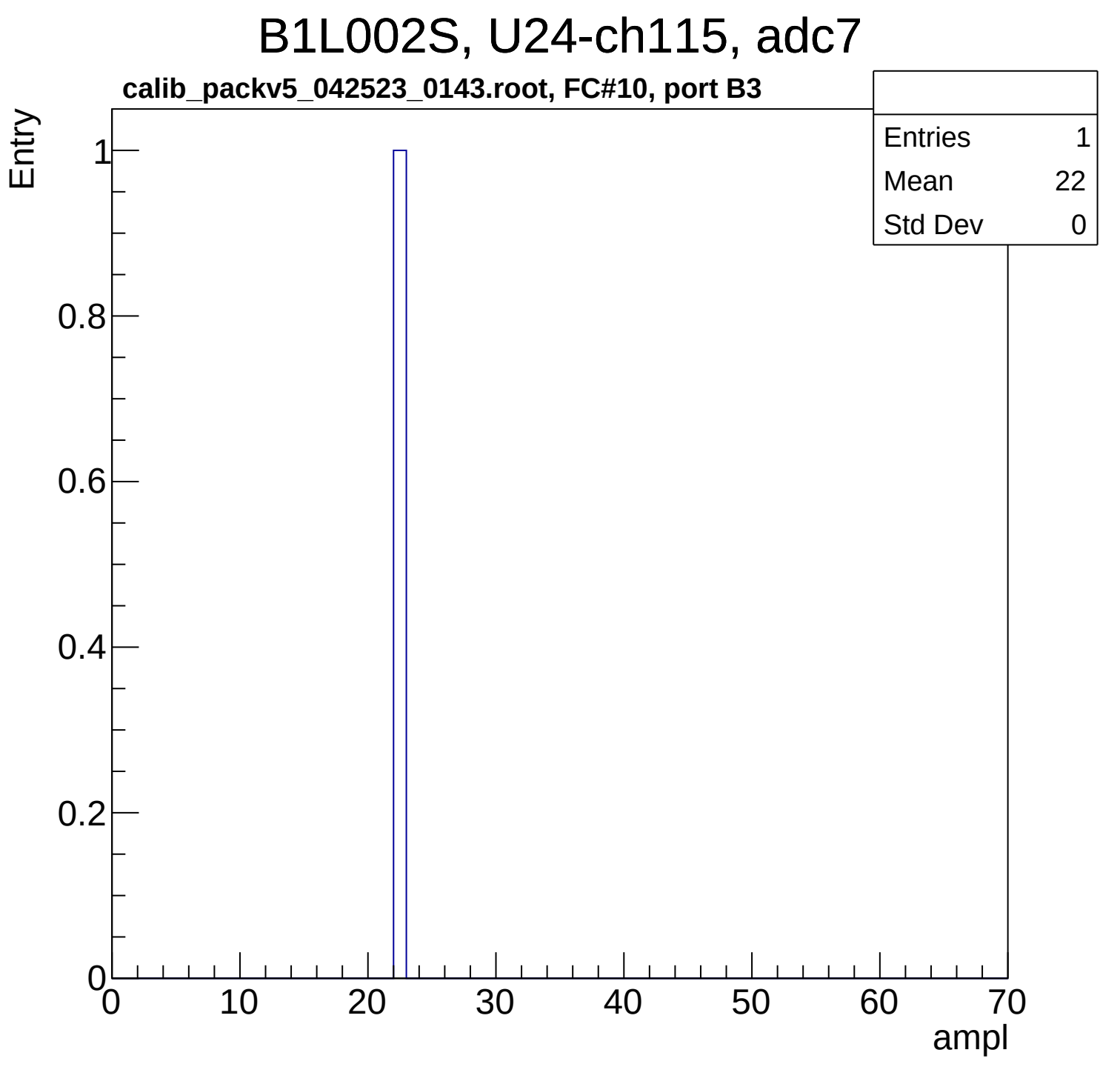
B1L002S, U24-ch115, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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0.6
0.4
0.2
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20
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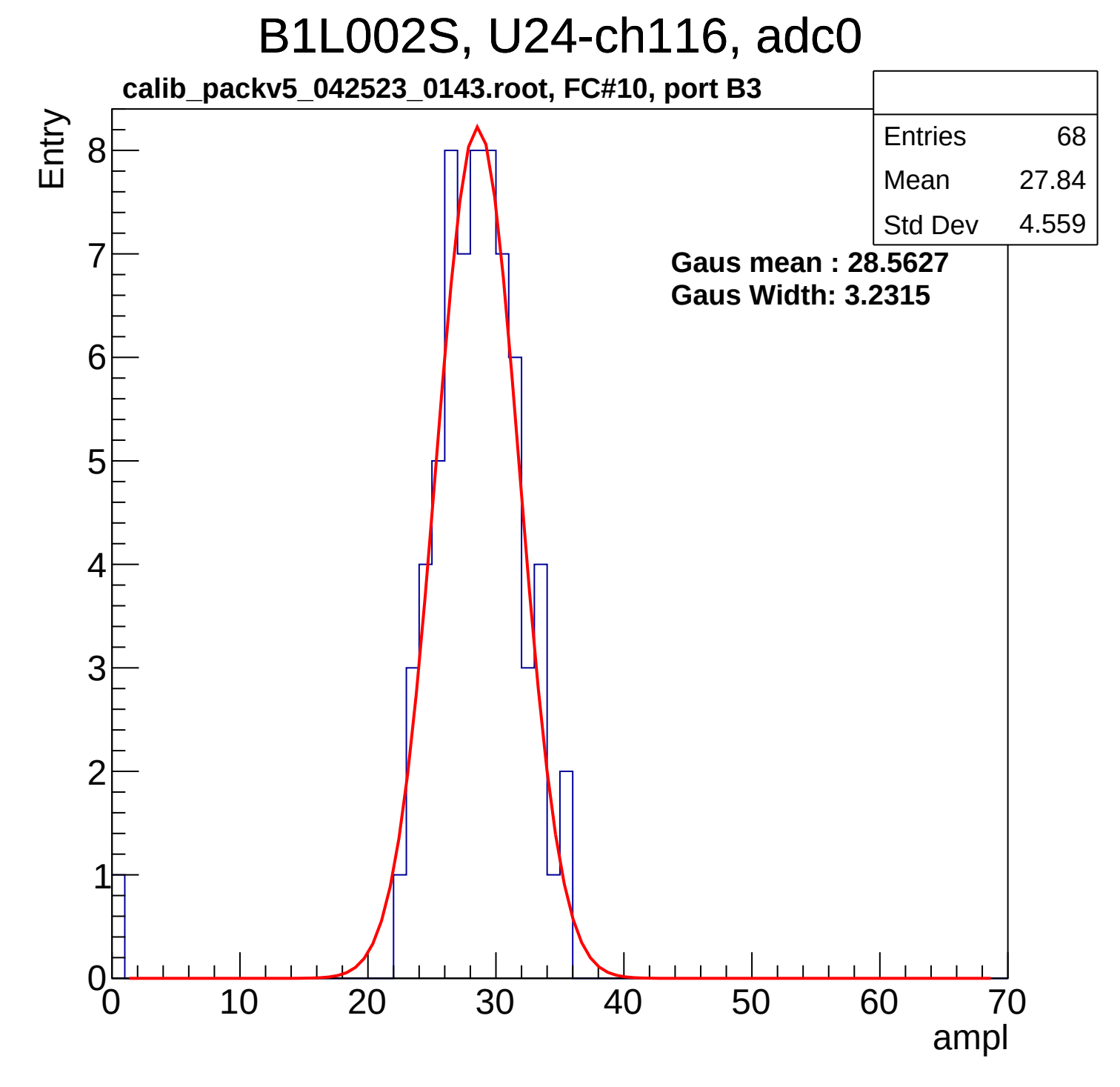
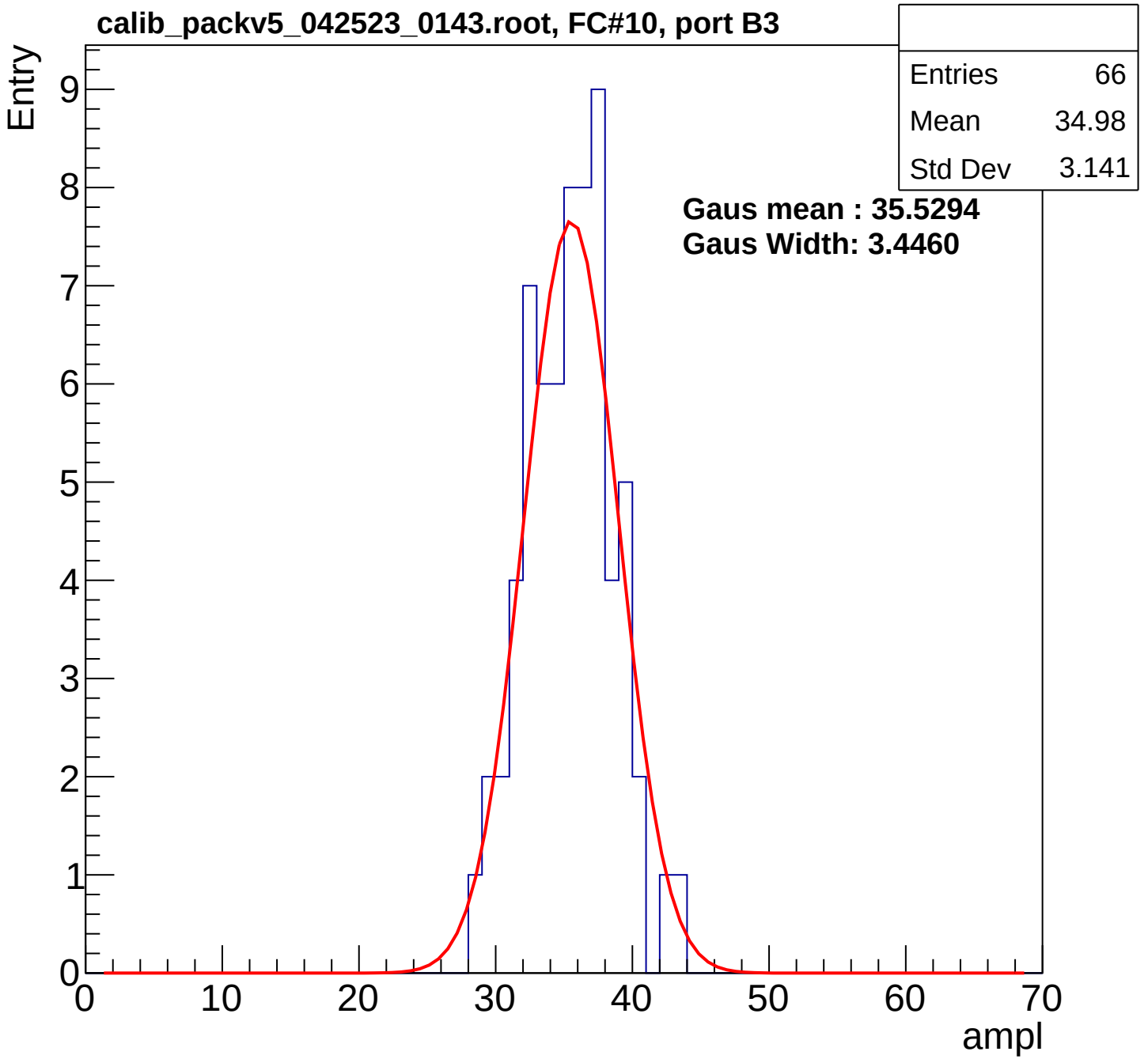
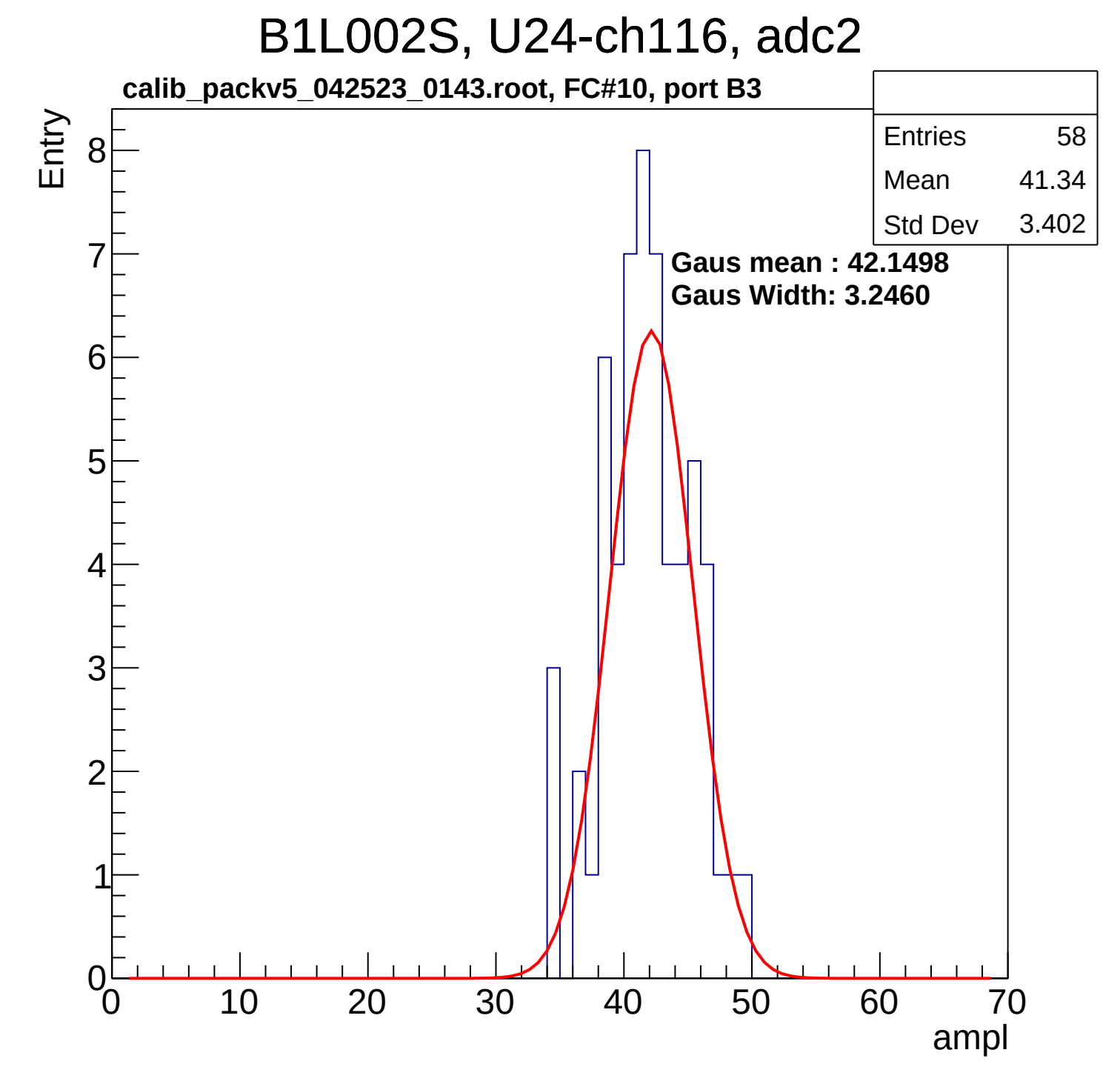
50
60
70
ampl


B1L002S, U24-ch116, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 68
Mean 27.84
Std Dev 4.559
Gaus mean : 28.5627
Gaus Width: 3.2315
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch116, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.98
Std Dev 3.141
Gaus mean : 35.5294
Gaus Width: 3.4460
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L002S, U24-ch116, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 41.34
Std Dev 3.402
Gaus mean : 42.1498
Gaus Width: 3.2460
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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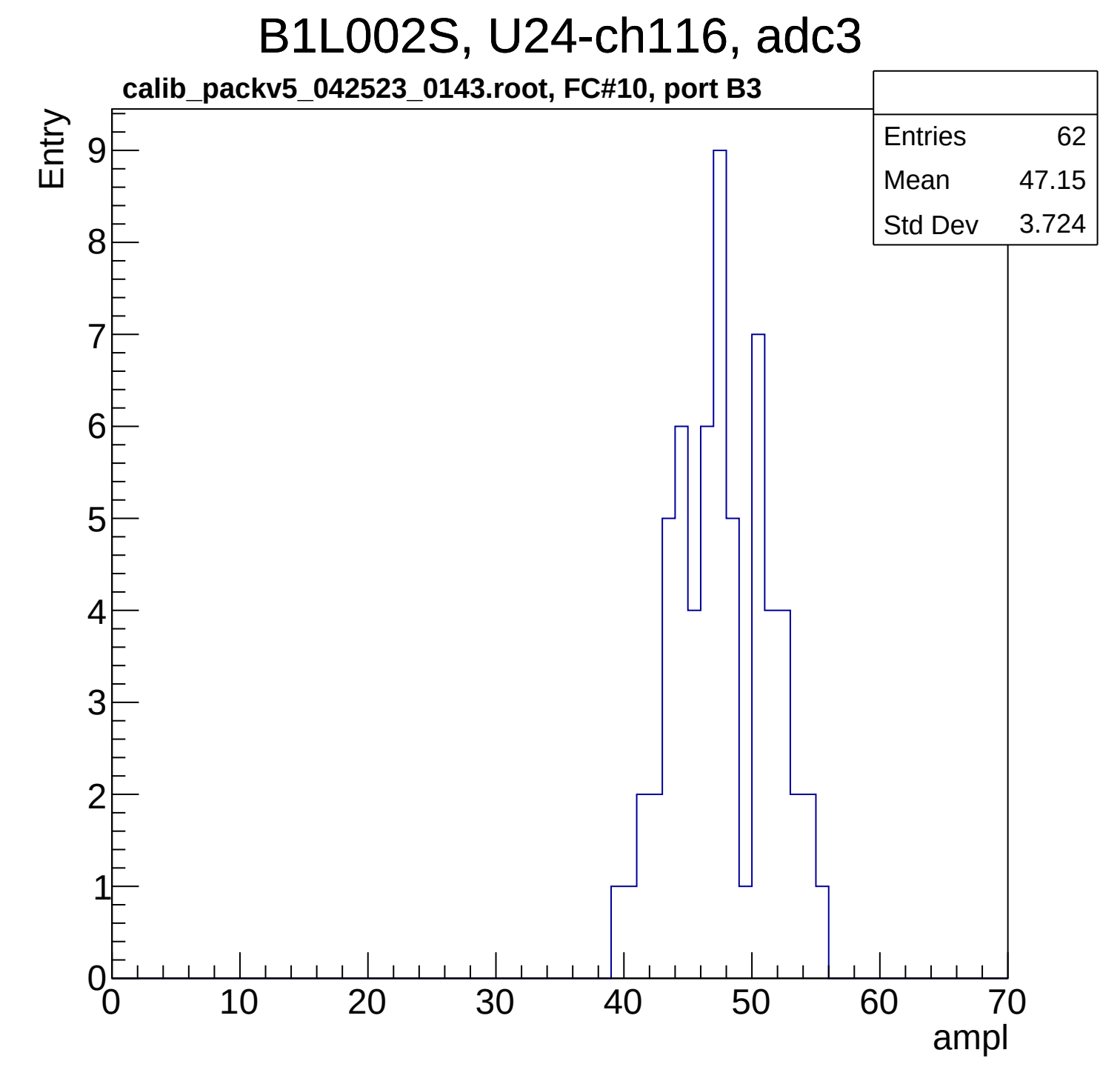
B1L002S, U24-ch116, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 62
Mean 47.15
Std Dev 3.724
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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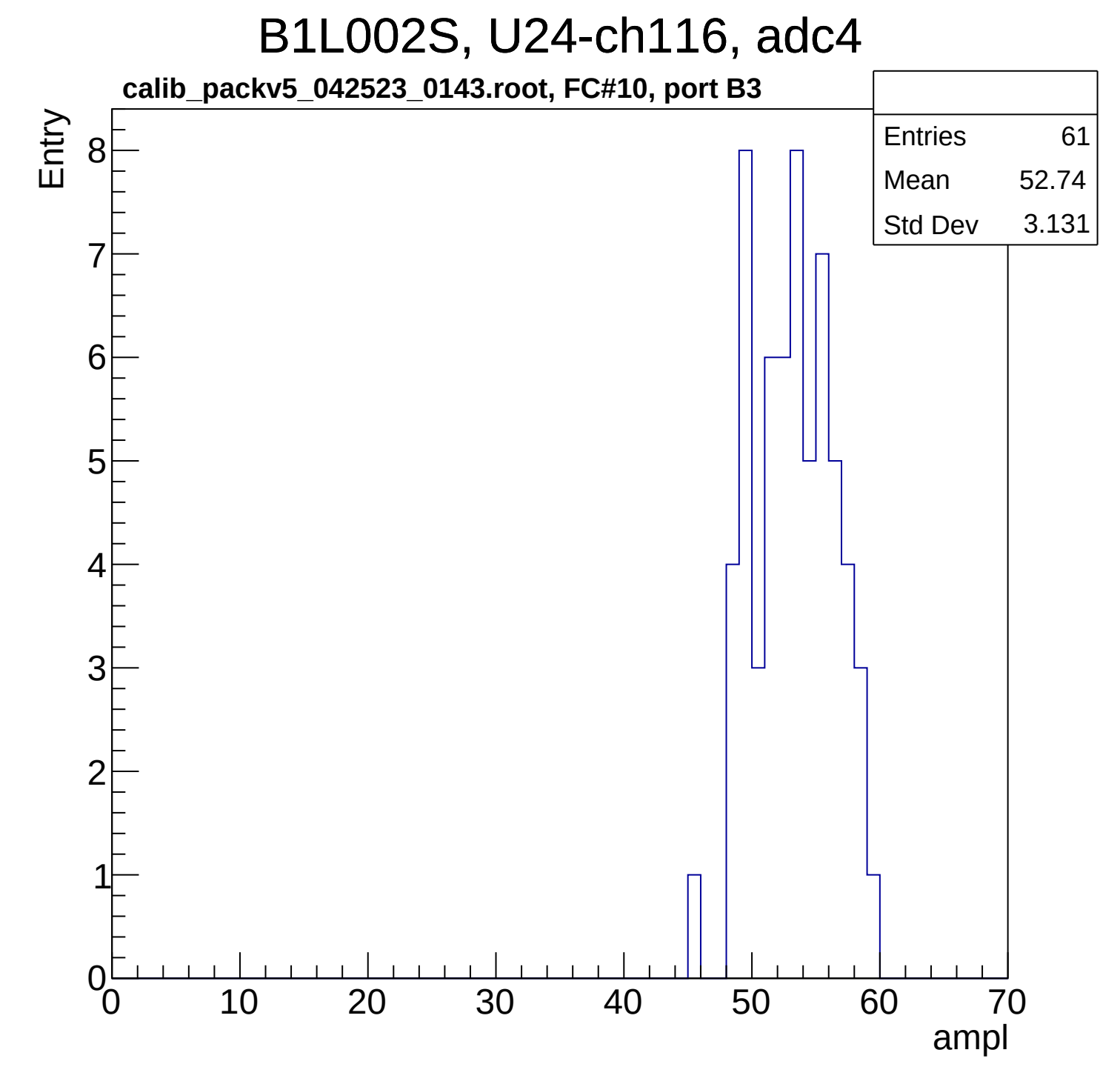
B1L002S, U24-ch116, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 52.74
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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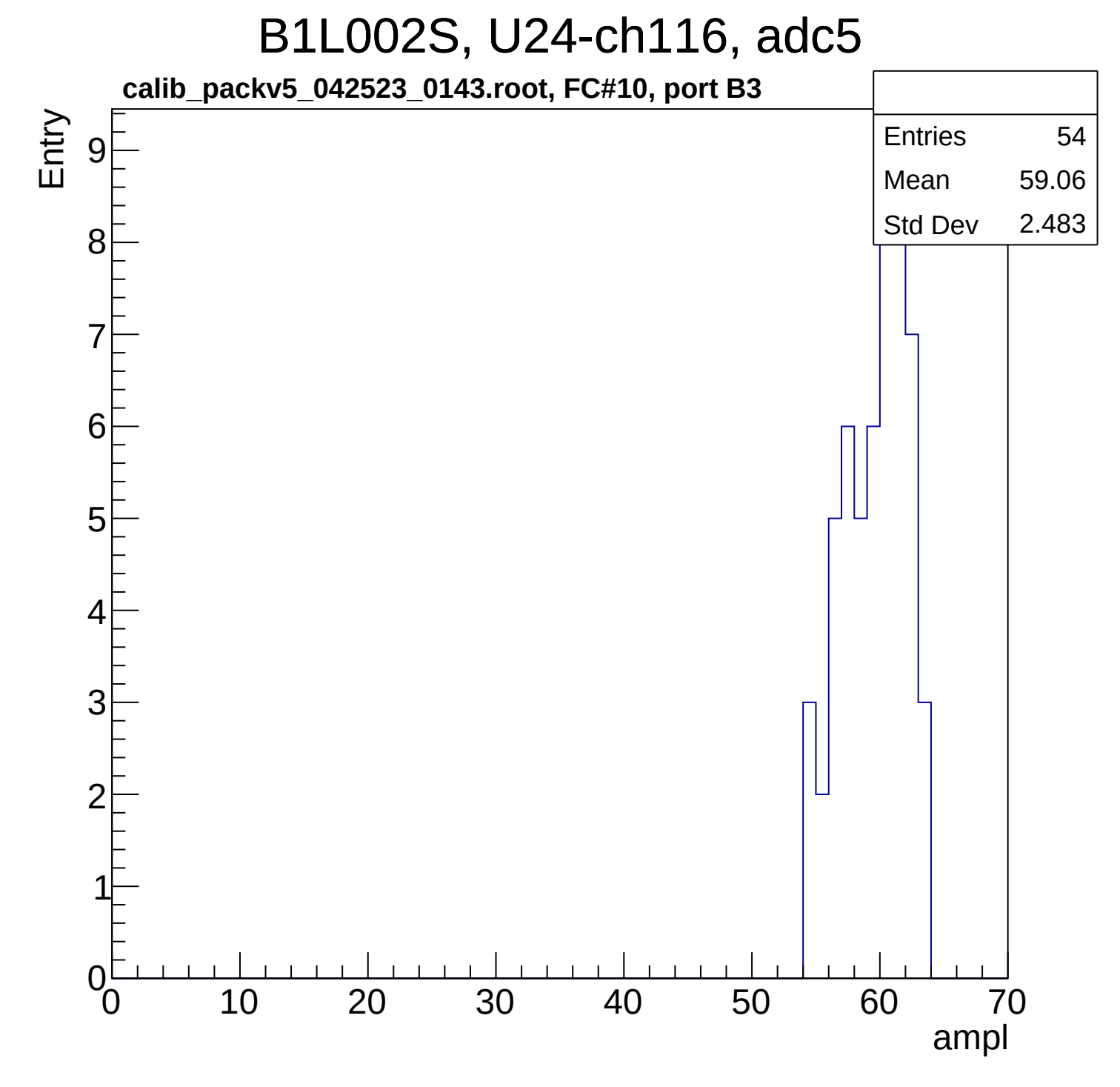
B1L002S, U24-ch116, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 59.06
Std Dev 2.483
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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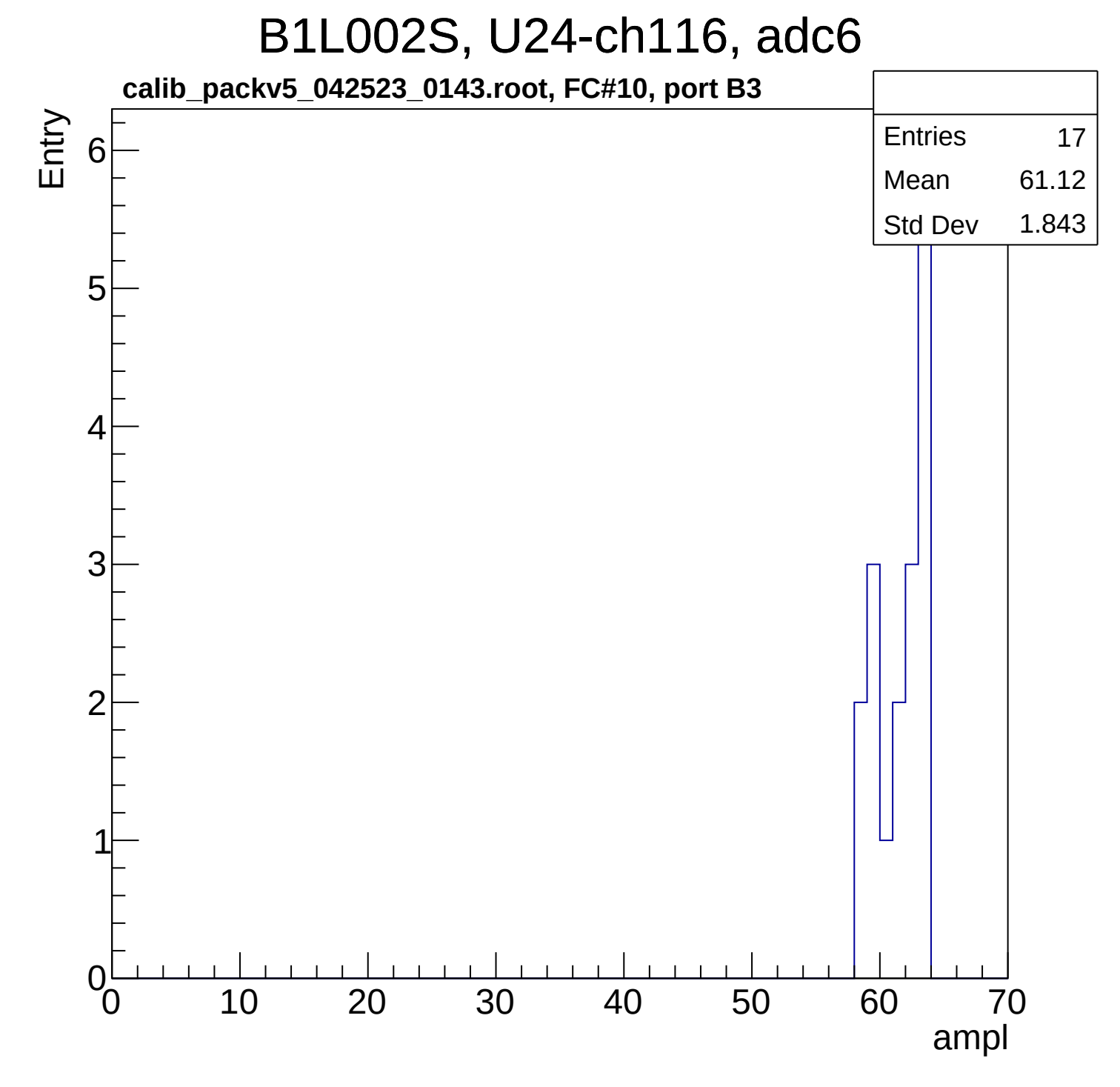
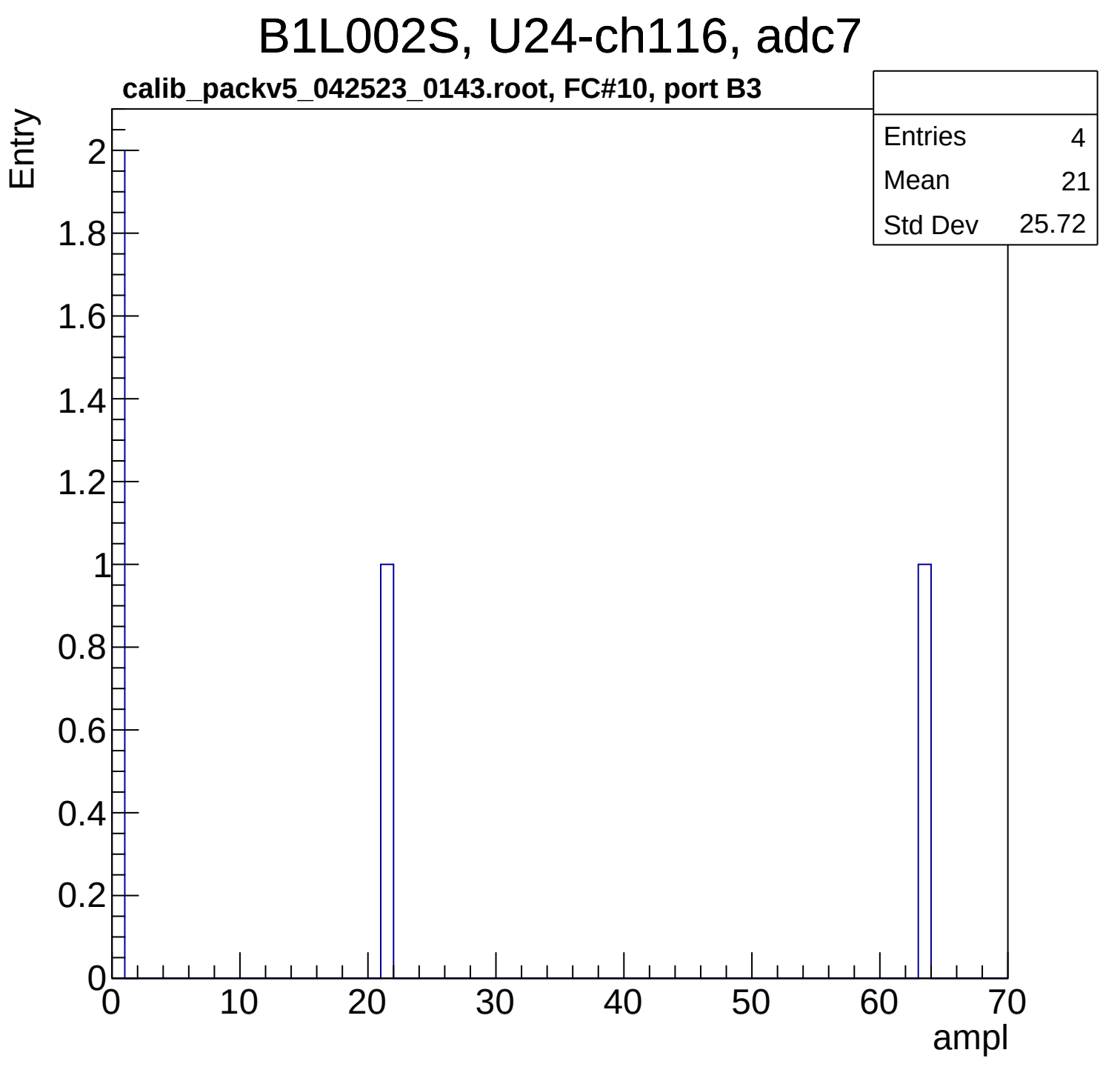
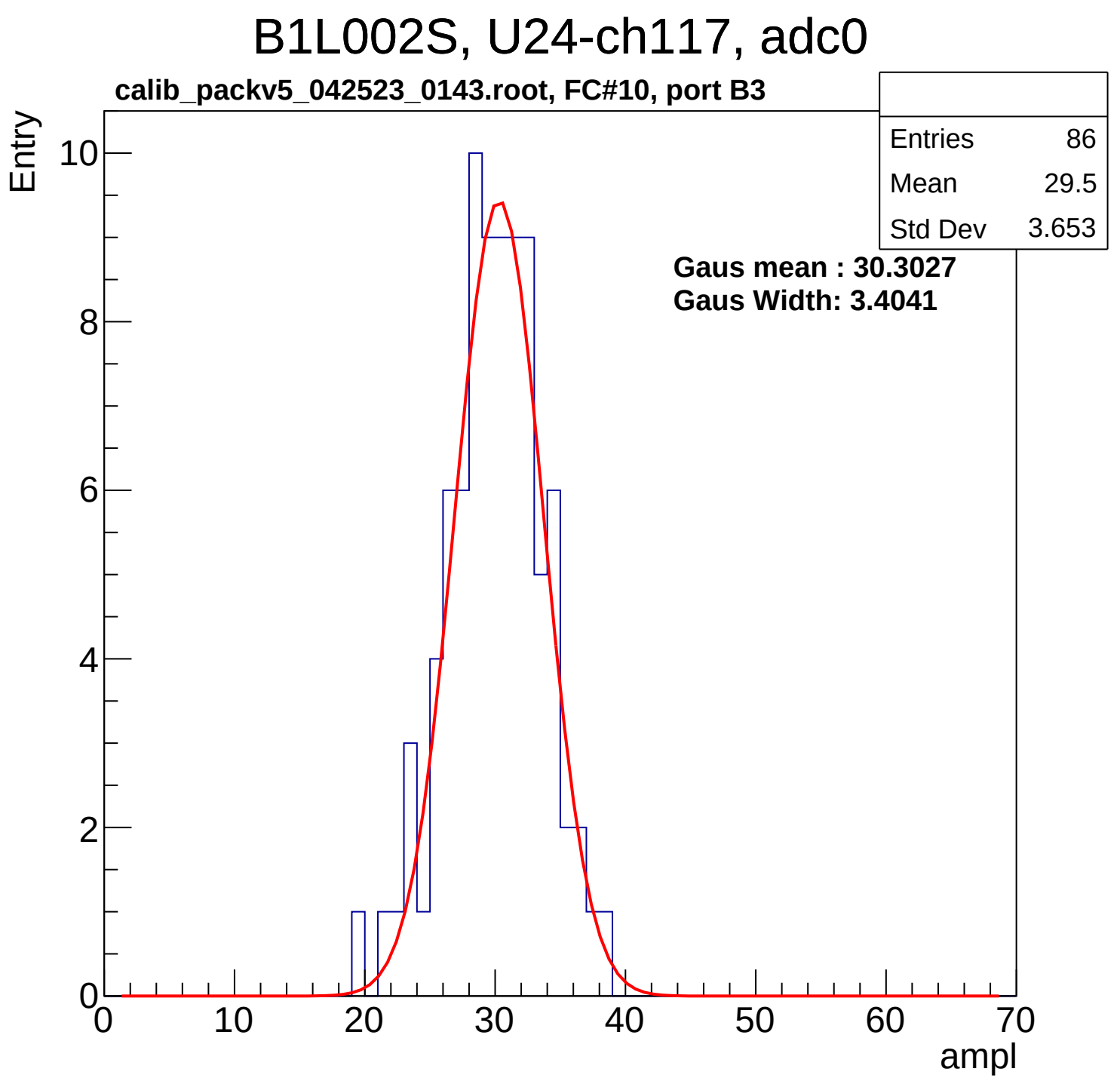

B1L002S, U24-ch116, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.12
Std Dev 1.843
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch116, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 4
Mean 21
Std Dev 25.72
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L002S, U24-ch117, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 86
Mean 29.5
Std Dev 3.653
Gaus mean : 30.3027
Gaus Width: 3.4041
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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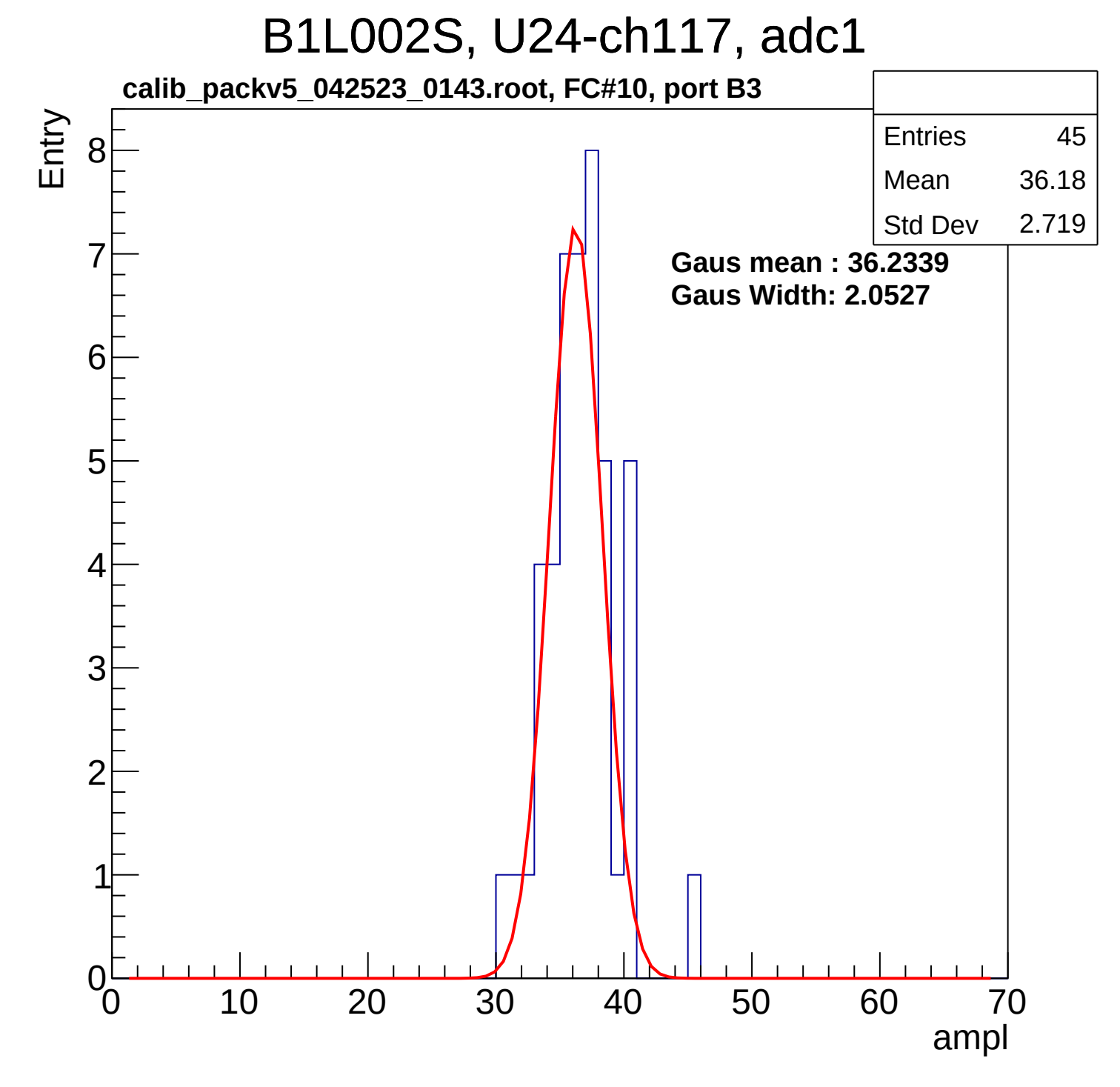
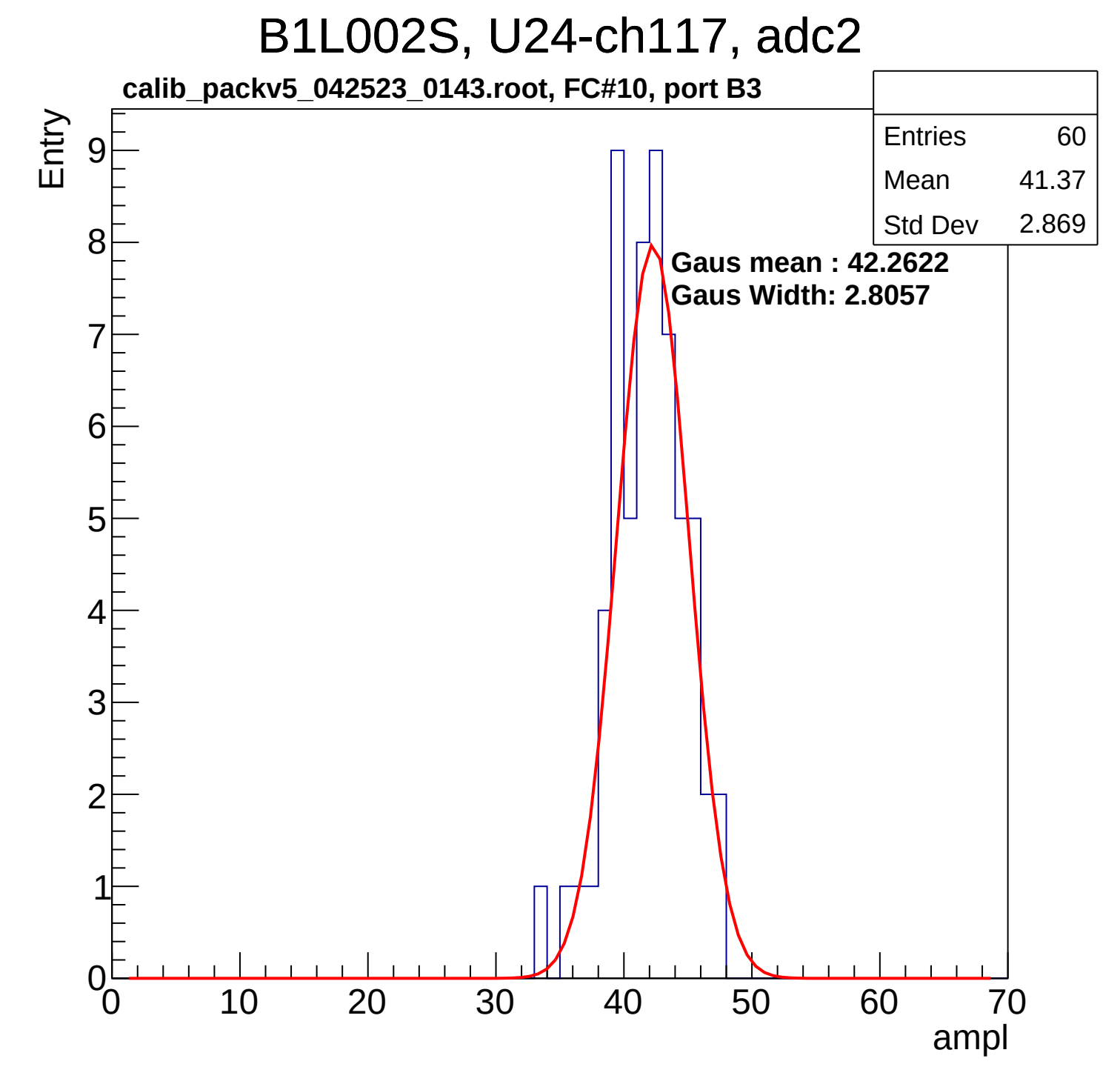
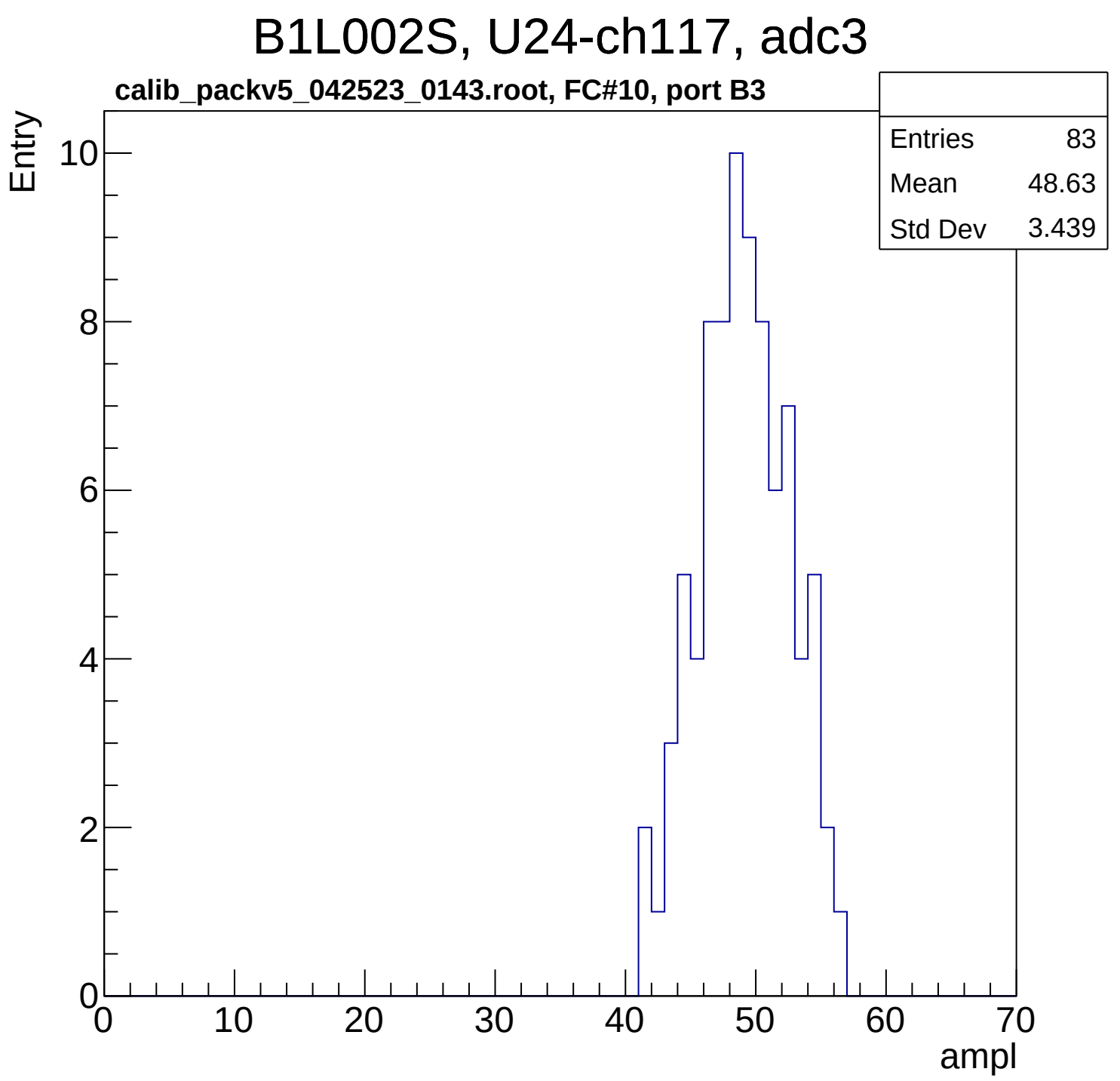
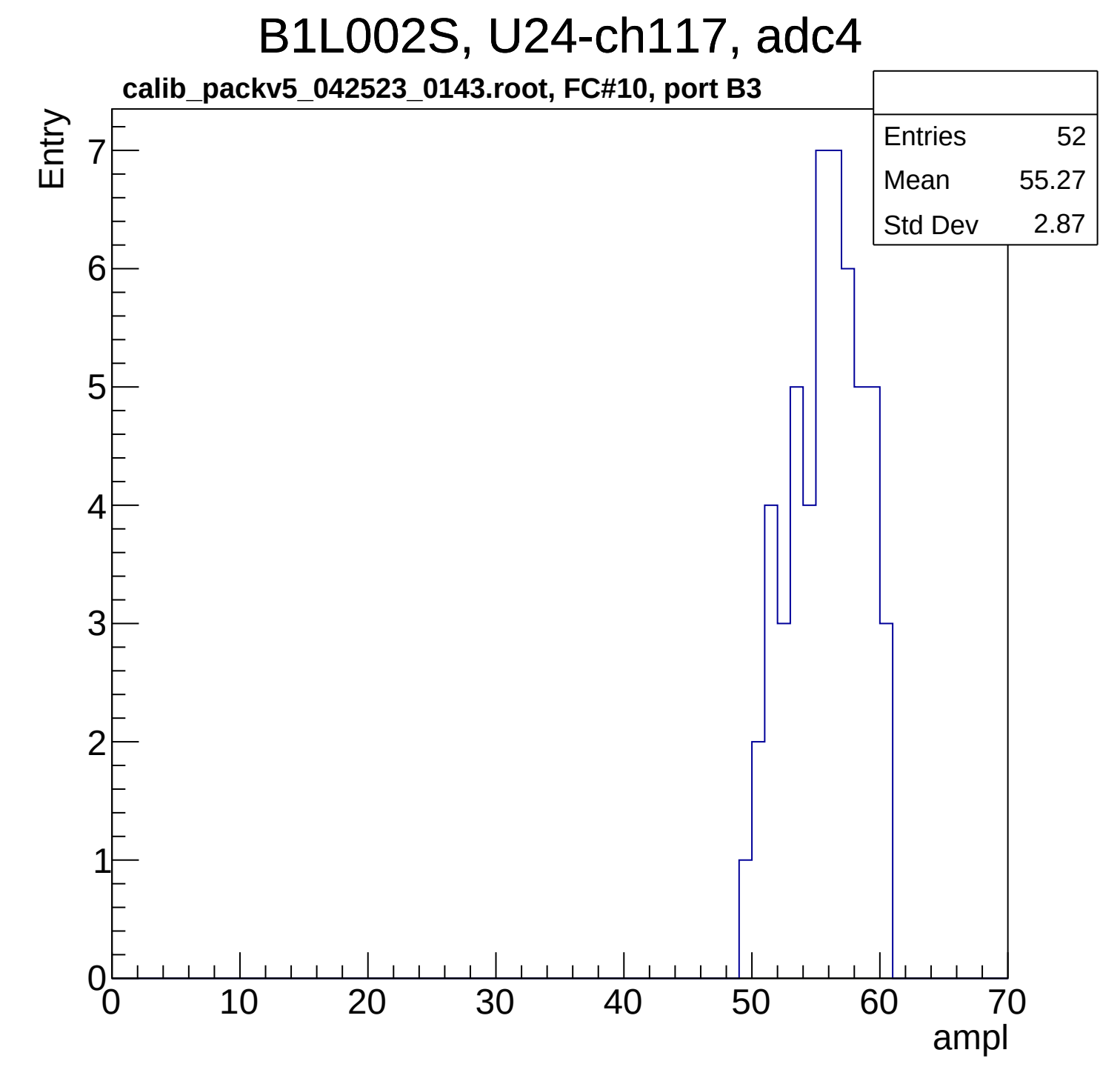
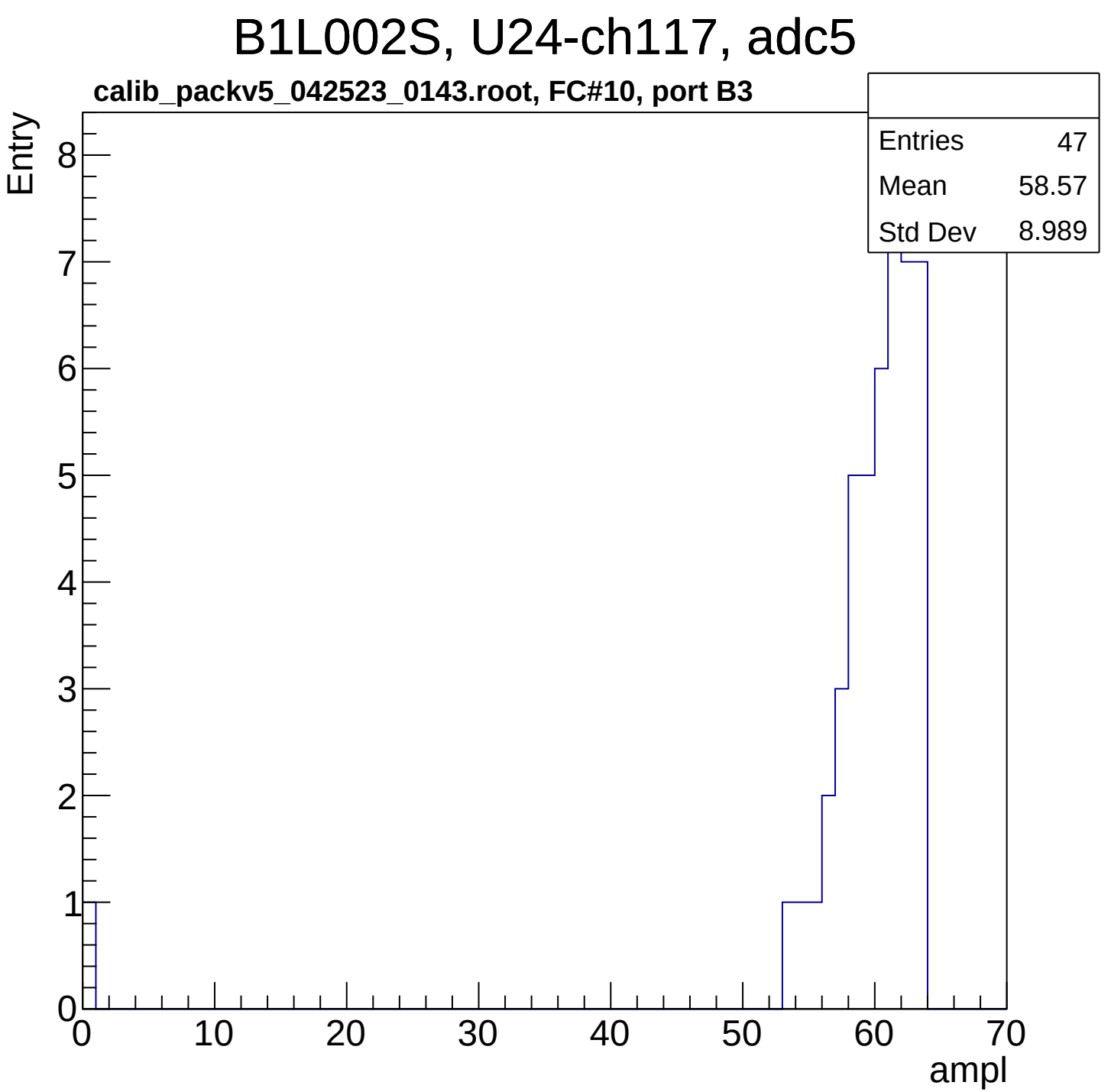
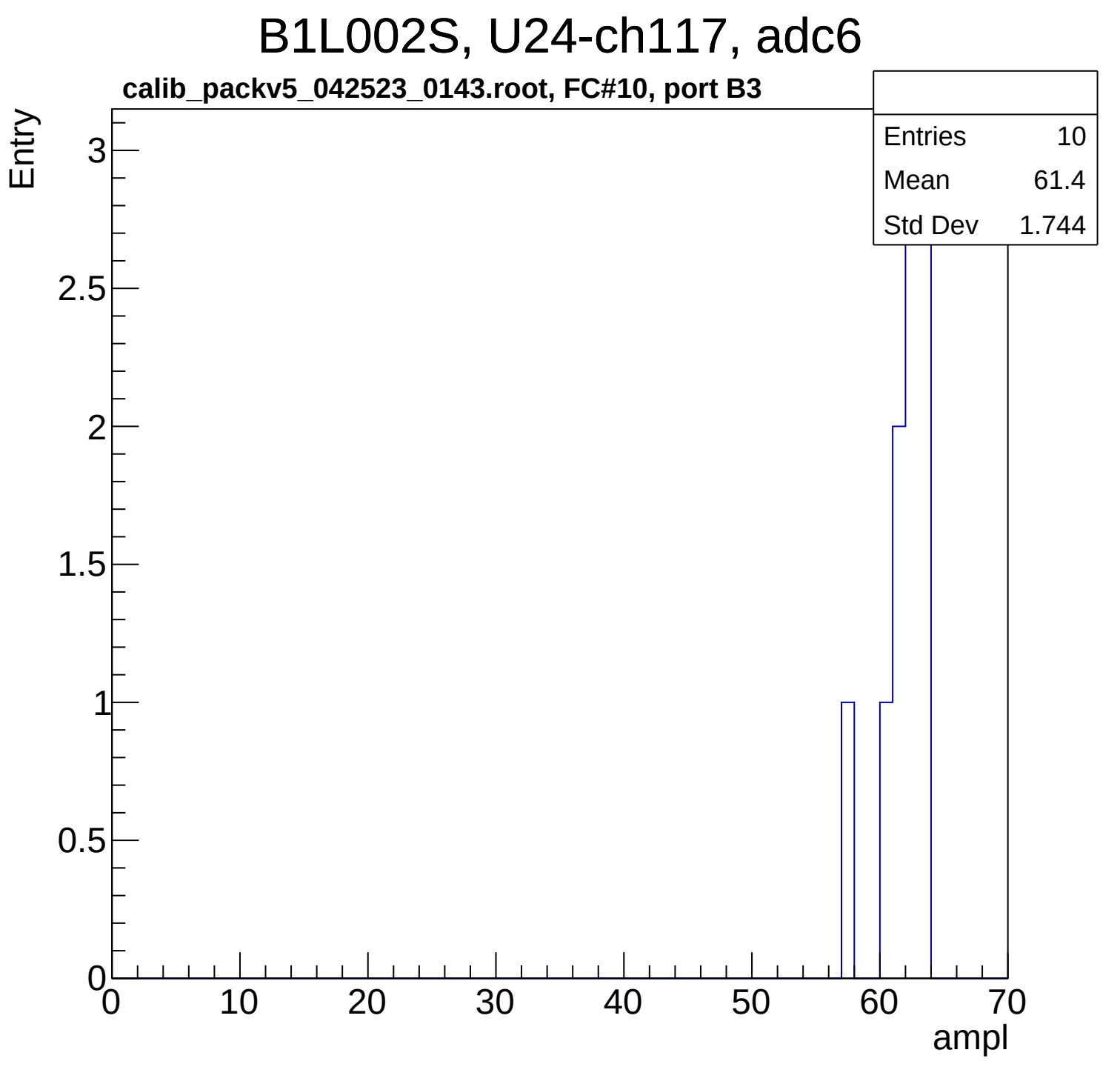

B1L002S, U24-ch117, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 45
Mean 36.18
Std Dev 2.719
Gaus mean : 36.2339
Gaus Width: 2.0527
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

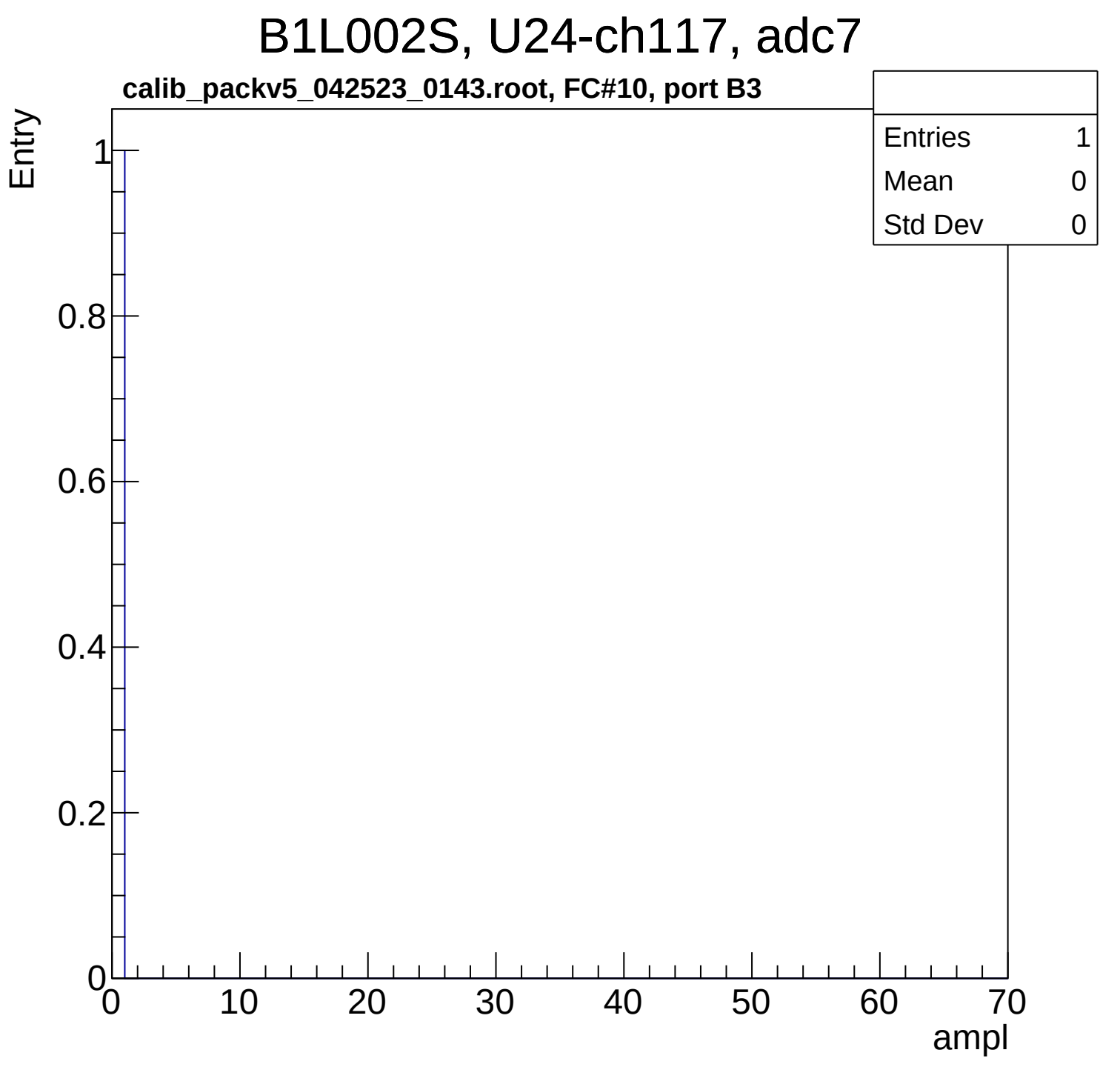

B1L002S, U24-ch117, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 41.37
Std Dev 2.869
Gaus mean : 42.2622
Gaus Width: 2.8057
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

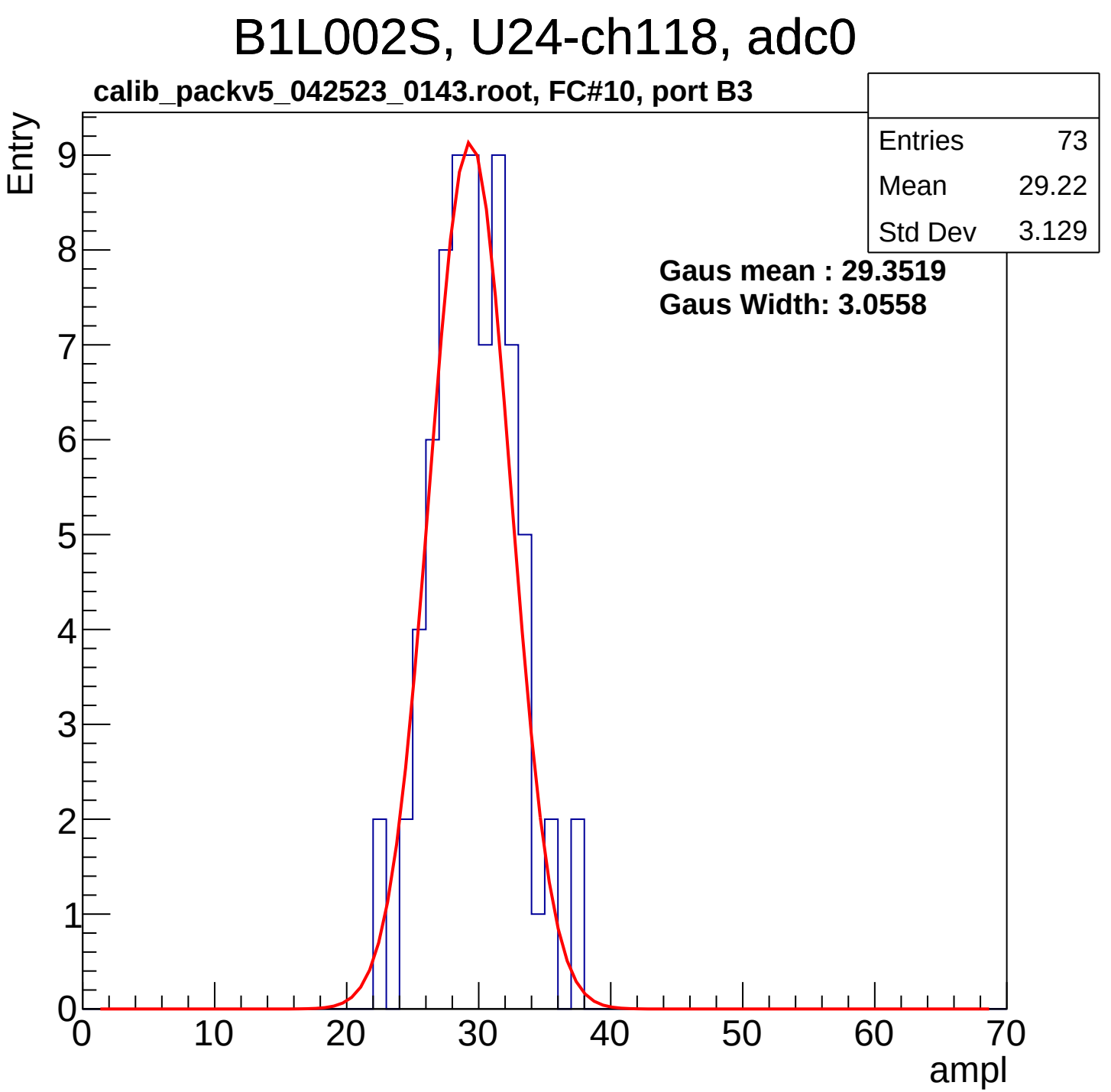

B1L002S, U24-ch117, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 83
Mean 48.63
Std Dev 3.439
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

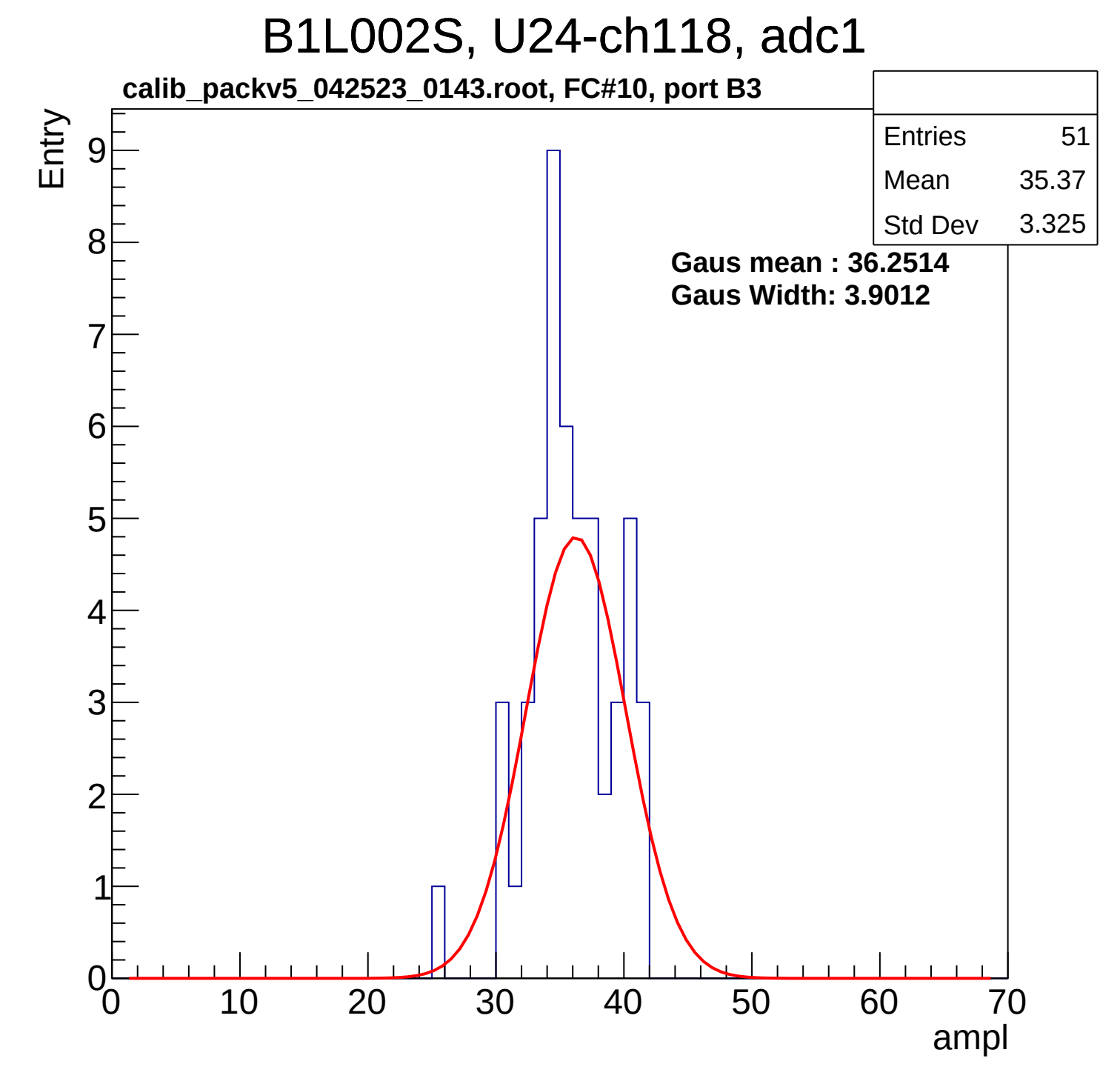

B1L002S, U24-ch117, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 55.27
Std Dev 2.87
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

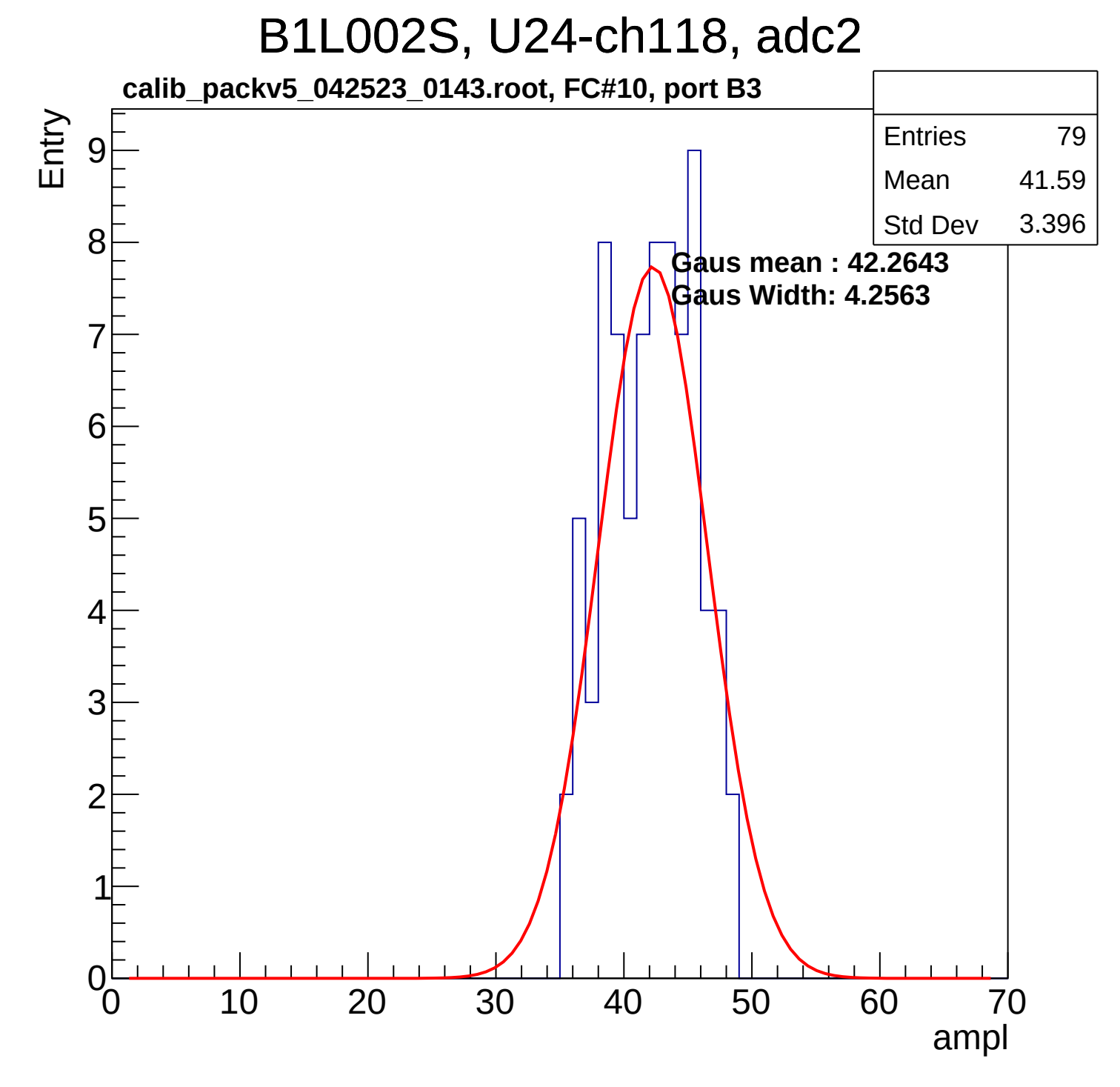

B1L002S, U24-ch117, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 58.57
Std Dev 8.989
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch117, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.4
Std Dev 1.744
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch117, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch118, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 29.22
Std Dev 3.129
Gaus mean : 29.3519
Gaus Width: 3.0558
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch118, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 35.37
Std Dev 3.325
Gaus mean : 36.2514
Gaus Width: 3.9012
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch118, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 41.59
Std Dev 3.396
Gaus mean : 42.2643
Gaus Width: 4.2563
Entry
ampl
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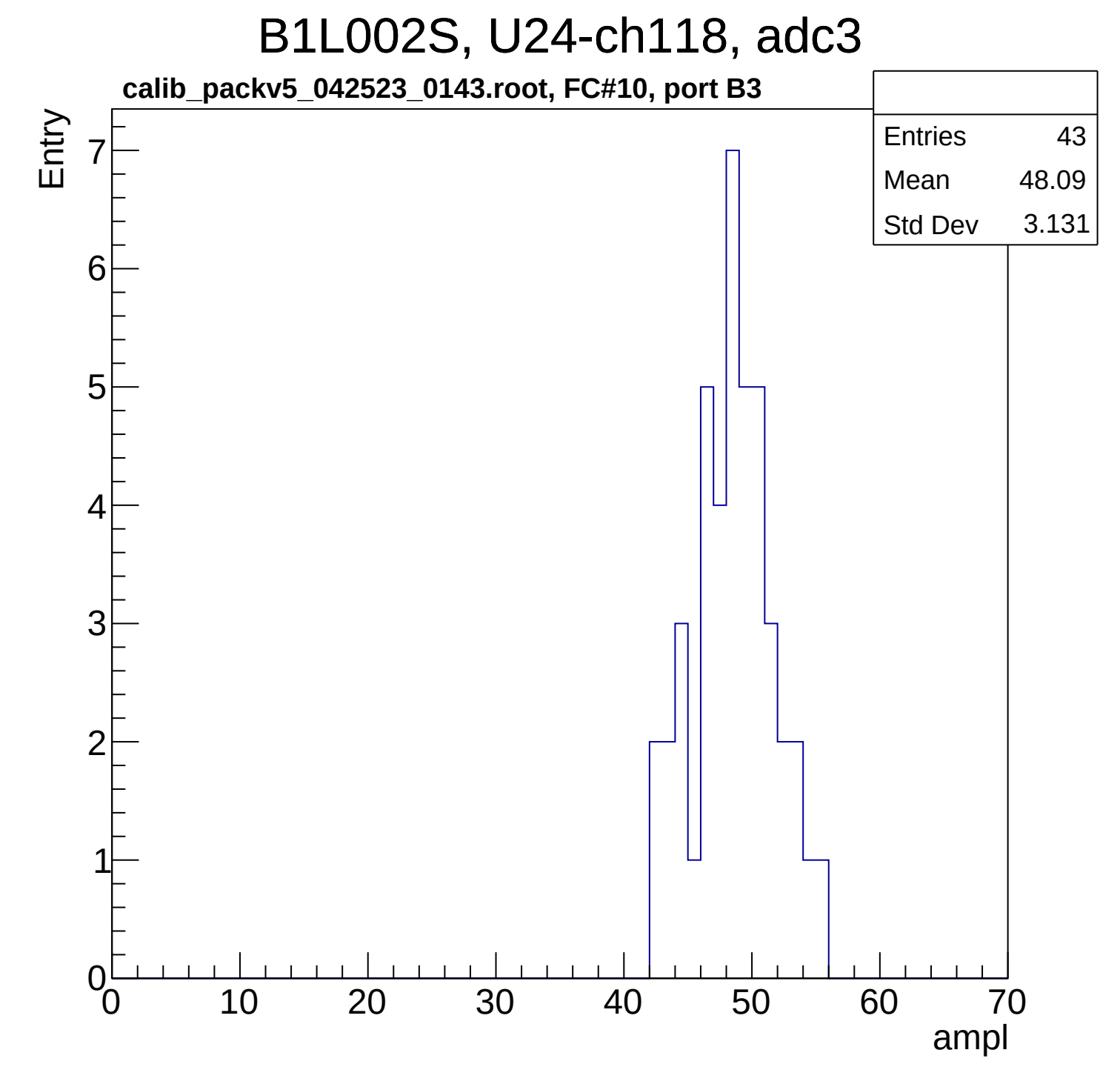
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch118, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 48.09
Std Dev 3.131
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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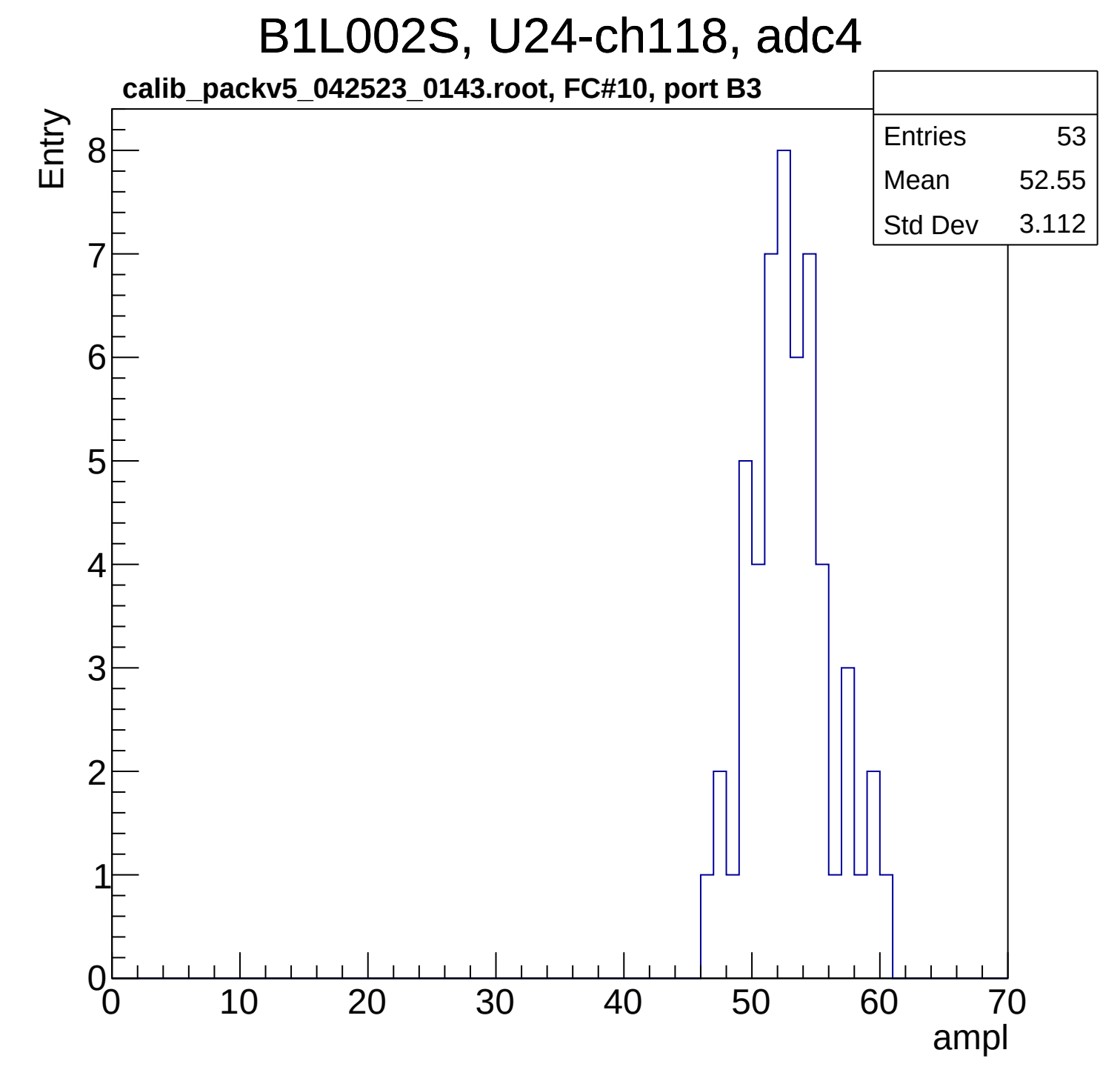
60
70


B1L002S, U24-ch118, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 52.55
Std Dev 3.112
Entry
ampl
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1
2
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4
5
6
7
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0
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20
30
40
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60
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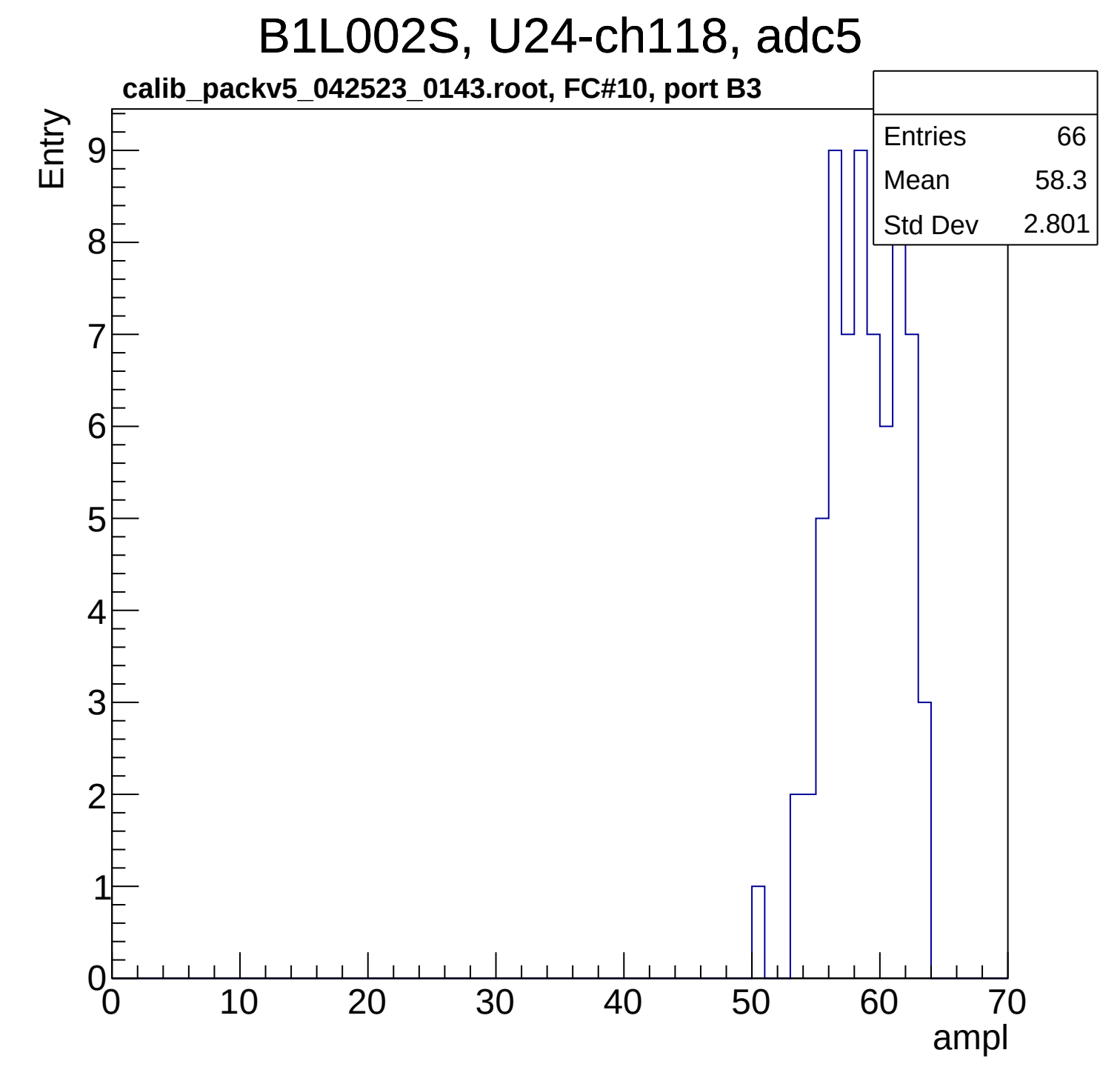
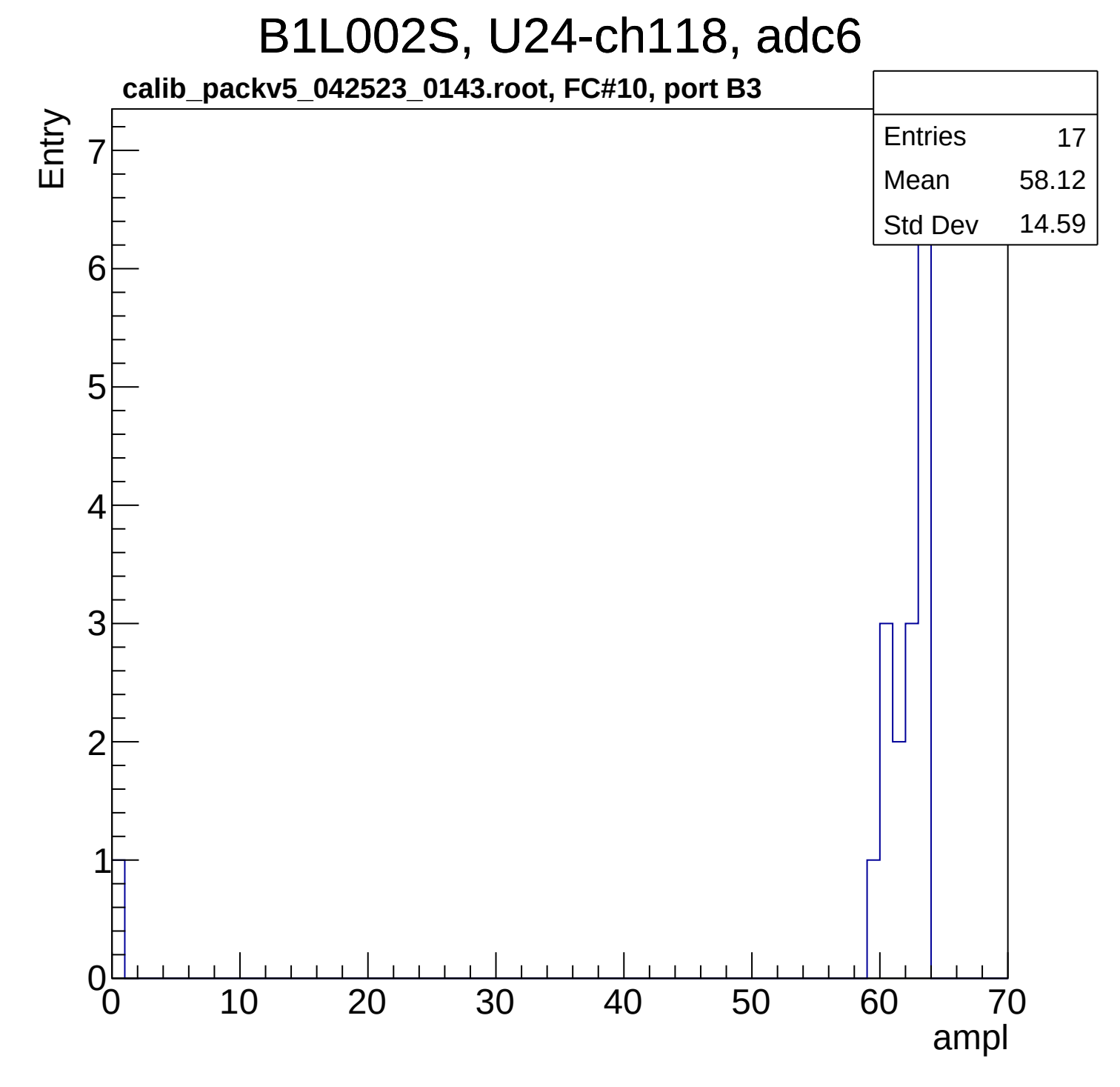
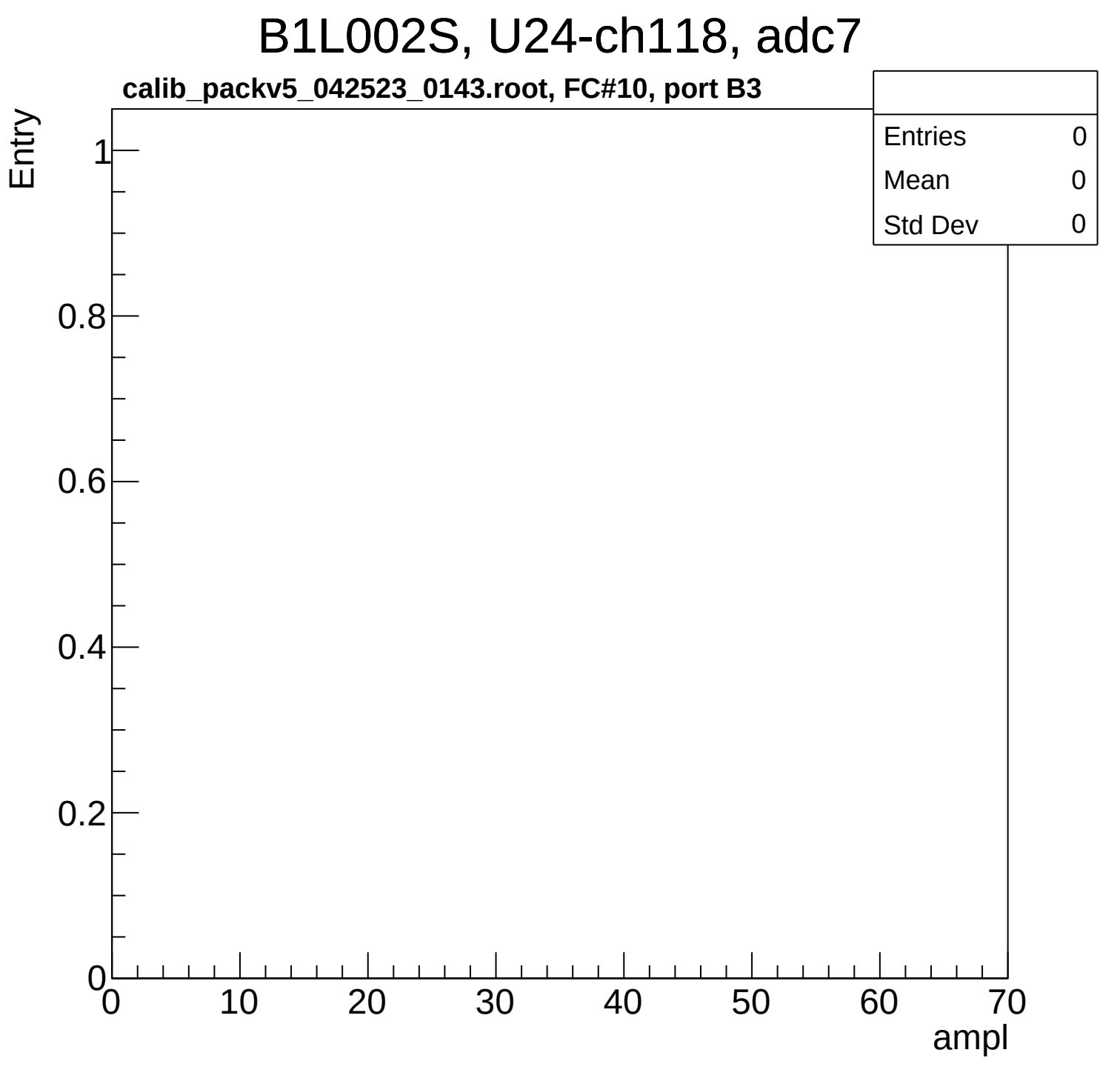

B1L002S, U24-ch118, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 58.3
Std Dev 2.801
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch118, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.12
Std Dev 14.59
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch118, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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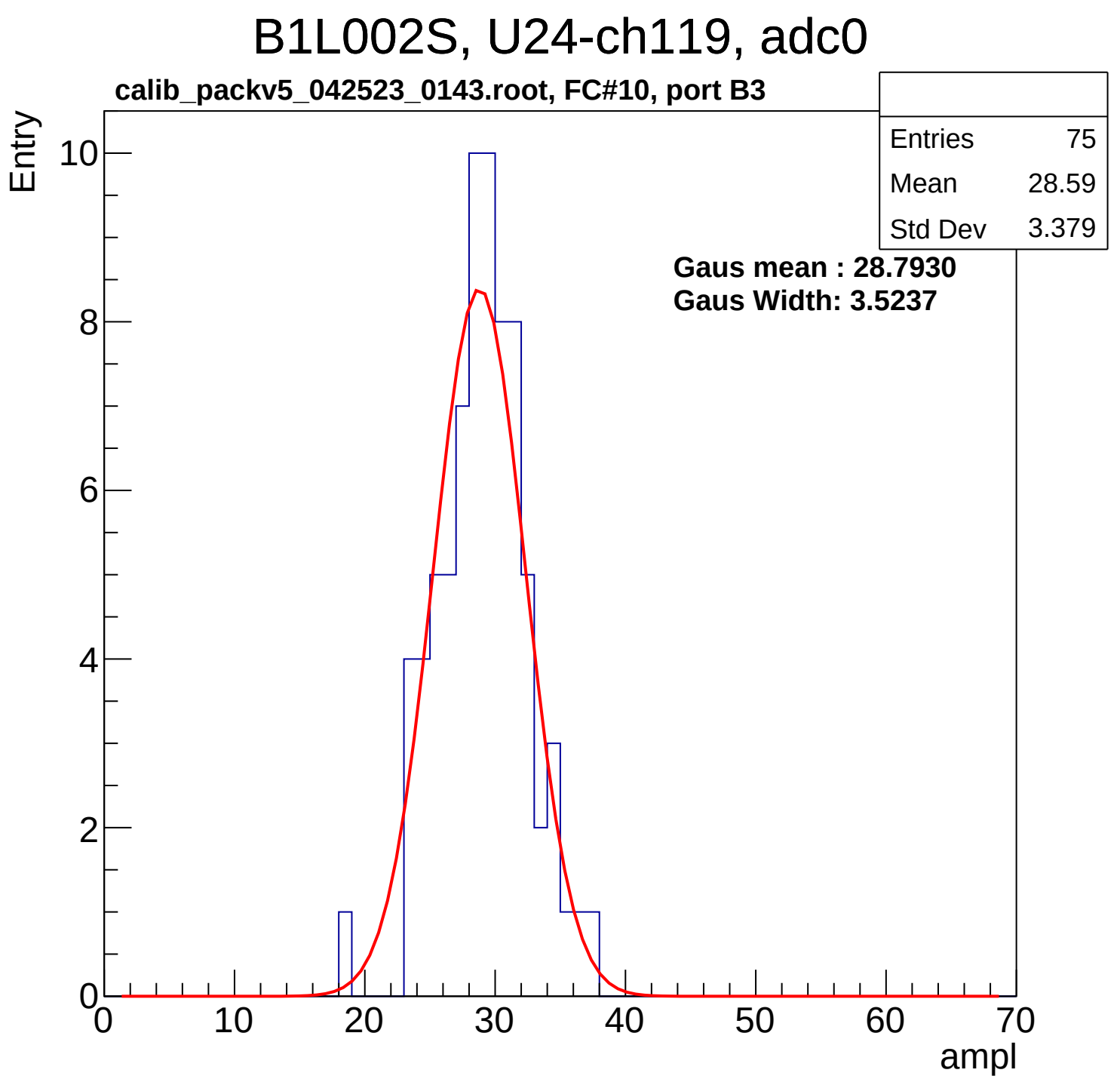
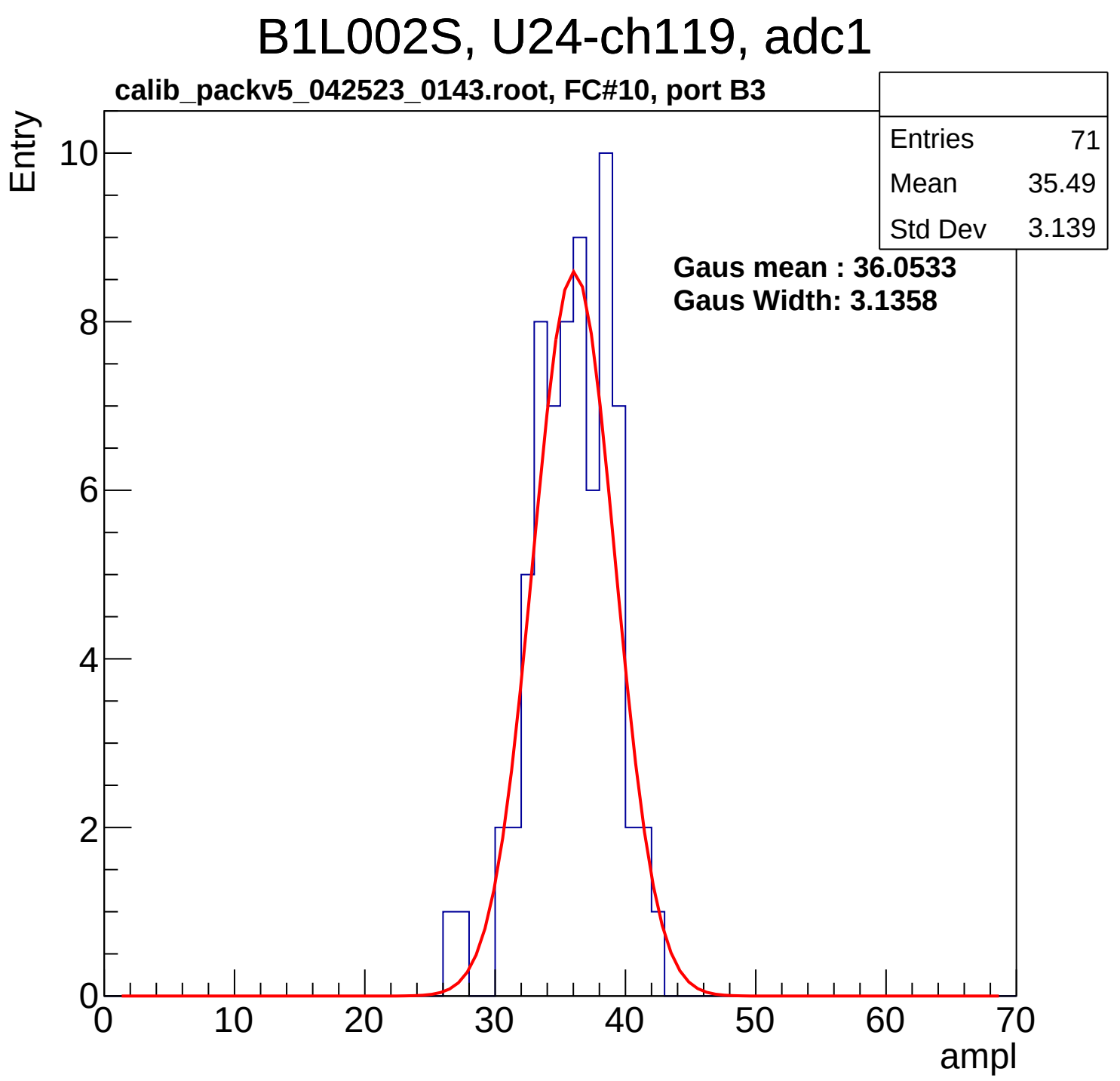
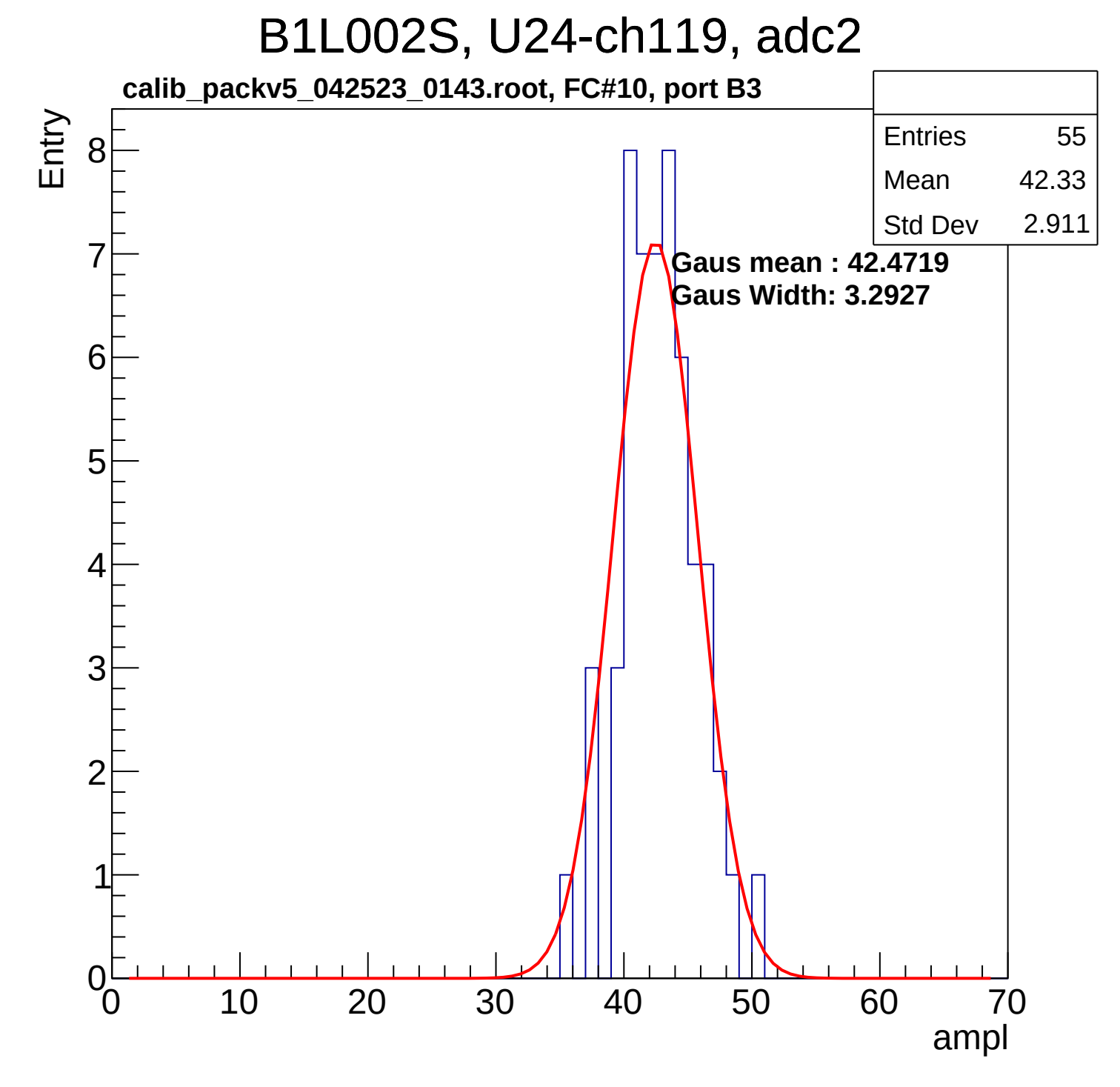
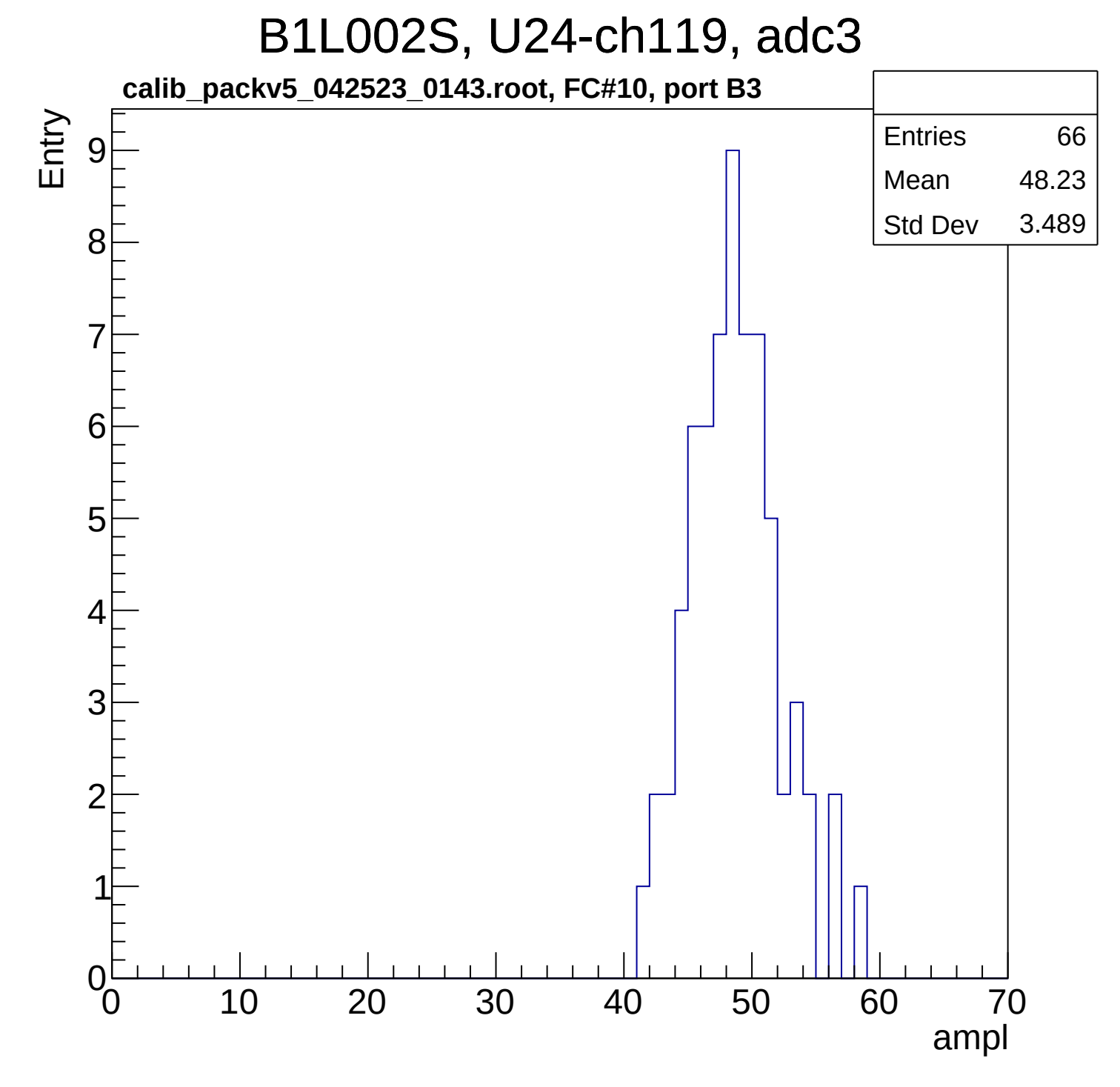
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch119, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 28.59
Std Dev 3.379
Gaus mean : 28.7930
Gaus Width: 3.5237
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch119, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 71
Mean 35.49
Std Dev 3.139
Gaus mean : 36.0533
Gaus Width: 3.1358
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch119, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 42.33
Std Dev 2.911
Gaus mean : 42.4719
Gaus Width: 3.2927
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch119, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 48.23
Std Dev 3.489
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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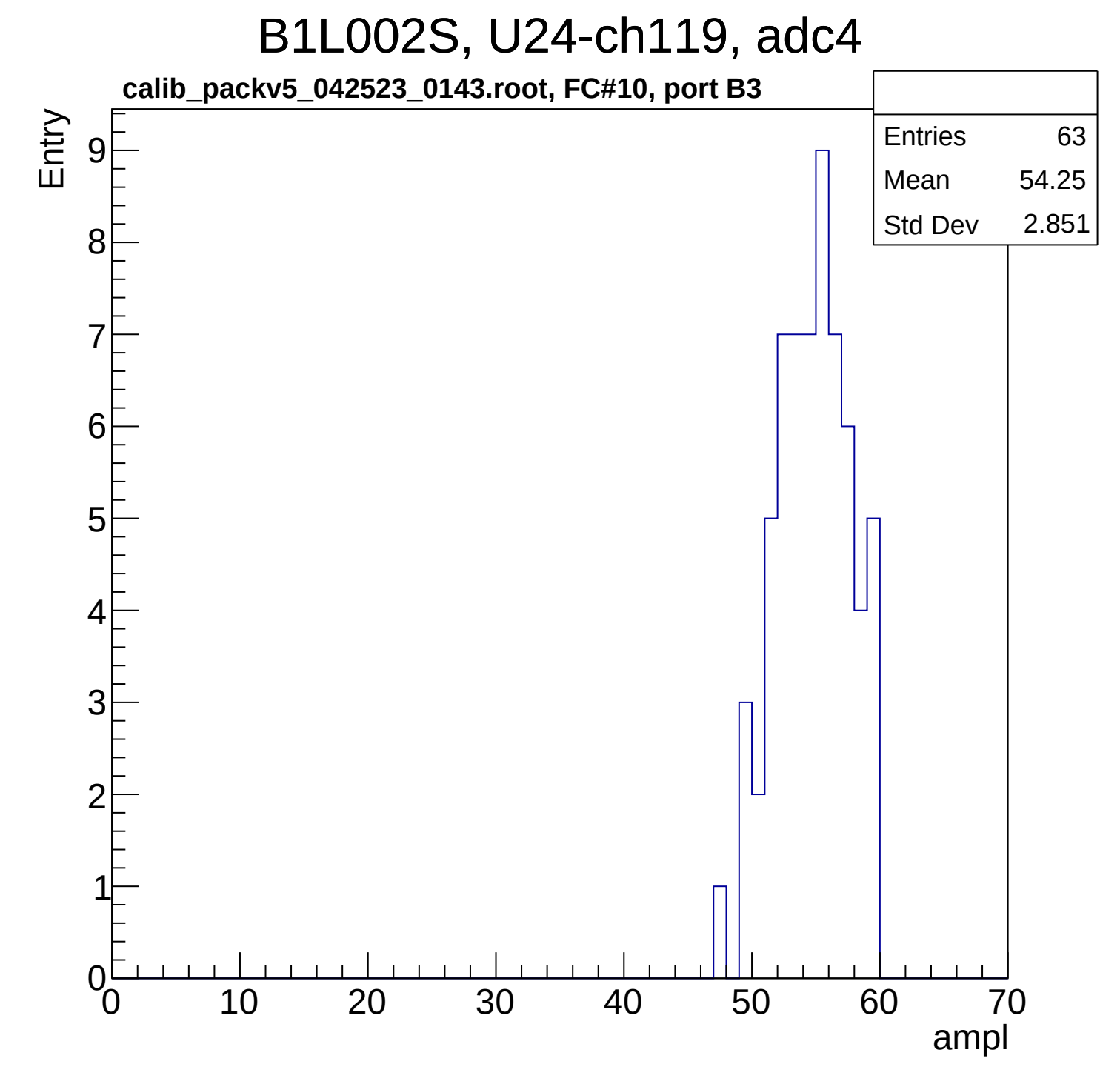
B1L002S, U24-ch119, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 63
Mean 54.25
Std Dev 2.851
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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40
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60
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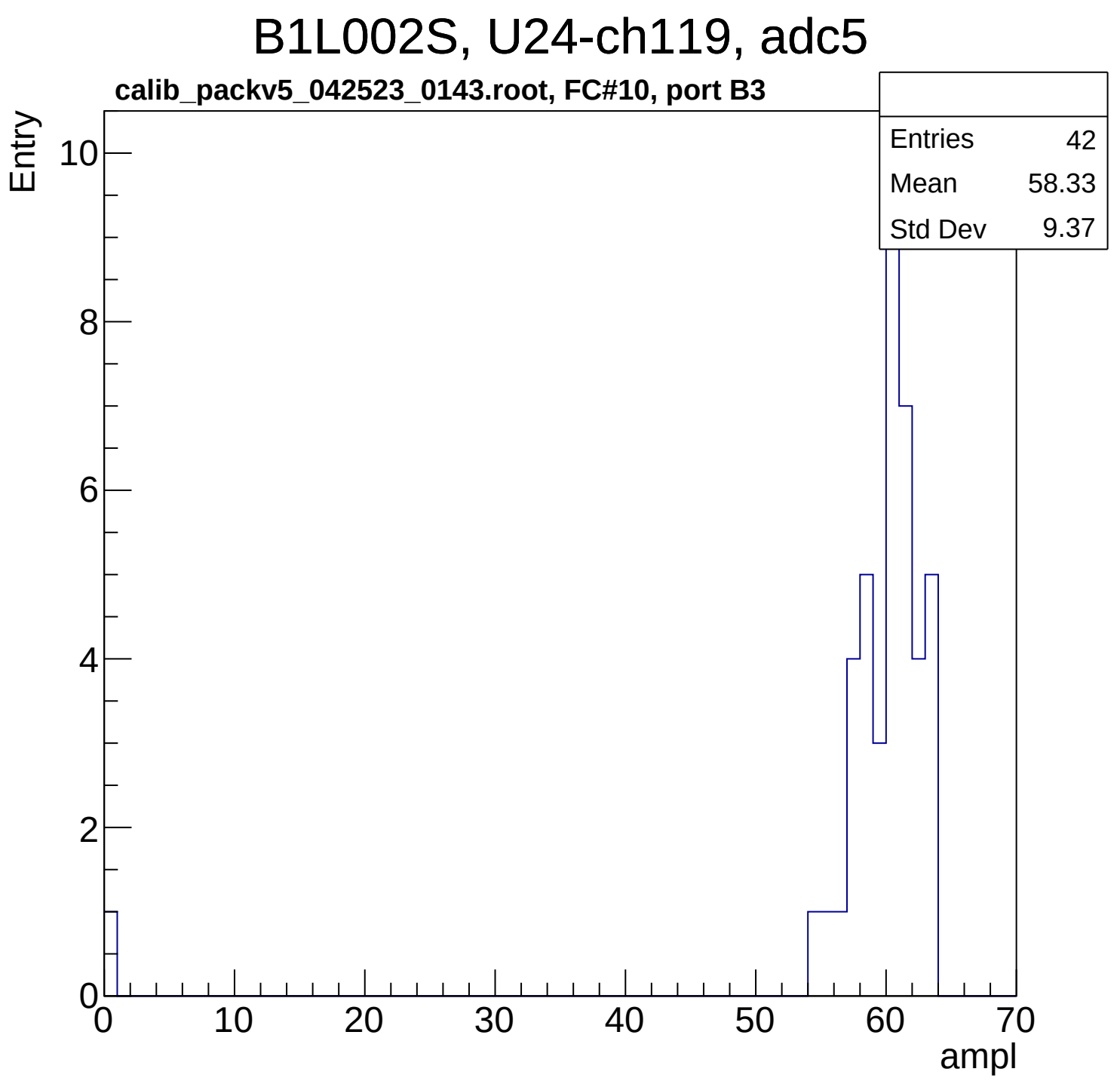
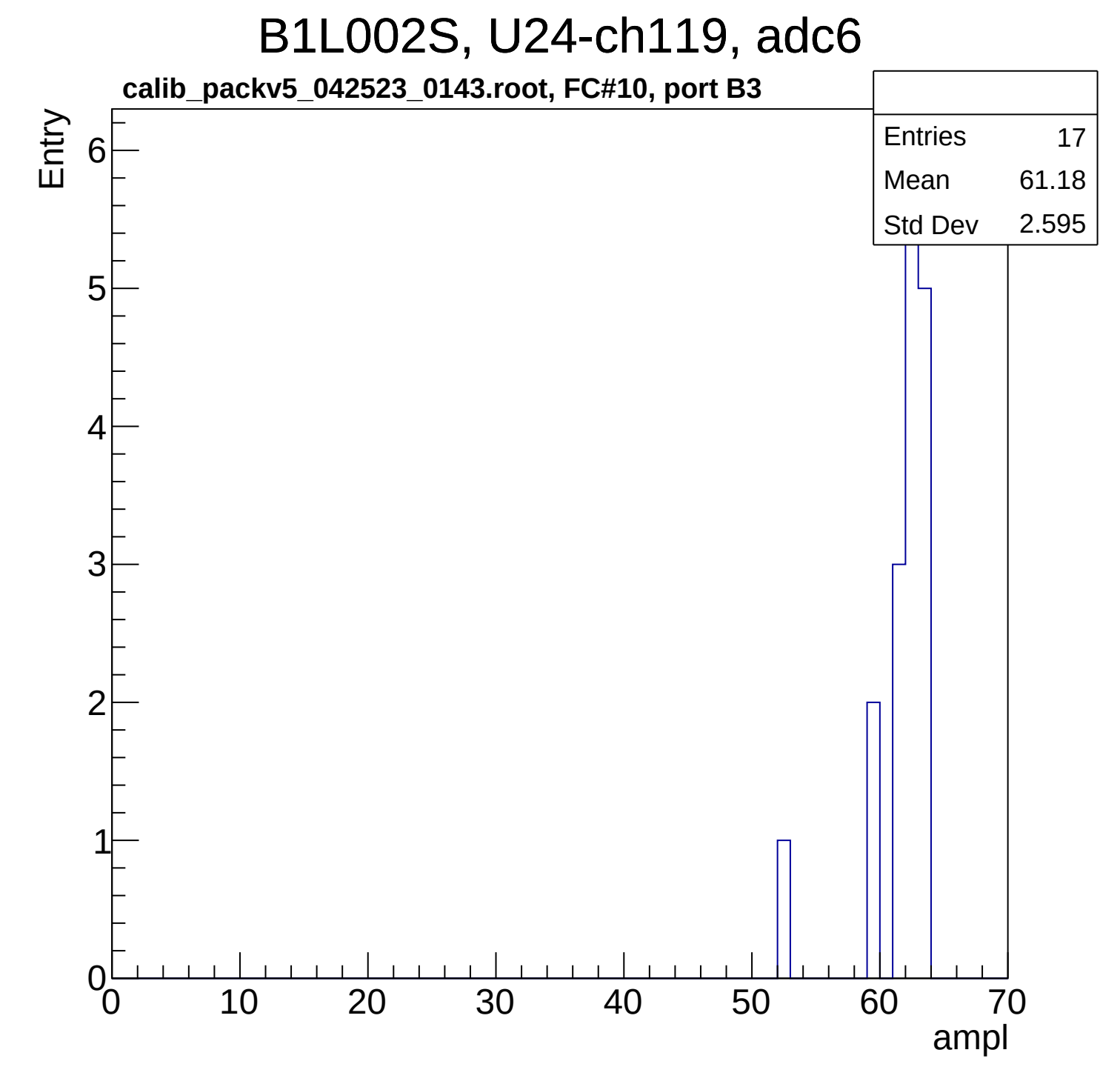
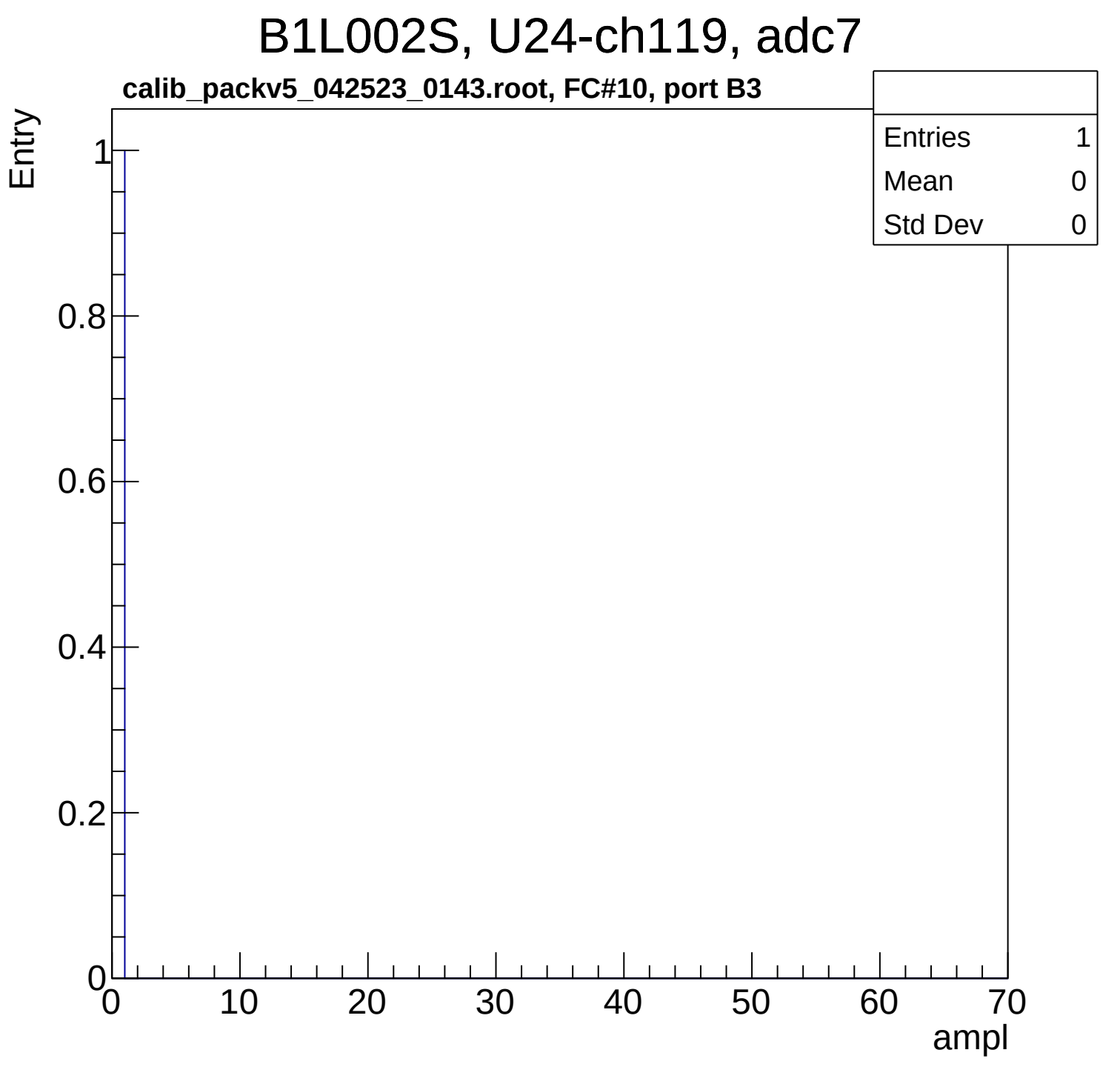
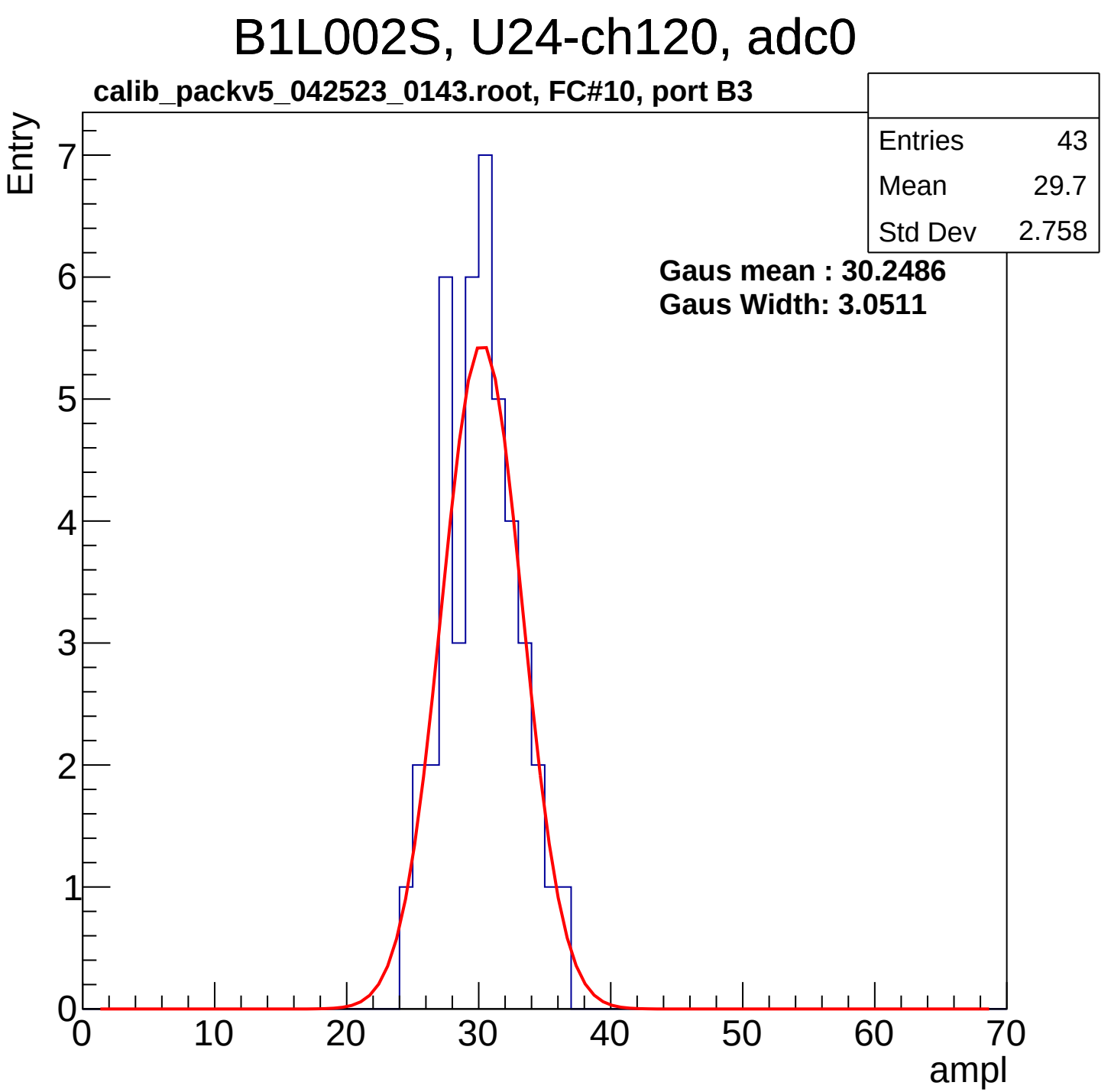

B1L002S, U24-ch119, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 58.33
Std Dev 9.37
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch119, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 61.18
Std Dev 2.595
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch119, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch120, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 29.7
Std Dev 2.758
Gaus mean : 30.2486
Gaus Width: 3.0511
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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60
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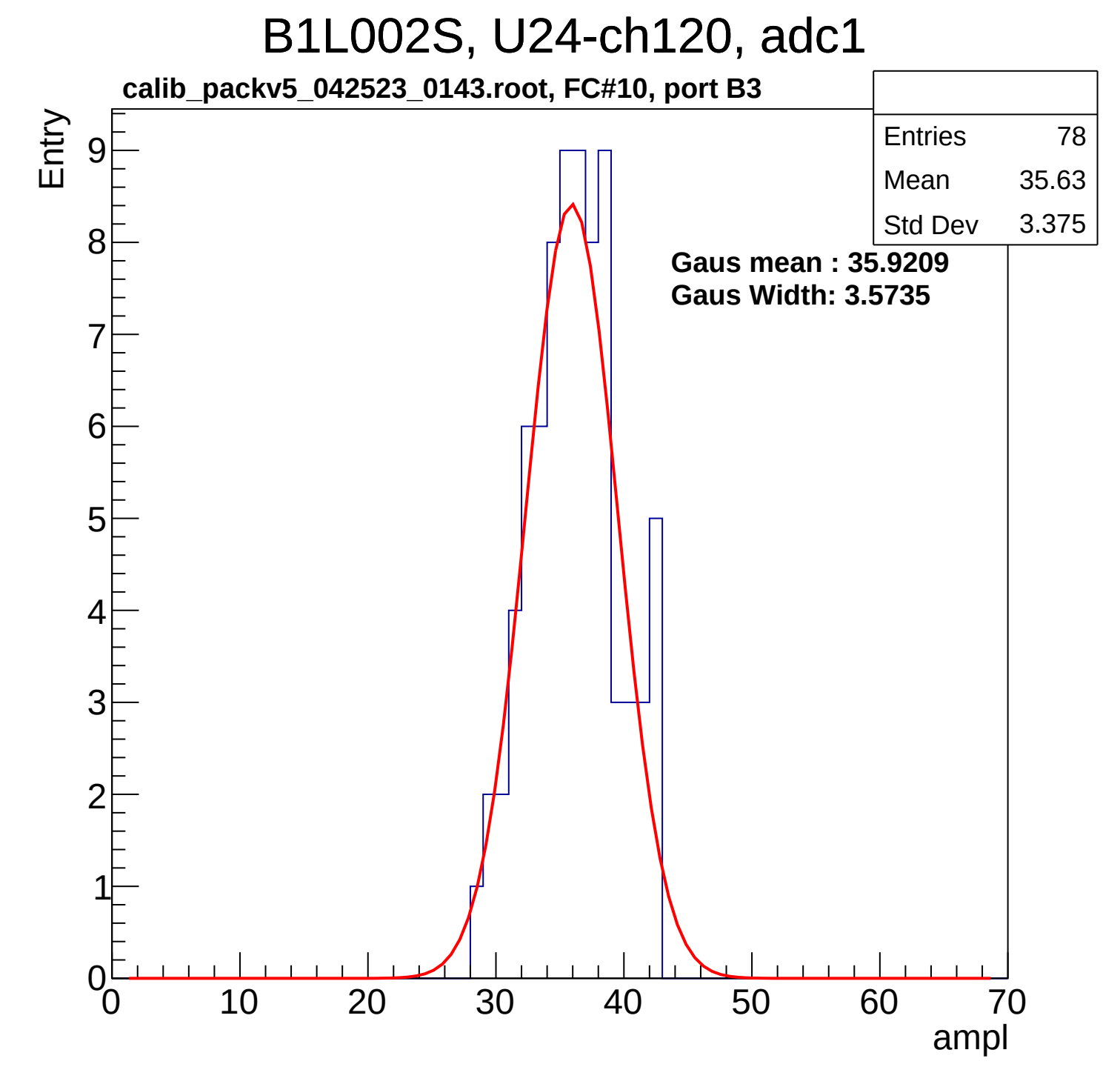
B1L002S, U24-ch120, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 78
Mean 35.63
Std Dev 3.375
Gaus mean : 35.9209
Gaus Width: 3.5735
Entry
ampl
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2
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6
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0
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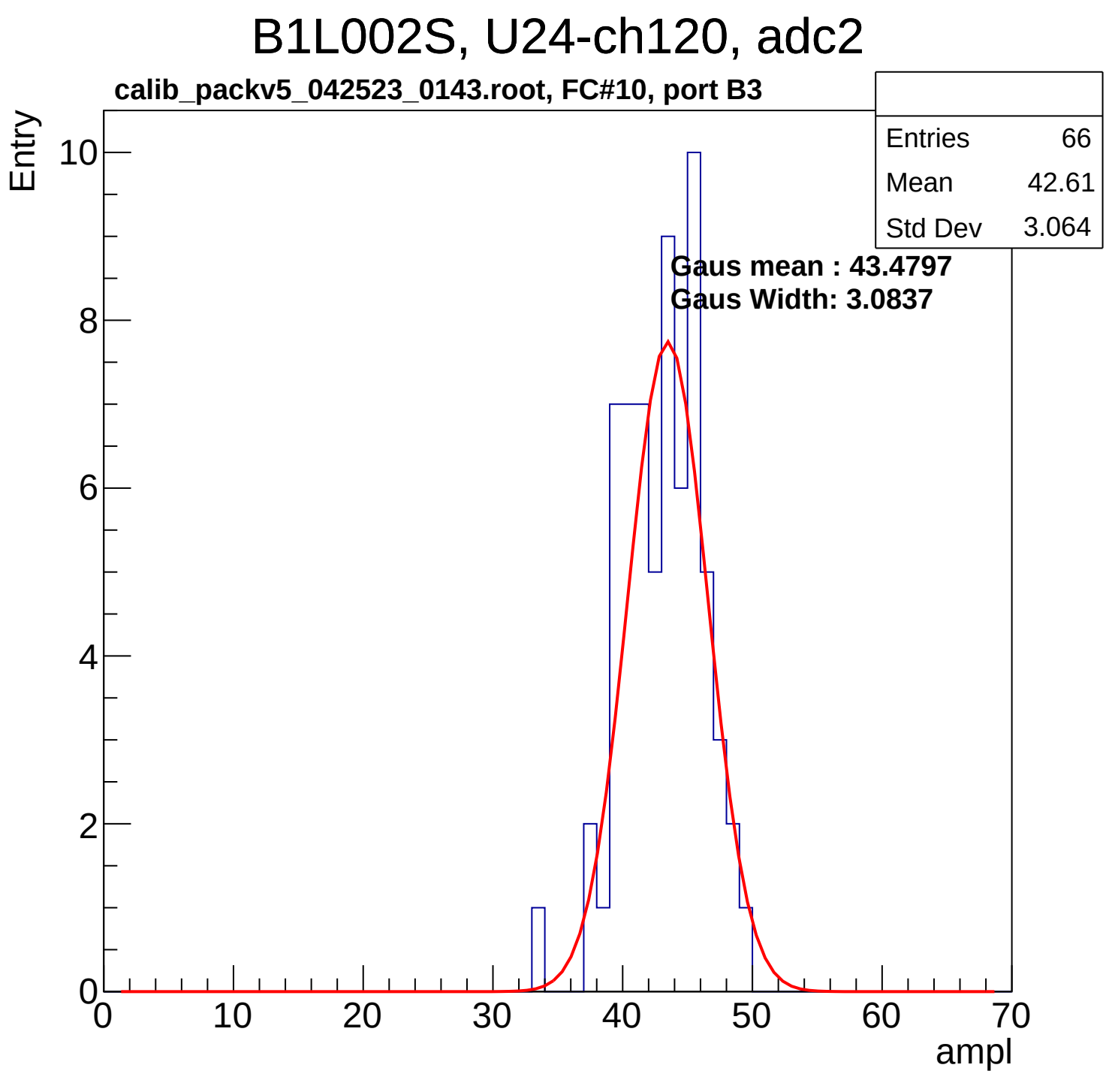
B1L002S, U24-ch120, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 42.61
Std Dev 3.064
Gaus mean : 43.4797
Gaus Width: 3.0837
Entry
ampl
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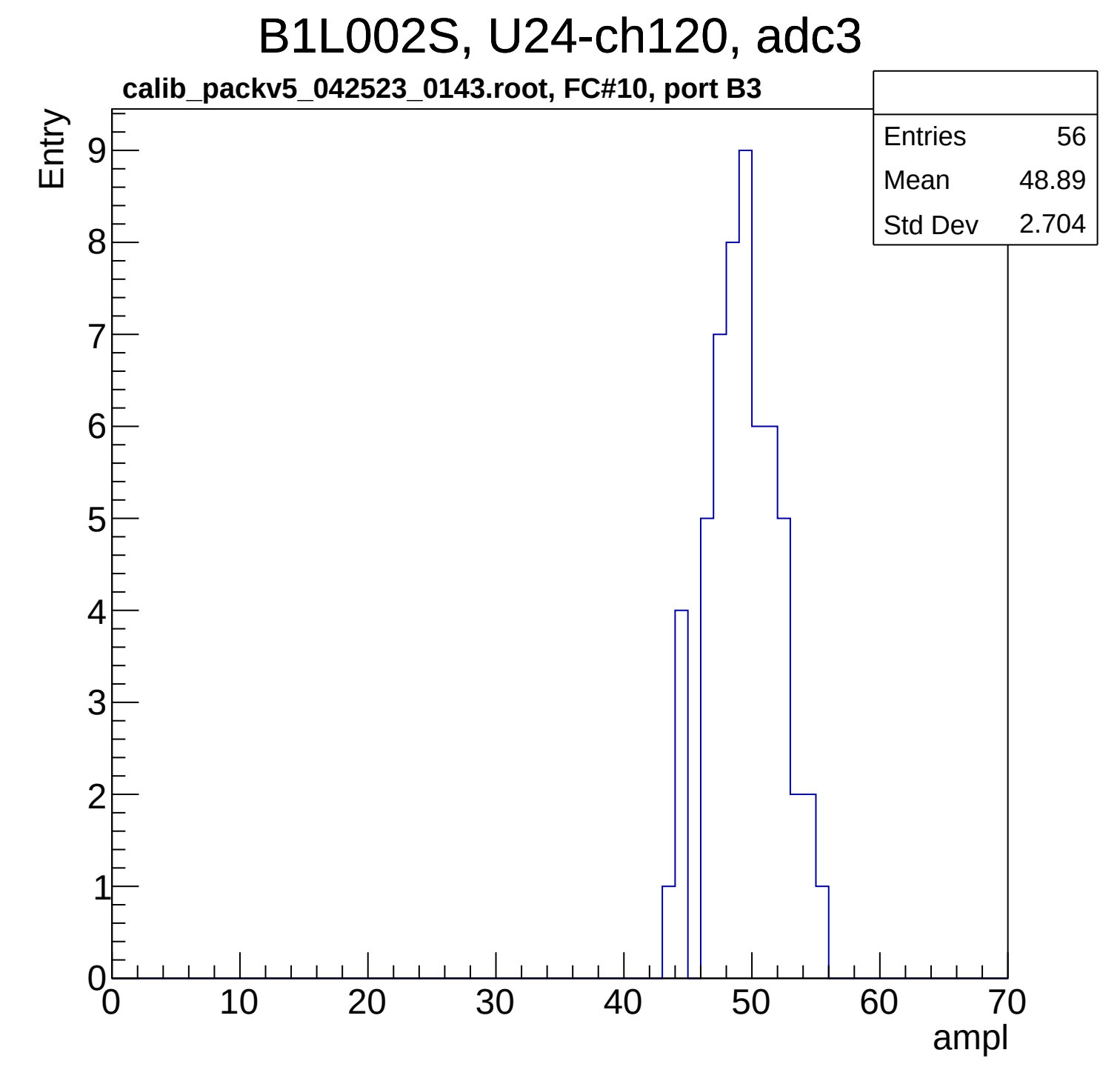
B1L002S, U24-ch120, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 48.89
Std Dev 2.704
Entry
ampl
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1
2
3
4
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6
7
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0
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60
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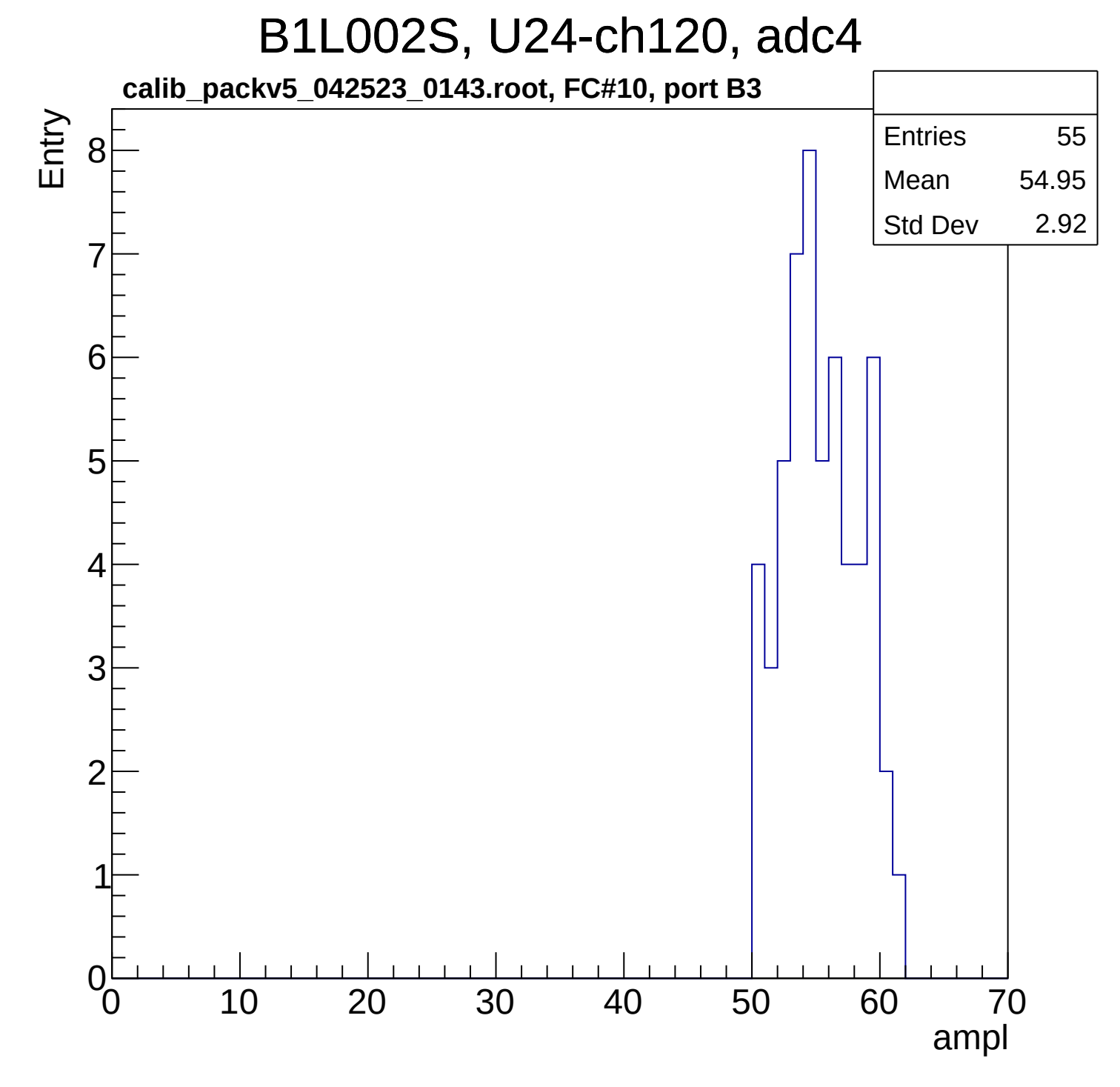
B1L002S, U24-ch120, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 54.95
Std Dev 2.92
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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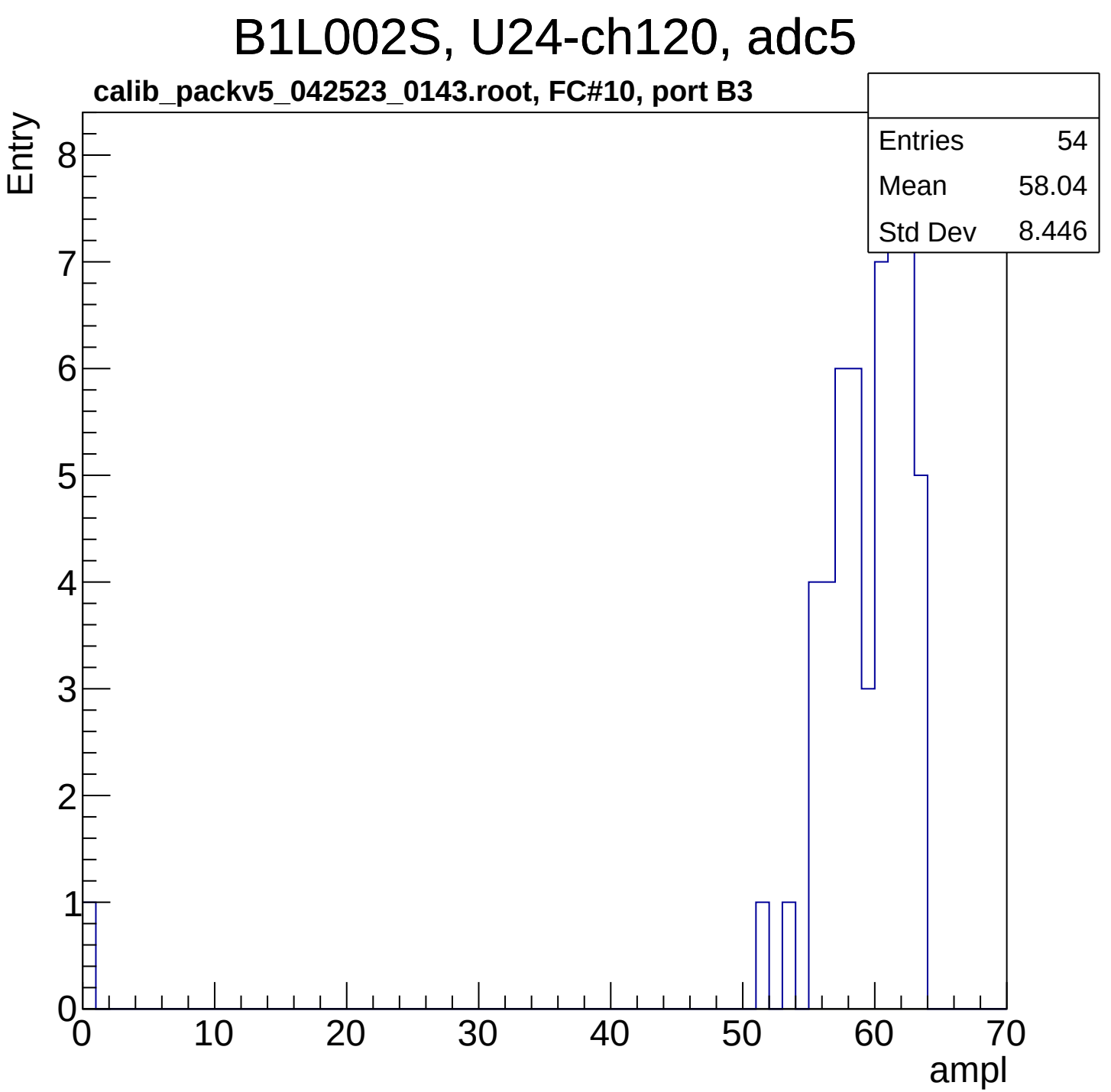
B1L002S, U24-ch120, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 58.04
Std Dev 8.446
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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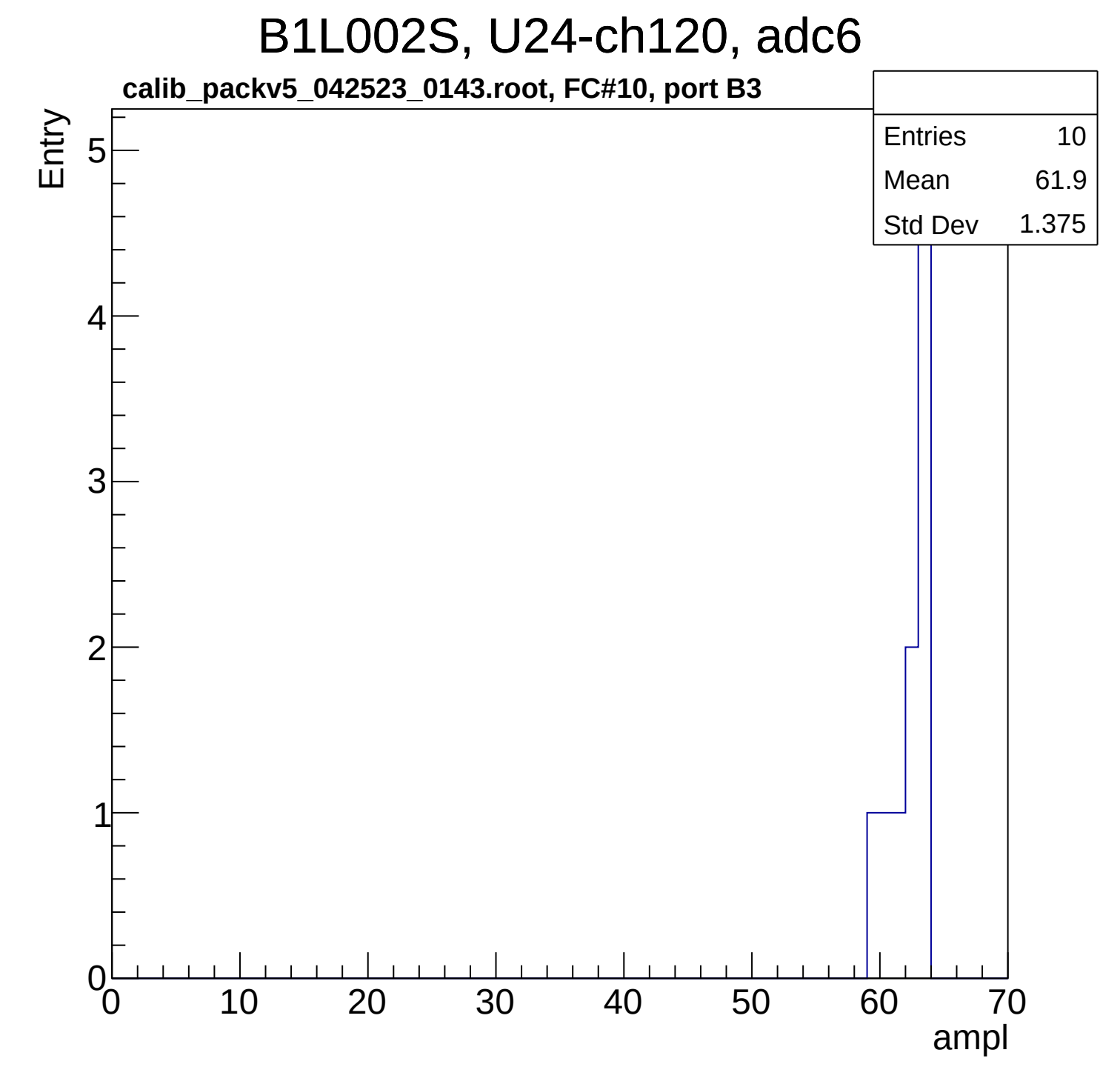
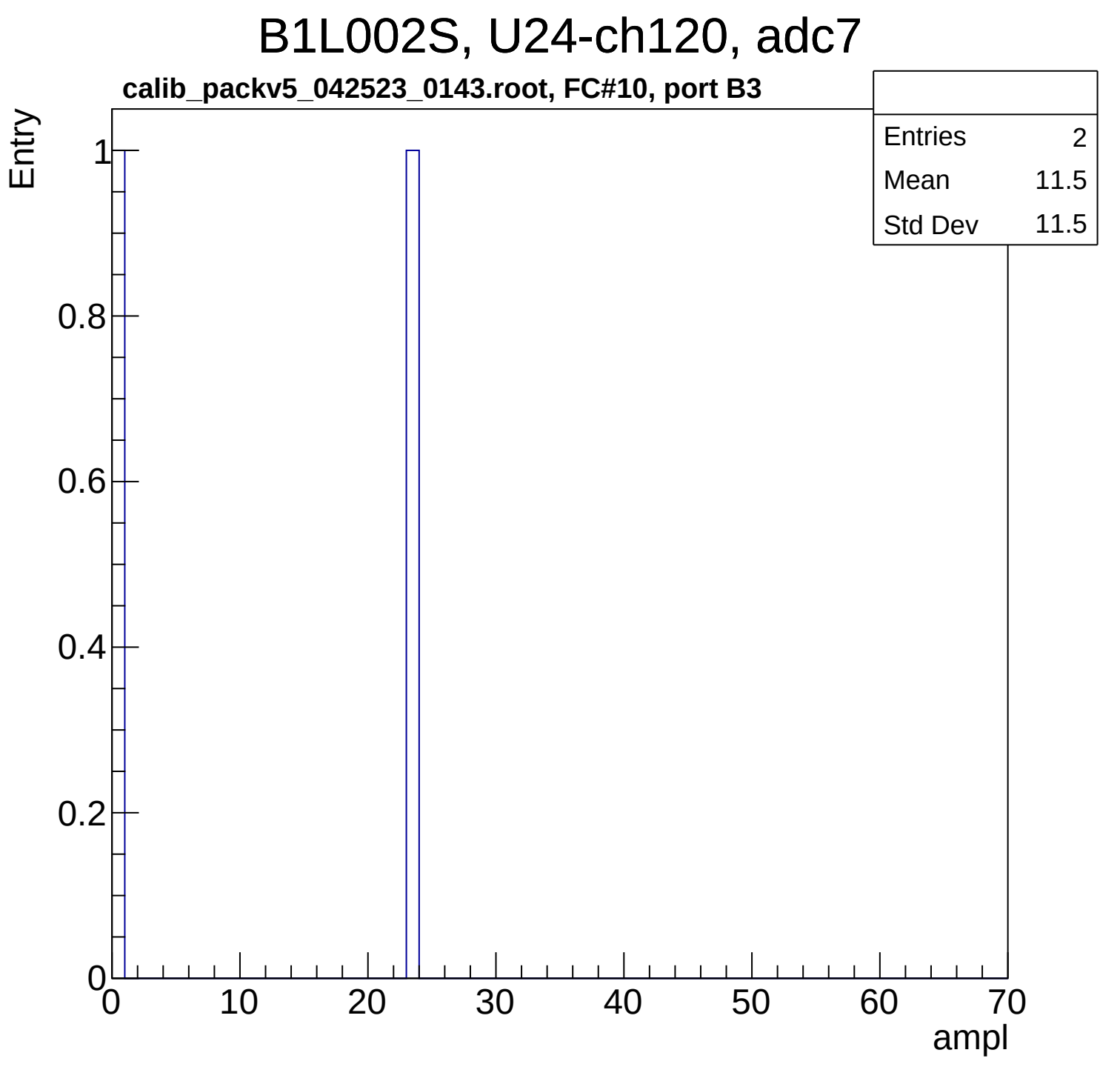
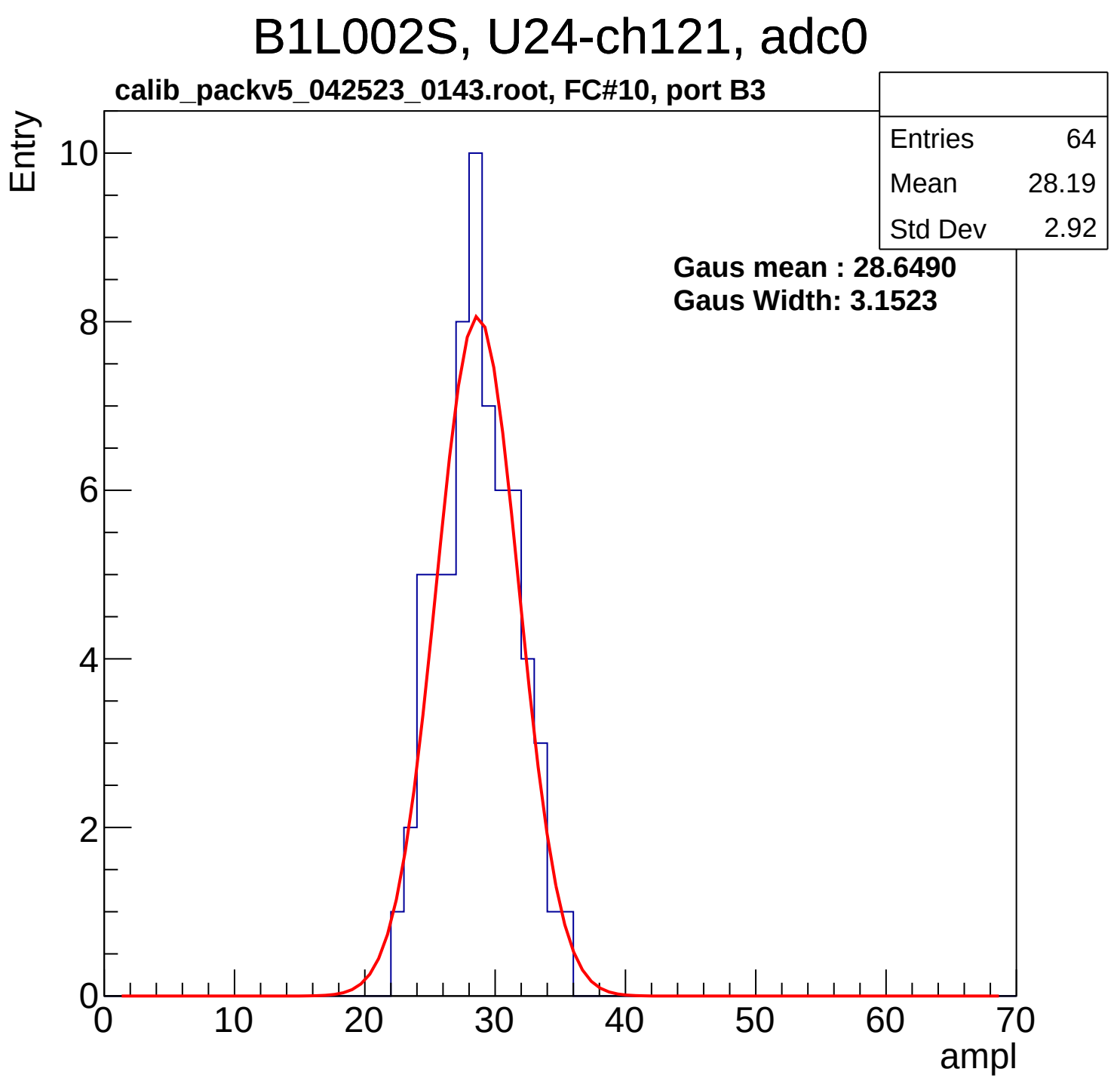
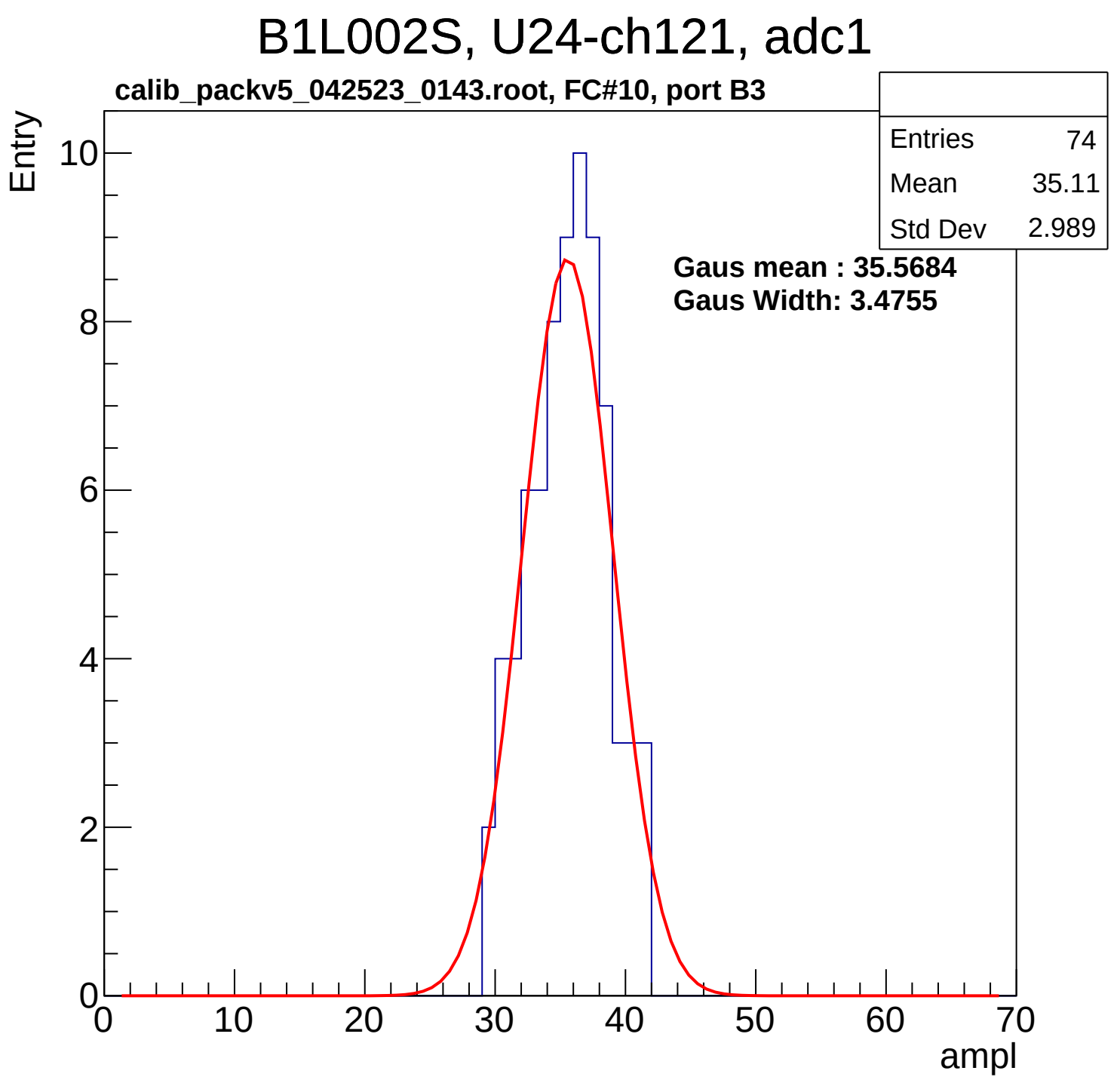

B1L002S, U24-ch120, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 10
Mean 61.9
Std Dev 1.375
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch120, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 11.5
Std Dev 11.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch121, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 28.19
Std Dev 2.92
Gaus mean : 28.6490
Gaus Width: 3.1523
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L002S, U24-ch121, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 74
Mean 35.11
Std Dev 2.989
Gaus mean : 35.5684
Gaus Width: 3.4755
Entry
ampl
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2
4
6
8
10
0
10
20
30
40
50
60
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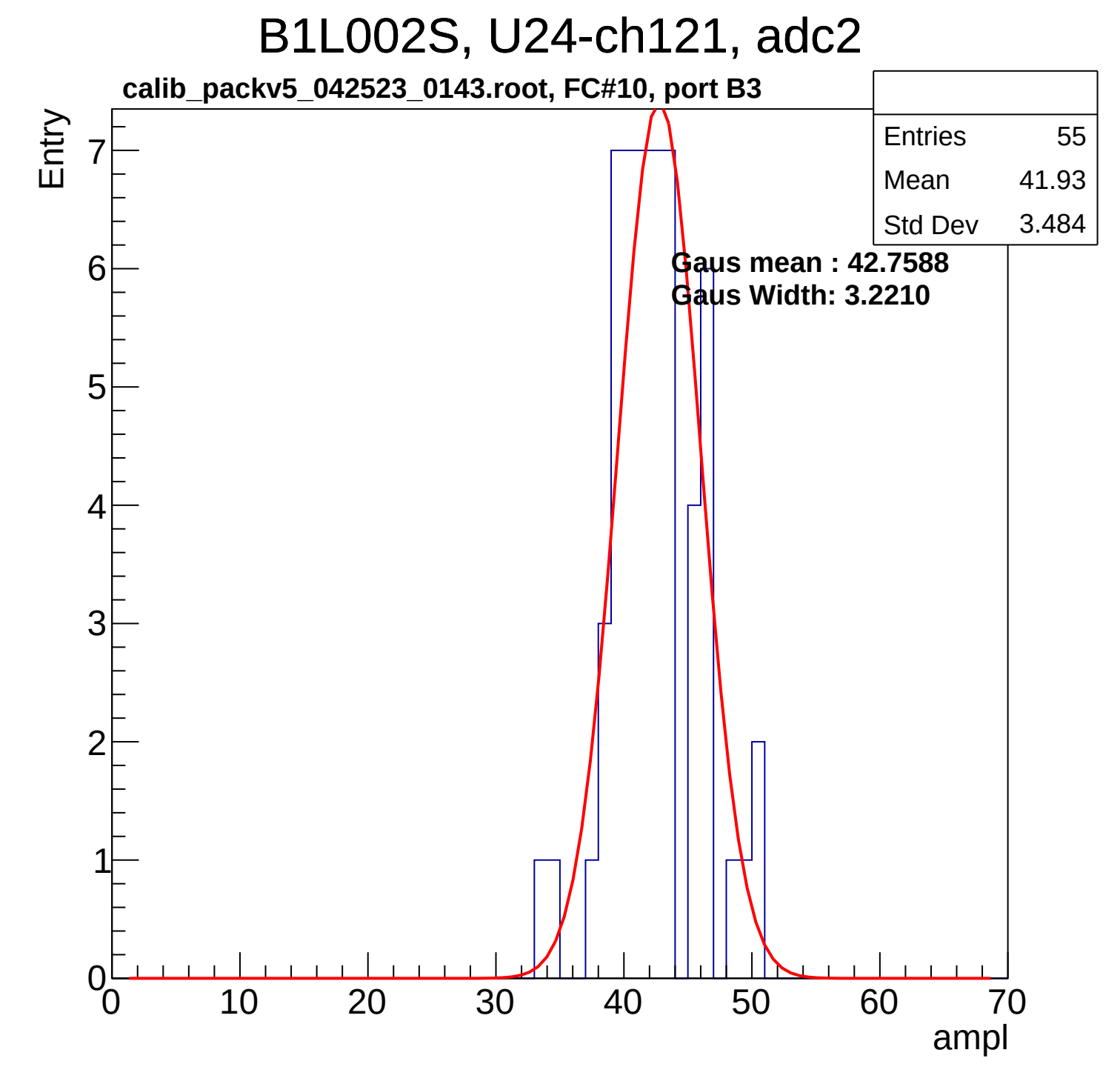
B1L002S, U24-ch121, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 41.93
Std Dev 3.484
Gaus mean : 42.7588
Gaus Width: 3.2210
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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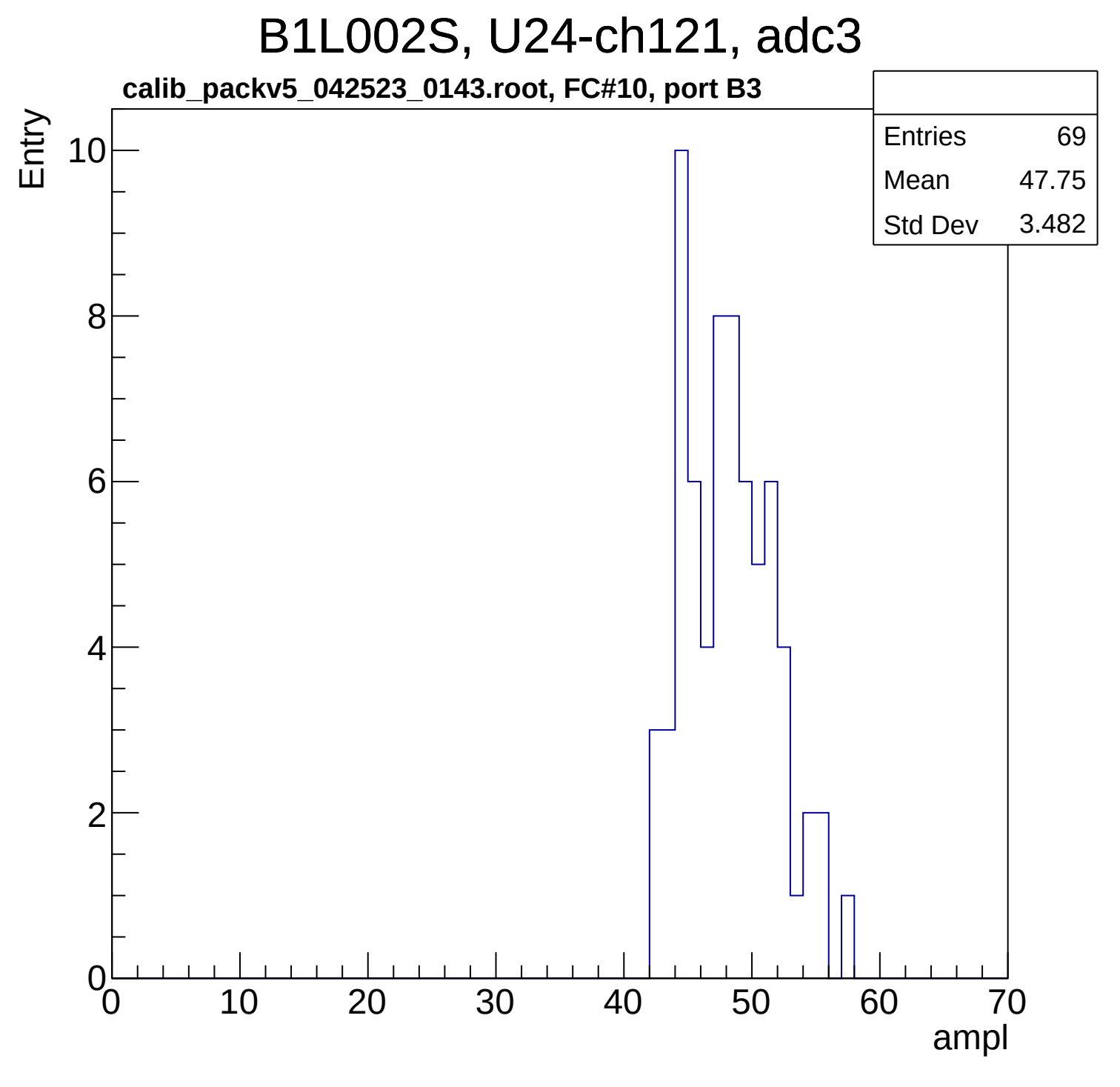
B1L002S, U24-ch121, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 47.75
Std Dev 3.482
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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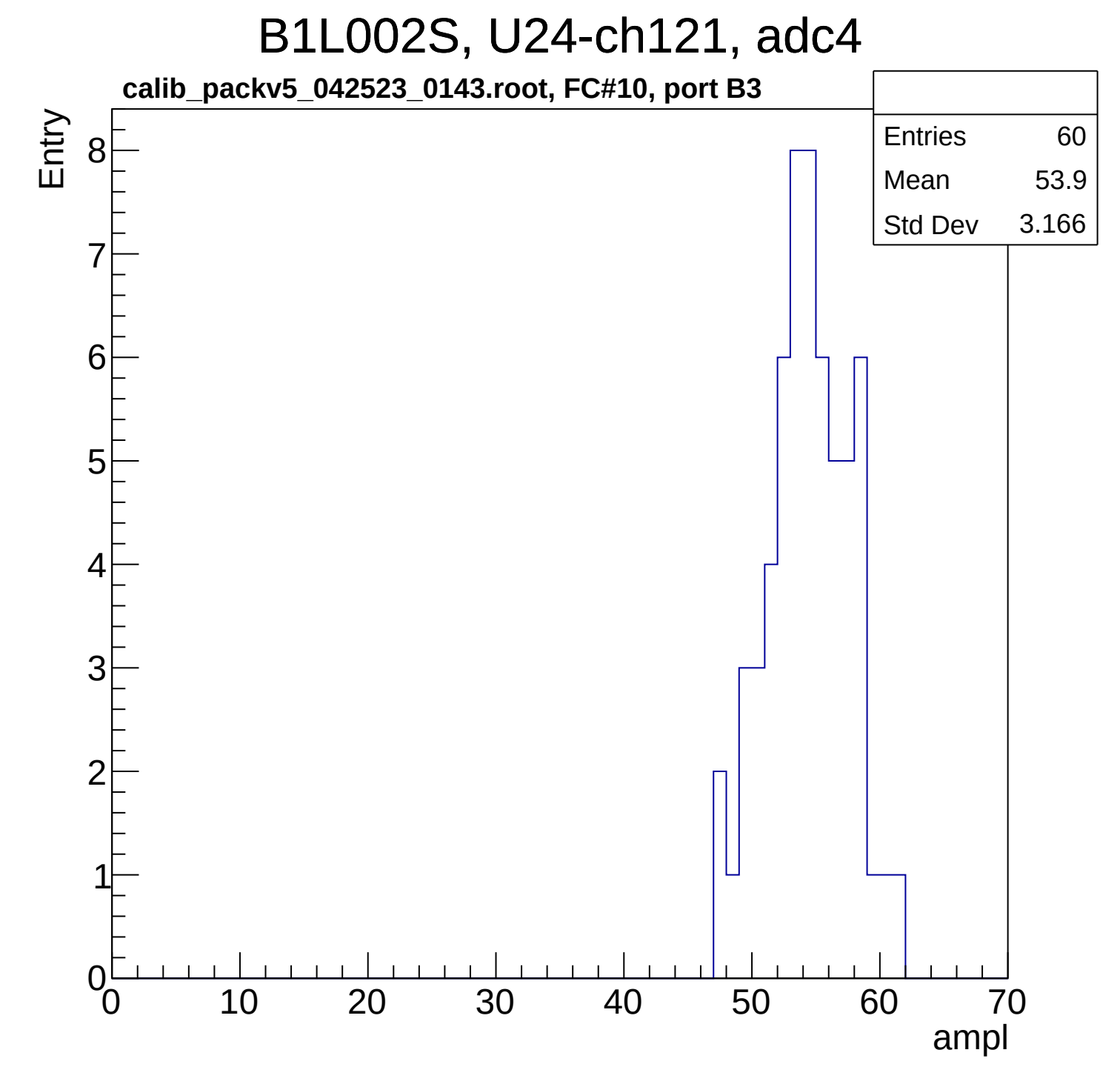
B1L002S, U24-ch121, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 60
Mean 53.9
Std Dev 3.166
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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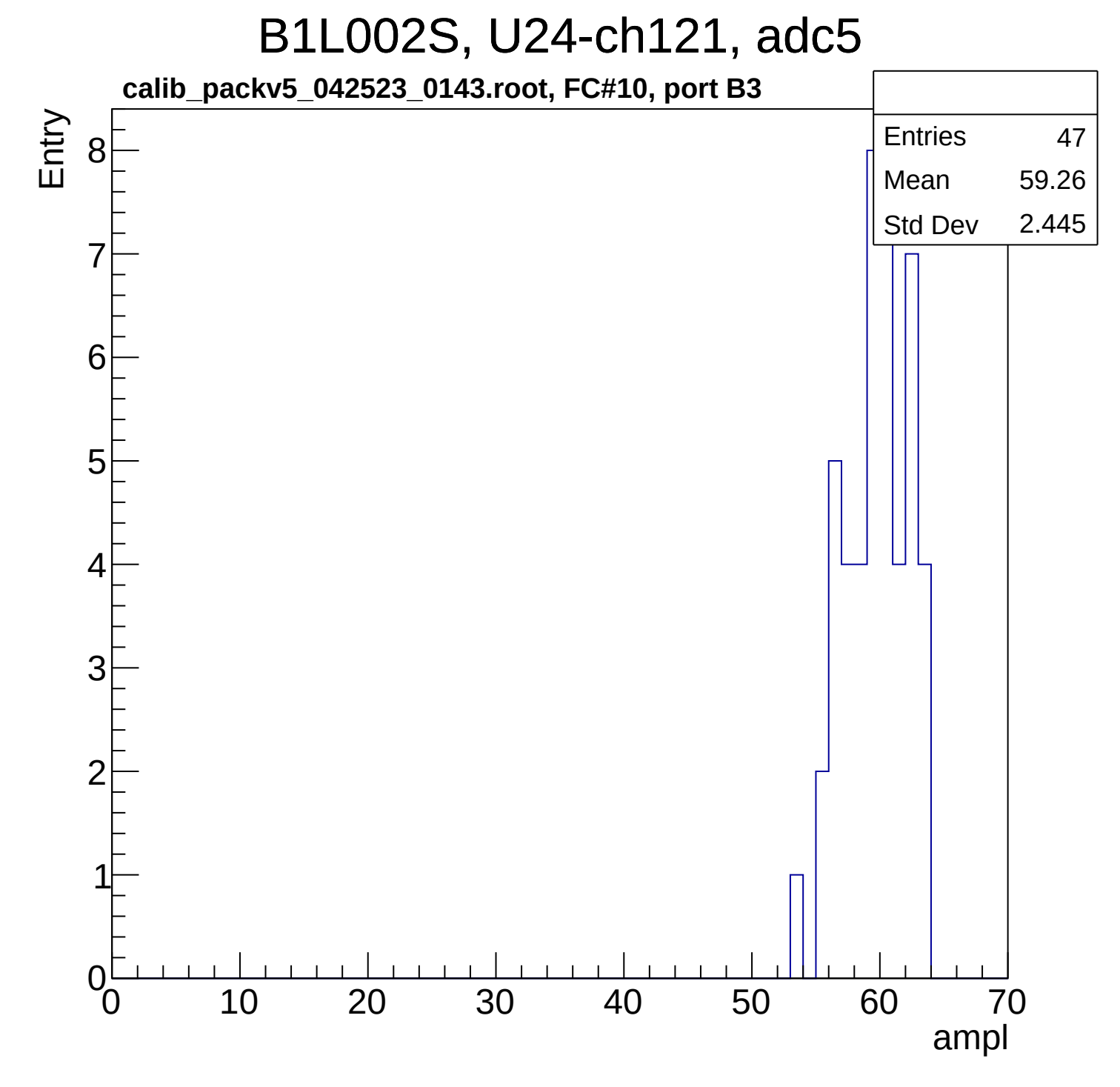
B1L002S, U24-ch121, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 47
Mean 59.26
Std Dev 2.445
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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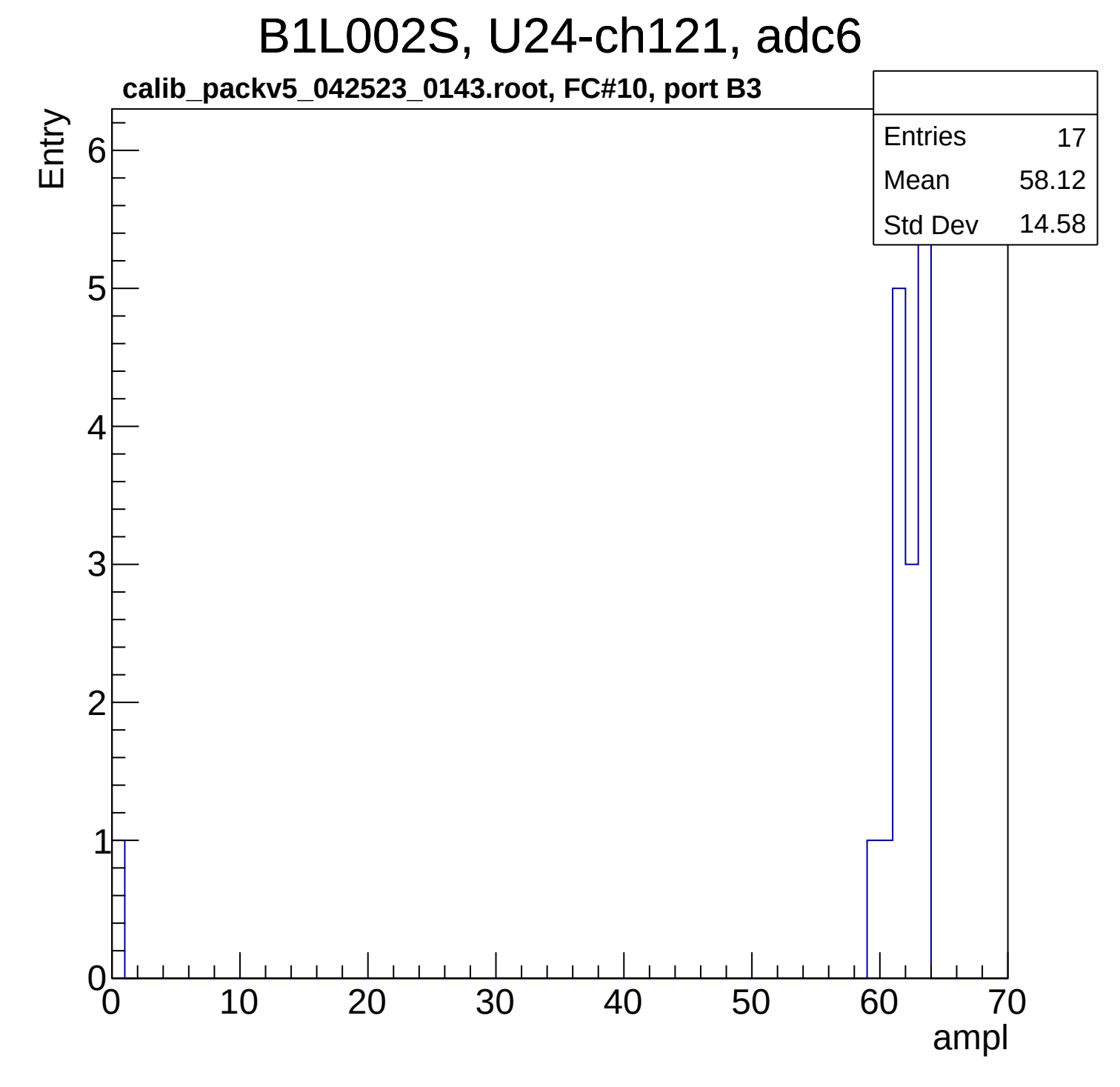
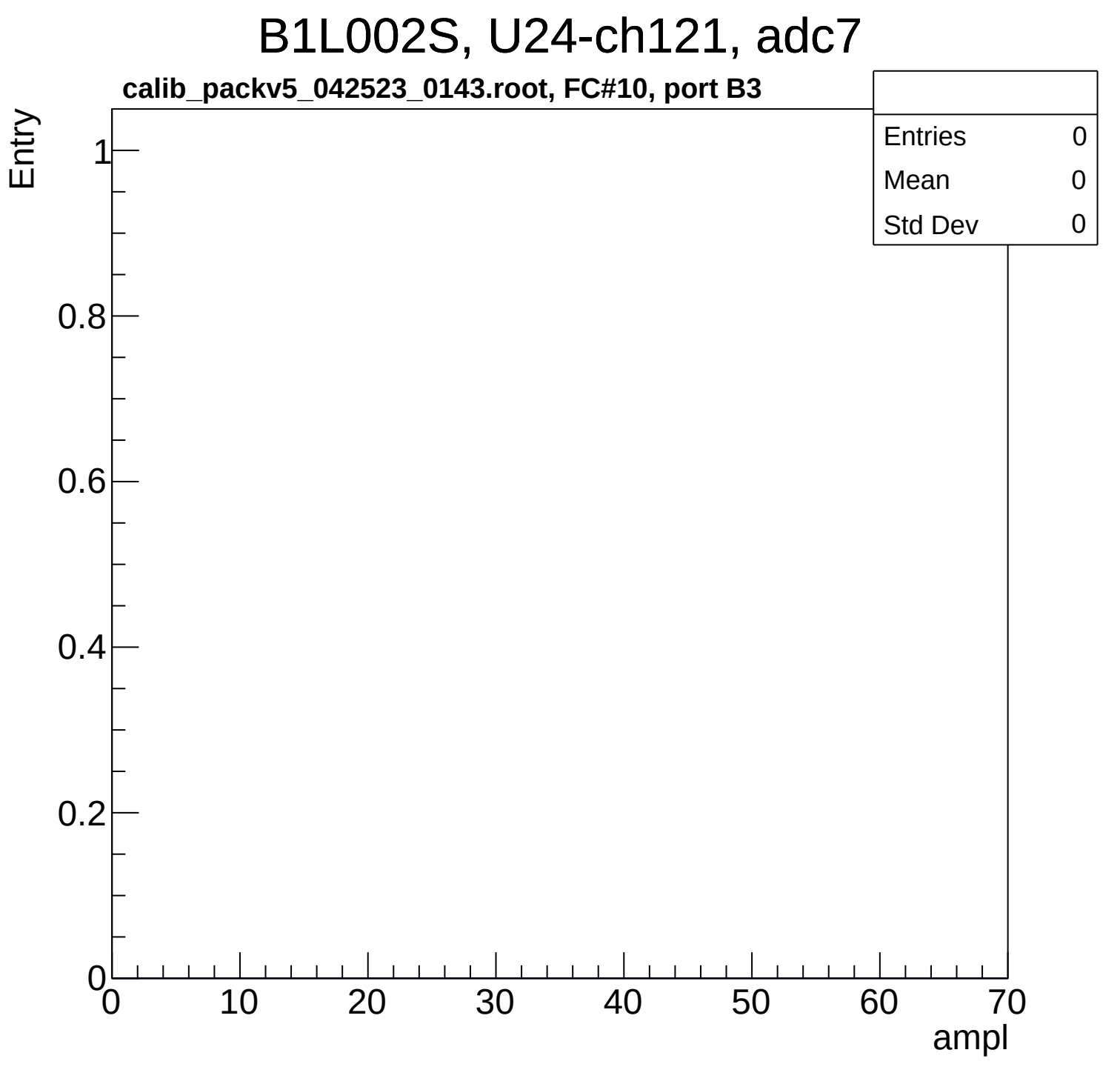
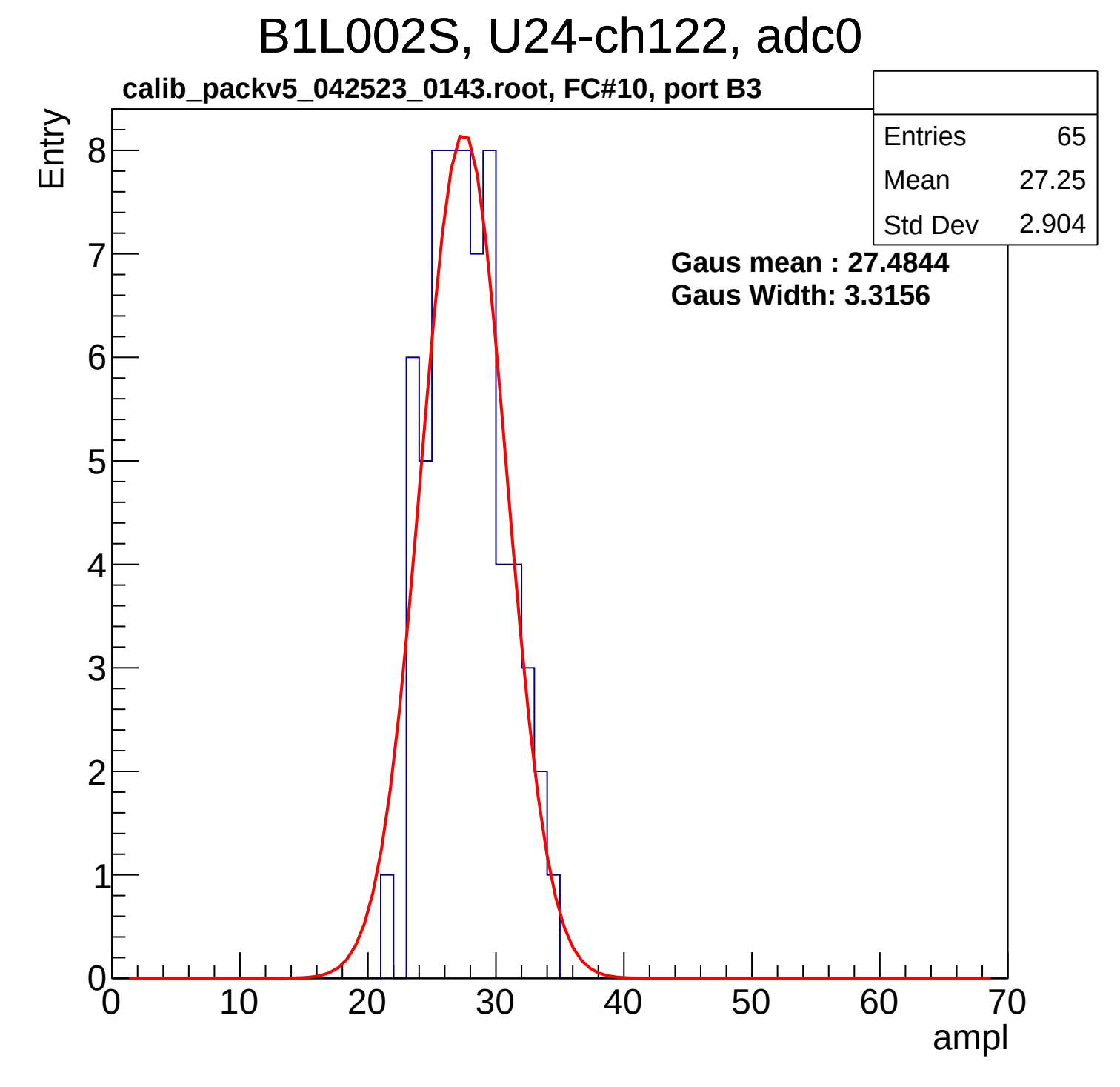

B1L002S, U24-ch121, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 17
Mean 58.12
Std Dev 14.58
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch121, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch122, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 27.25
Std Dev 2.904
Gaus mean : 27.4844
Gaus Width: 3.3156
Entry
ampl
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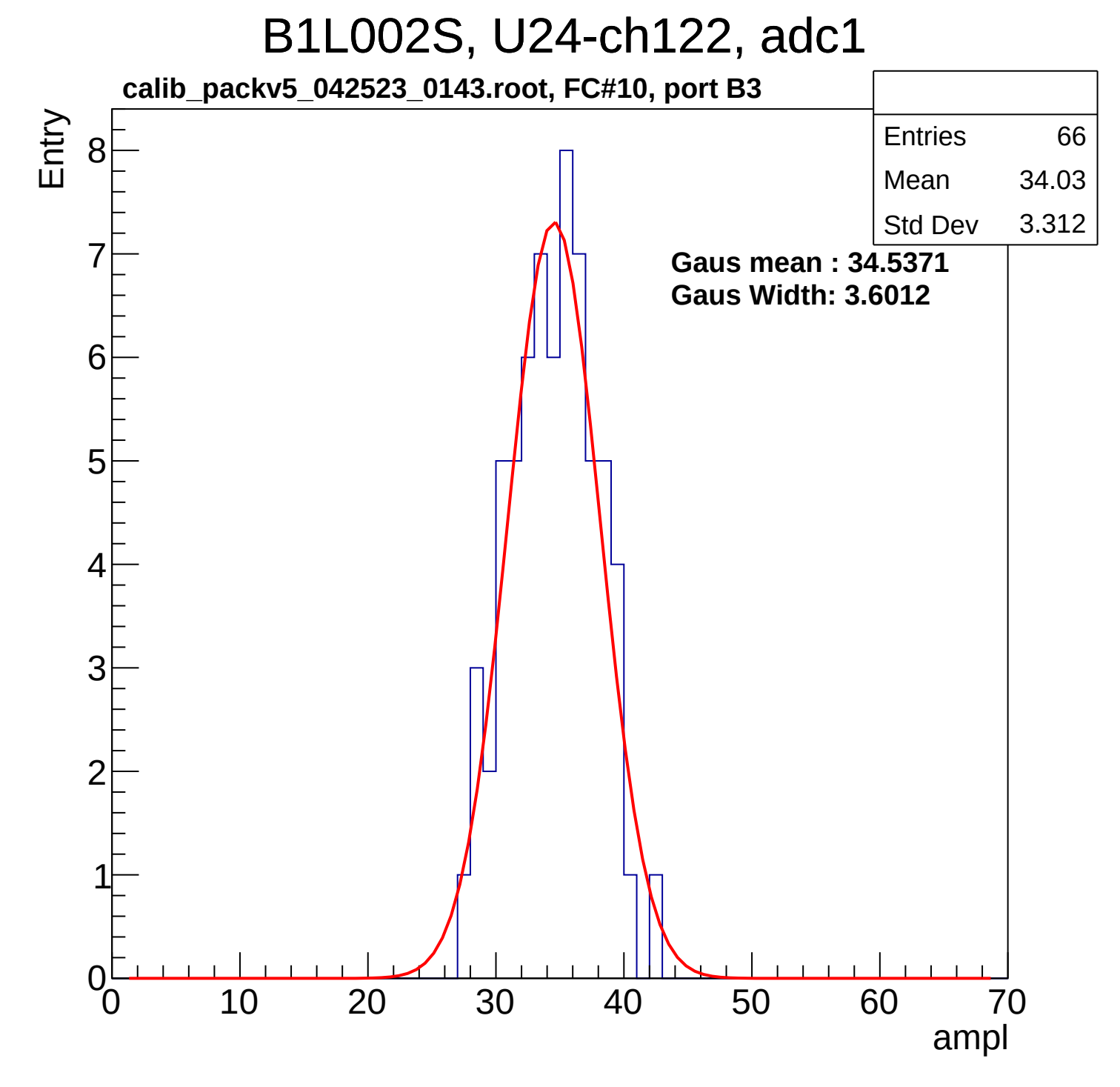
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch122, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 34.03
Std Dev 3.312
Gaus mean : 34.5371
Gaus Width: 3.6012
Entry
ampl
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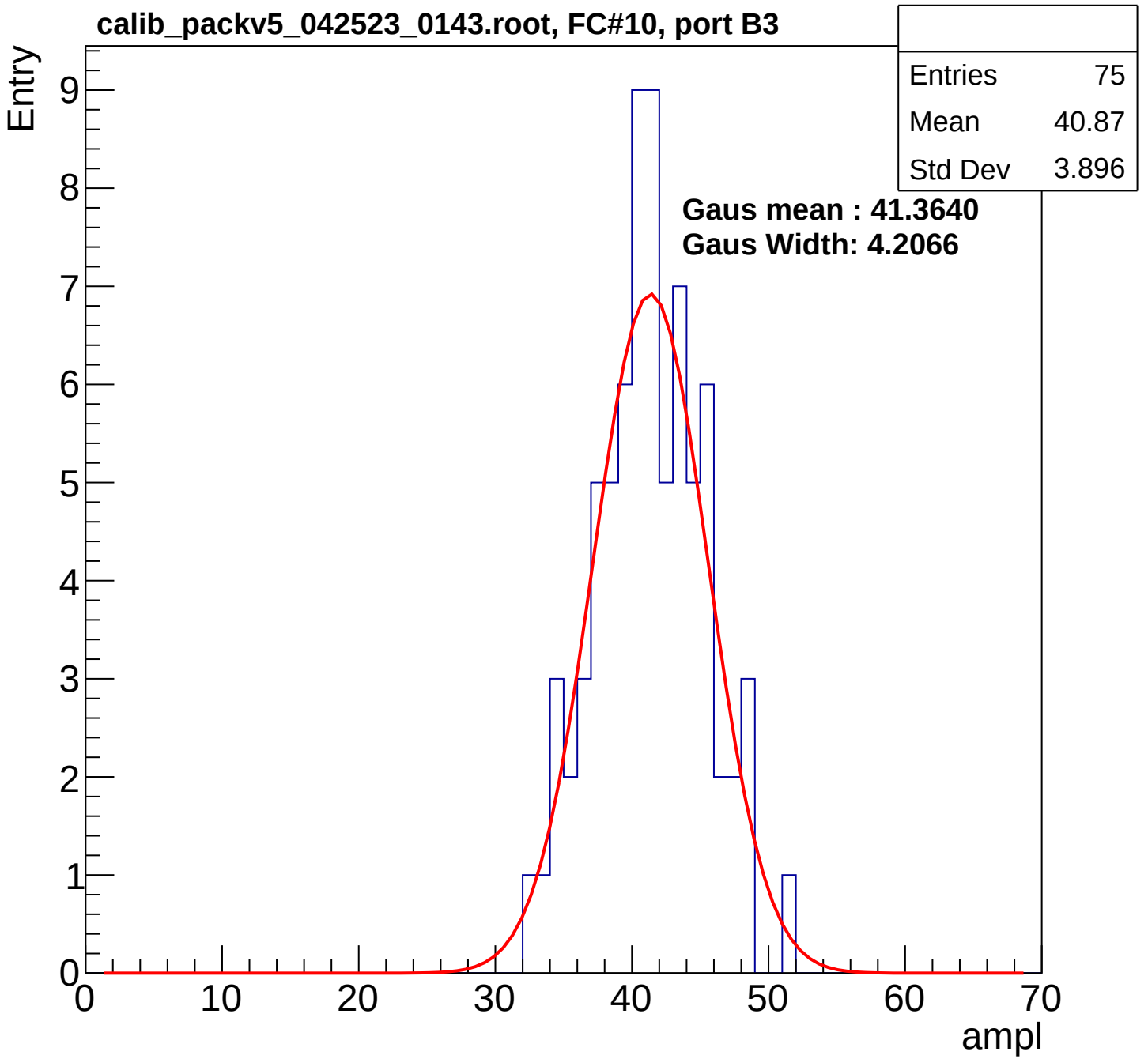
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch122, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 75
Mean 40.87
Std Dev 3.896
Gaus mean : 41.3640
Gaus Width: 4.2066
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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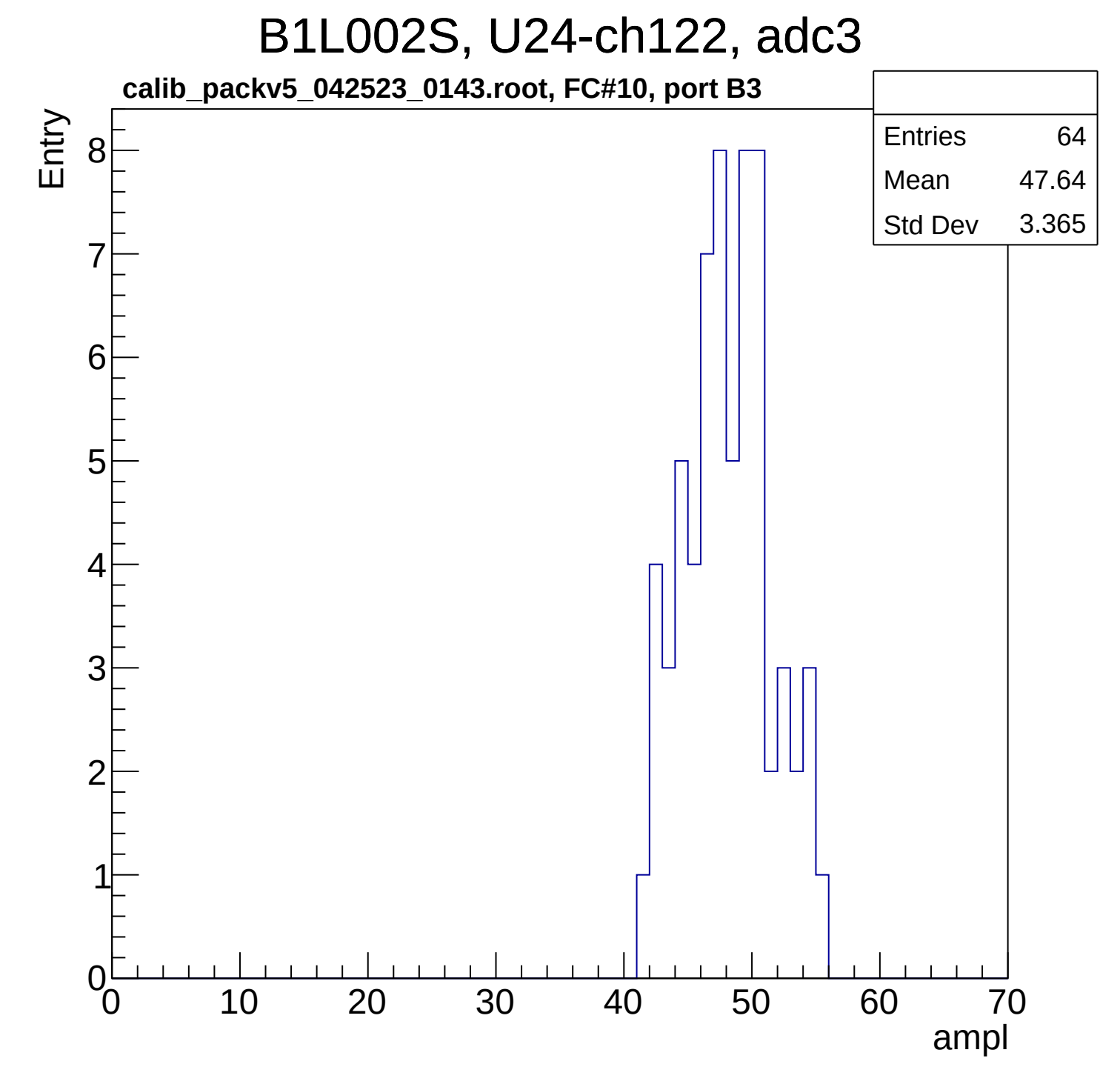
B1L002S, U24-ch122, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 47.64
Std Dev 3.365
Entry
ampl
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0
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60
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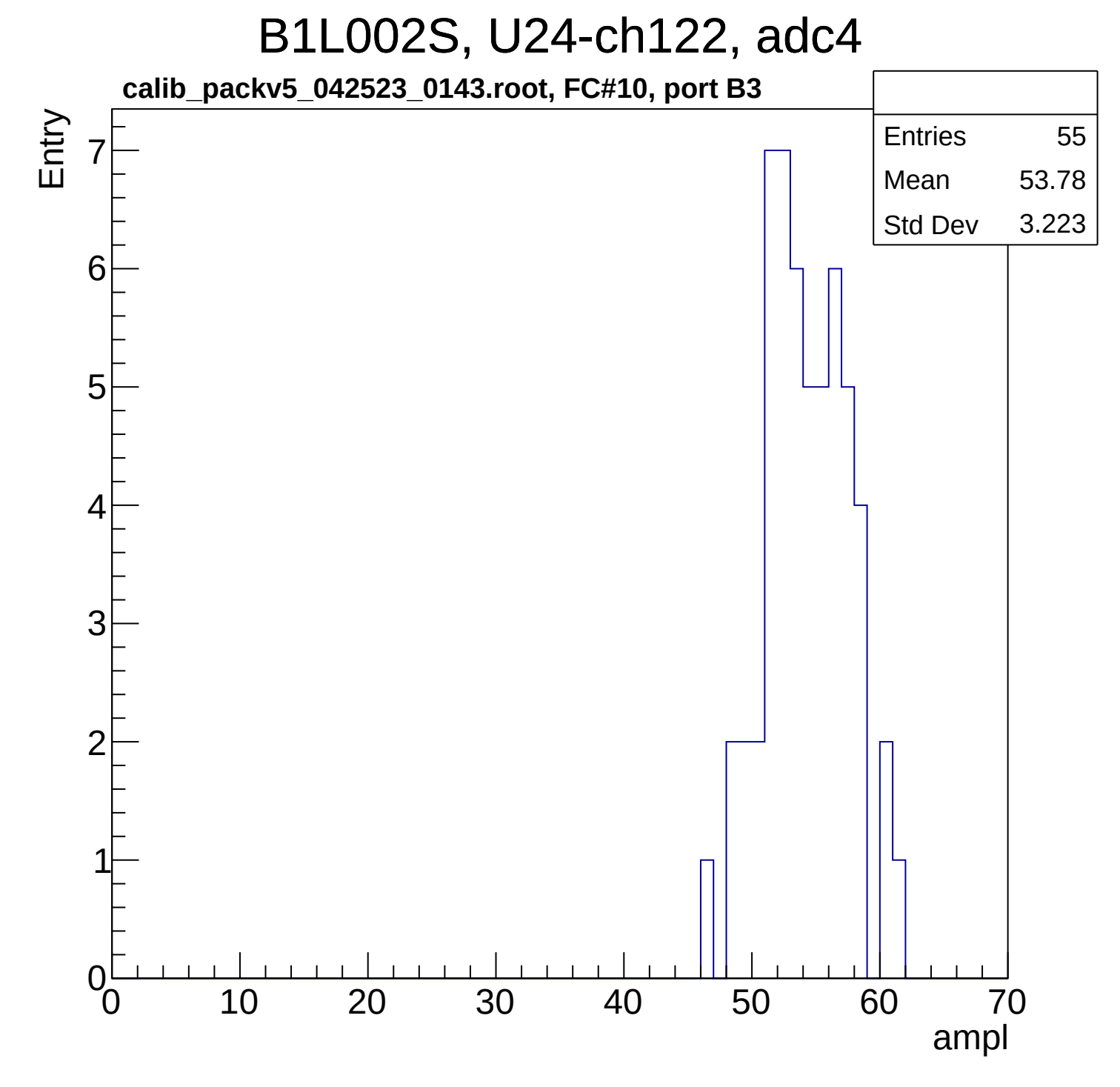
B1L002S, U24-ch122, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 53.78
Std Dev 3.223
Entry
ampl
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1
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6
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0
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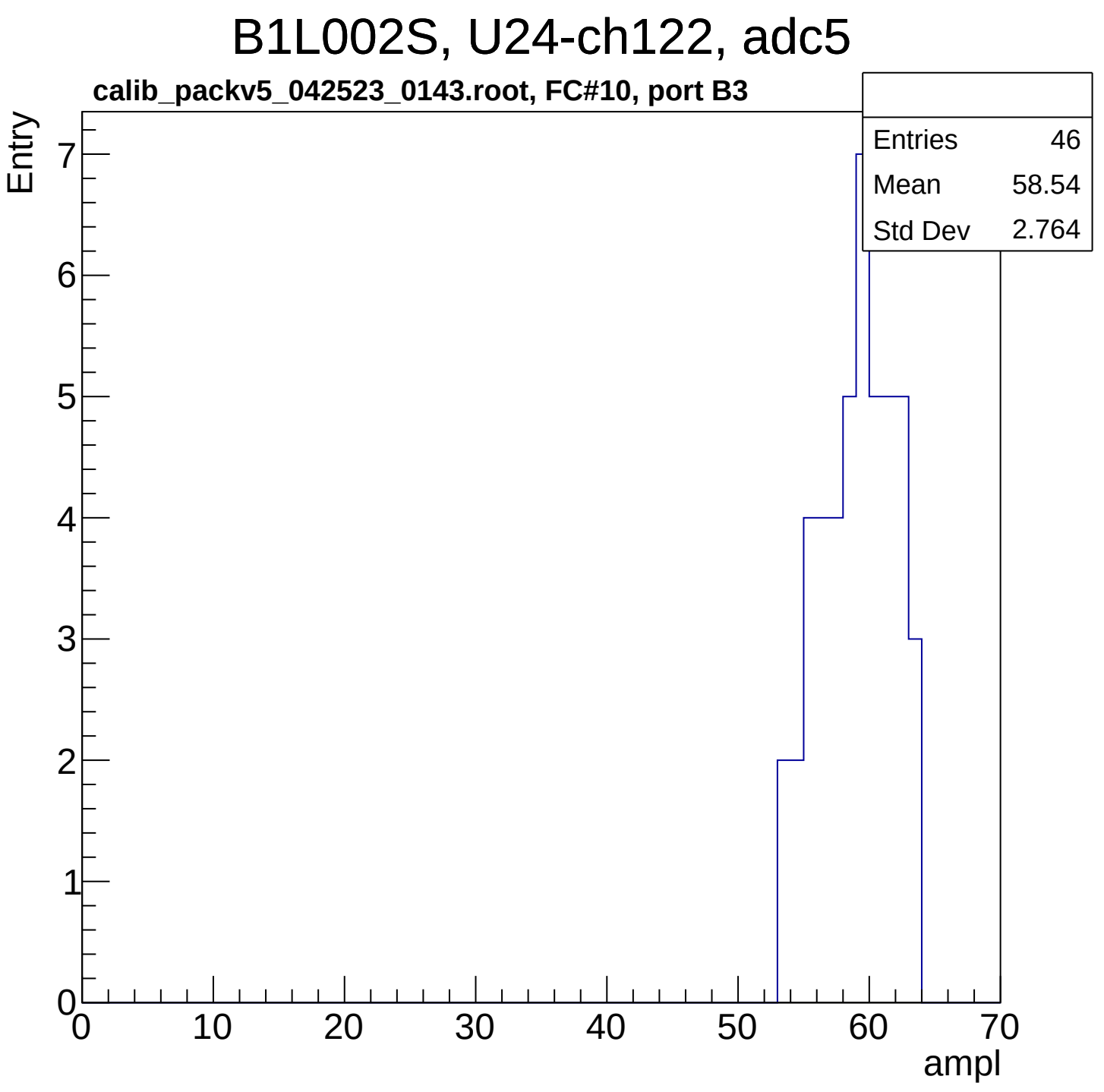
40
50
60
70


B1L002S, U24-ch122, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 46
Mean 58.54
Std Dev 2.764
Entry
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3
4
5
6
7
0
10
20
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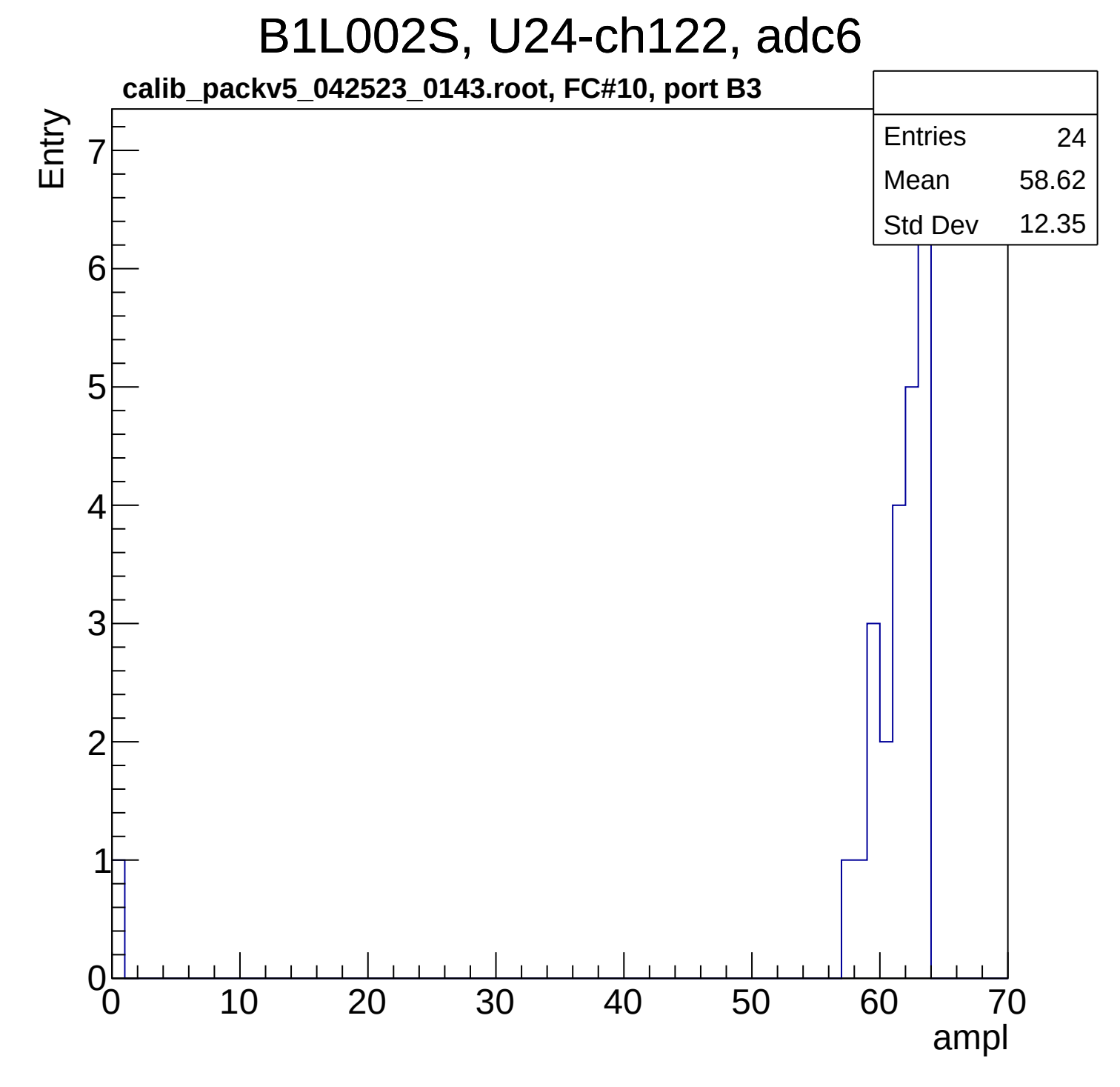
50
60
70
ampl


B1L002S, U24-ch122, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 24
Mean 58.62
Std Dev 12.35
Entry
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5
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10
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30
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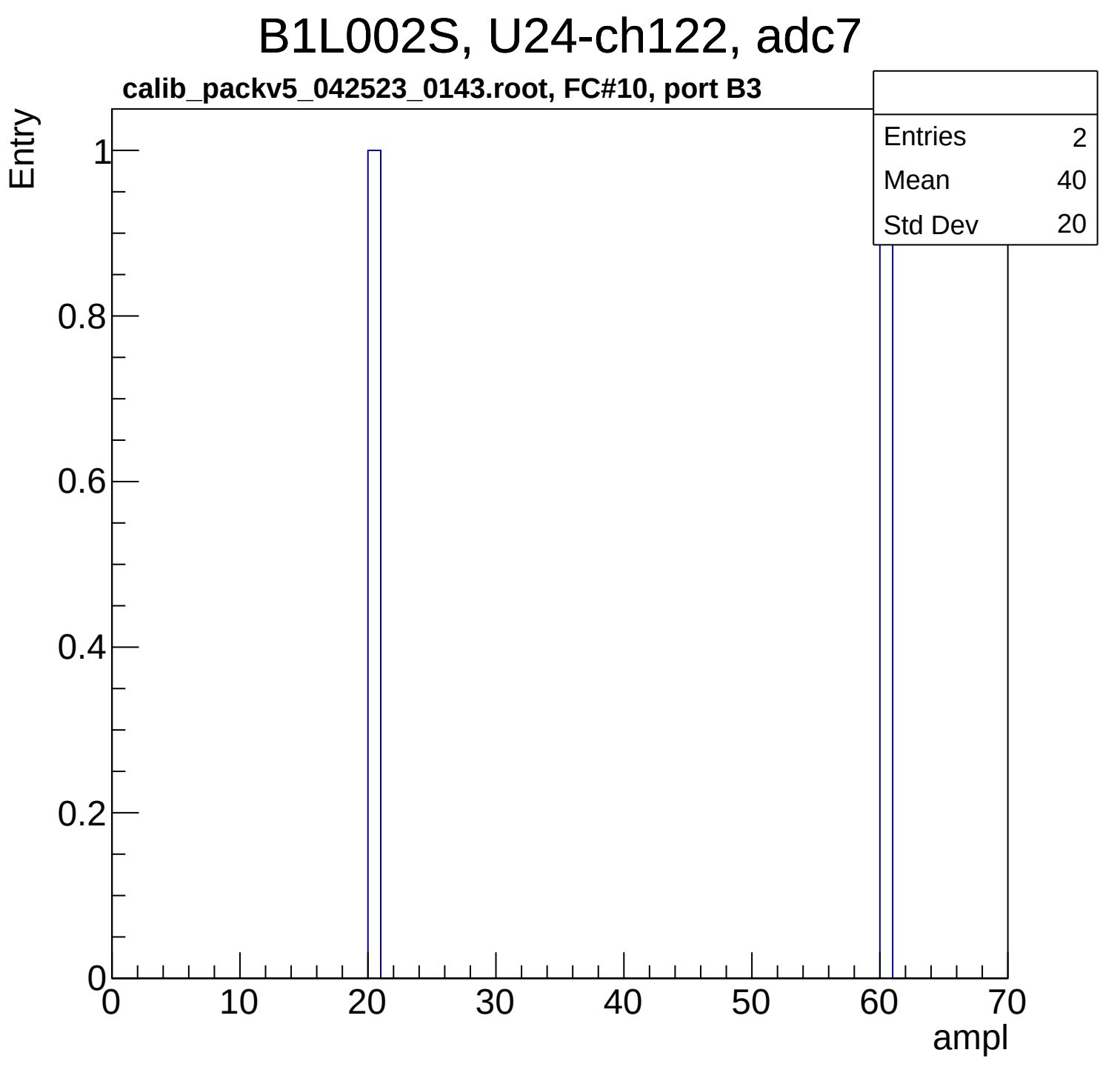
50
60
70
ampl


B1L002S, U24-ch122, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 2
Mean 40
Std Dev 20
Entry
ampl
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0.4
0.6
0.8
1
0
10
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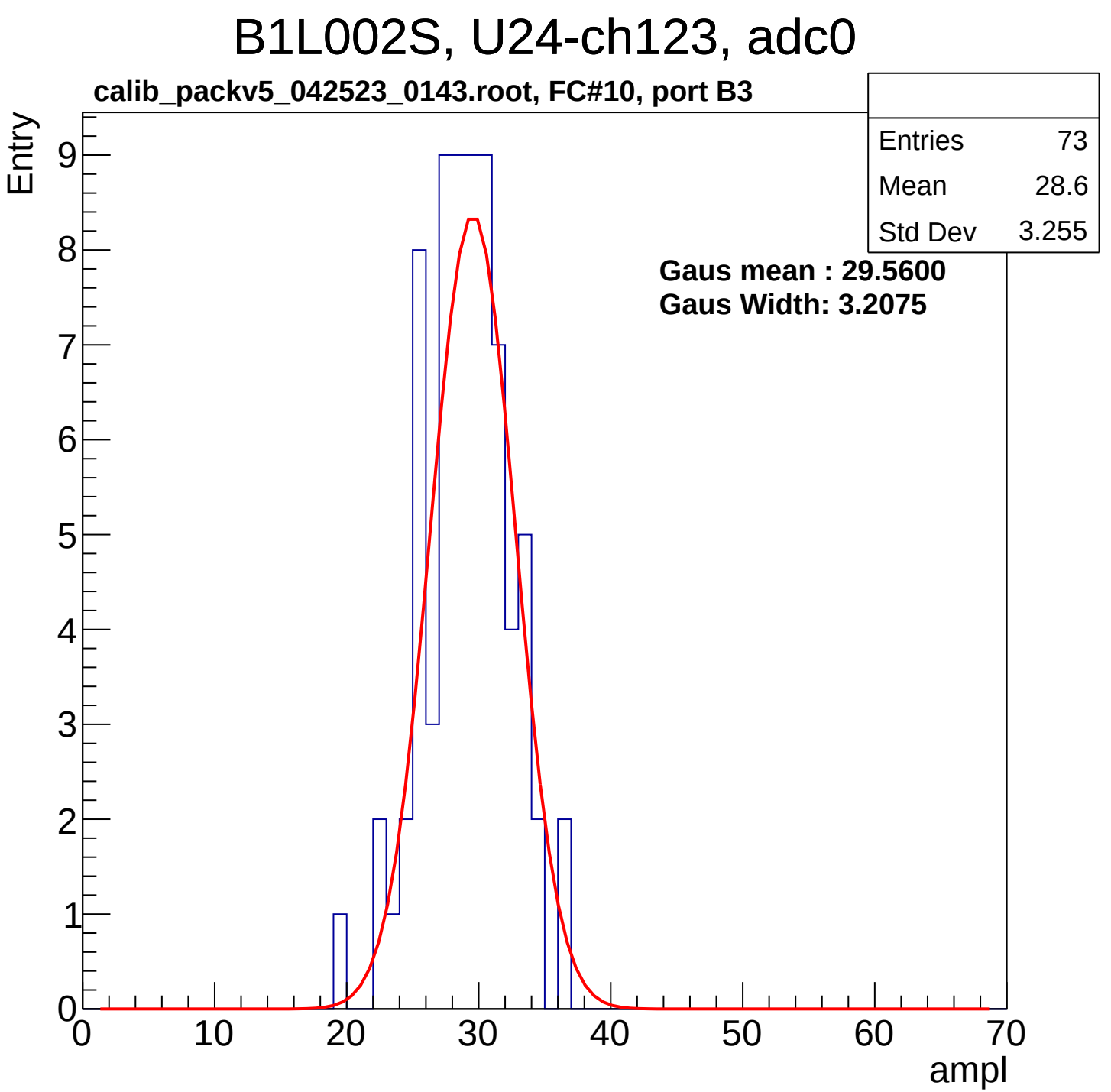
30
40
50
60
70


B1L002S, U24-ch123, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 73
Mean 28.6
Std Dev 3.255
Gaus mean : 29.5600
Gaus Width: 3.2075
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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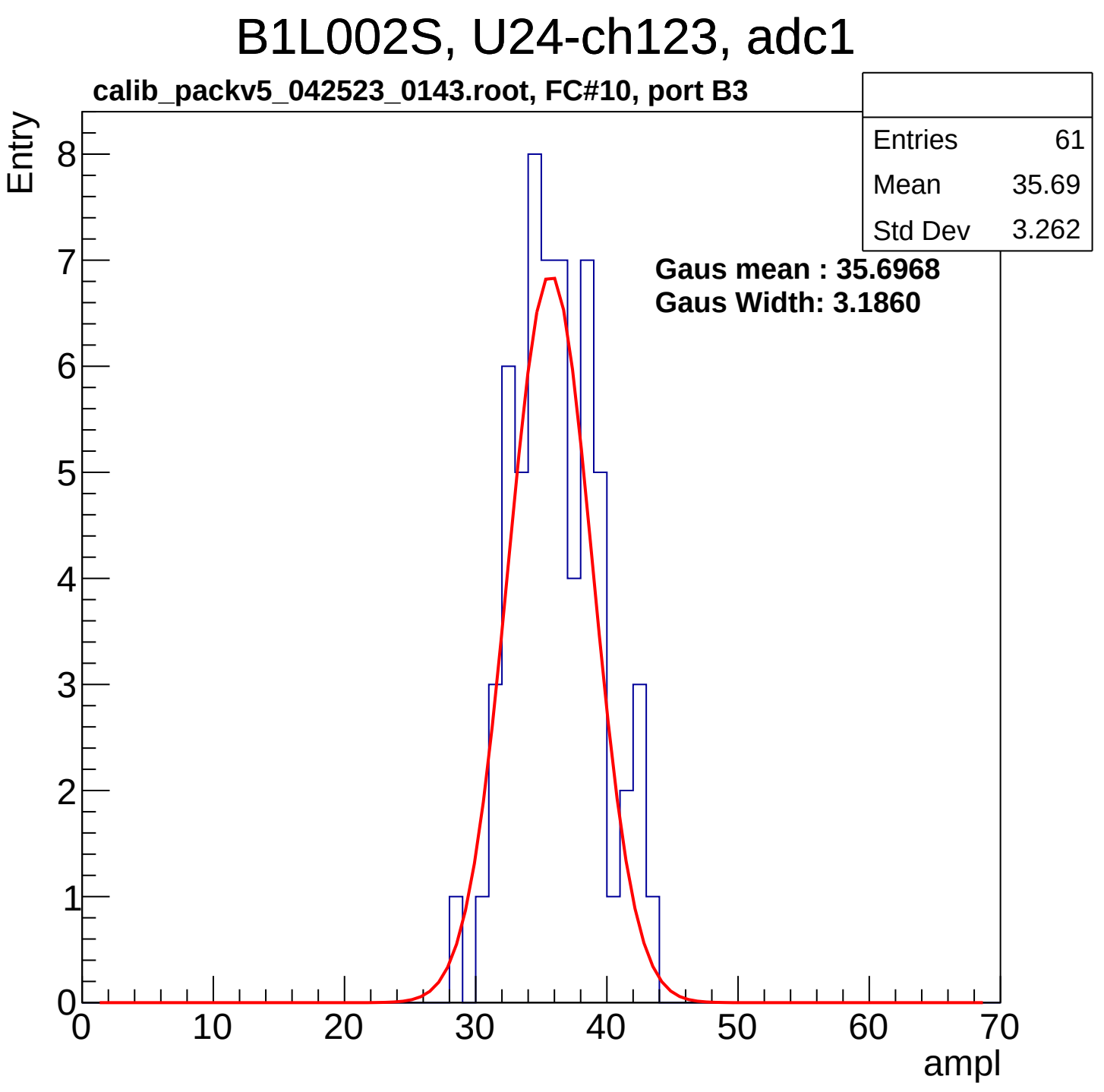
B1L002S, U24-ch123, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 61
Mean 35.69
Std Dev 3.262
Gaus mean : 35.6968
Gaus Width: 3.1860
Entry
ampl
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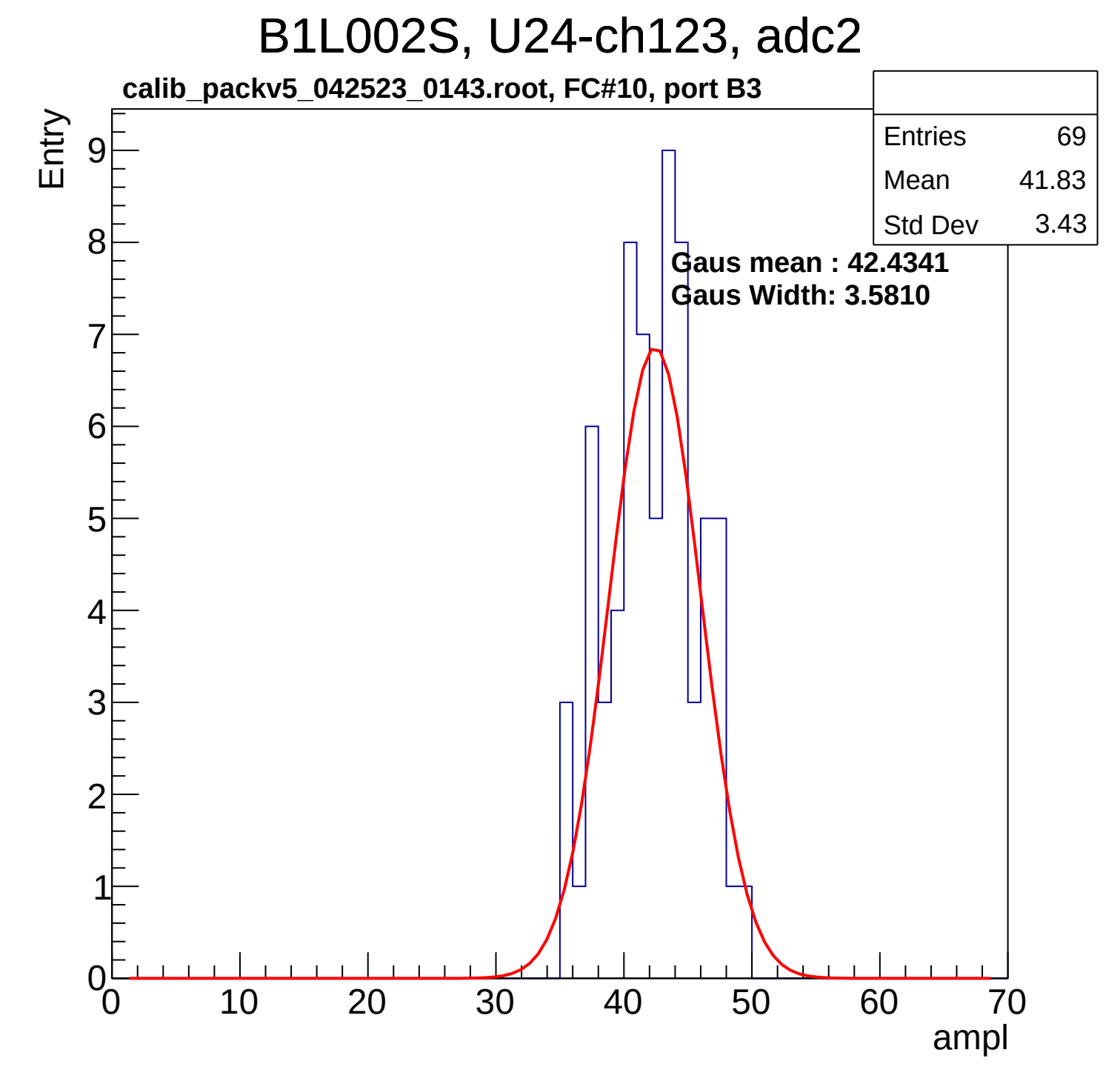
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch123, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 41.83
Std Dev 3.43
Gaus mean : 42.4341
Gaus Width: 3.5810
Entry
ampl
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2
3
4
5
6
7
8
9
0
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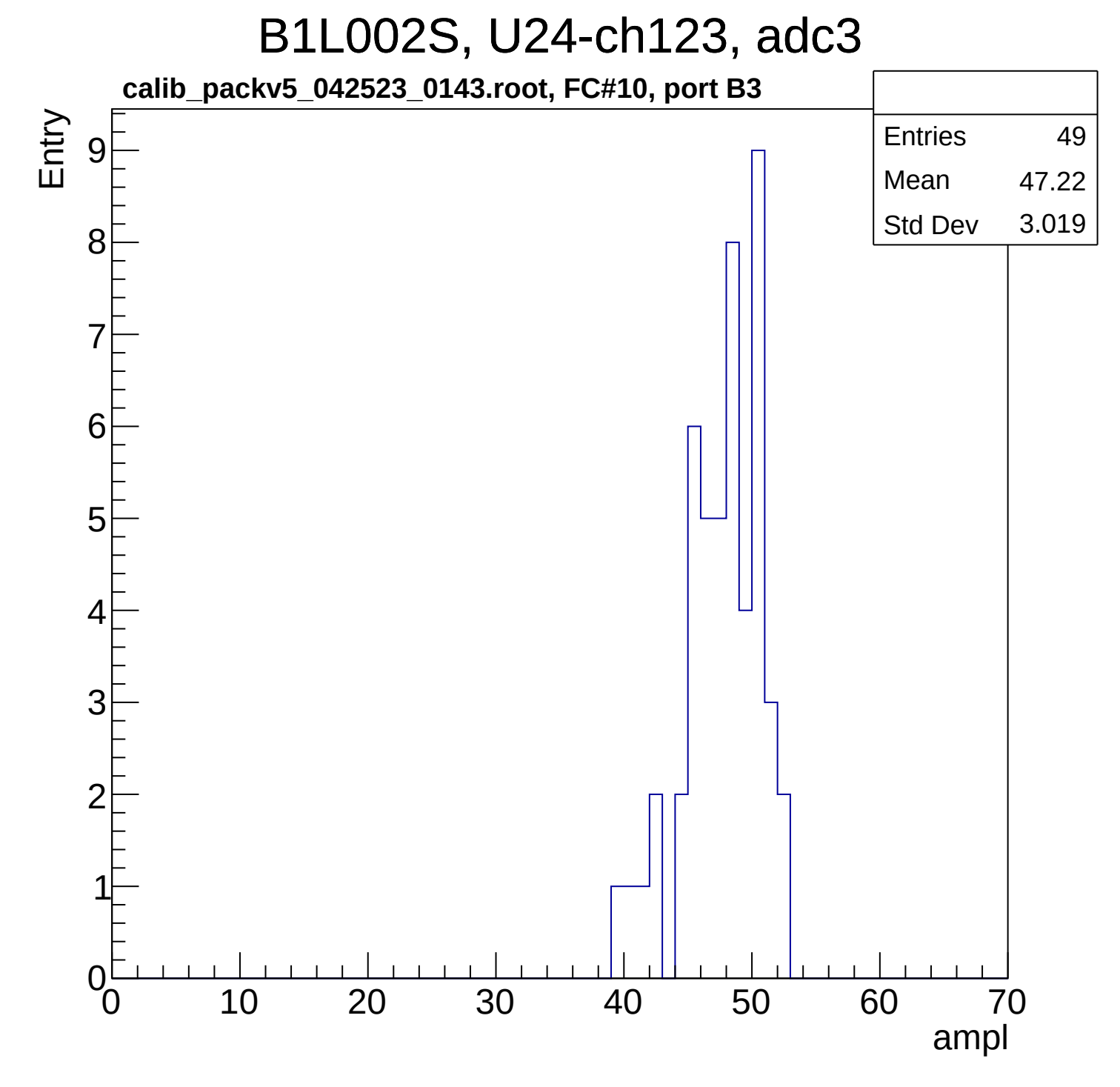
B1L002S, U24-ch123, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 49
Mean 47.22
Std Dev 3.019
Entry
ampl
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3
4
5
6
7
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0
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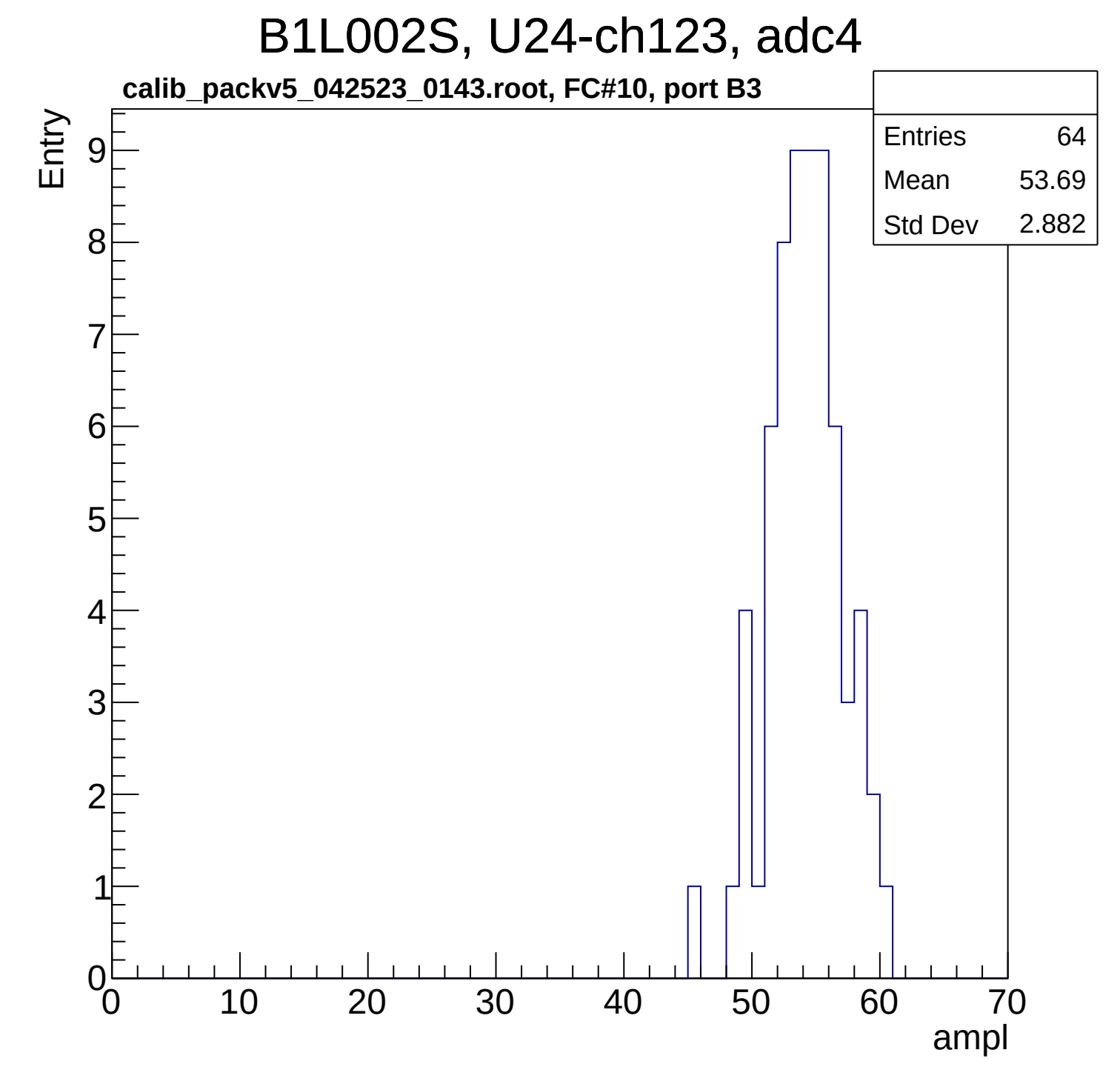
B1L002S, U24-ch123, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 64
Mean 53.69
Std Dev 2.882
Entry
ampl
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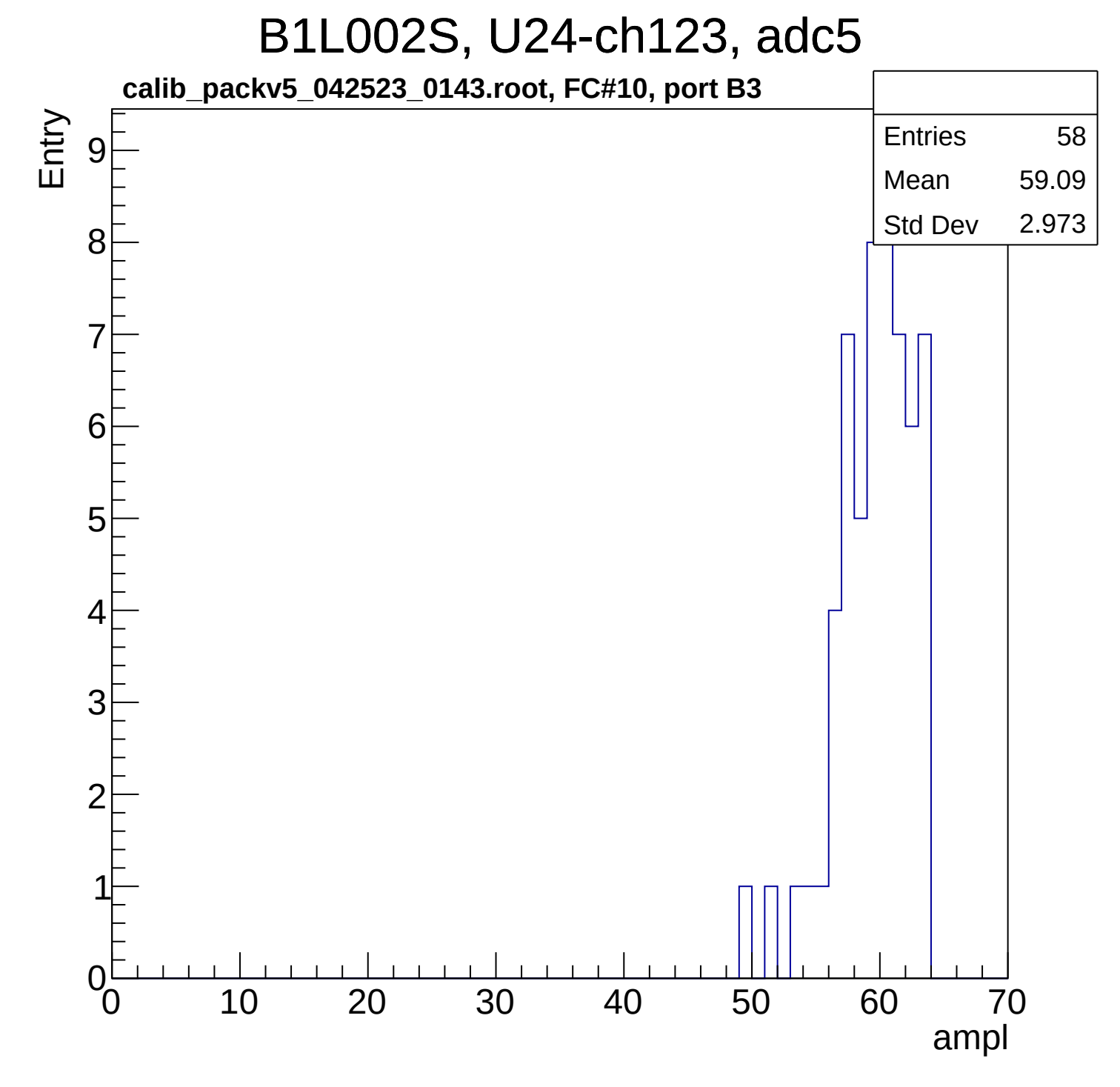
B1L002S, U24-ch123, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 58
Mean 59.09
Std Dev 2.973
Entry
ampl
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3
4
5
6
7
8
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0
10
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60
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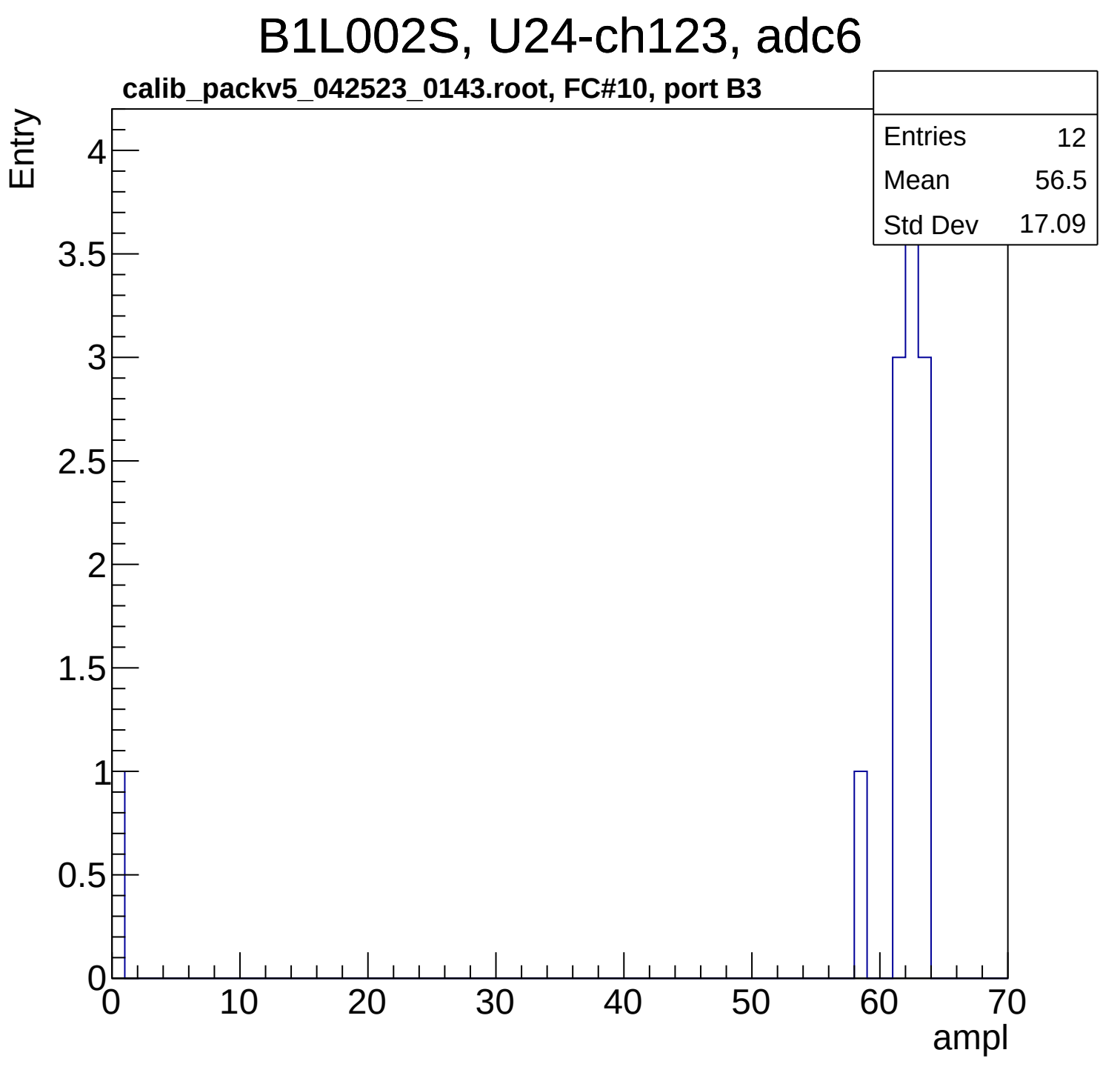
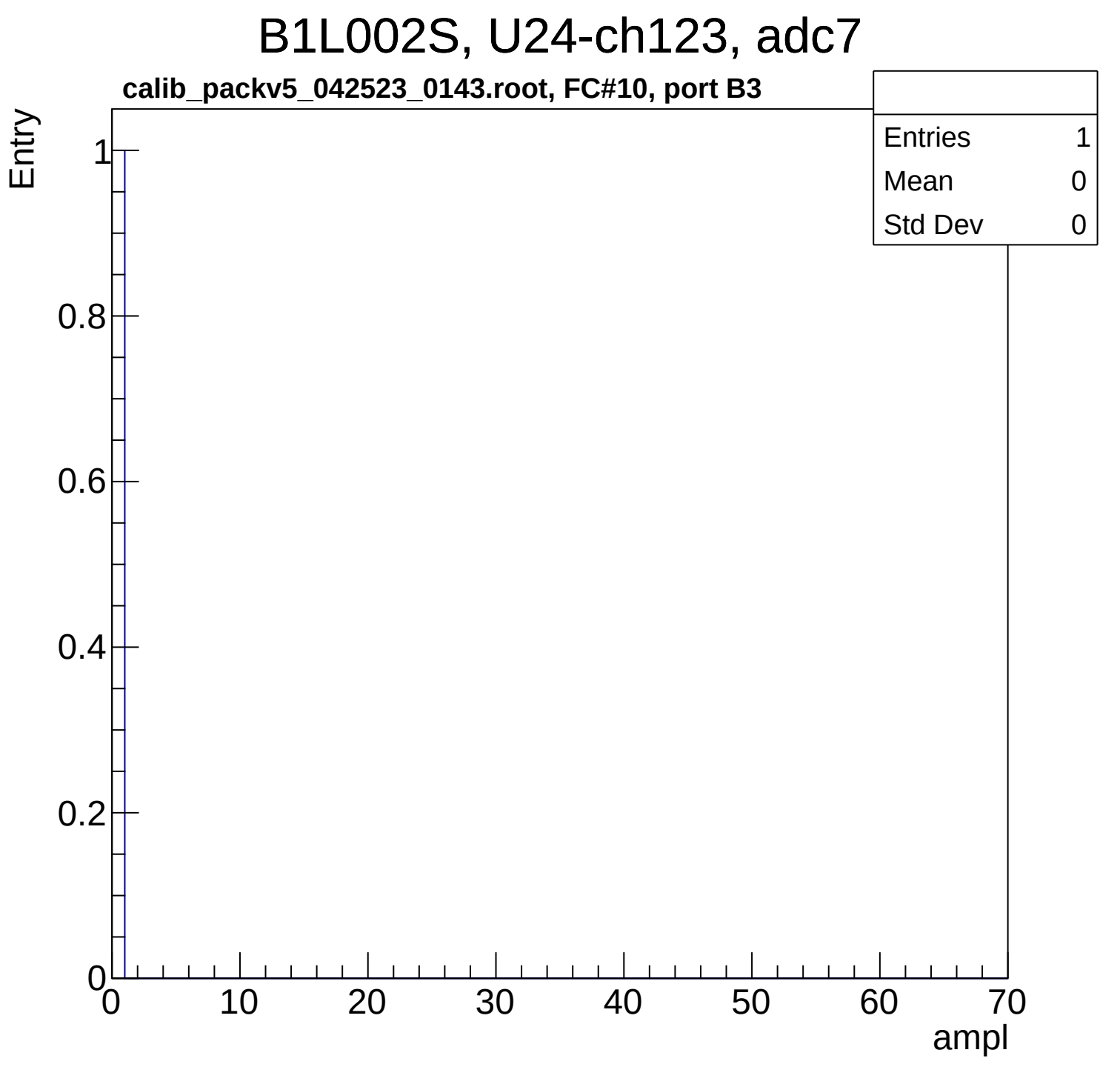

B1L002S, U24-ch123, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 12
Mean 56.5
Std Dev 17.09
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L002S, U24-ch123, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
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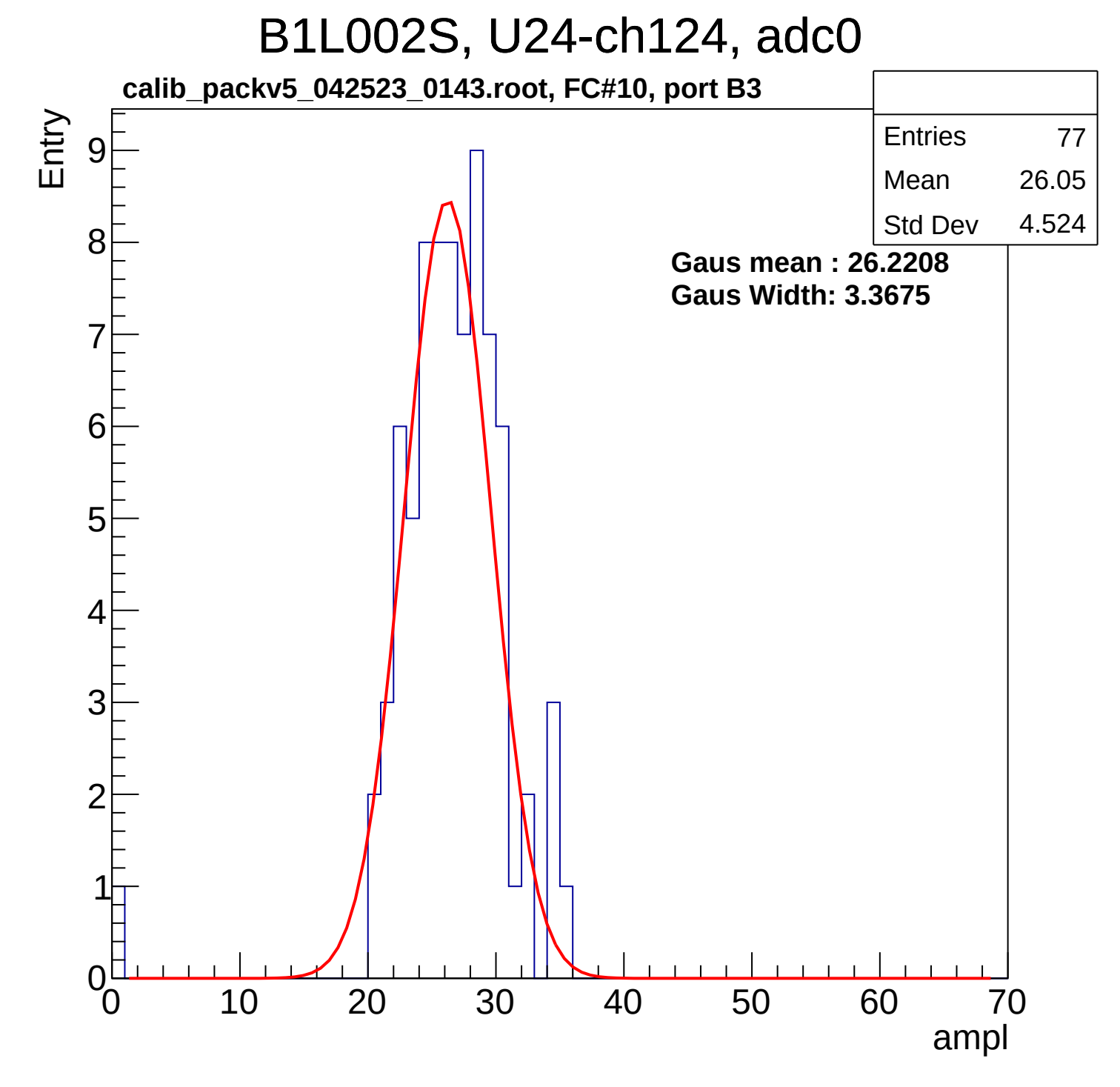
50
60
70
ampl


B1L002S, U24-ch124, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 26.05
Std Dev 4.524
Gaus mean : 26.2208
Gaus Width: 3.3675
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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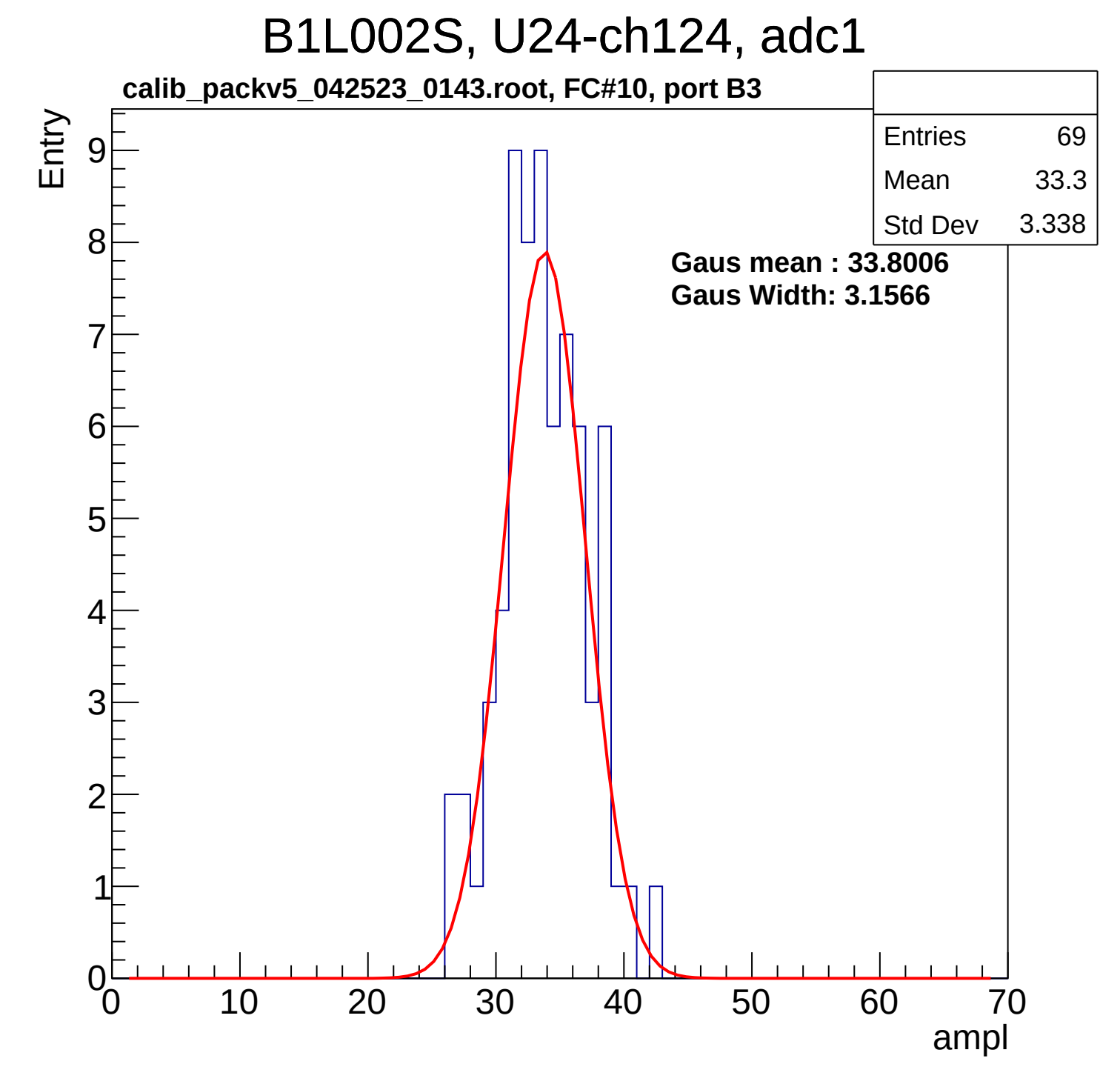
B1L002S, U24-ch124, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 33.3
Std Dev 3.338
Gaus mean : 33.8006
Gaus Width: 3.1566
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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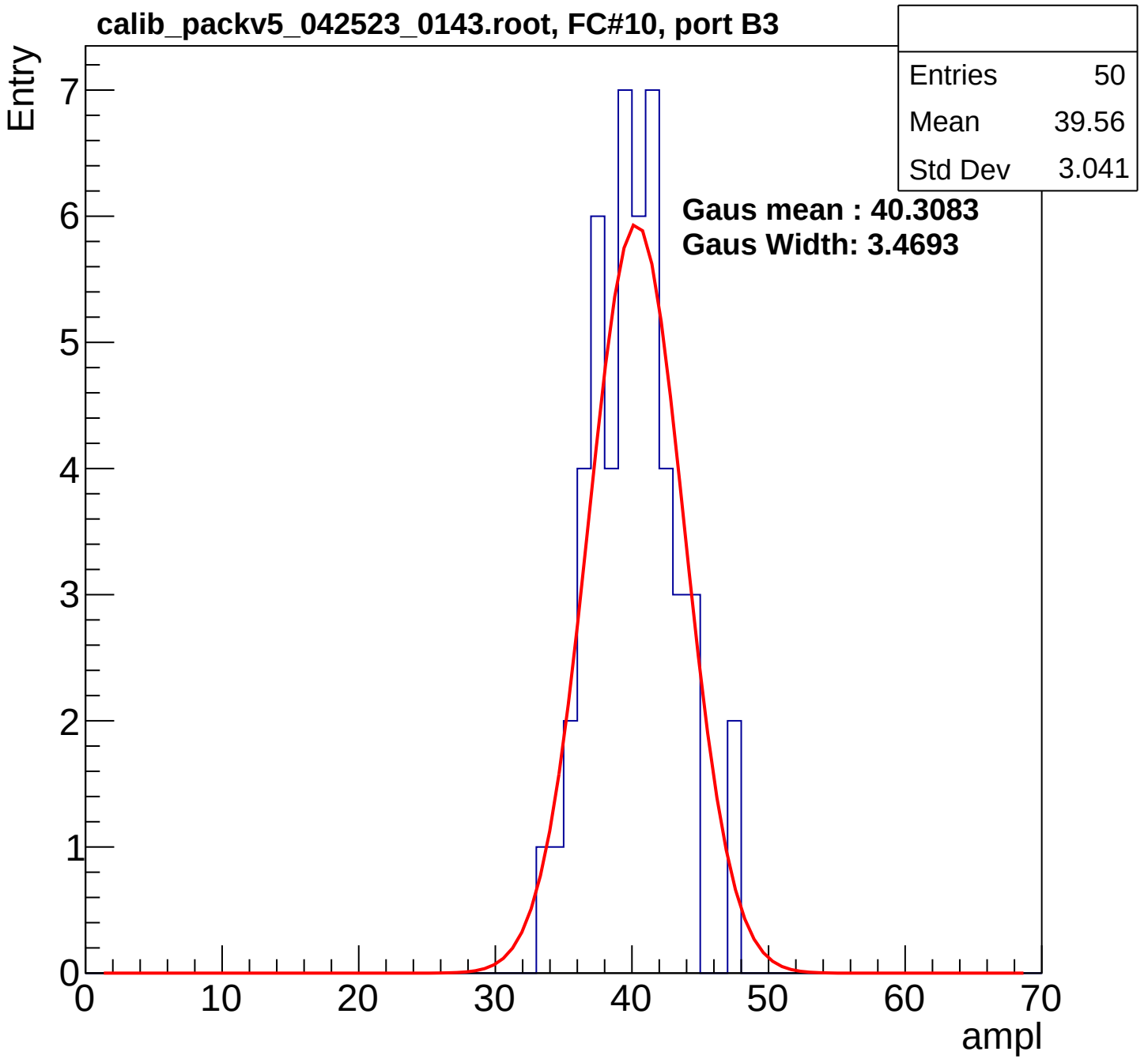
B1L002S, U24-ch124, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 50
Mean 39.56
Std Dev 3.041
Gaus mean : 40.3083
Gaus Width: 3.4693
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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60
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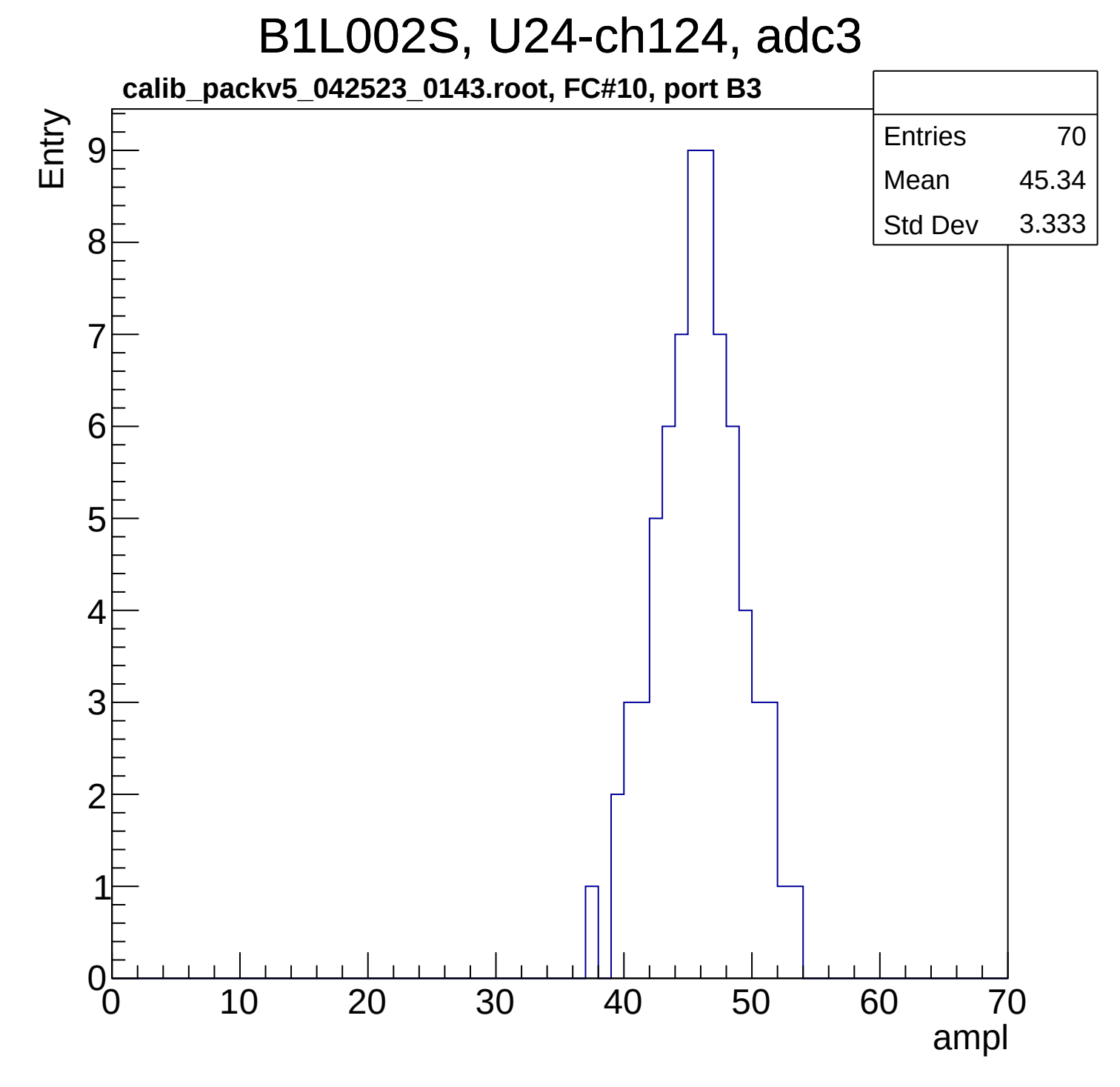
B1L002S, U24-ch124, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 45.34
Std Dev 3.333
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
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60
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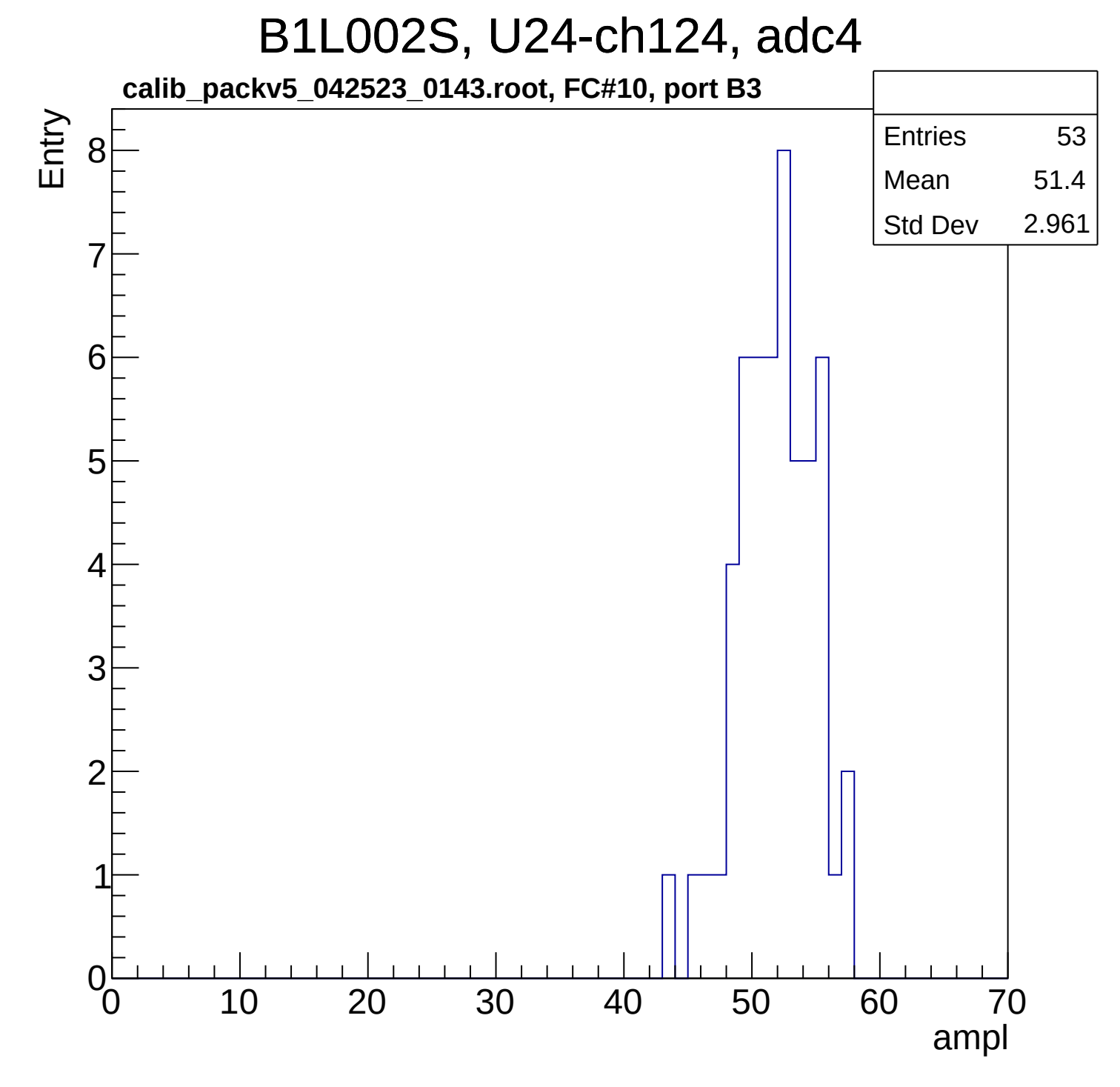
B1L002S, U24-ch124, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 53
Mean 51.4
Std Dev 2.961
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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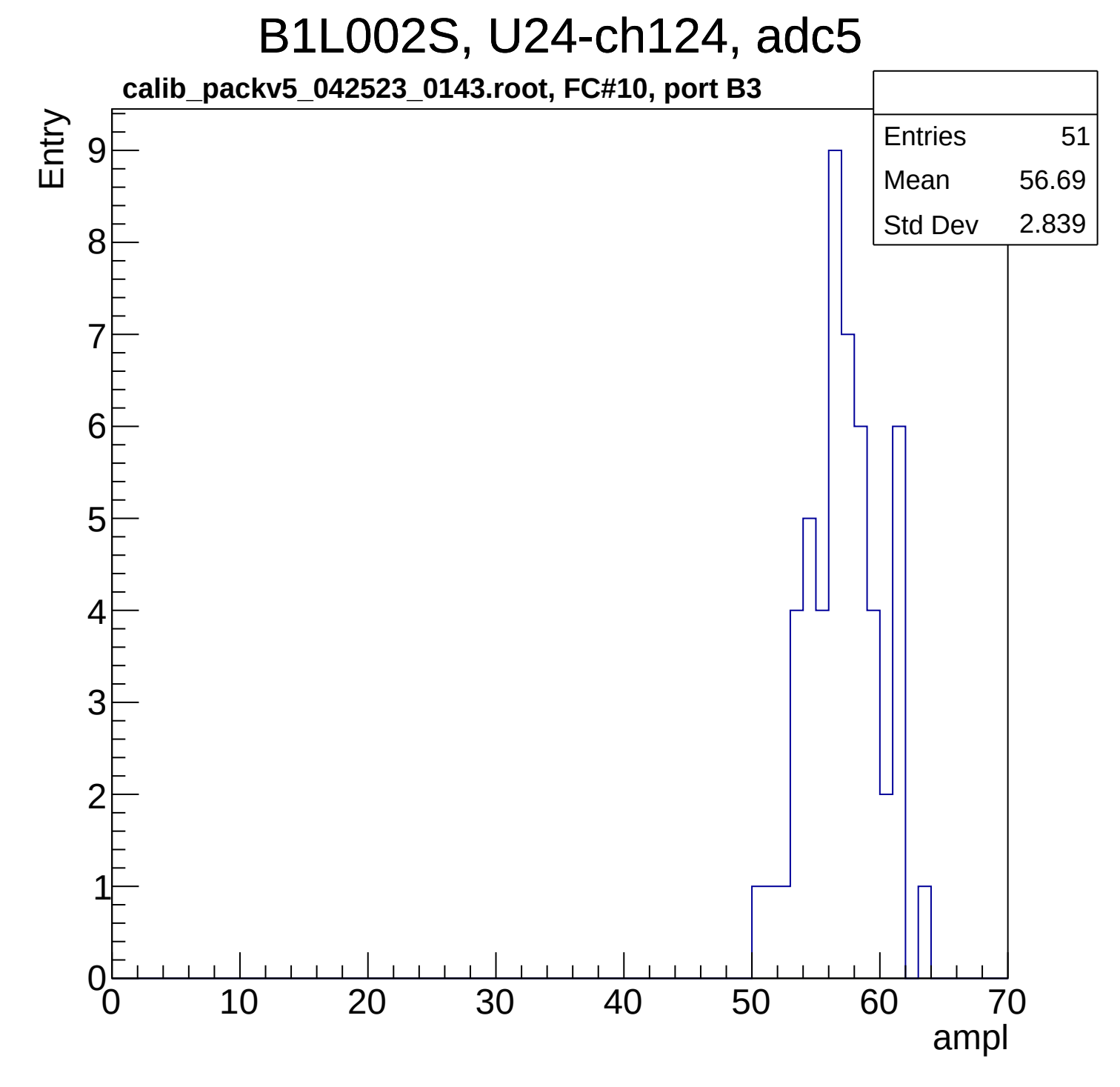
B1L002S, U24-ch124, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 56.69
Std Dev 2.839
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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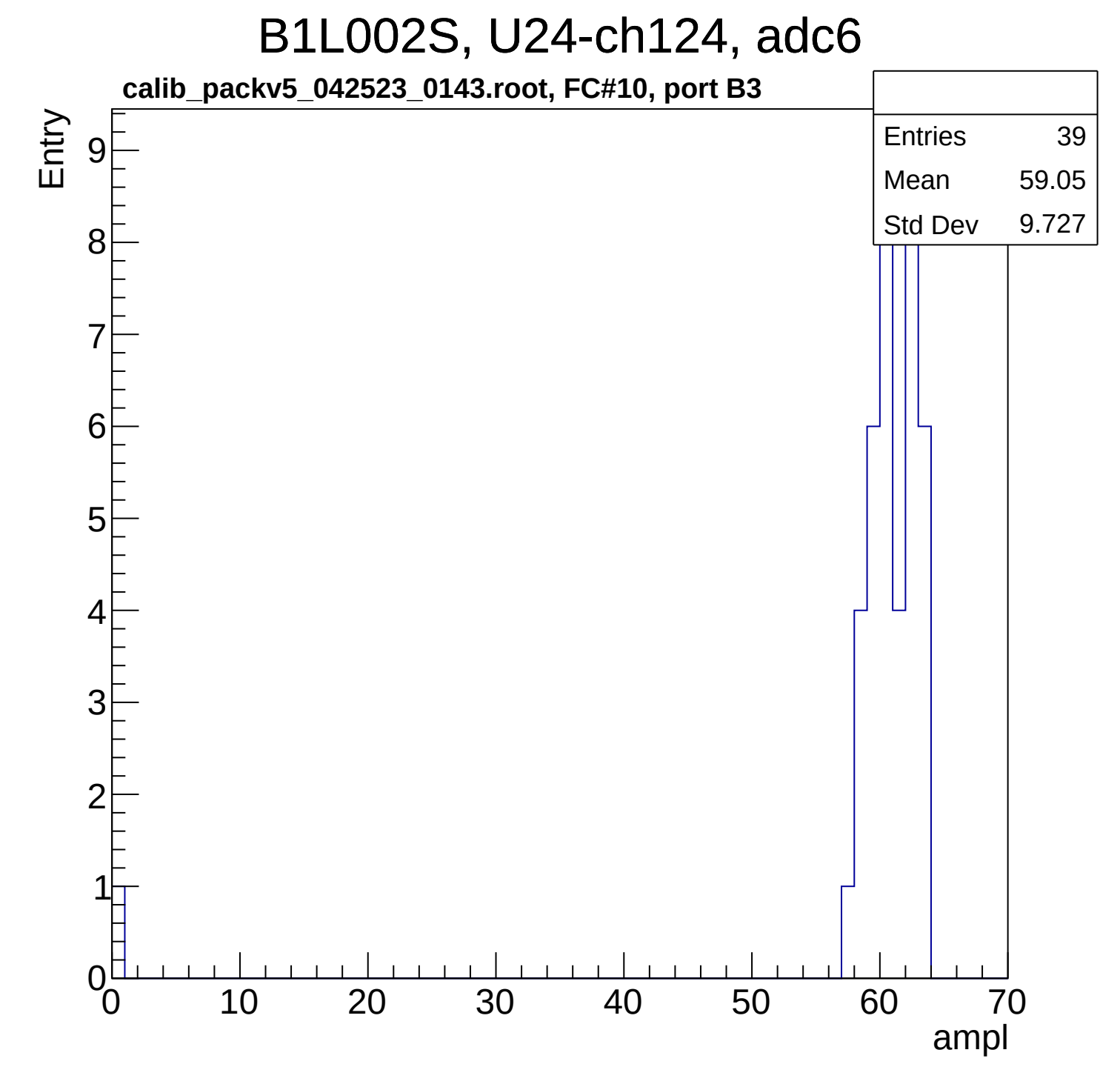
B1L002S, U24-ch124, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 39
Mean 59.05
Std Dev 9.727
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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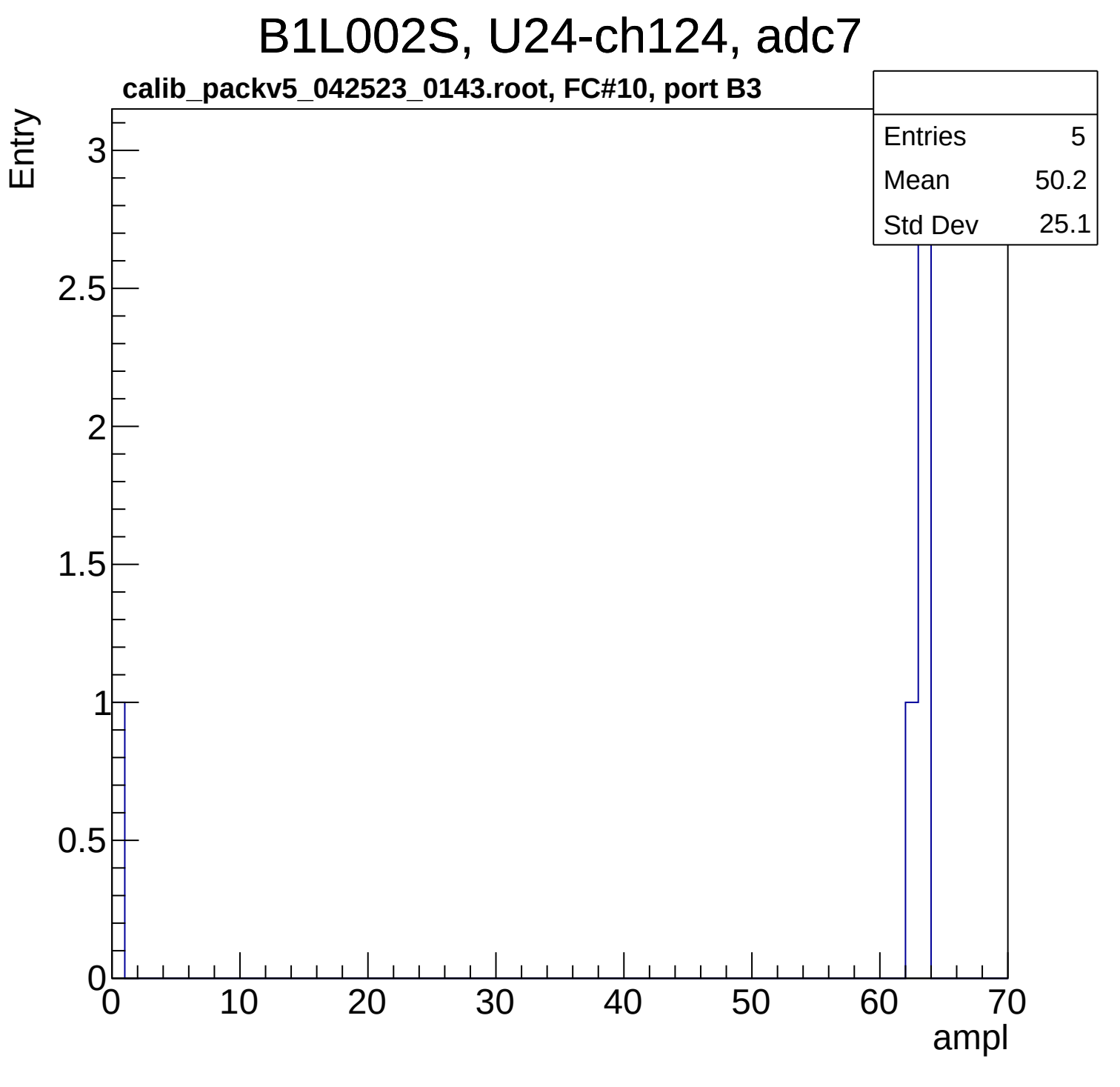
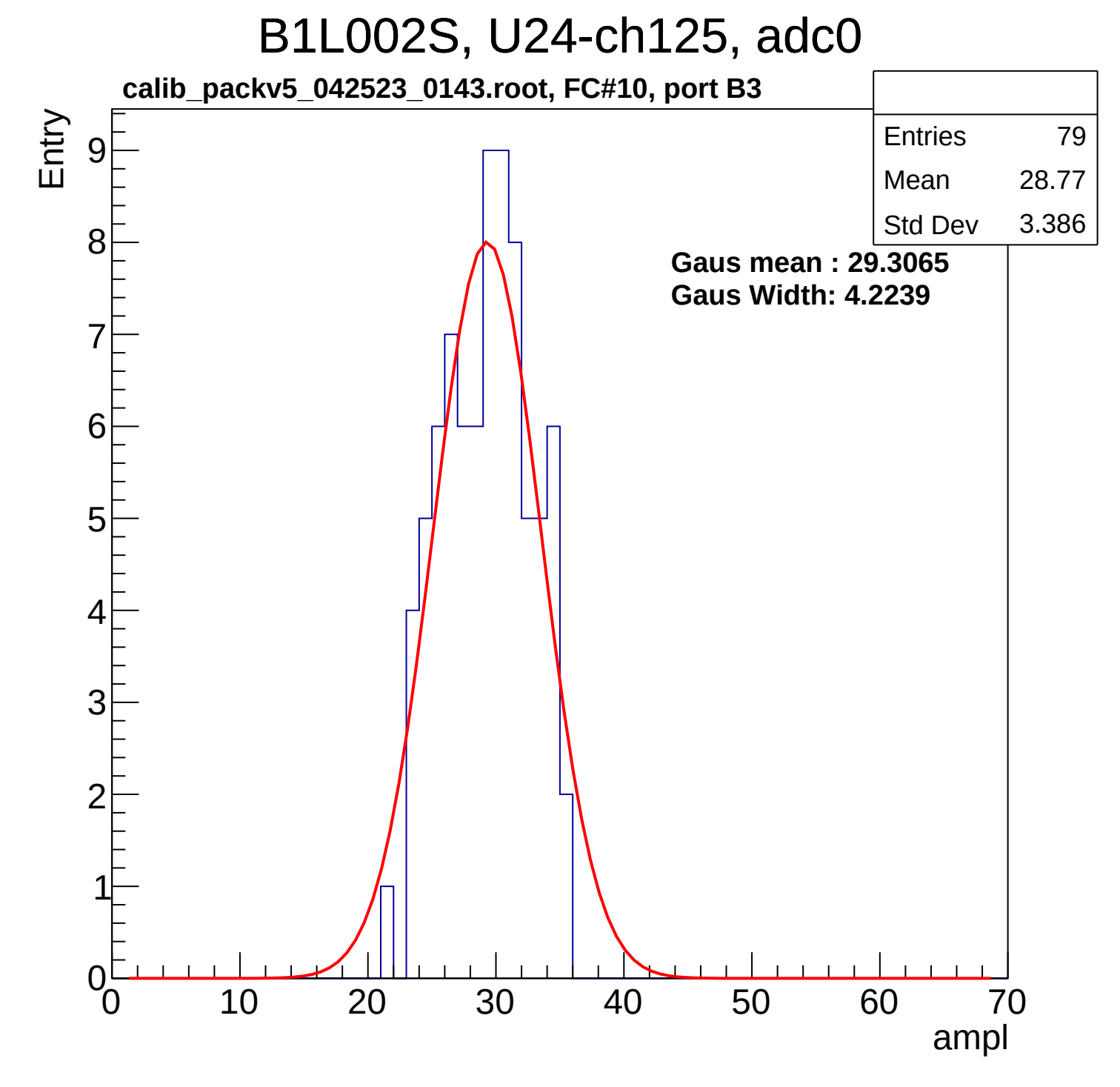
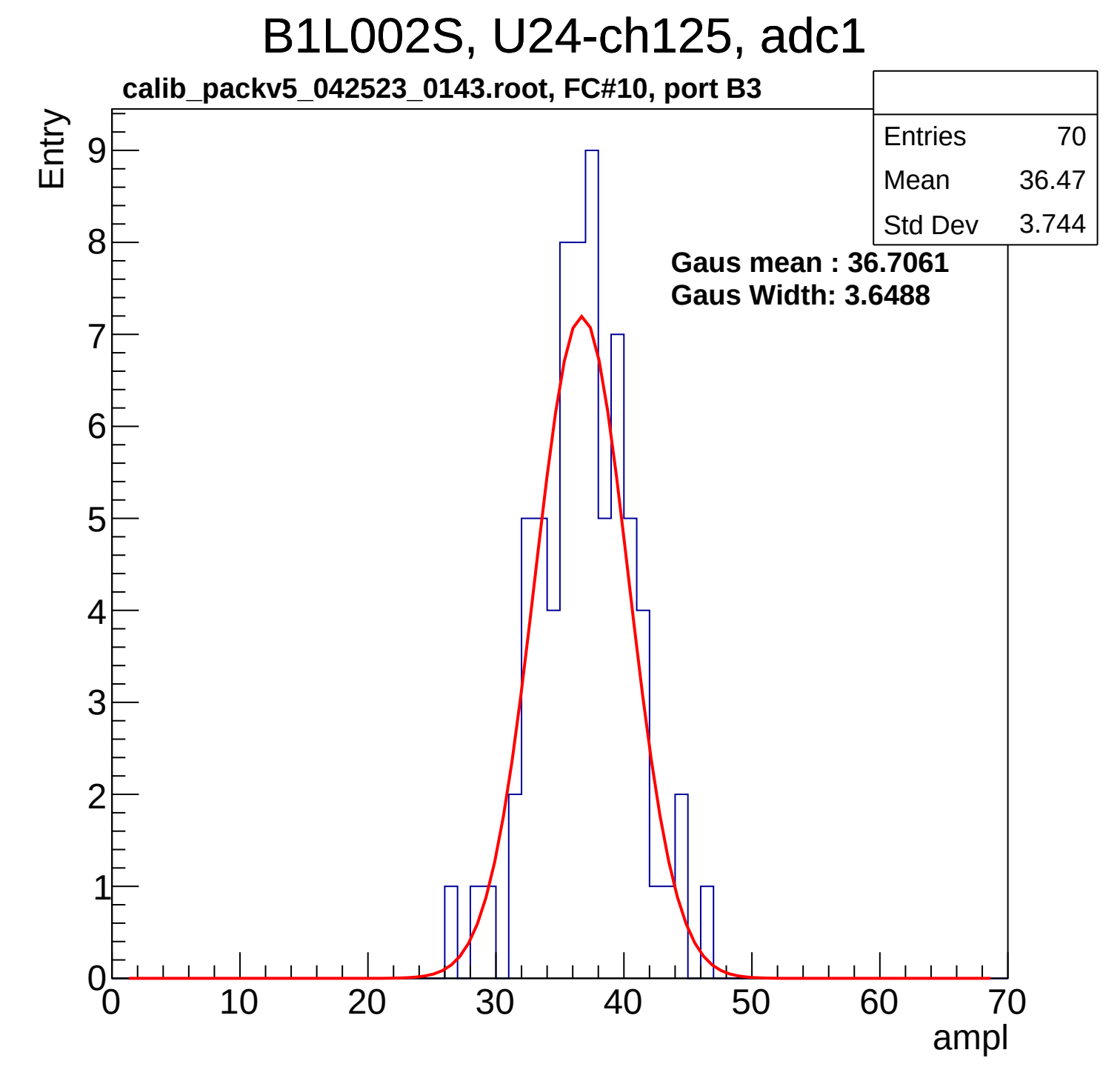

B1L002S, U24-ch124, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 5
Mean 50.2
Std Dev 25.1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L002S, U24-ch125, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 79
Mean 28.77
Std Dev 3.386
Gaus mean : 29.3065
Gaus Width: 4.2239
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L002S, U24-ch125, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 36.47
Std Dev 3.744
Gaus mean : 36.7061
Gaus Width: 3.6488
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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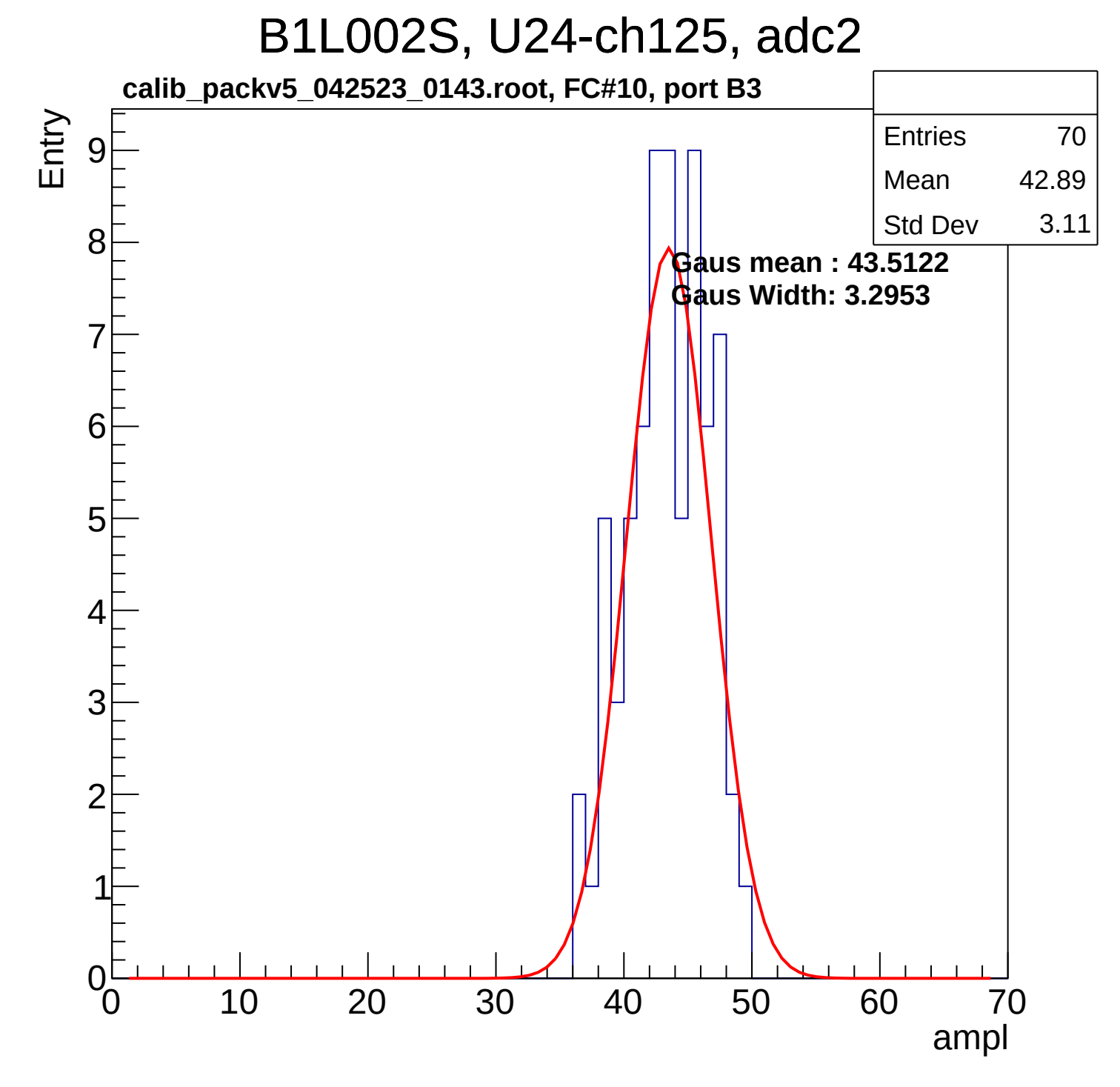
B1L002S, U24-ch125, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 70
Mean 42.89
Std Dev 3.11
Gaus mean : 43.5122
Gaus Width: 3.2953
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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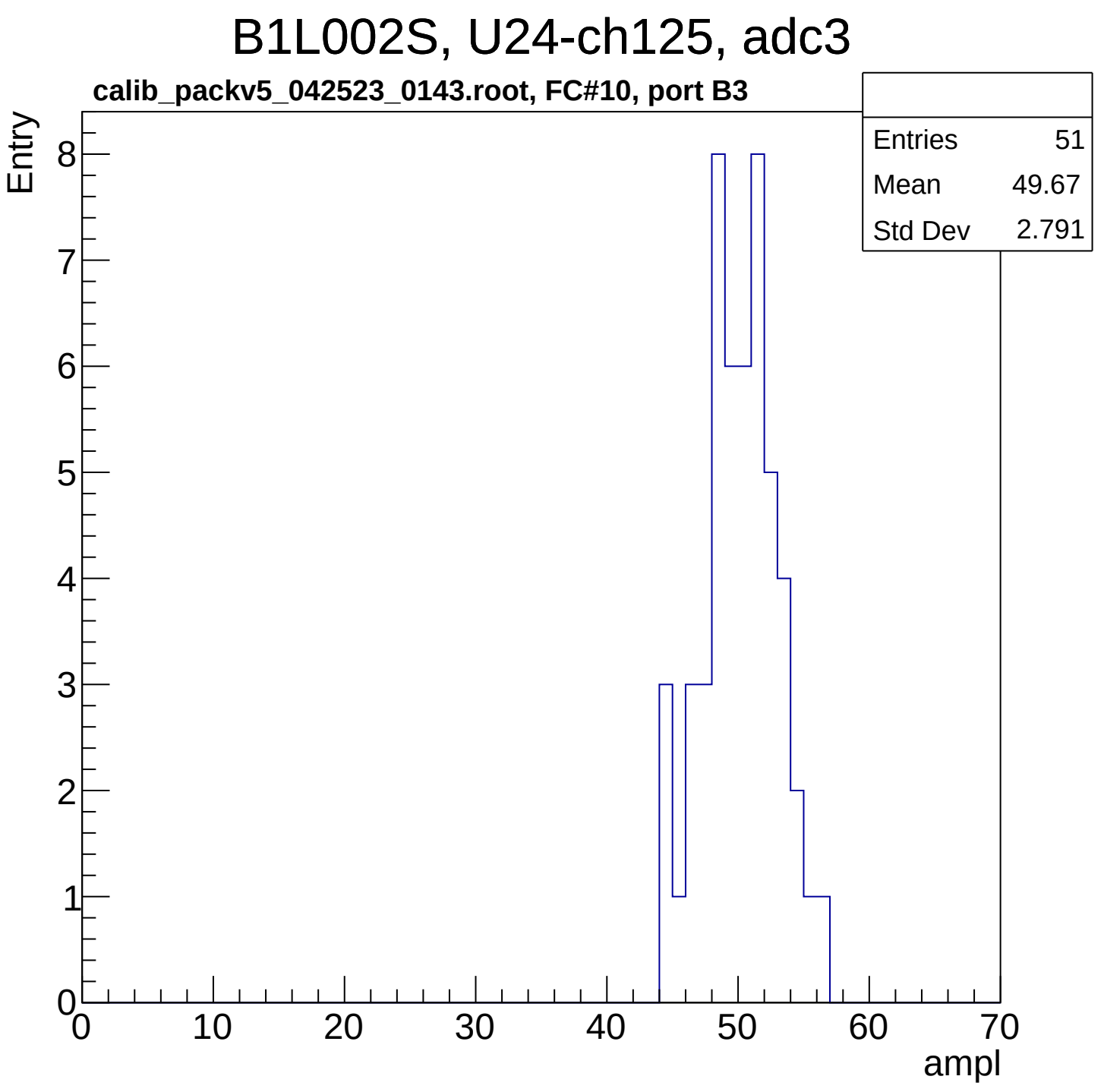
B1L002S, U24-ch125, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 51
Mean 49.67
Std Dev 2.791
Entry
ampl
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2
3
4
5
6
7
8
0
10
20
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40
50
60
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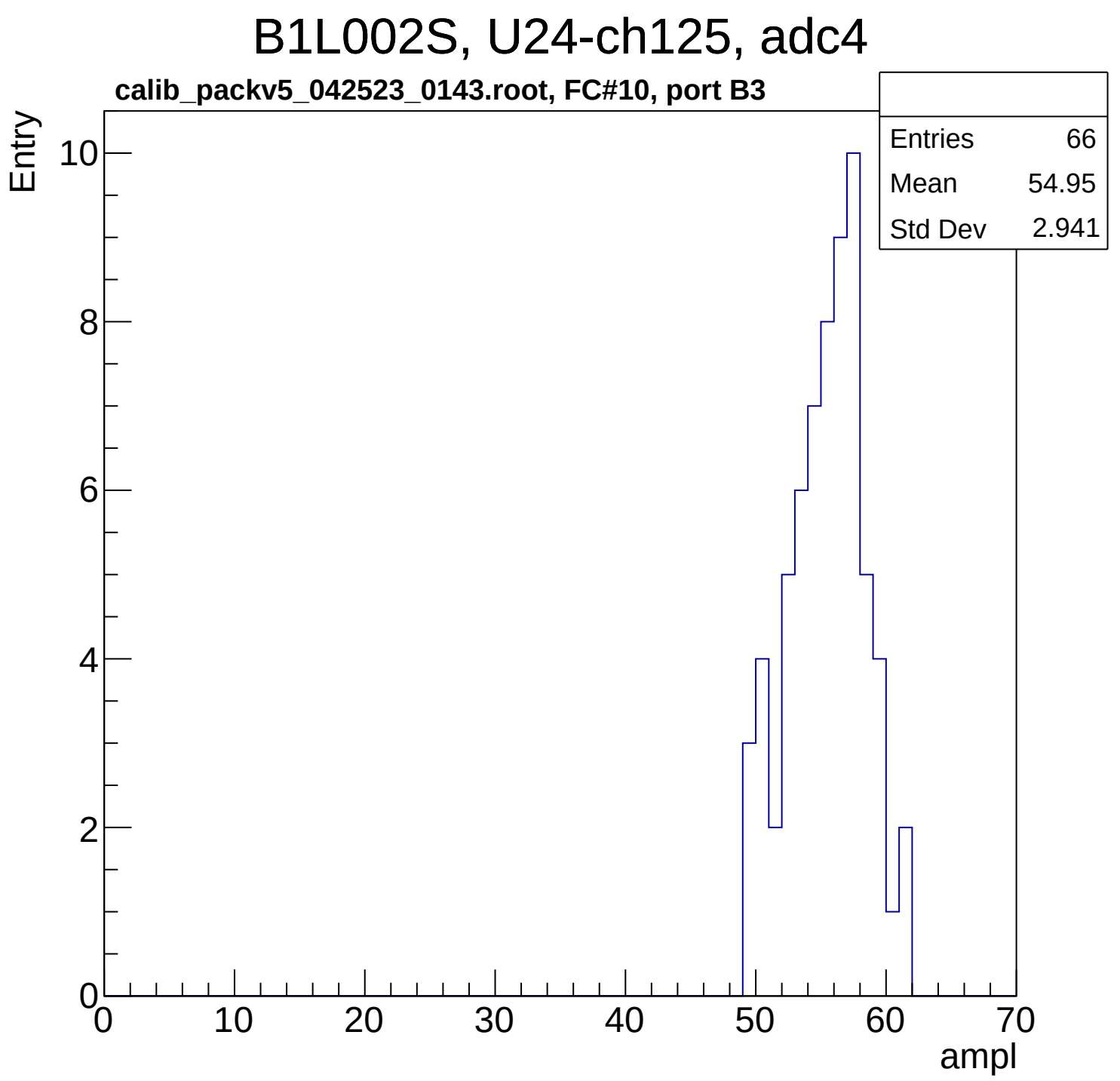
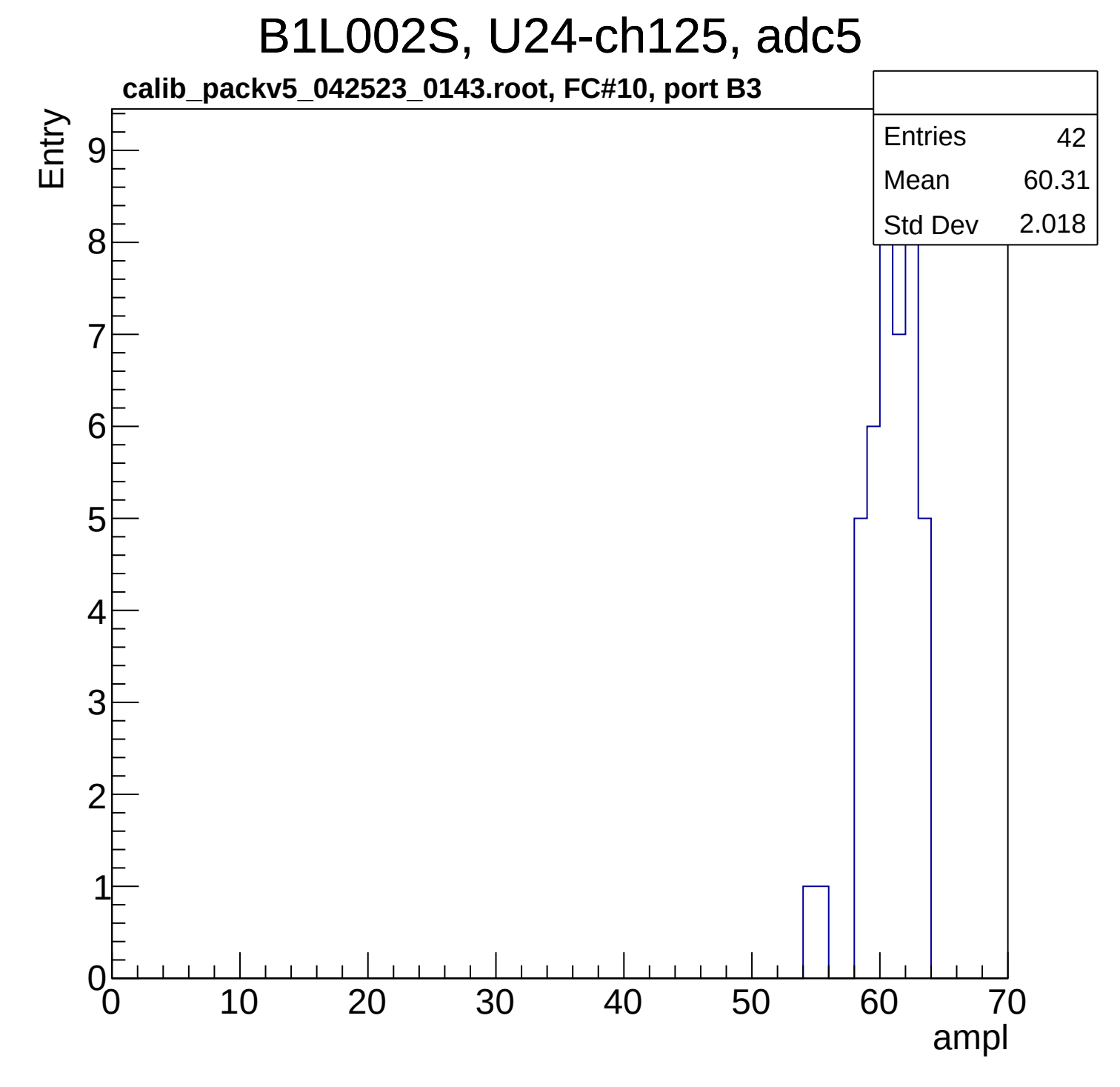

B1L002S, U24-ch125, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 66
Mean 54.95
Std Dev 2.941
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch125, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 42
Mean 60.31
Std Dev 2.018
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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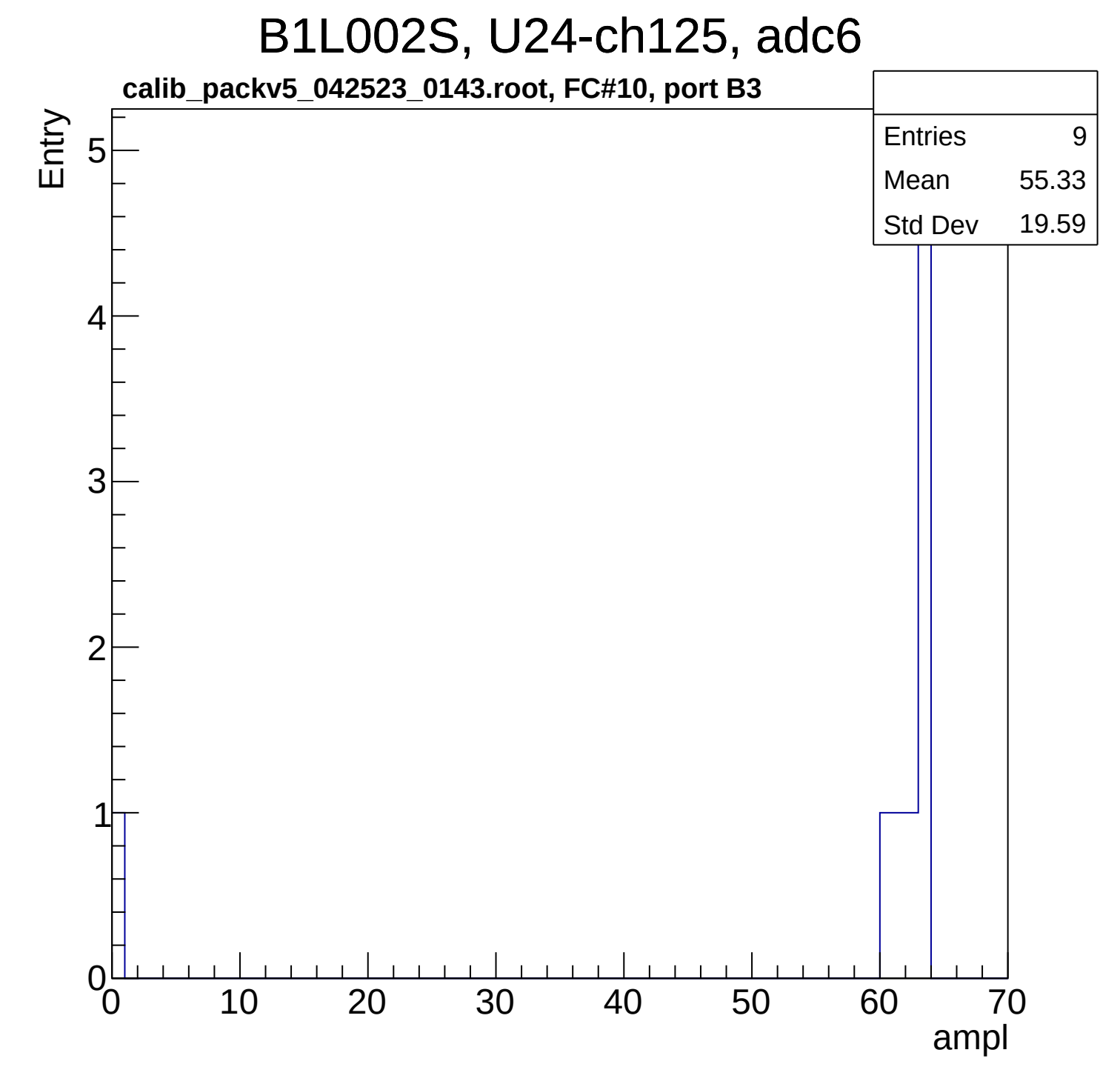
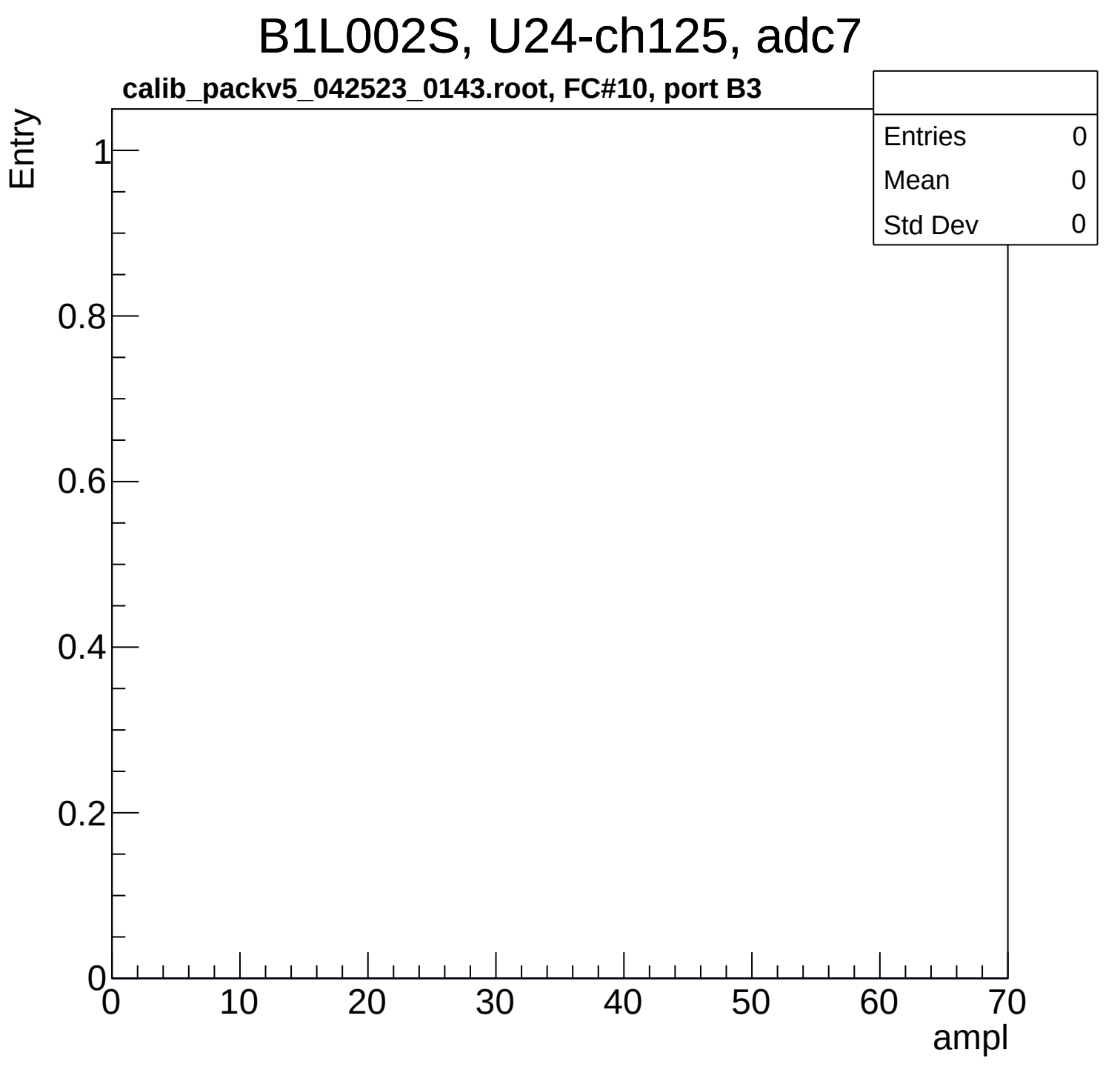
40
50
60
70


B1L002S, U24-ch125, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 9
Mean 55.33
Std Dev 19.59
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch125, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 0
Mean 0
Std Dev 0
Entry
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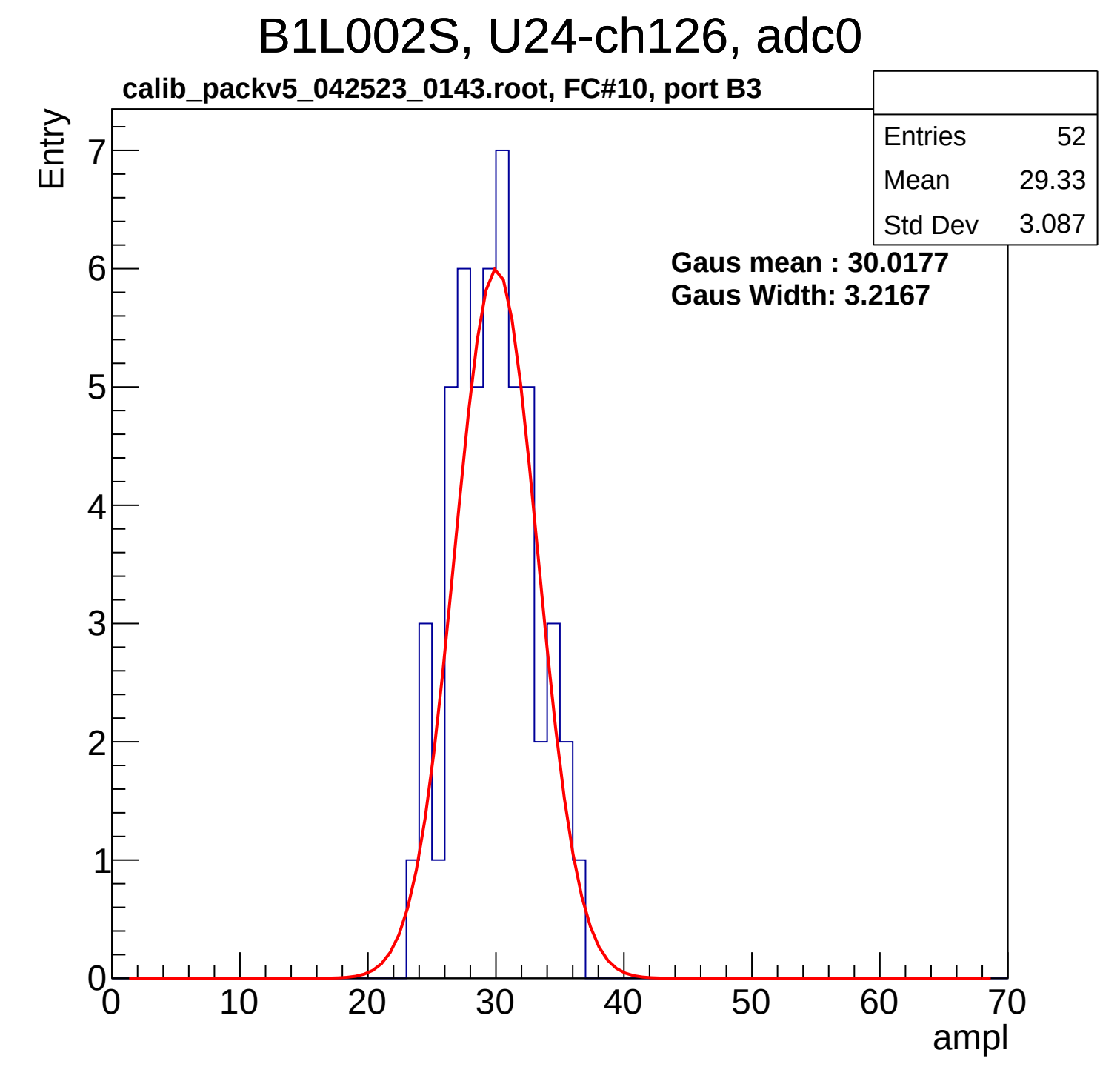
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch126, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 52
Mean 29.33
Std Dev 3.087
Gaus mean : 30.0177
Gaus Width: 3.2167
Entry
ampl
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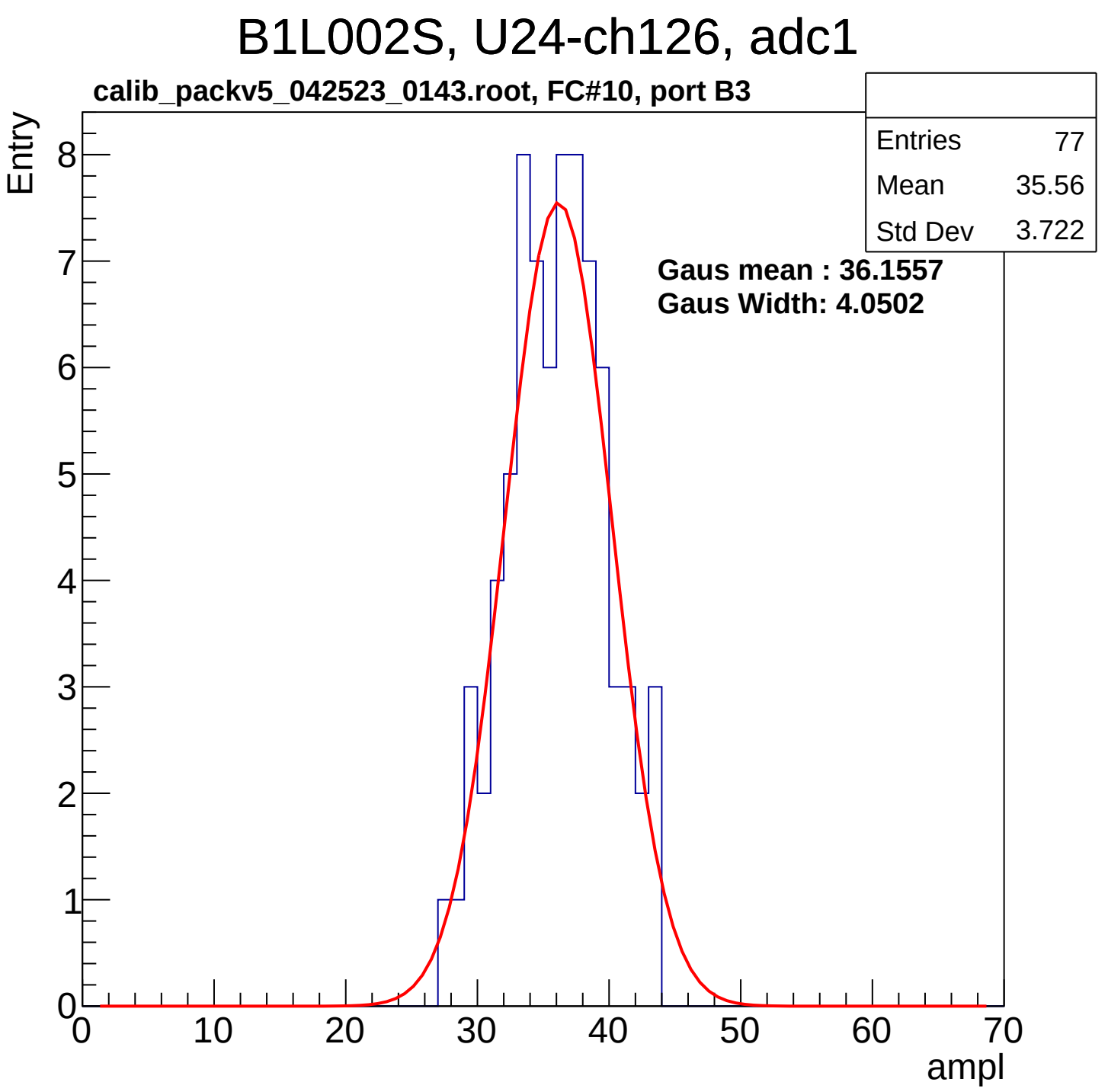
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L002S, U24-ch126, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 77
Mean 35.56
Std Dev 3.722
Gaus mean : 36.1557
Gaus Width: 4.0502
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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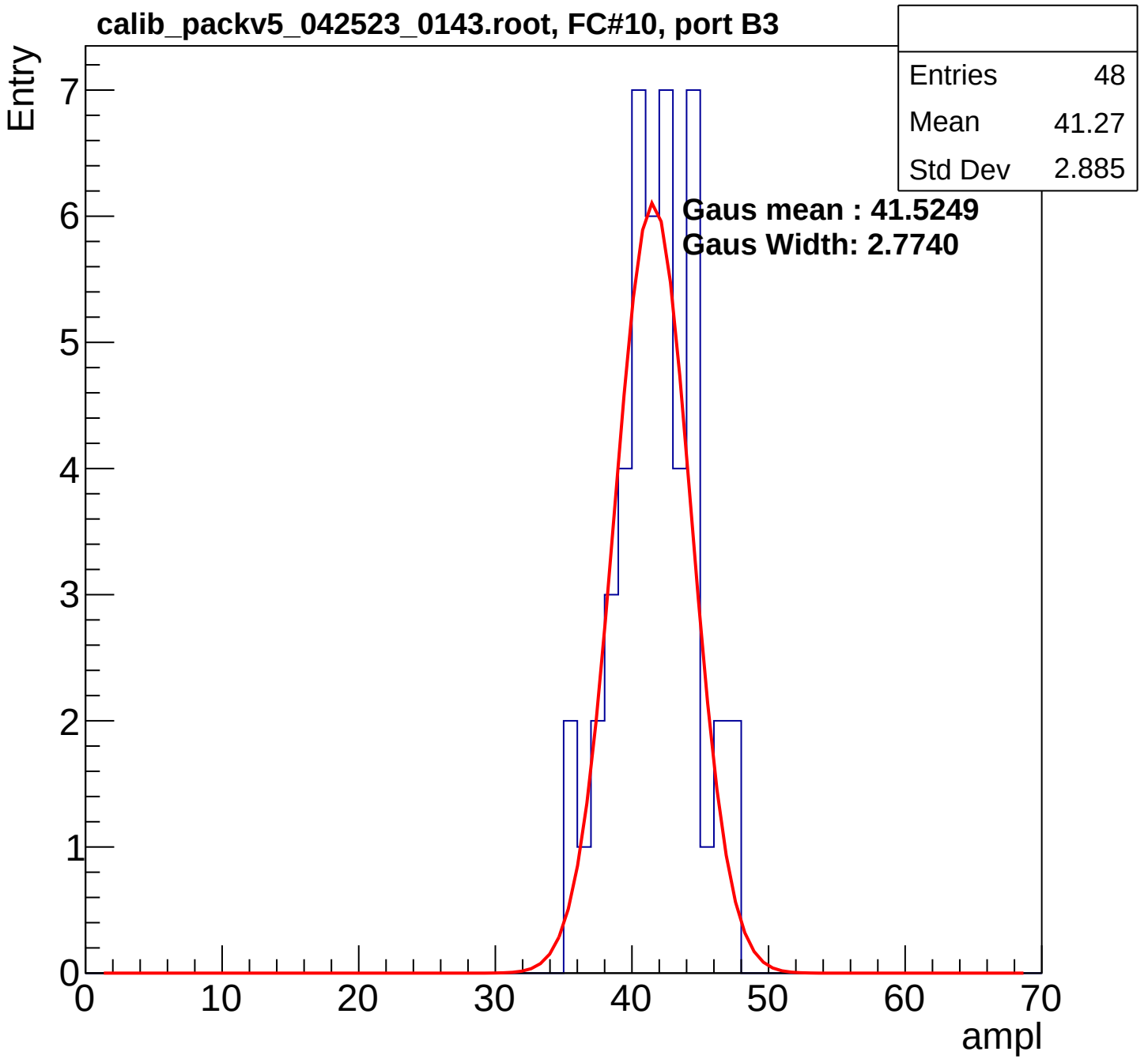
B1L002S, U24-ch126, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 48
Mean 41.27
Std Dev 2.885
Gaus mean : 41.5249
Gaus Width: 2.7740
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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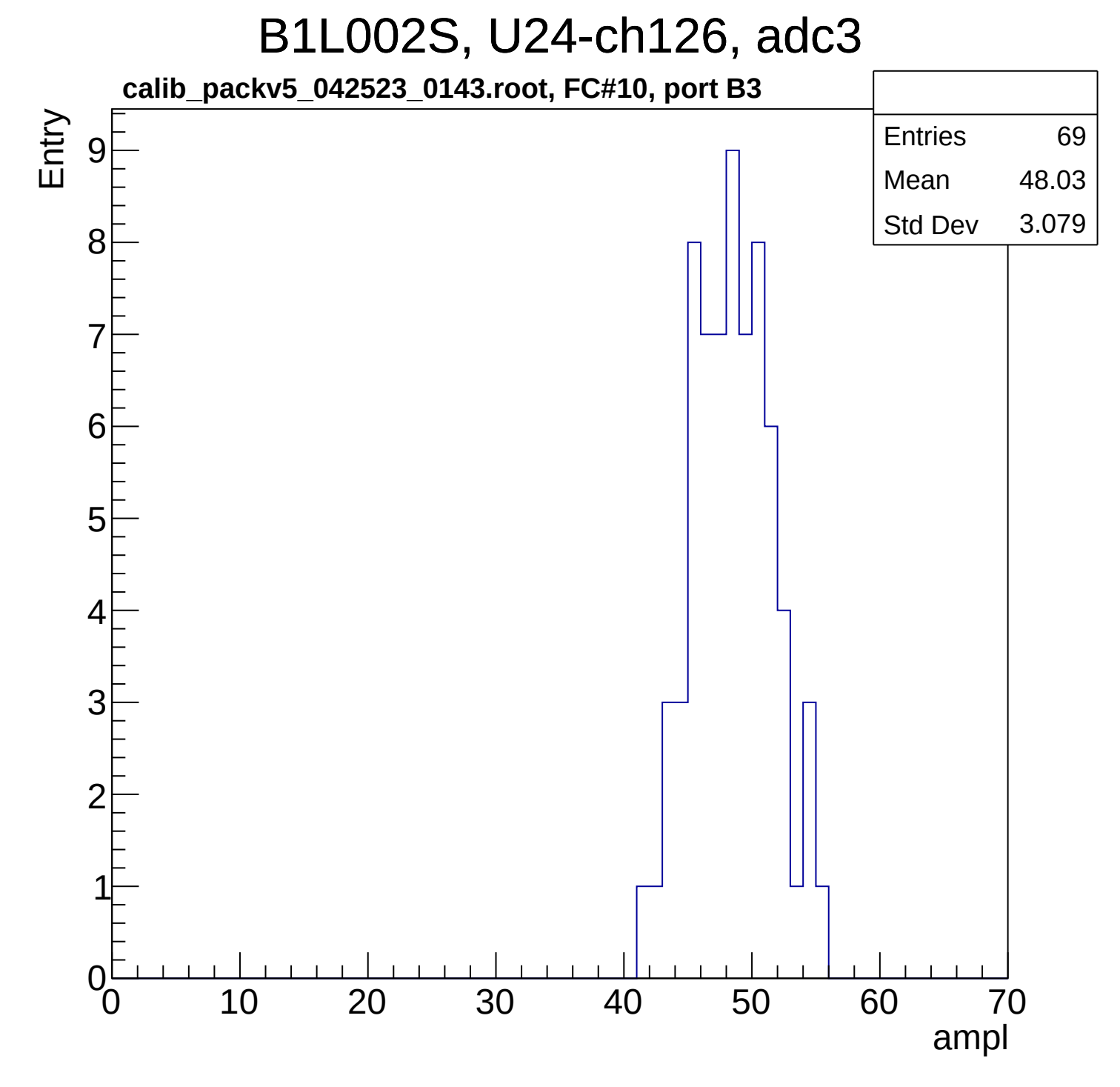
B1L002S, U24-ch126, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 69
Mean 48.03
Std Dev 3.079
Entry
ampl
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3
4
5
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0
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60
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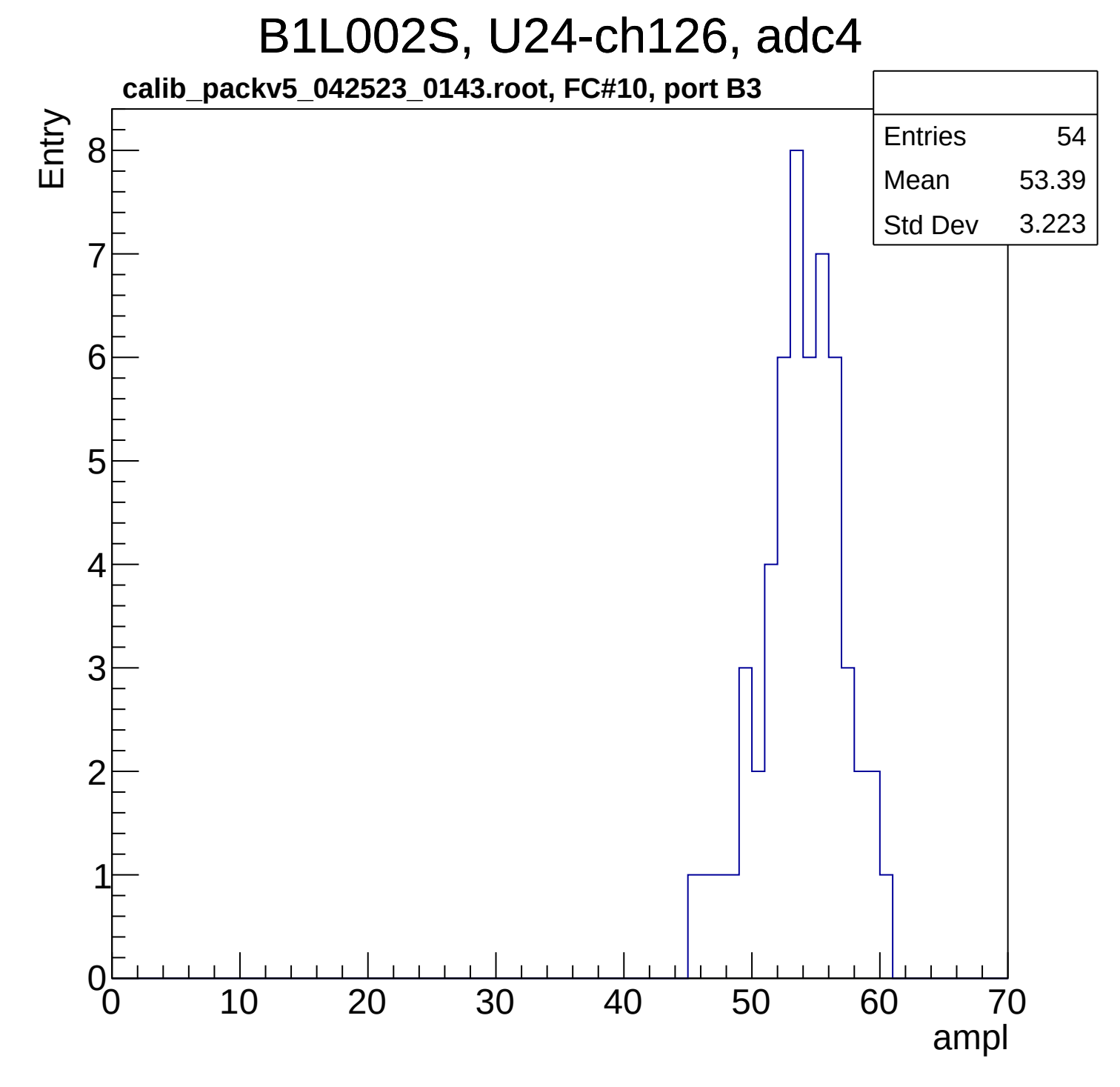
B1L002S, U24-ch126, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 54
Mean 53.39
Std Dev 3.223
Entry
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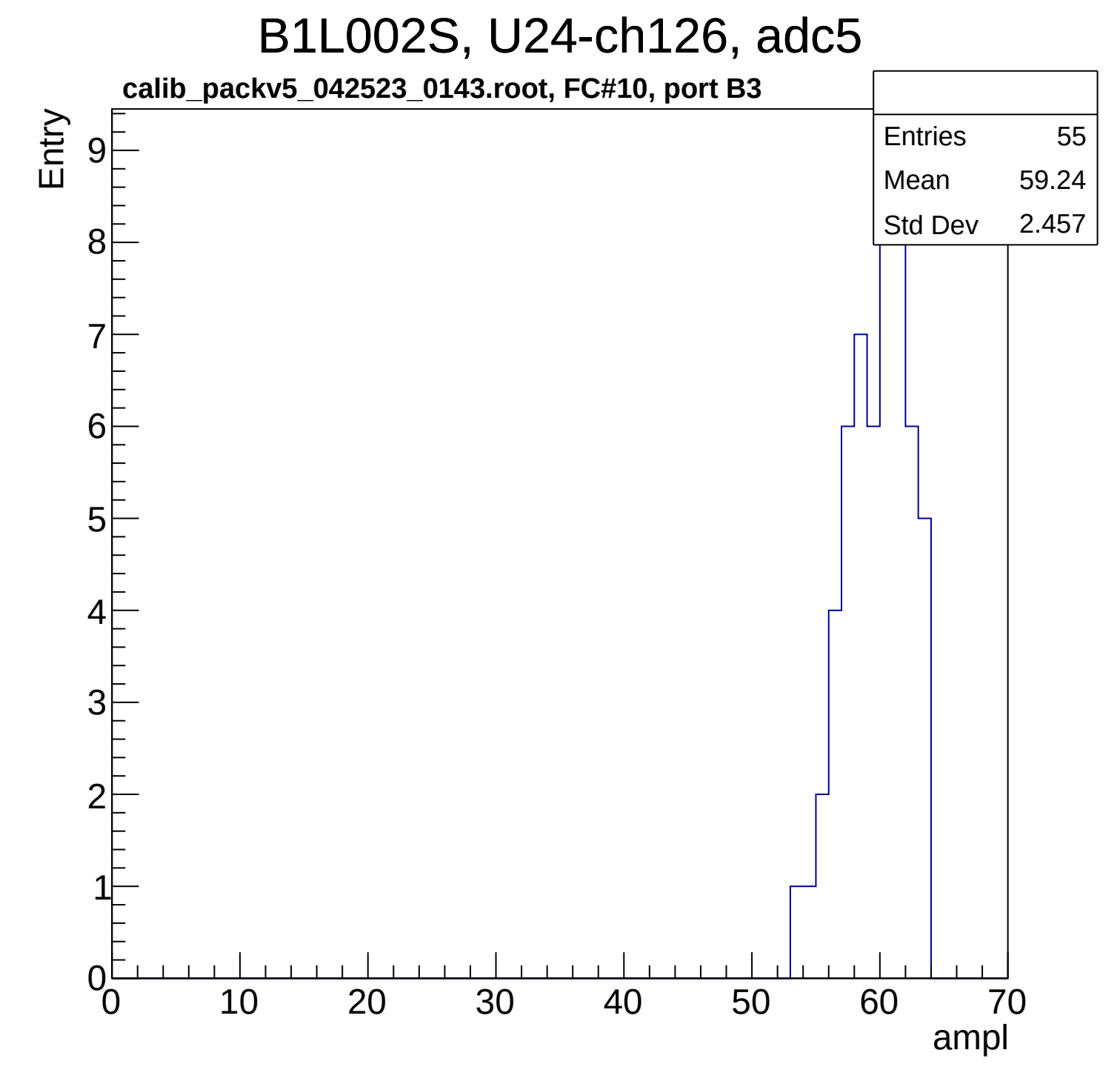
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch126, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 59.24
Std Dev 2.457
Entry
ampl
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3
4
5
6
7
8
9
0
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20
30
40
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60
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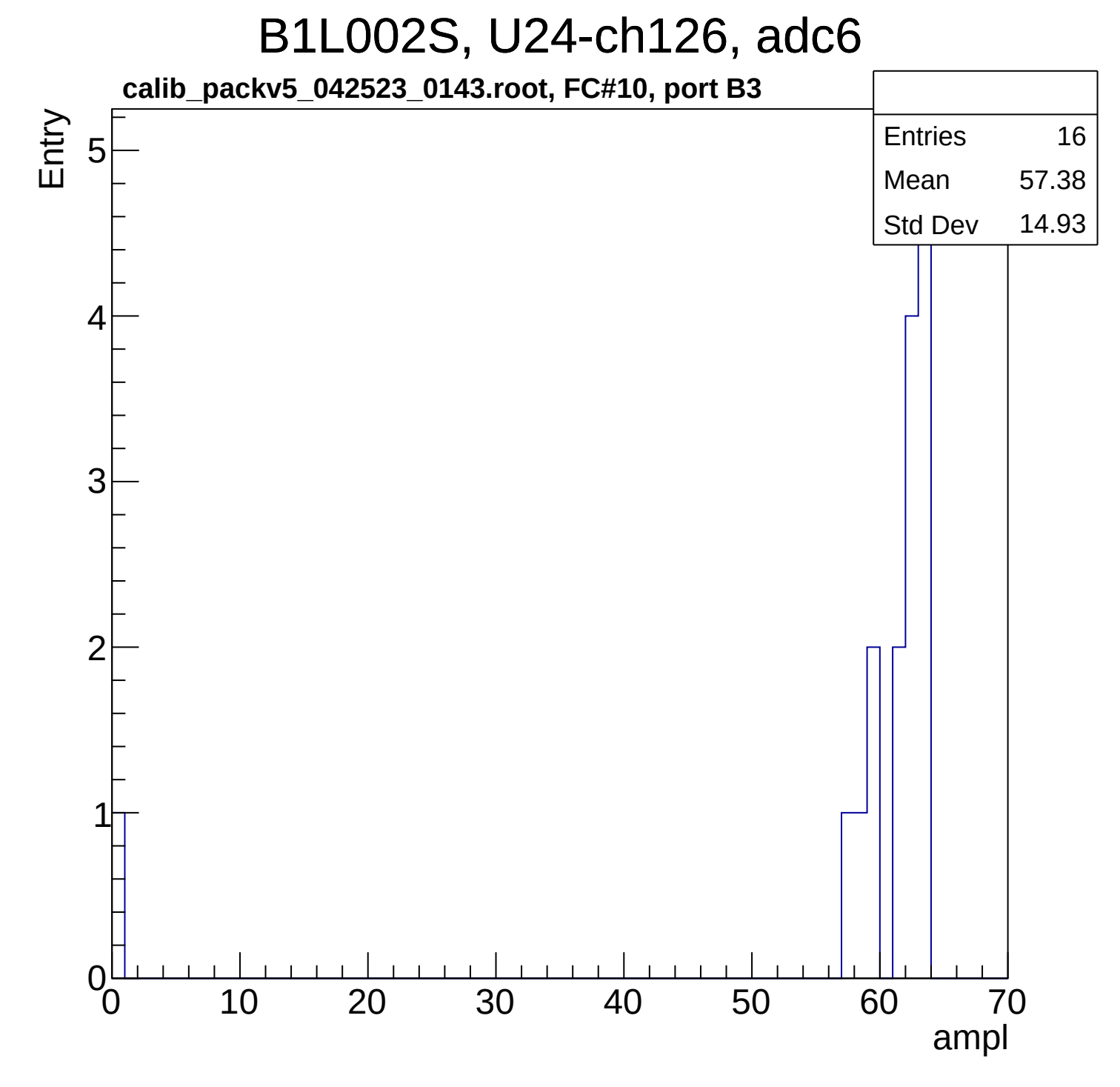
B1L002S, U24-ch126, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 16
Mean 57.38
Std Dev 14.93
Entry
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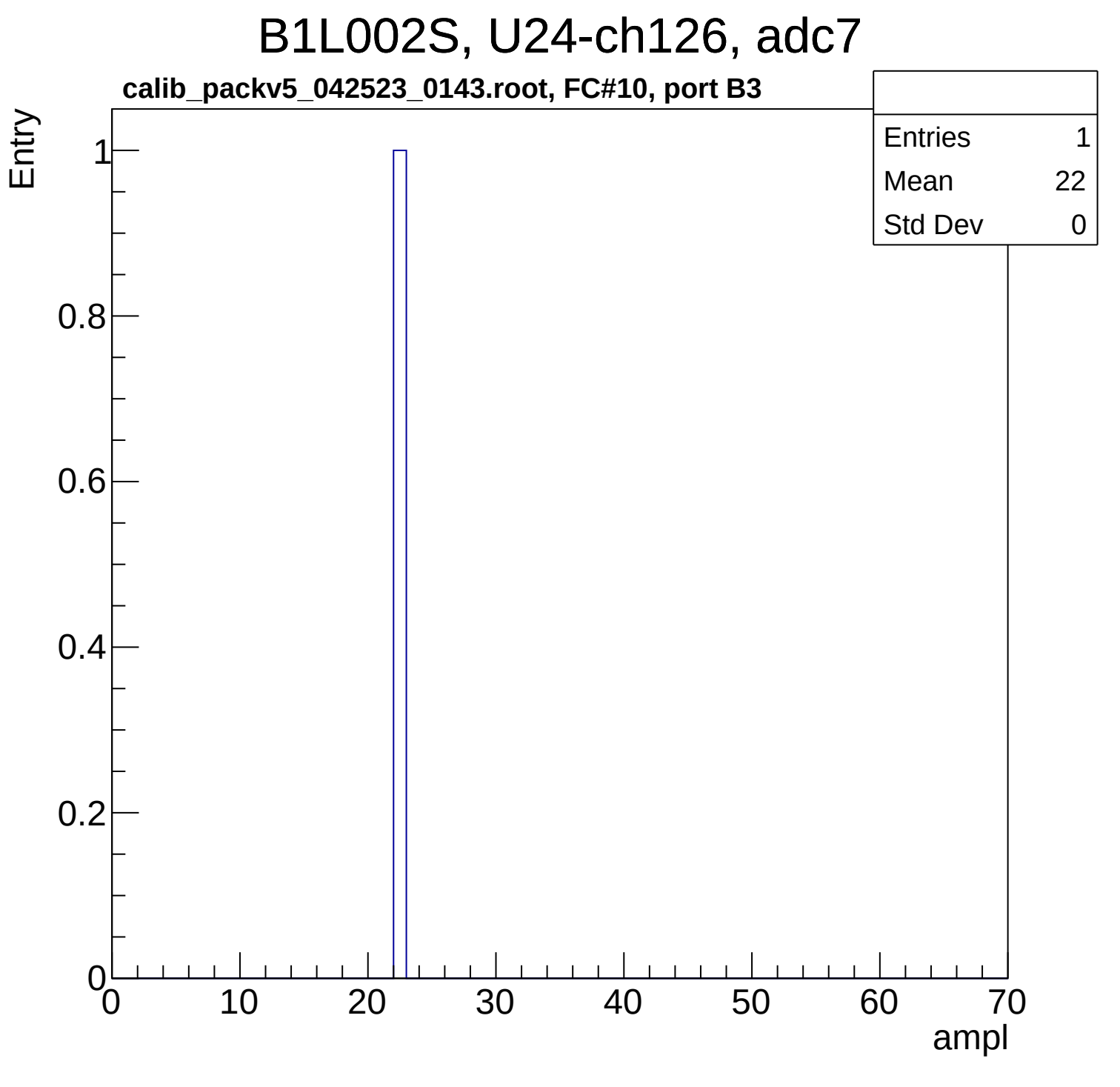
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch126, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 22
Std Dev 0
Entry
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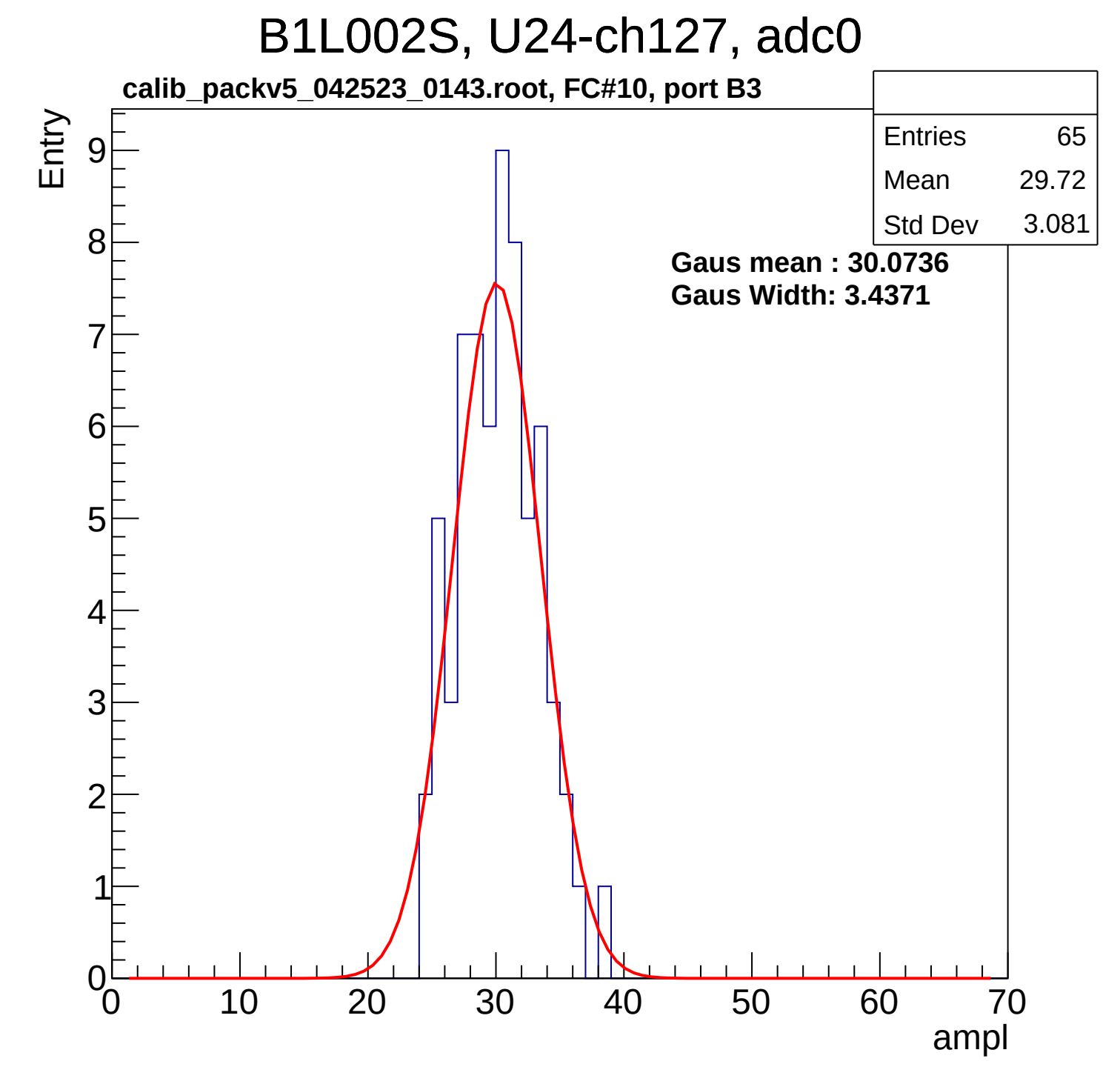
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch127, adc0
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 29.72
Std Dev 3.081
Gaus mean : 30.0736
Gaus Width: 3.4371
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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30
40
50
60
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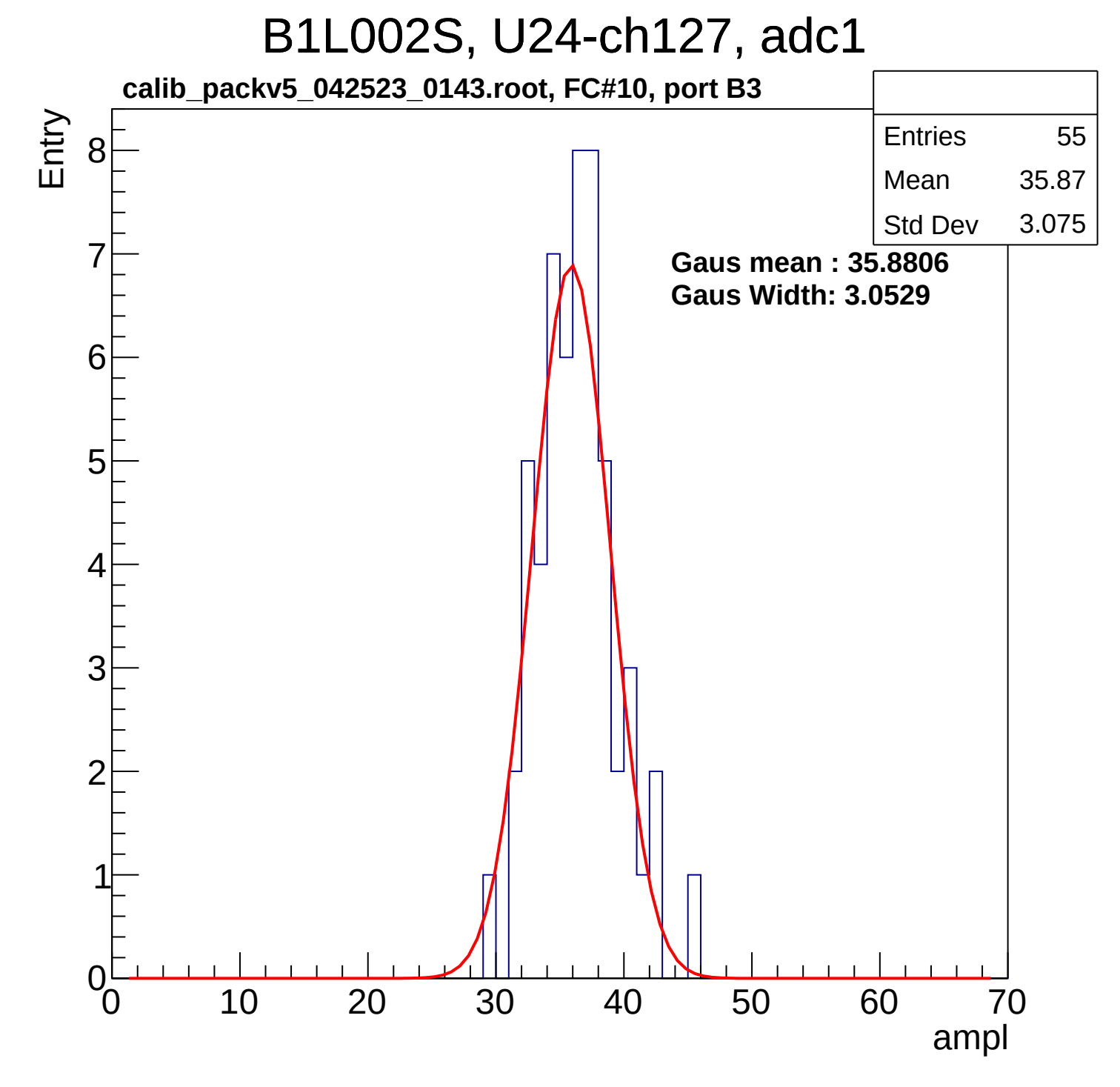
B1L002S, U24-ch127, adc1
calib_packv5_042523_0143.root, FC#10, port B3
Entries 55
Mean 35.87
Std Dev 3.075
Gaus mean : 35.8806
Gaus Width: 3.0529
Entry
ampl
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1
2
3
4
5
6
7
8
0
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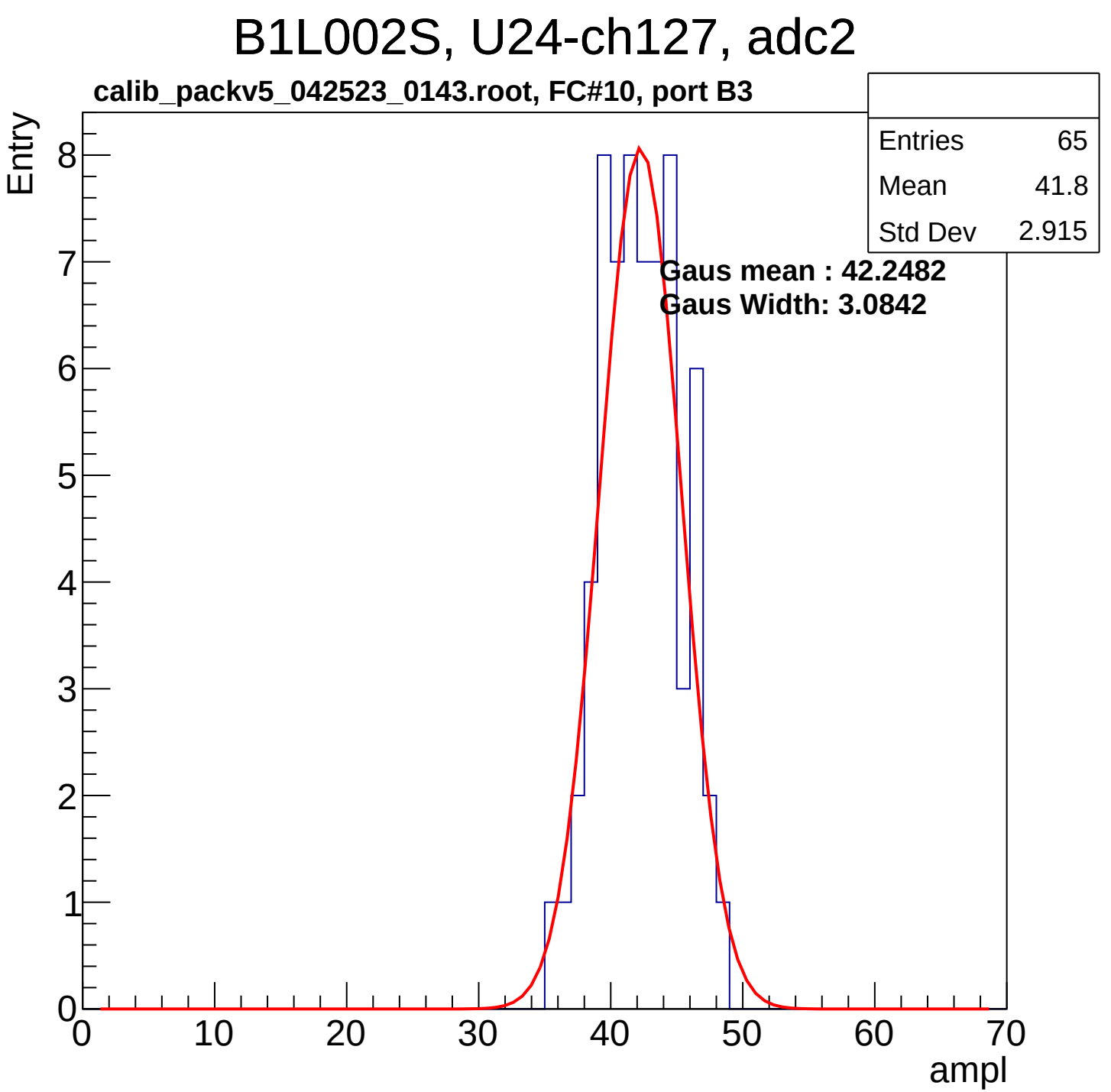
20
30
40
50
60
70


B1L002S, U24-ch127, adc2
calib_packv5_042523_0143.root, FC#10, port B3
Entries 65
Mean 41.8
Std Dev 2.915
Gaus mean : 42.2482
Gaus Width: 3.0842
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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50
60
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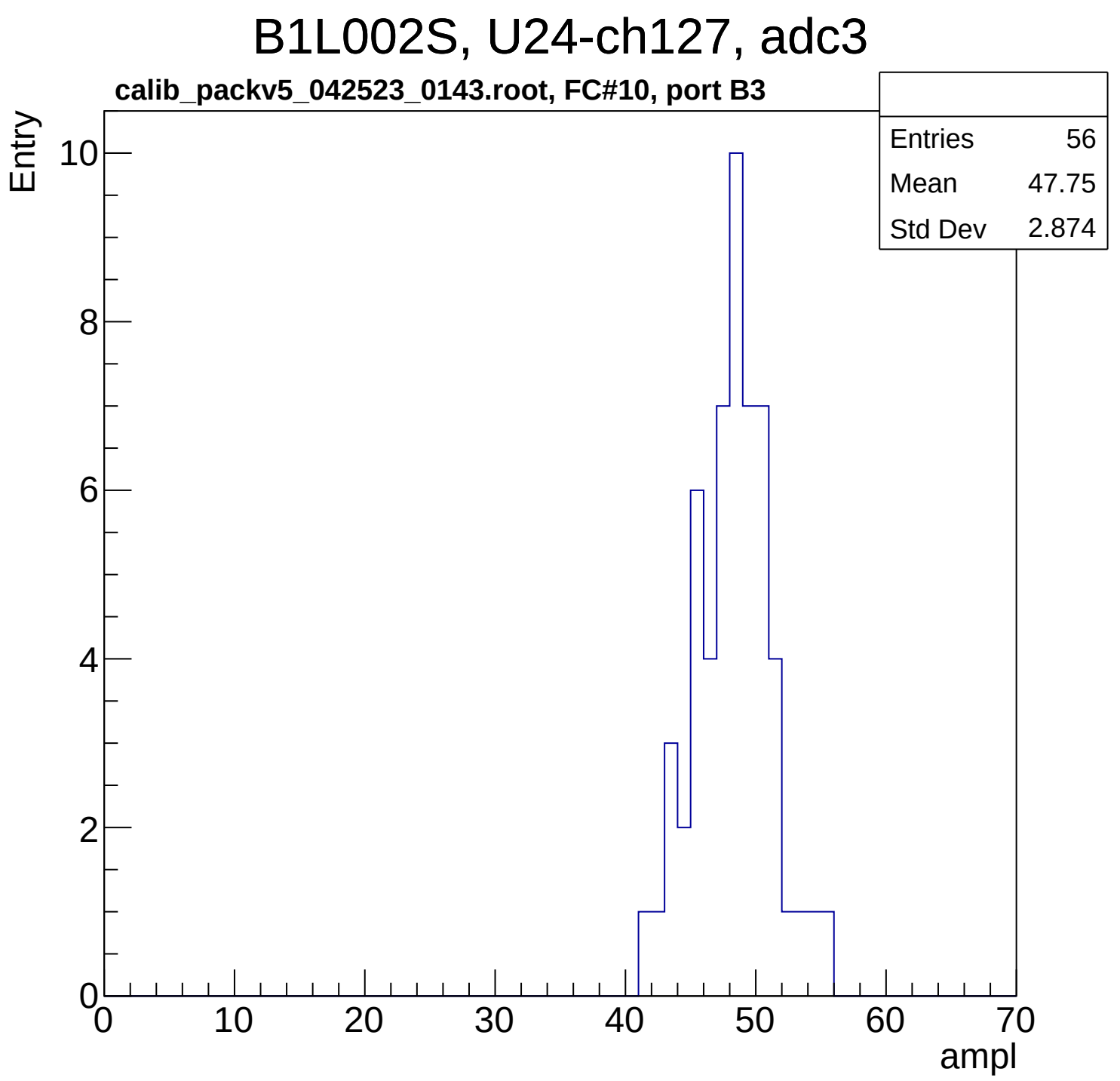
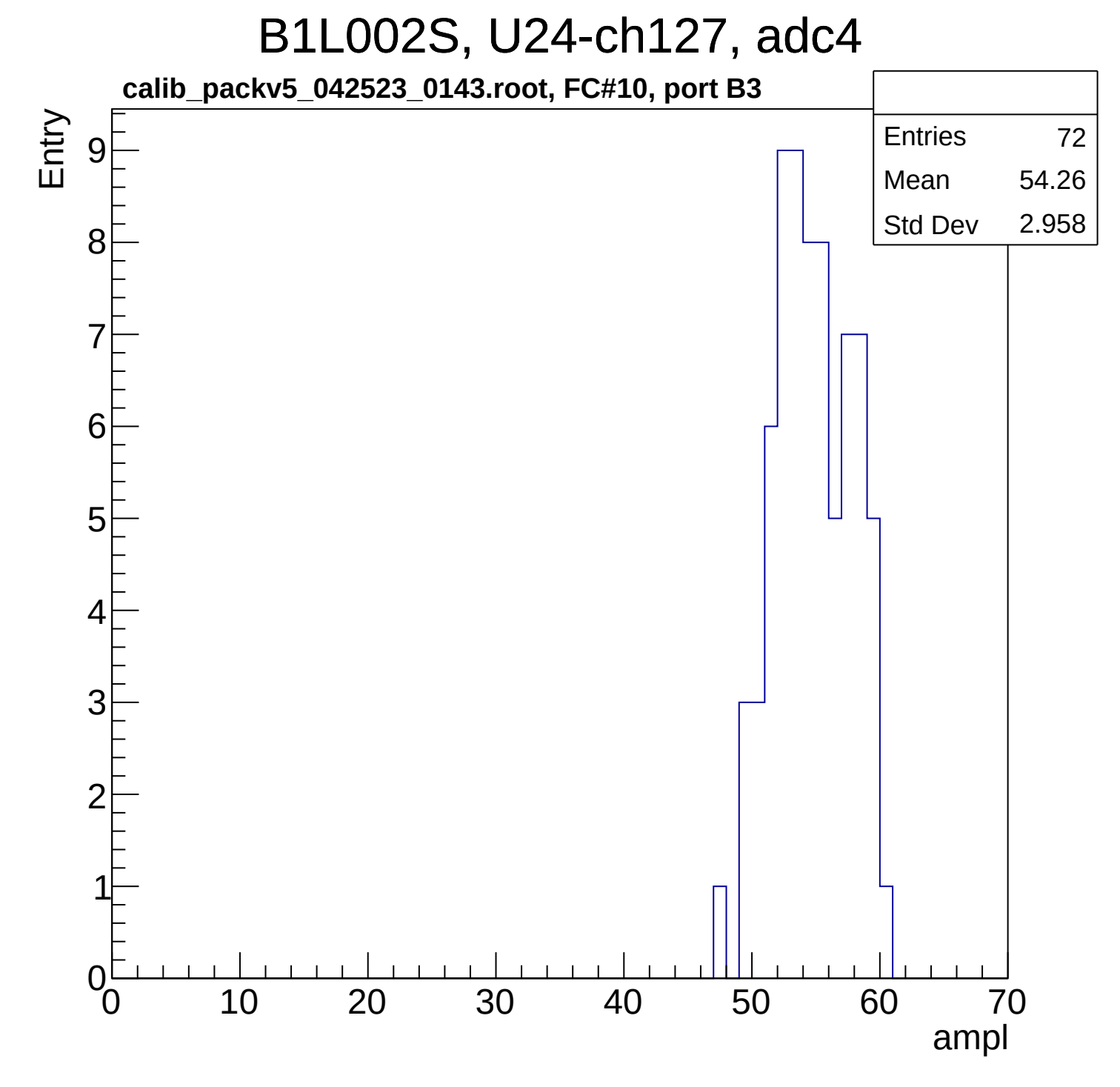

B1L002S, U24-ch127, adc3
calib_packv5_042523_0143.root, FC#10, port B3
Entries 56
Mean 47.75
Std Dev 2.874
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch127, adc4
calib_packv5_042523_0143.root, FC#10, port B3
Entries 72
Mean 54.26
Std Dev 2.958
Entry
ampl
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2
3
4
5
6
7
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9
0
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40
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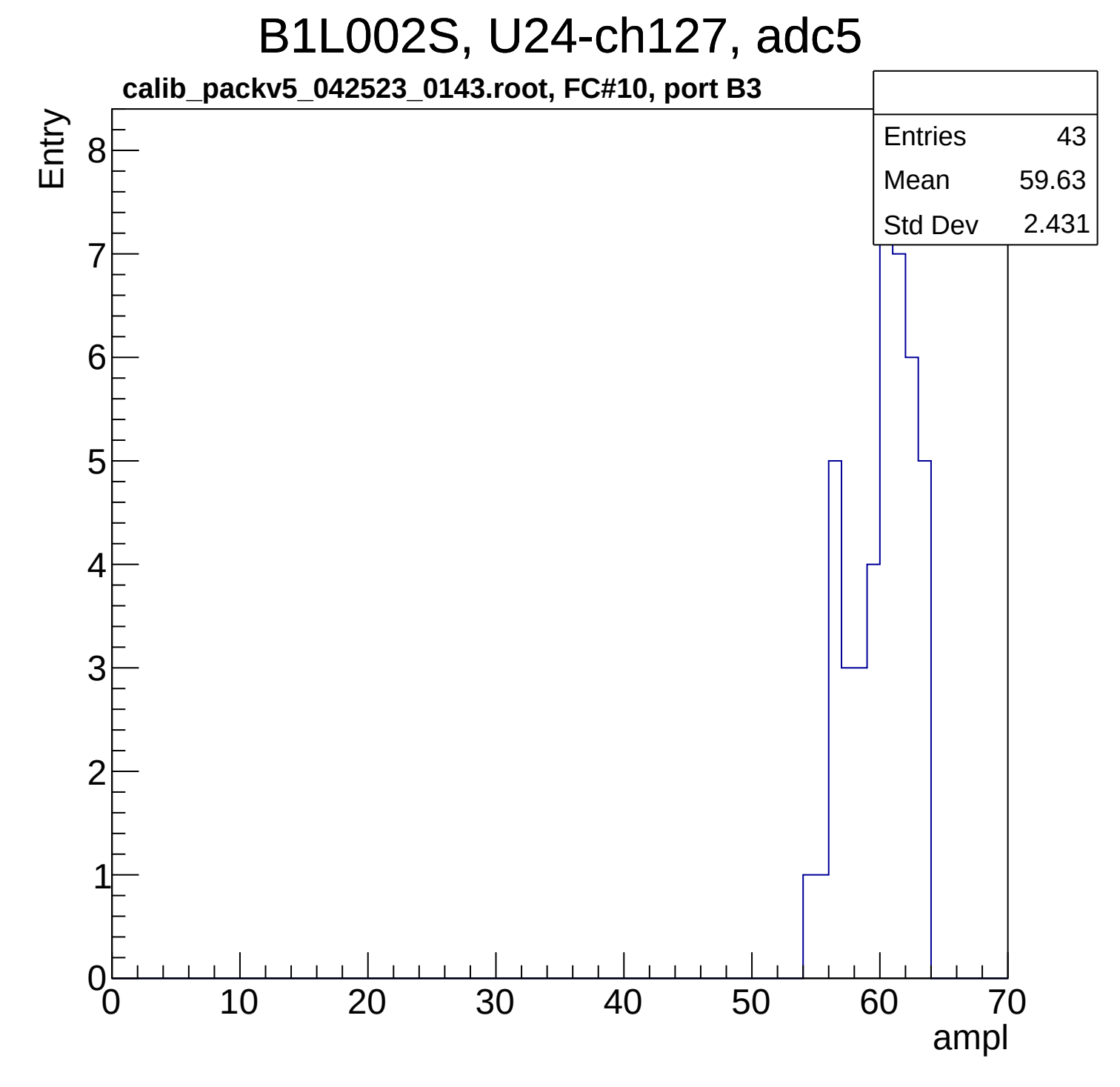
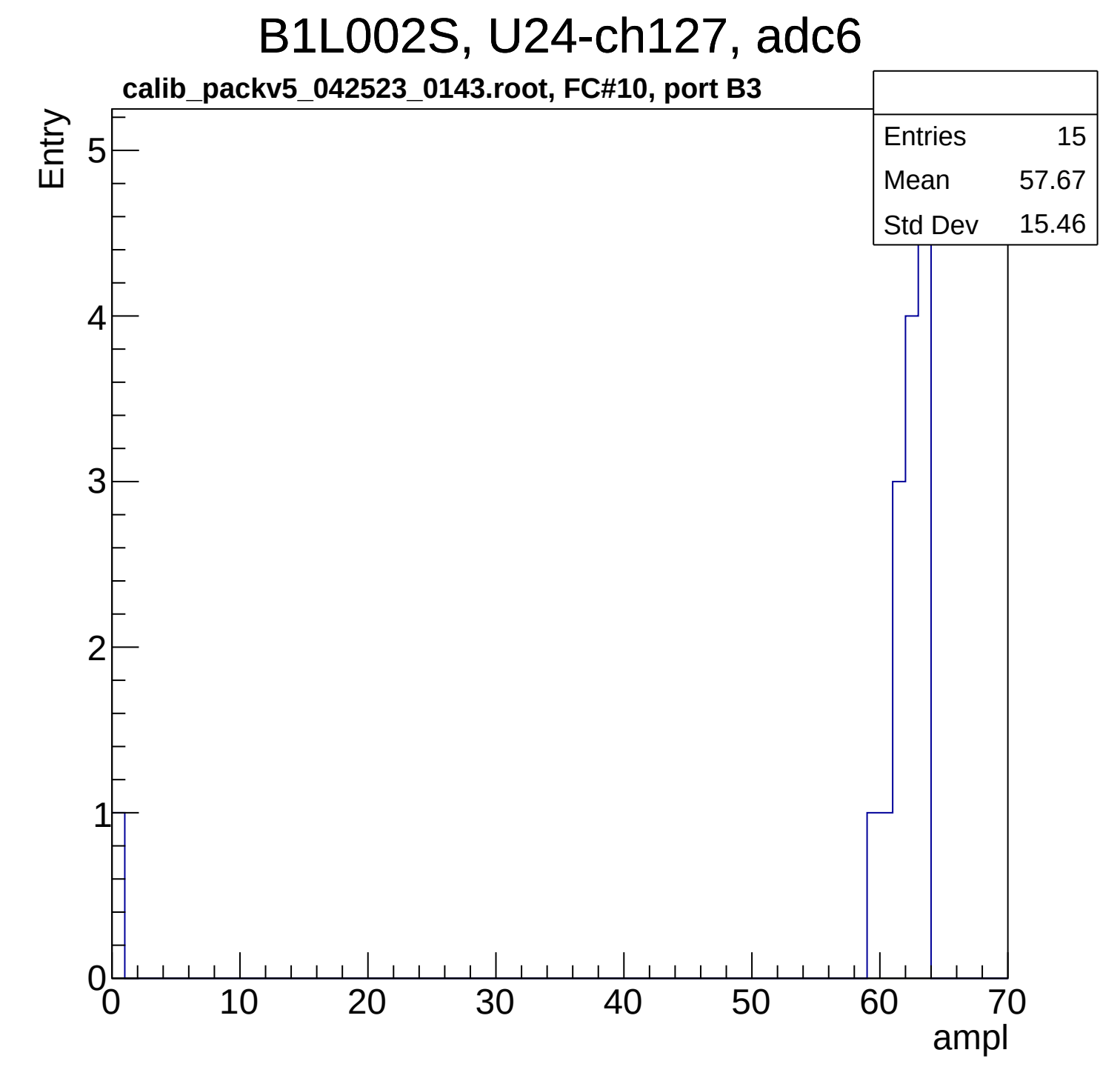
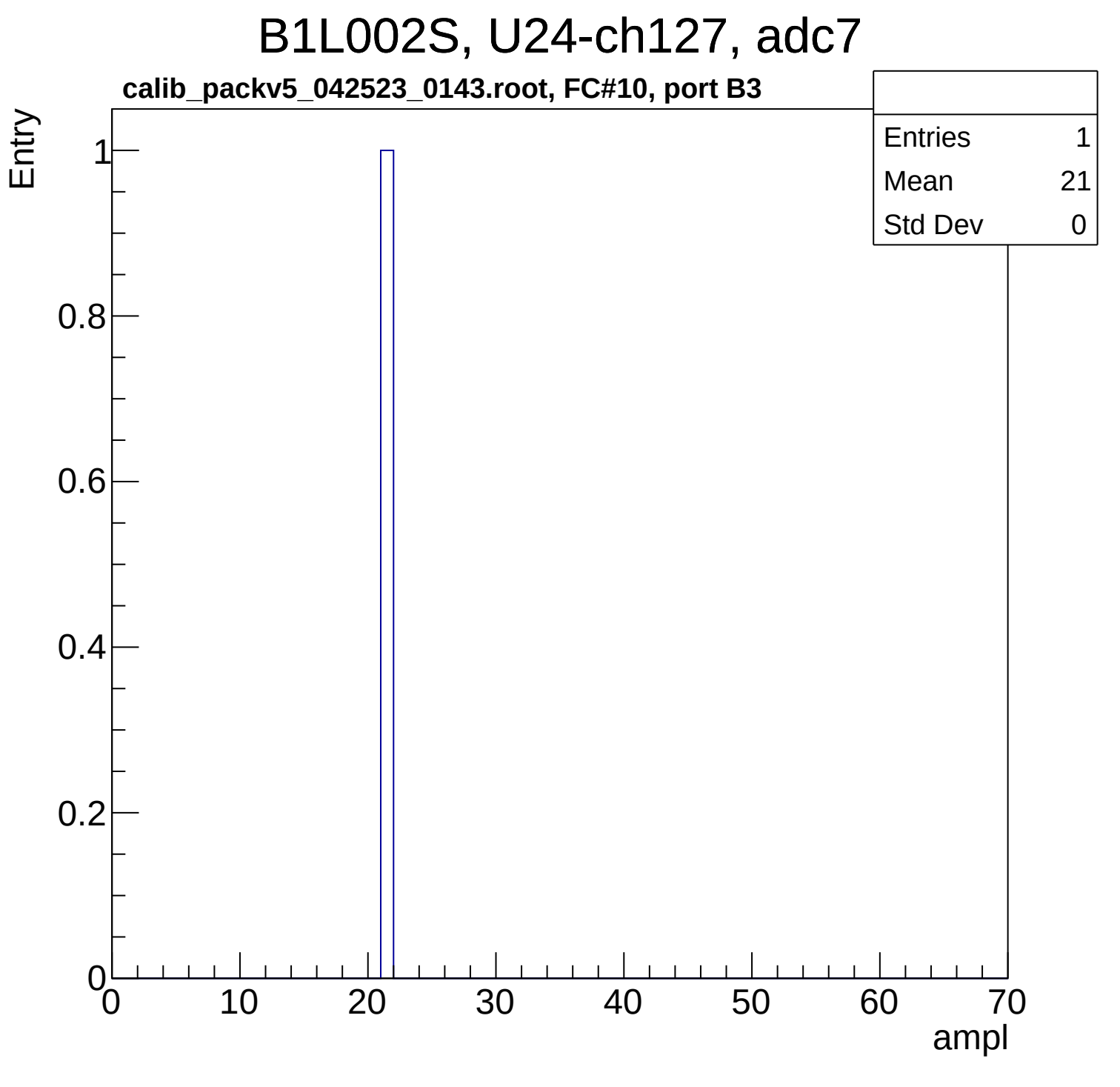
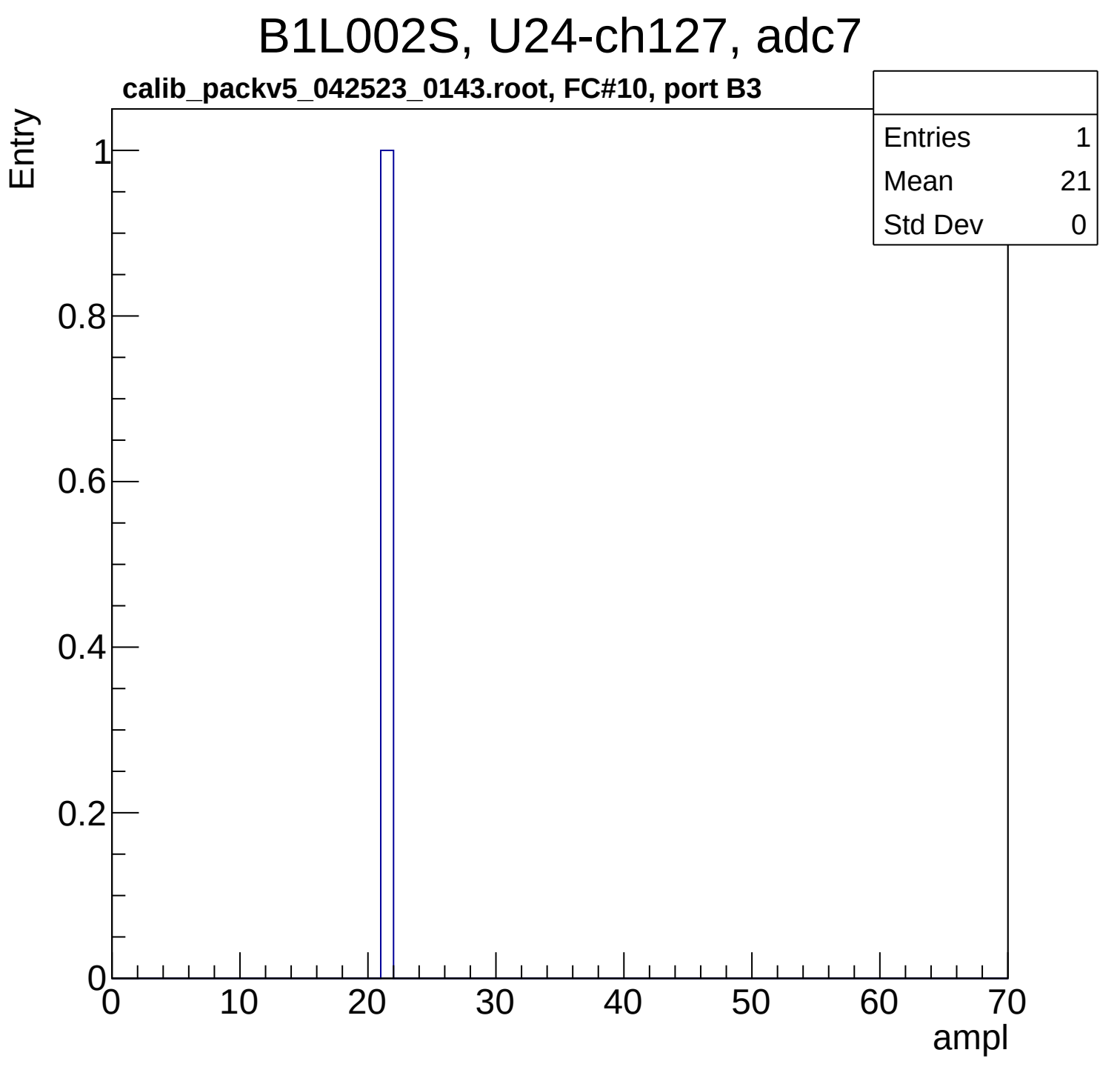

B1L002S, U24-ch127, adc5
calib_packv5_042523_0143.root, FC#10, port B3
Entries 43
Mean 59.63
Std Dev 2.431
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L002S, U24-ch127, adc6
calib_packv5_042523_0143.root, FC#10, port B3
Entries 15
Mean 57.67
Std Dev 15.46
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L002S, U24-ch127, adc7
calib_packv5_042523_0143.root, FC#10, port B3
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl