

B1L000S, U18-ch0

calib_packv5_042523_0143.root, FC#3, port B3

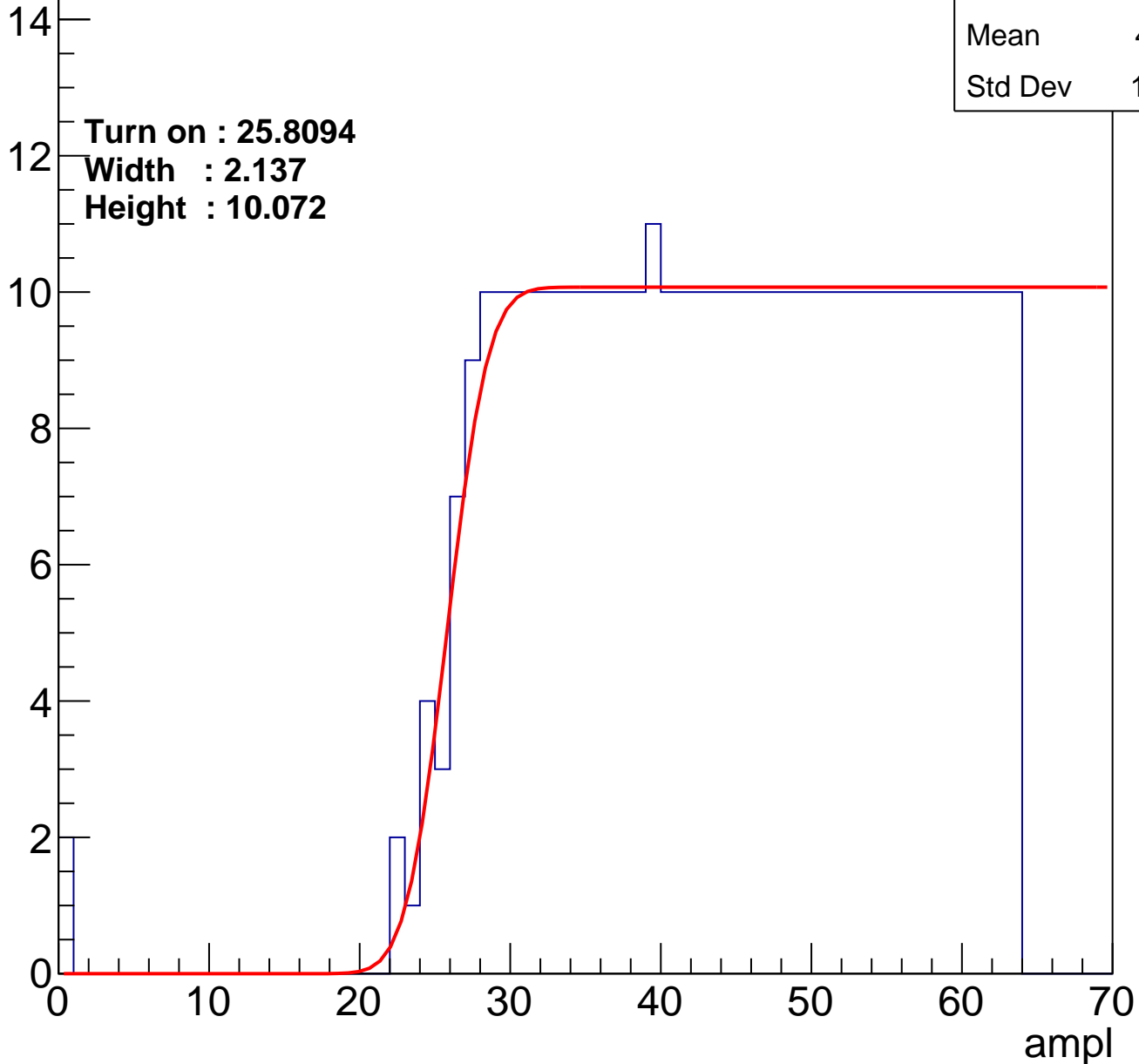
Entries	389
Mean	43.91
Std Dev	11.62

Turn on : 25.8094

Width : 2.137

Height : 10.072

Entry



B1L000S, U18-ch1

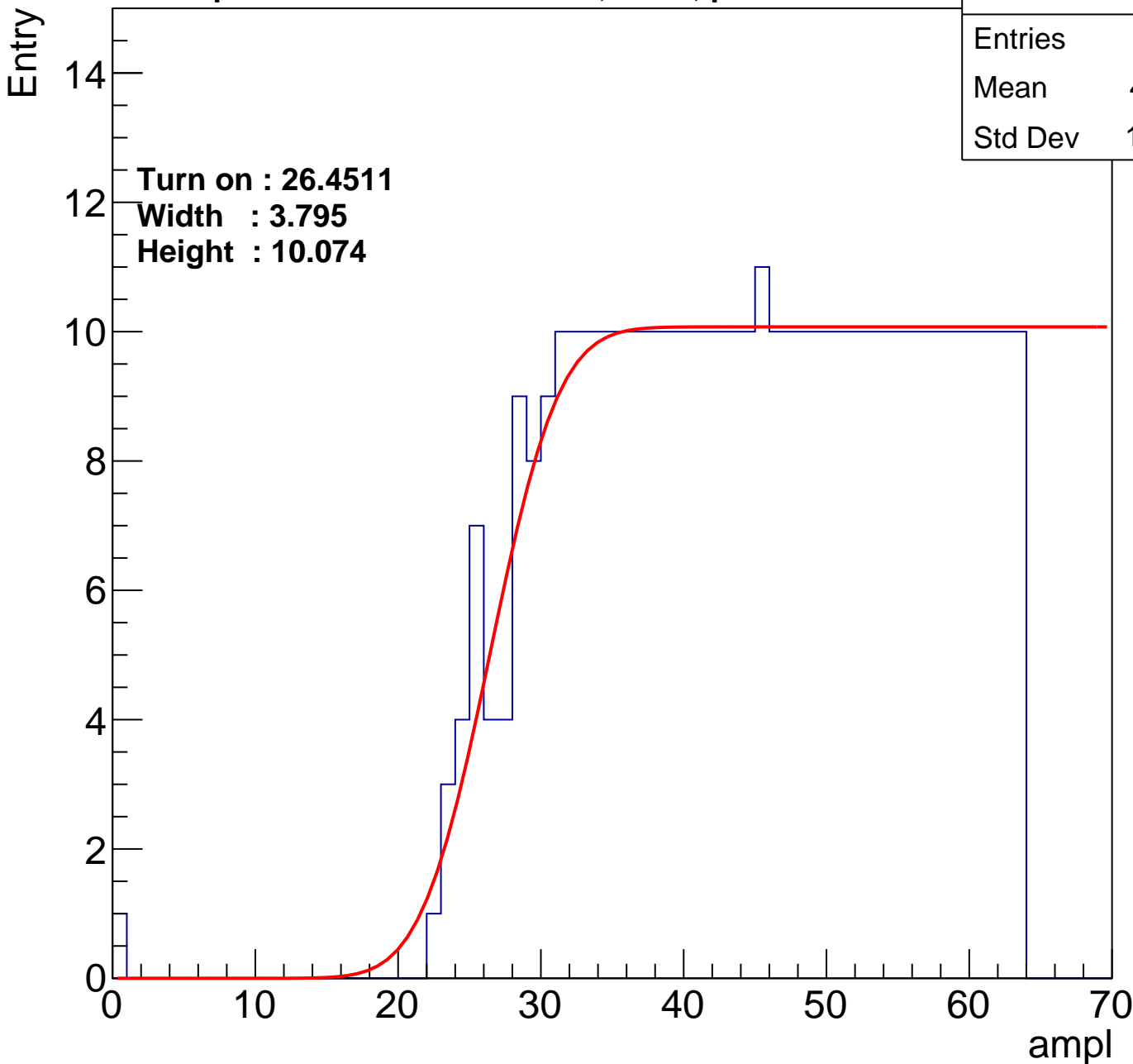
calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.31
Std Dev	11.35

Turn on : 26.4511

Width : 3.795

Height : 10.074



B1L000S, U18-ch2

calib_packv5_042523_0143.root, FC#3, port B3

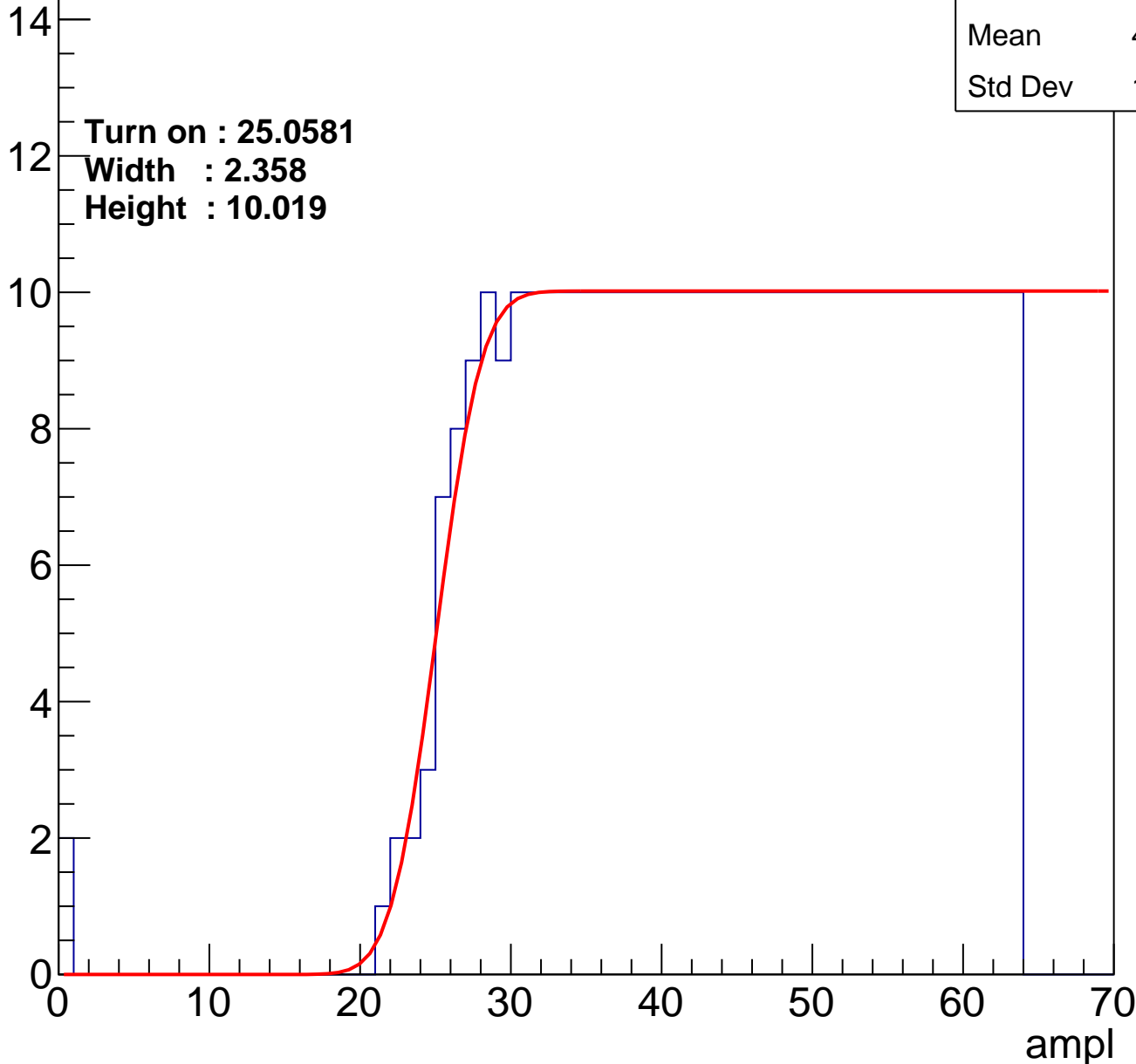
Entries	393
Mean	43.66
Std Dev	11.78

Turn on : 25.0581

Width : 2.358

Height : 10.019

Entry



B1L000S, U18-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.41
Std Dev	11.39

Turn on : 26.4080

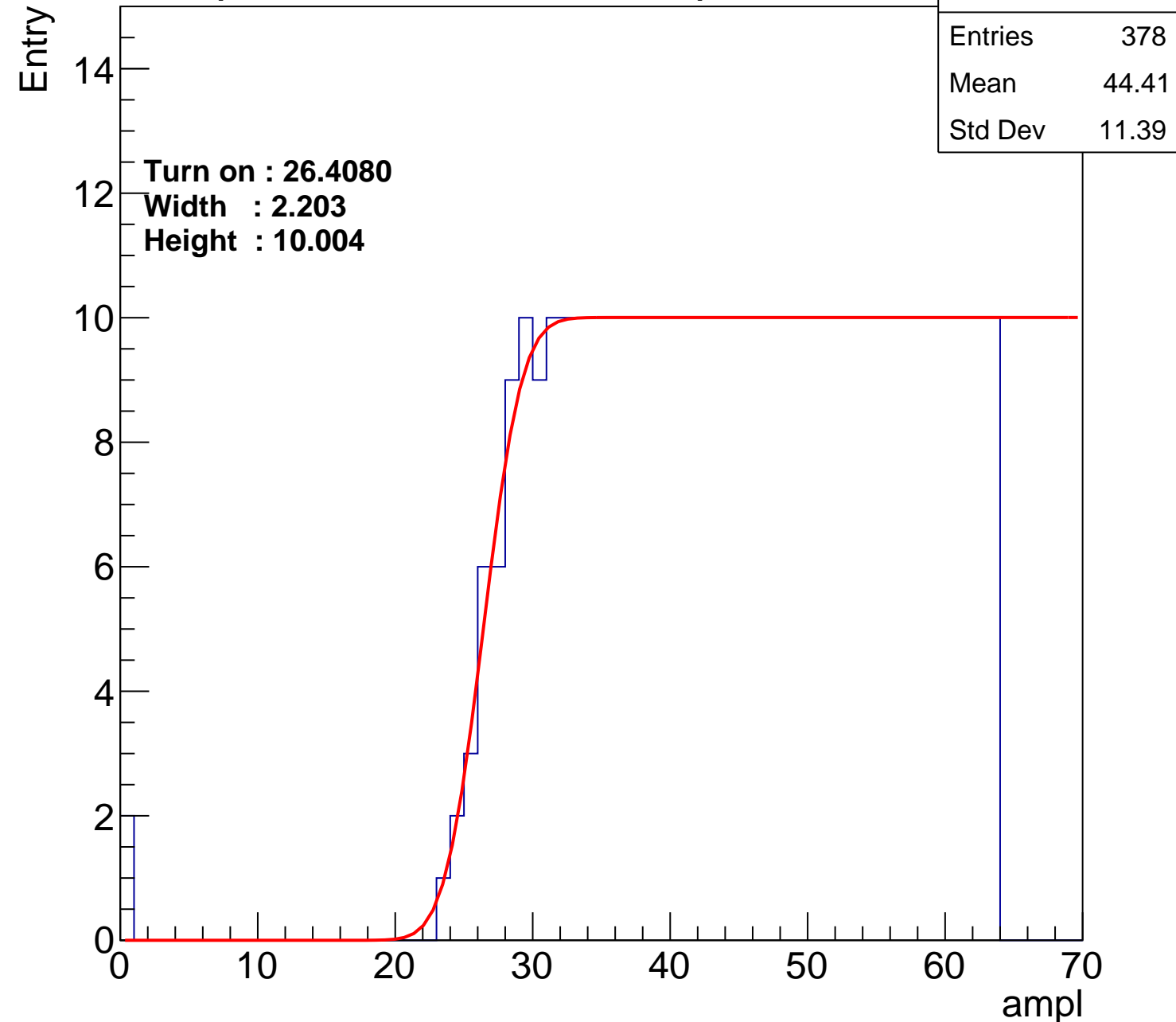
Width : 2.203

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.41
Std Dev	11.31

Turn on : 26.6452

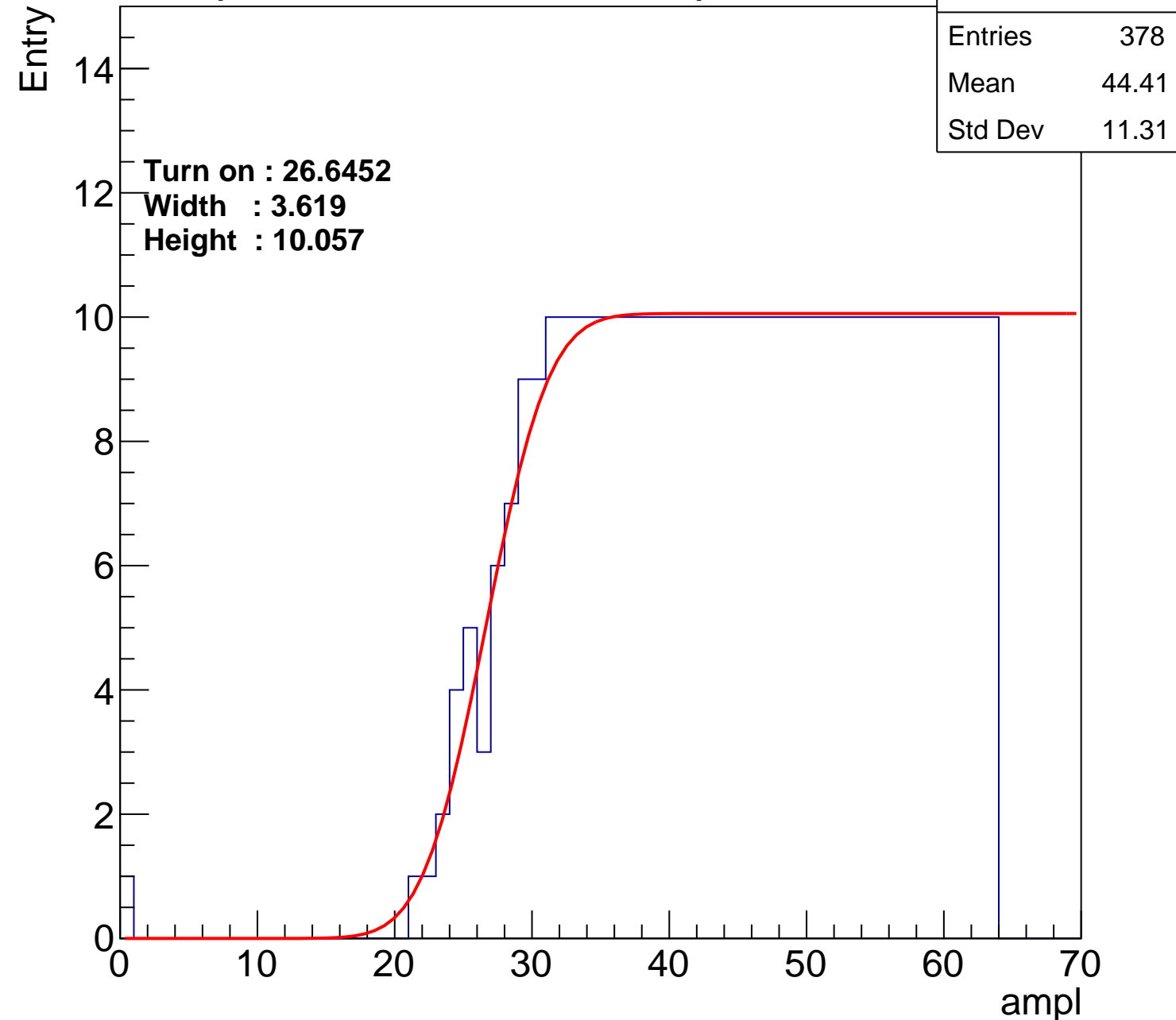
Width : 3.619

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch5

calib_packv5_042523_0143.root, FC#3, port B3

Entries	358
Mean	45.48
Std Dev	10.64

Turn on : 28.3667

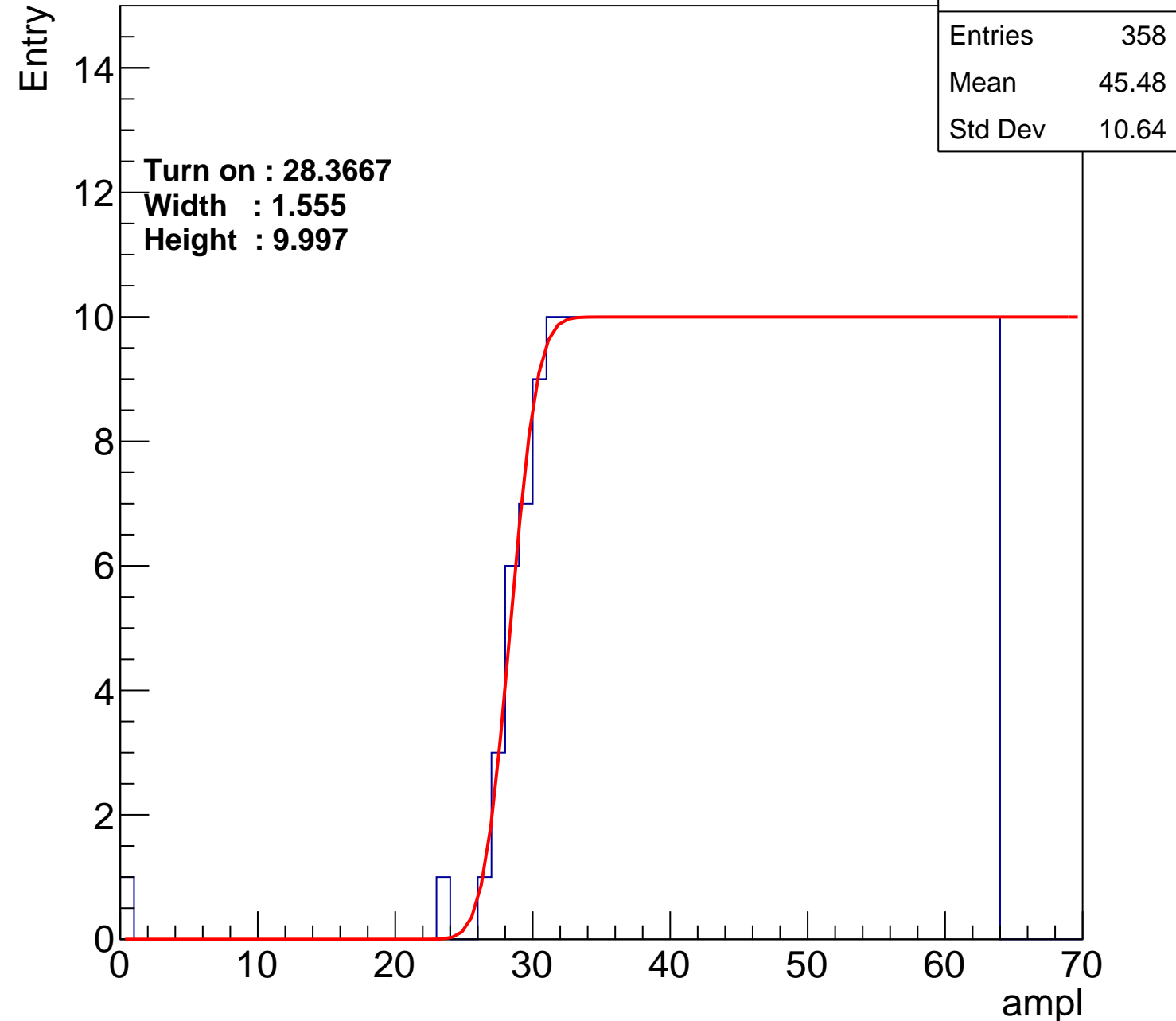
Width : 1.555

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch6

calib_packv5_042523_0143.root, FC#3, port B3

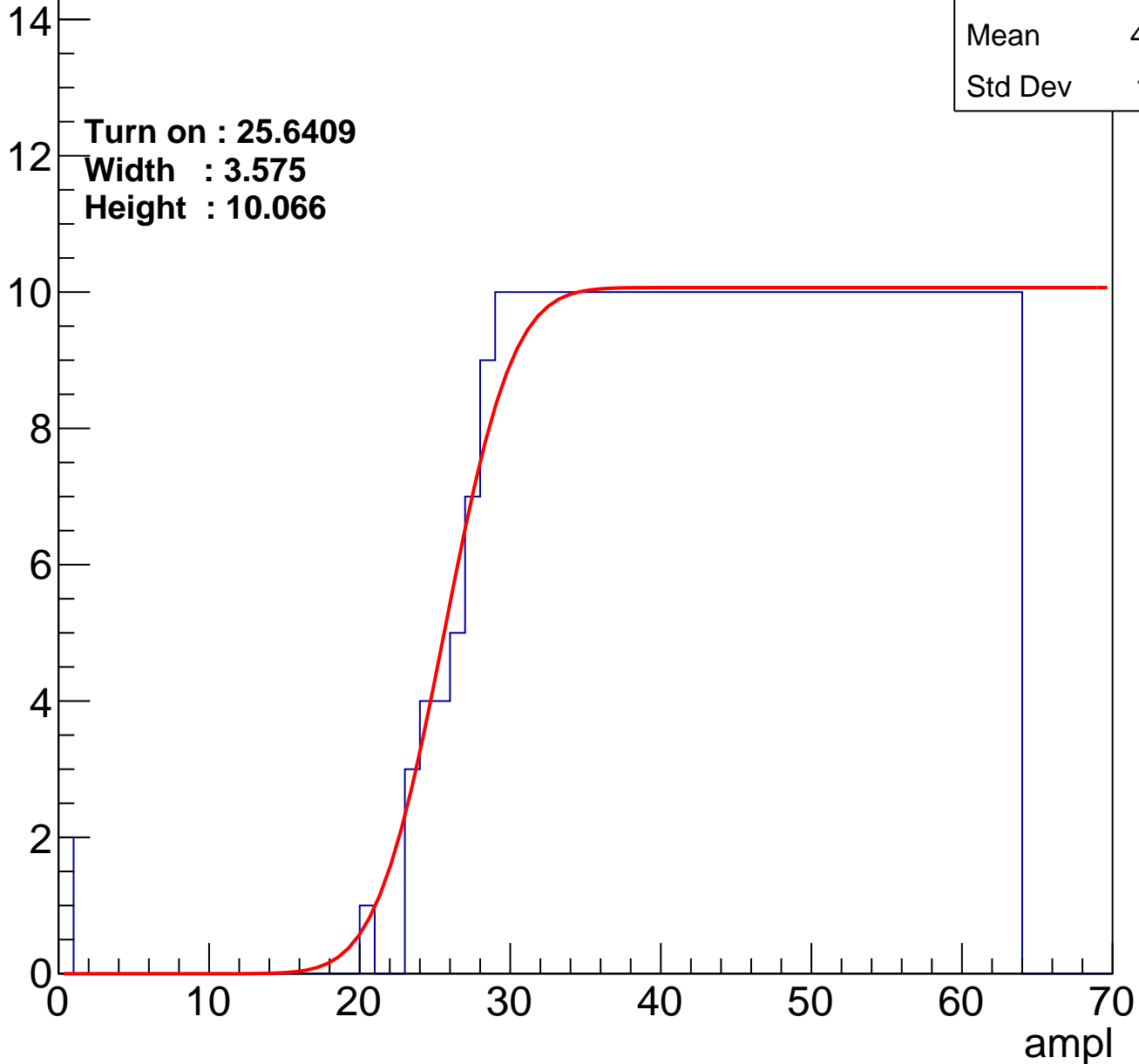
Entries	385
Mean	44.04
Std Dev	11.61

Turn on : 25.6409

Width : 3.575

Height : 10.066

Entry



B1L000S, U18-ch7

calib_packv5_042523_0143.root, FC#3, port B3

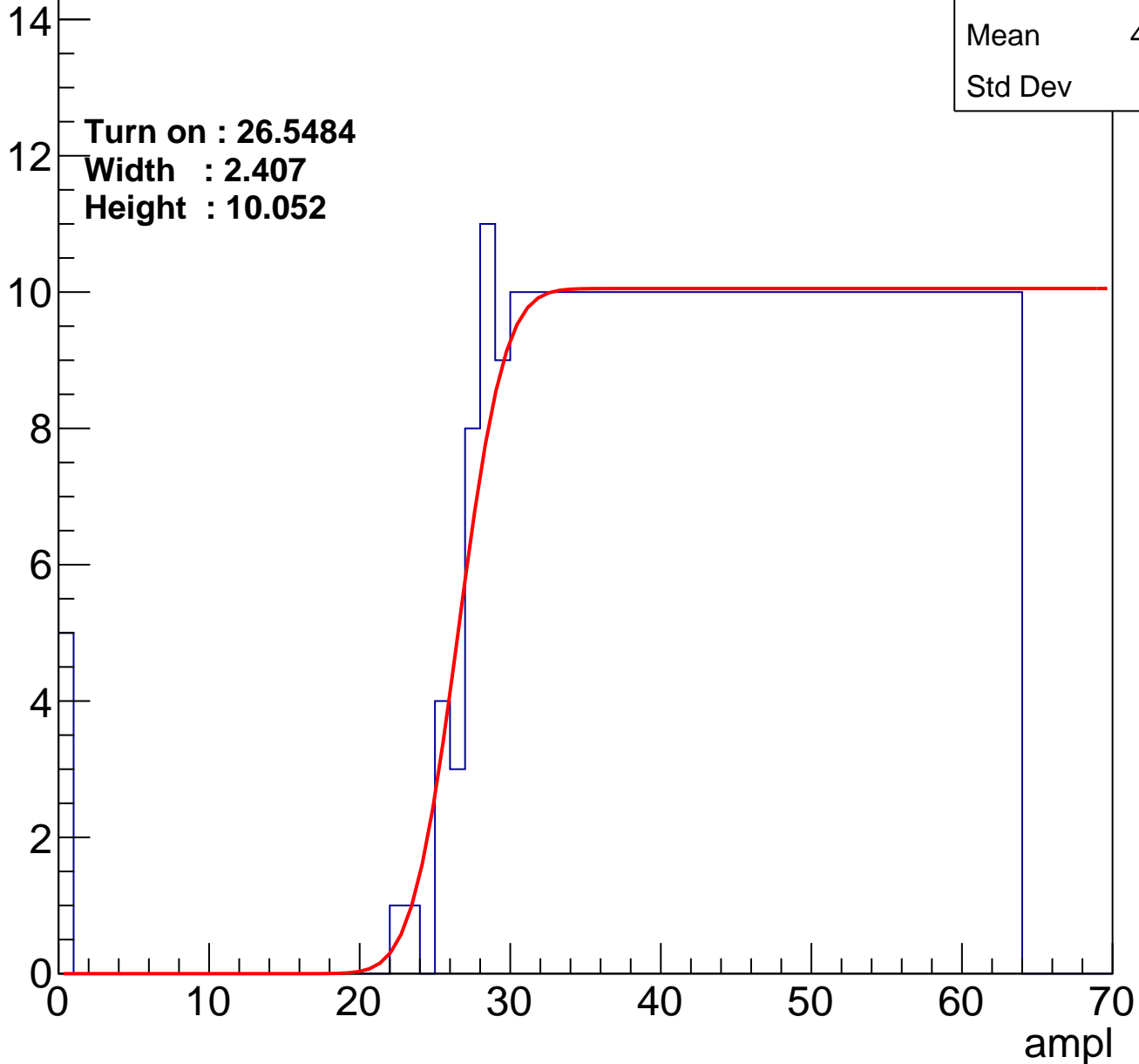
Entries	382
Mean	44.03
Std Dev	12

Turn on : 26.5484

Width : 2.407

Height : 10.052

Entry



B1L000S, U18-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.57
Std Dev	11.21

Turn on : 26.7082

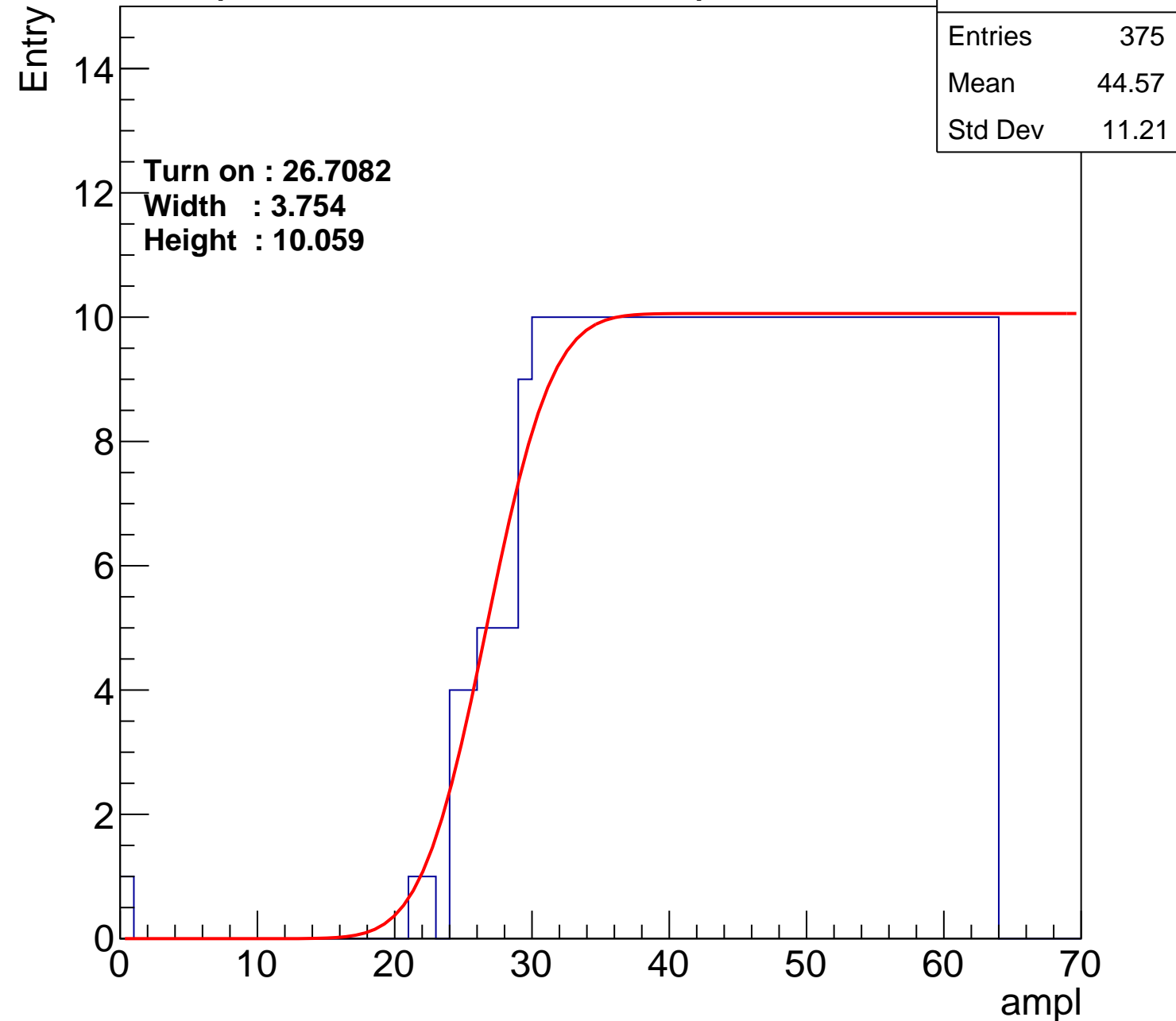
Width : 3.754

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch9

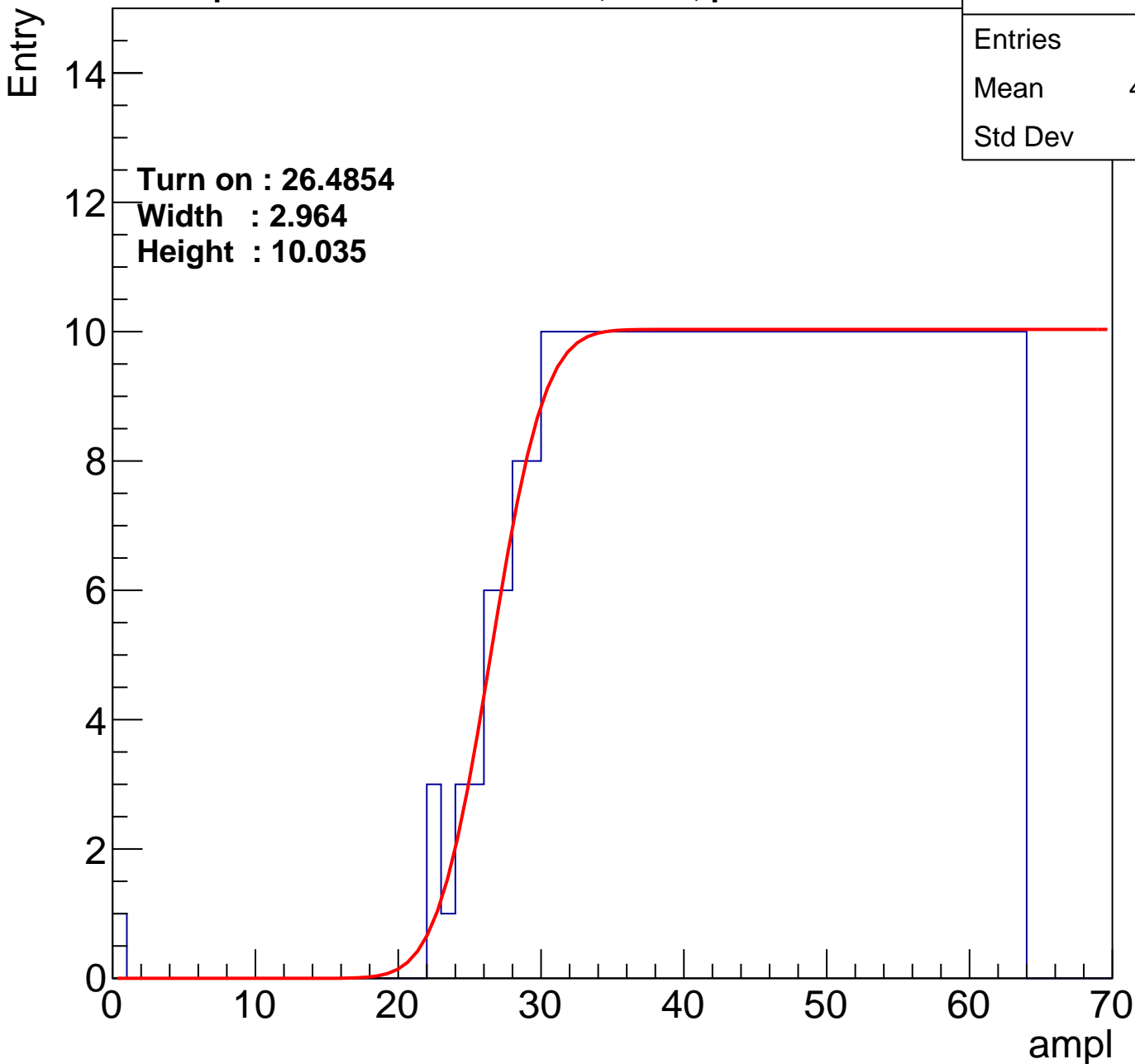
calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.38
Std Dev	11.3

Turn on : 26.4854

Width : 2.964

Height : 10.035



B1L000S, U18-ch10

calib_packv5_042523_0143.root, FC#3, port B3

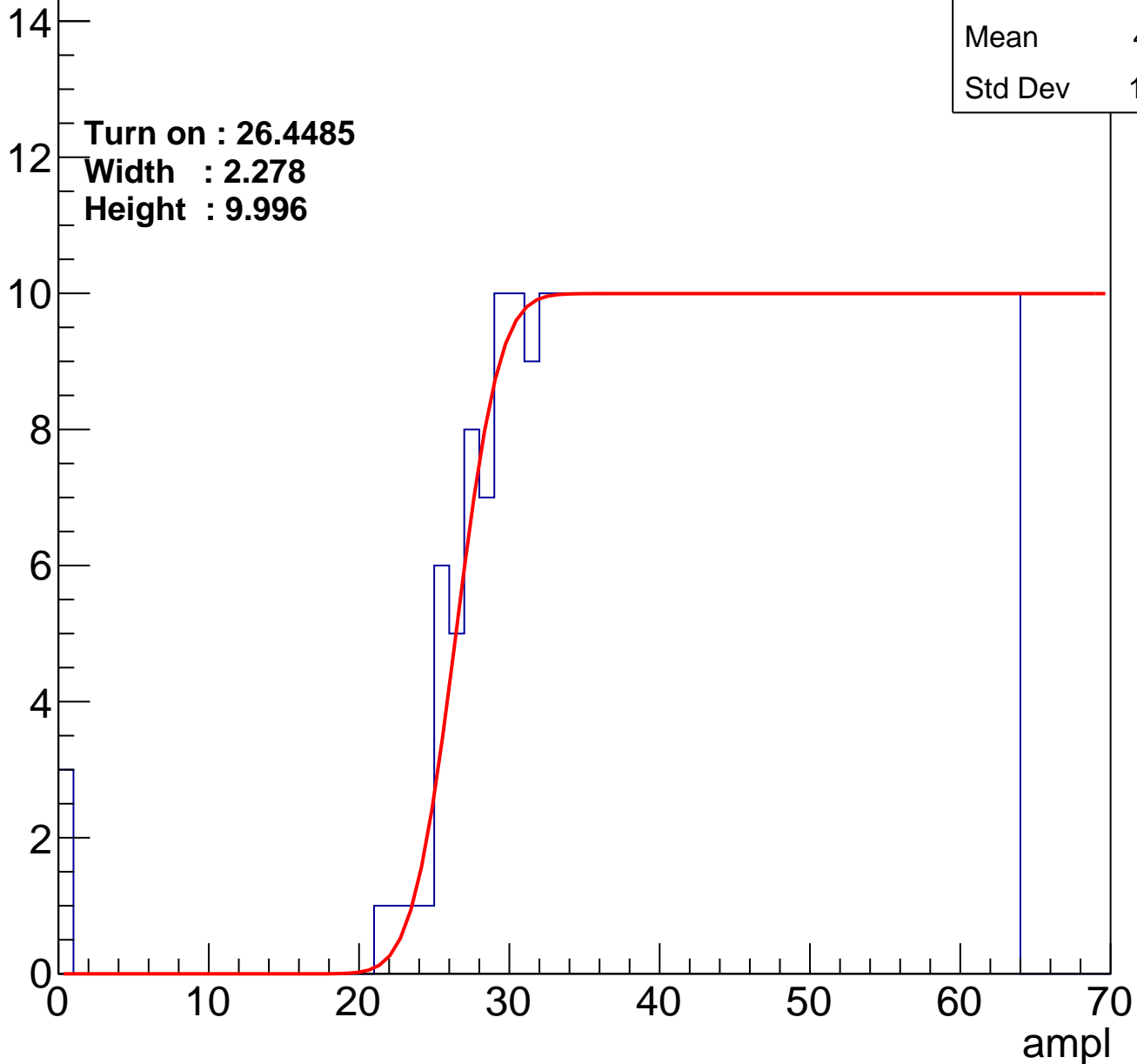
Entries	382
Mean	44.11
Std Dev	11.72

Turn on : 26.4485

Width : 2.278

Height : 9.996

Entry



B1L000S, U18-ch11

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	45.04
Std Dev	10.93

Turn on : 27.8425

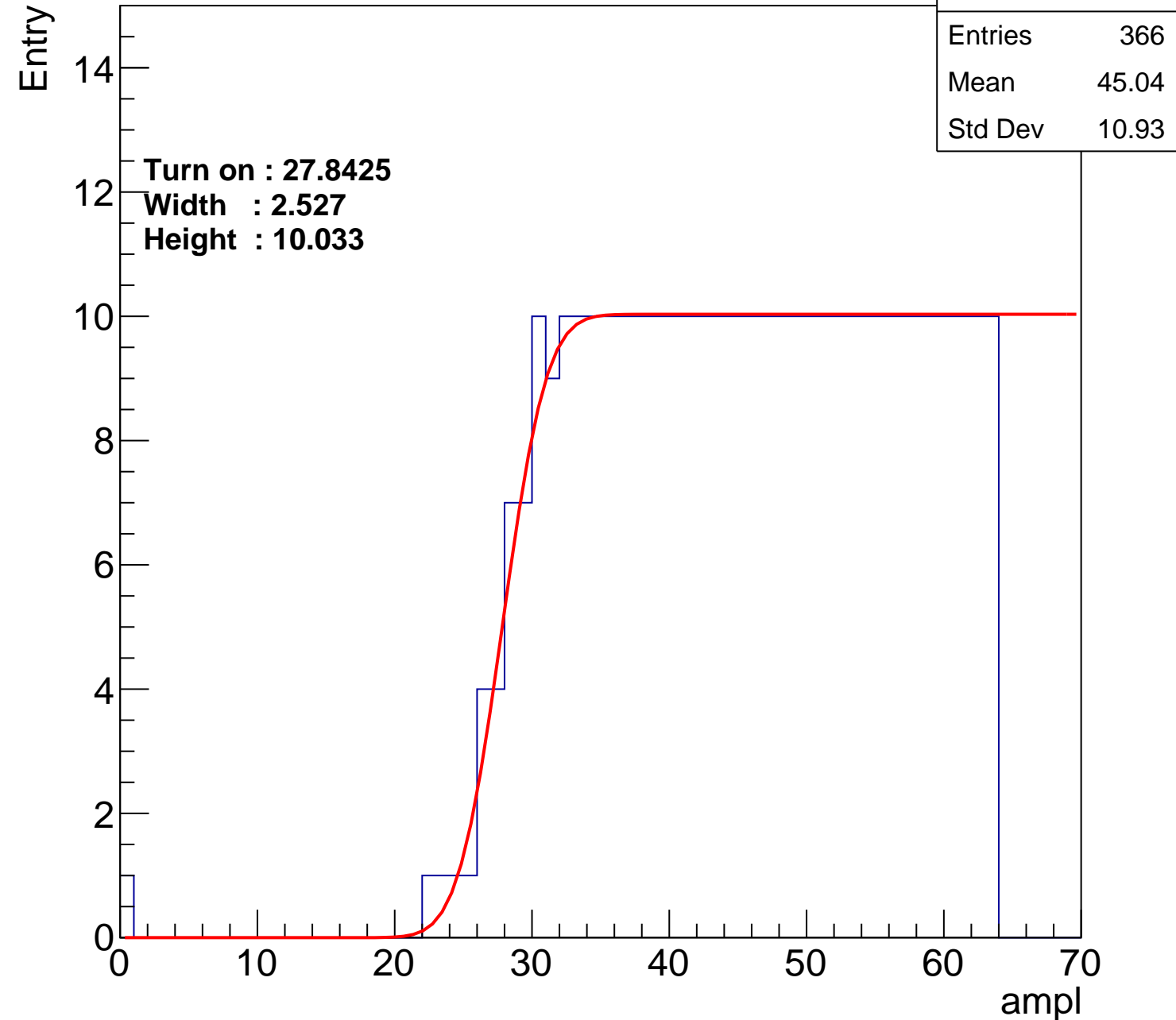
Width : 2.527

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch12

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45
Std Dev	11.28

Turn on : 28.2136

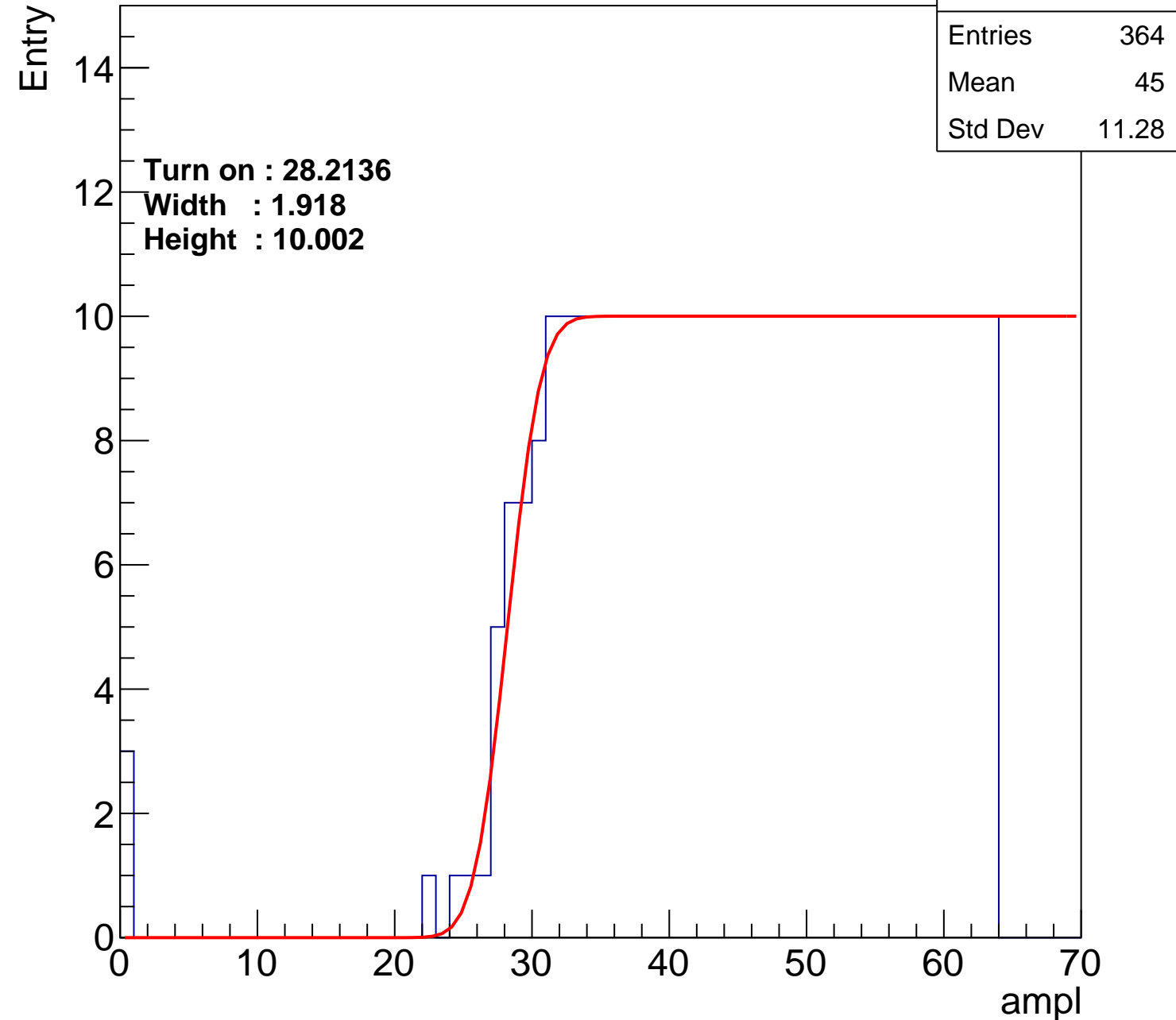
Width : 1.918

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch13

calib_packv5_042523_0143.root, FC#3, port B3

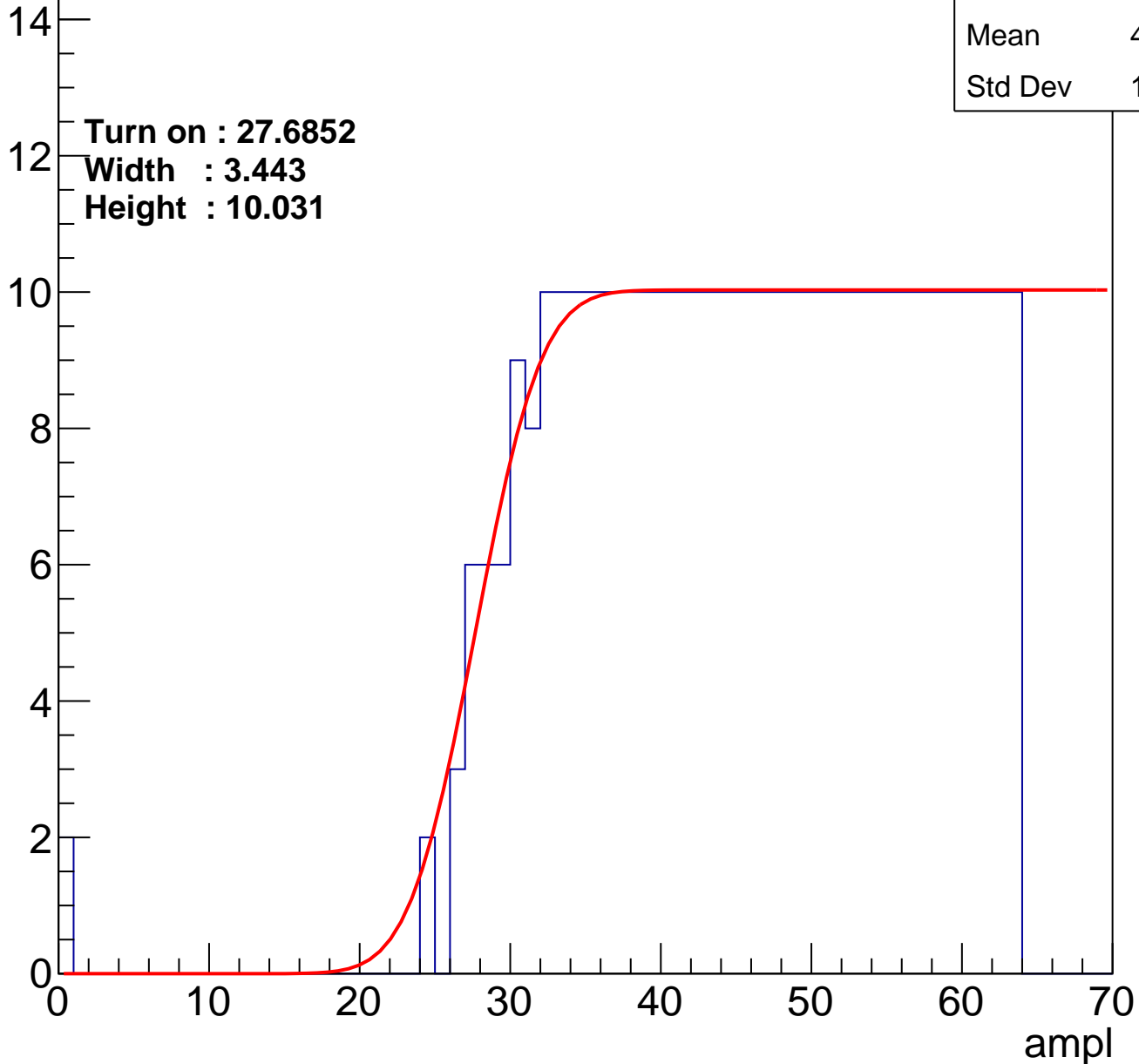
Entries	362
Mean	45.16
Std Dev	11.04

Turn on : 27.6852

Width : 3.443

Height : 10.031

Entry



B1L000S, U18-ch14

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.54
Std Dev	11.33

Turn on : 27.3044

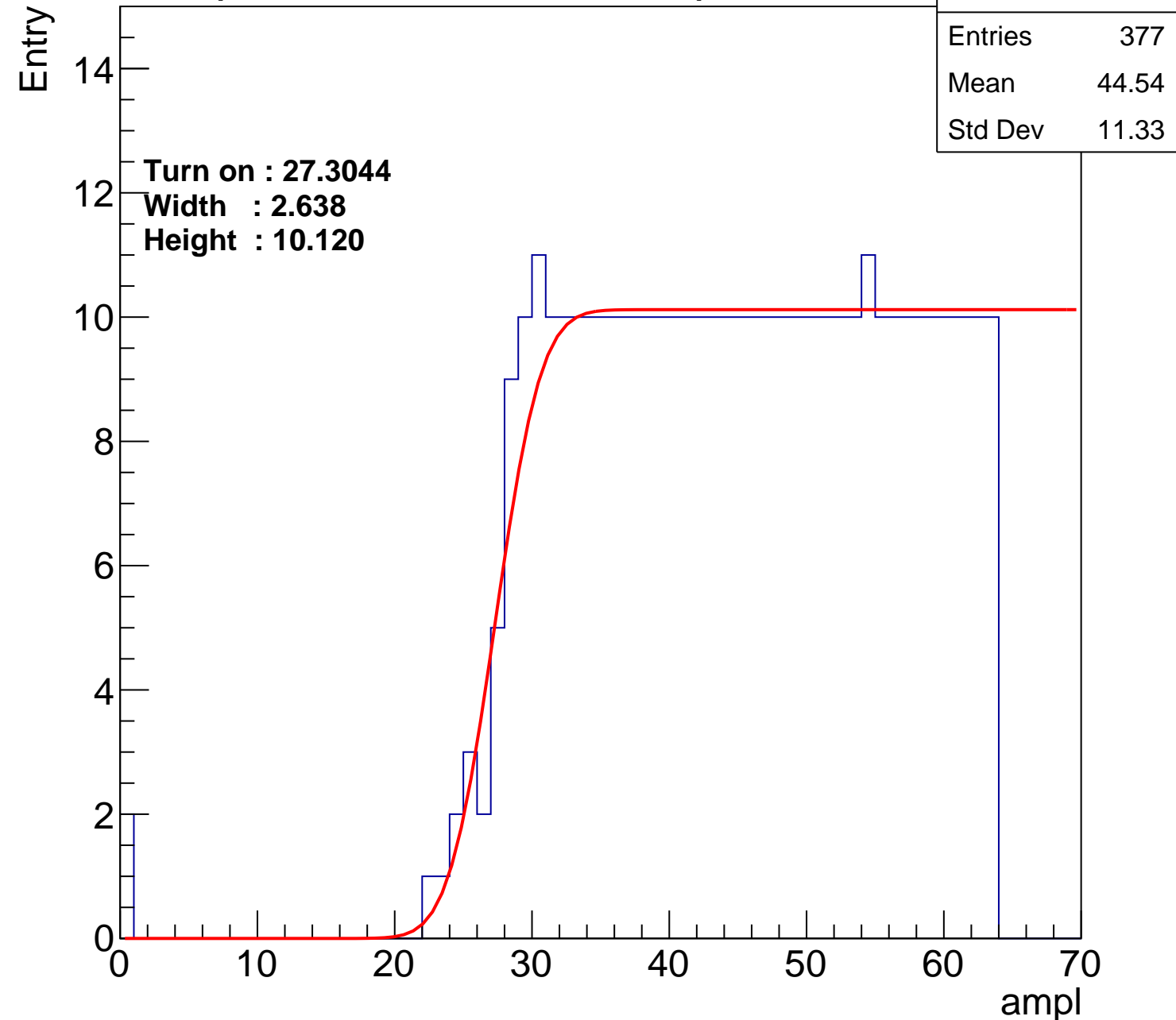
Width : 2.638

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch15

calib_packv5_042523_0143.root, FC#3, port B3

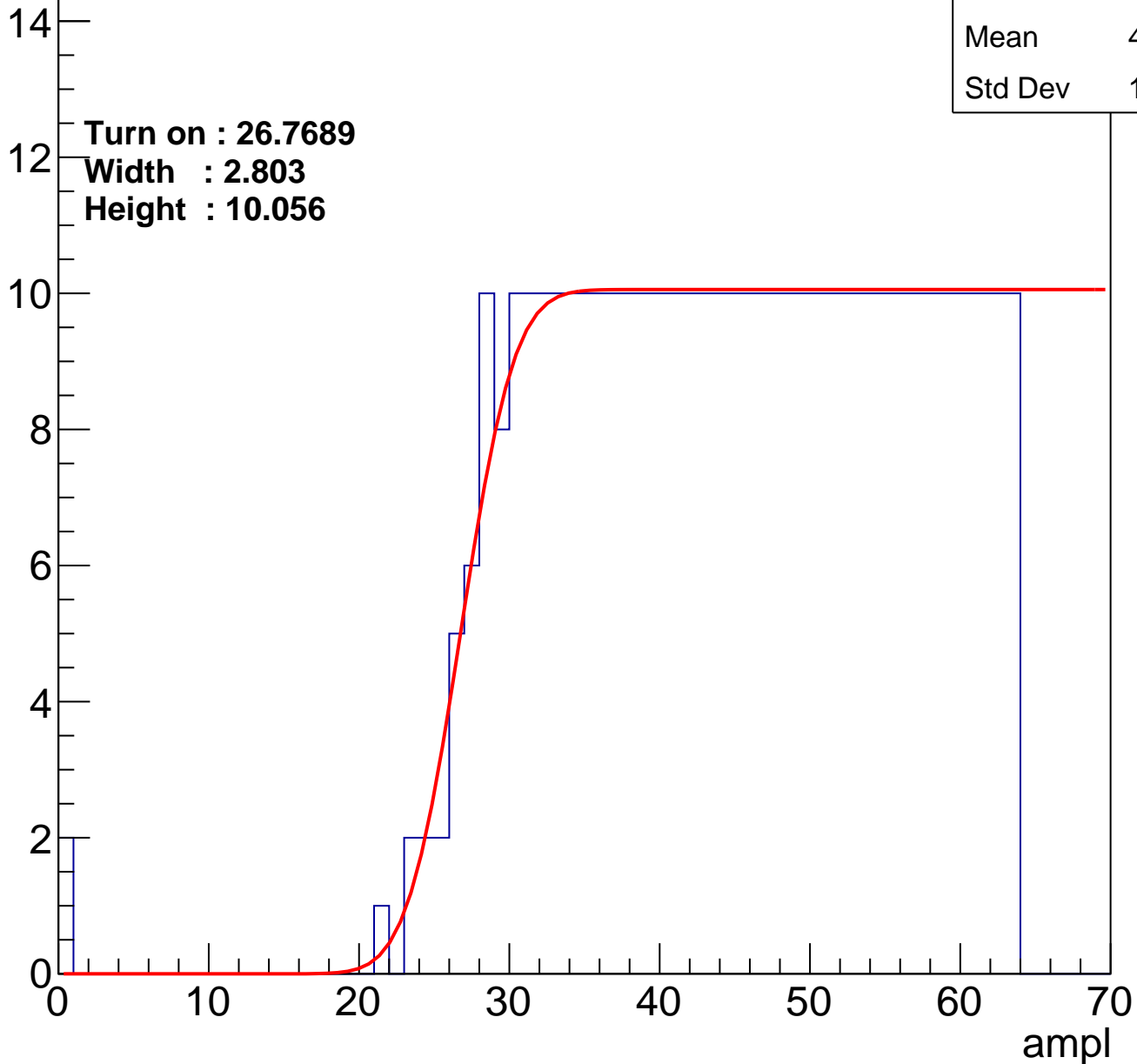
Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.7689

Width : 2.803

Height : 10.056

Entry



B1L000S, U18-ch16

calib_packv5_042523_0143.root, FC#3, port B3

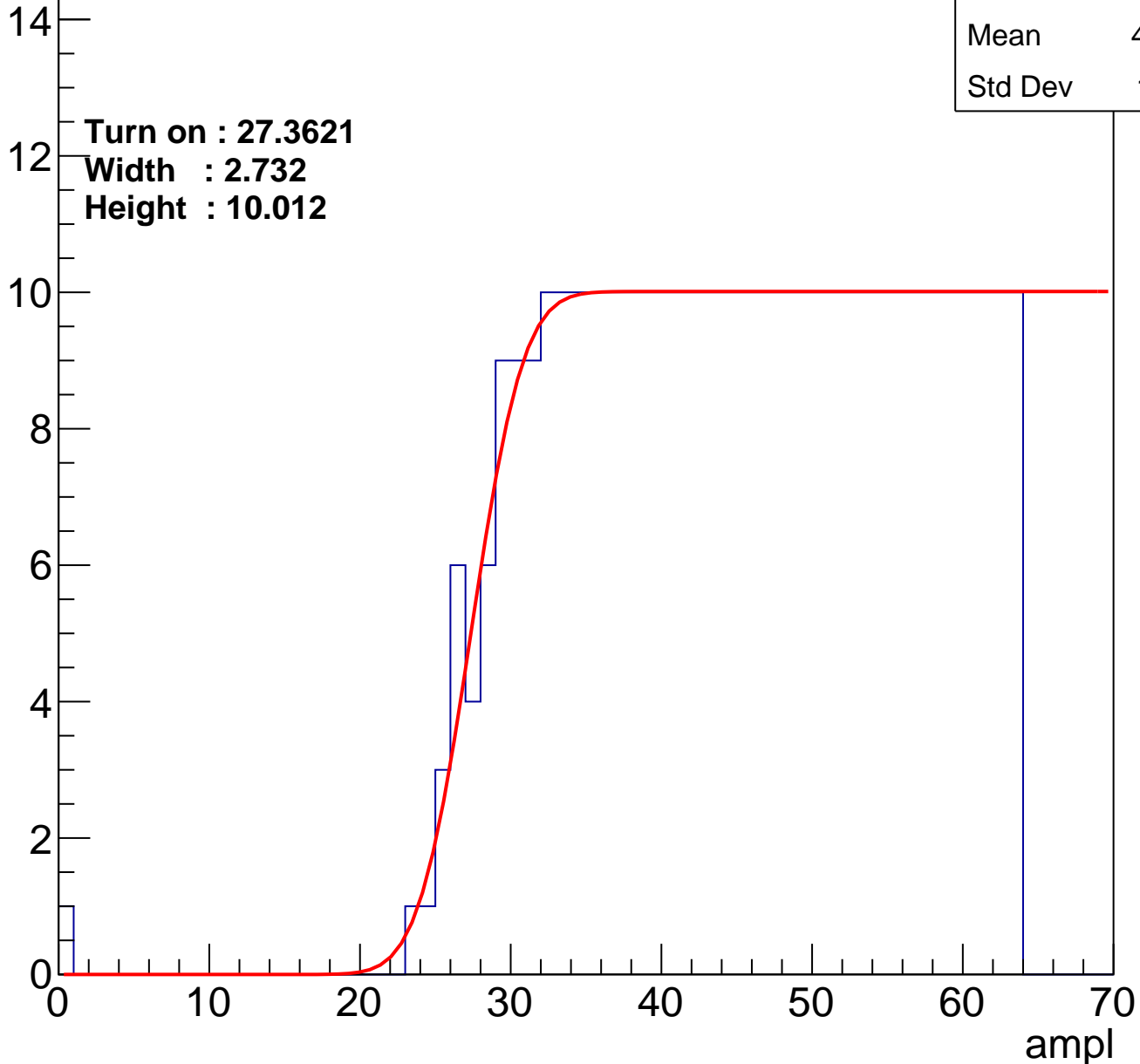
Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.3621

Width : 2.732

Height : 10.012

Entry



B1L000S, U18-ch17

calib_packv5_042523_0143.root, FC#3, port B3

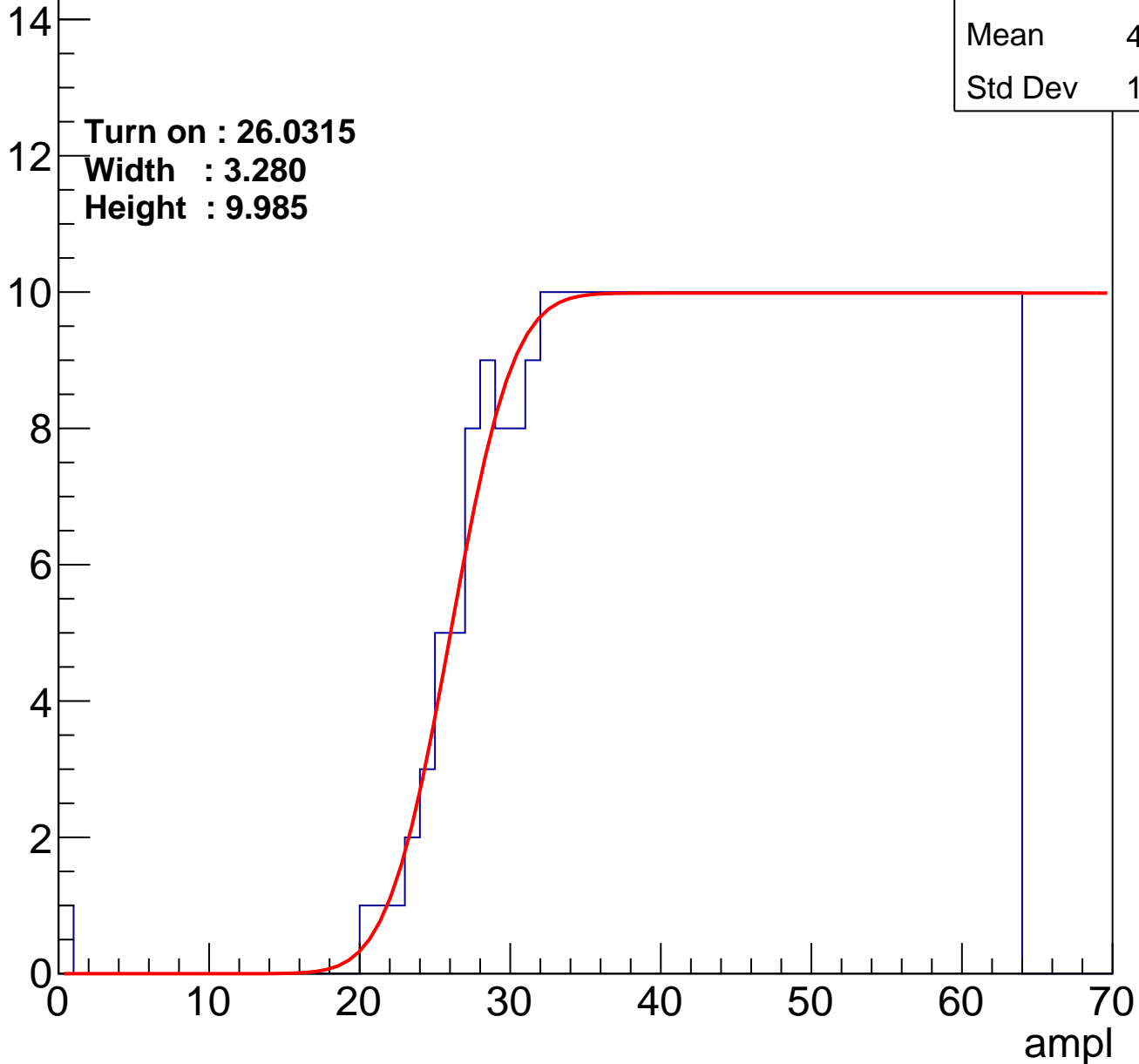
Entries	381
Mean	44.24
Std Dev	11.42

Turn on : 26.0315

Width : 3.280

Height : 9.985

Entry



B1L000S, U18-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	397
Mean	43.5
Std Dev	11.77

Turn on : 24.2906

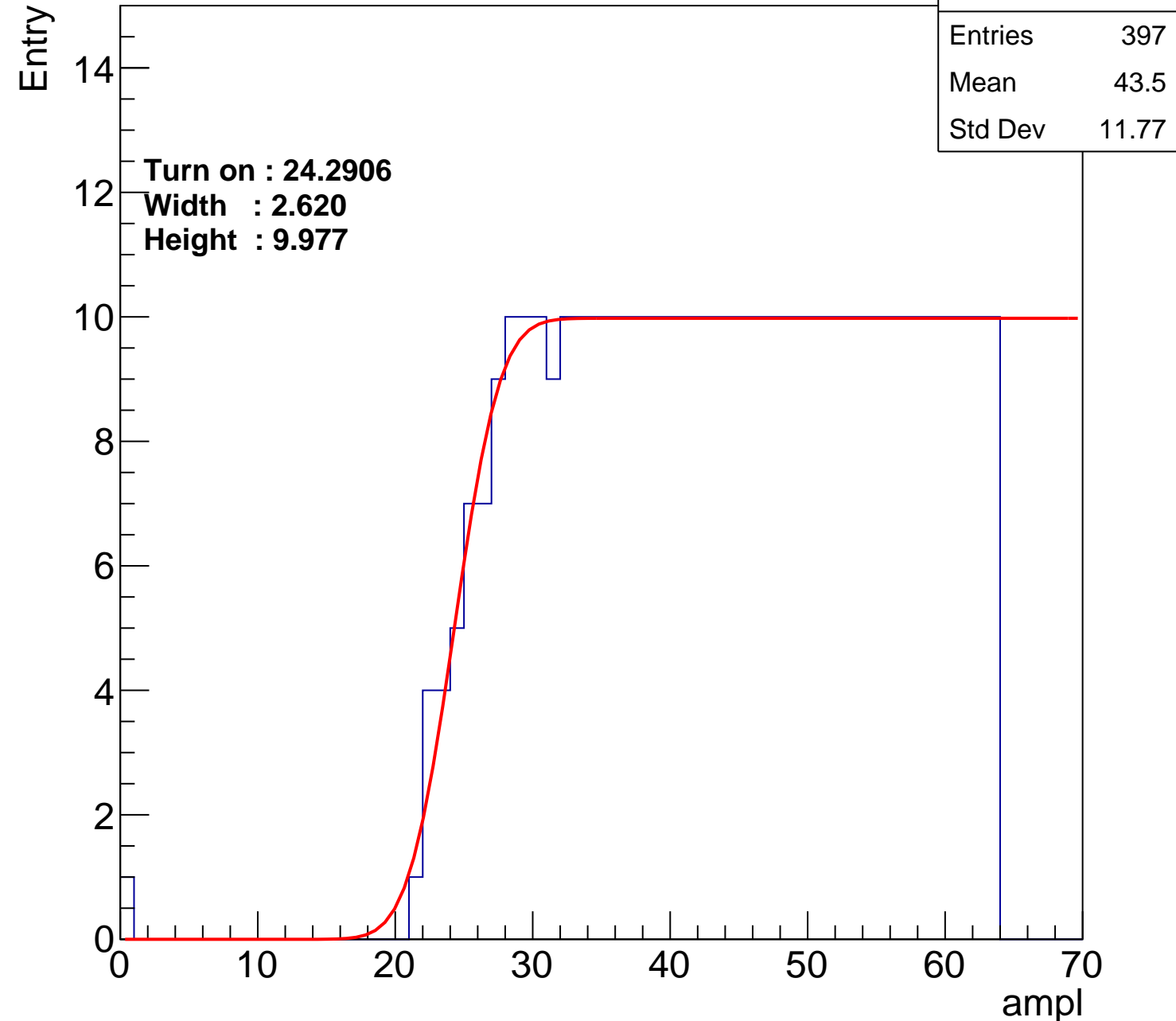
Width : 2.620

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.9237

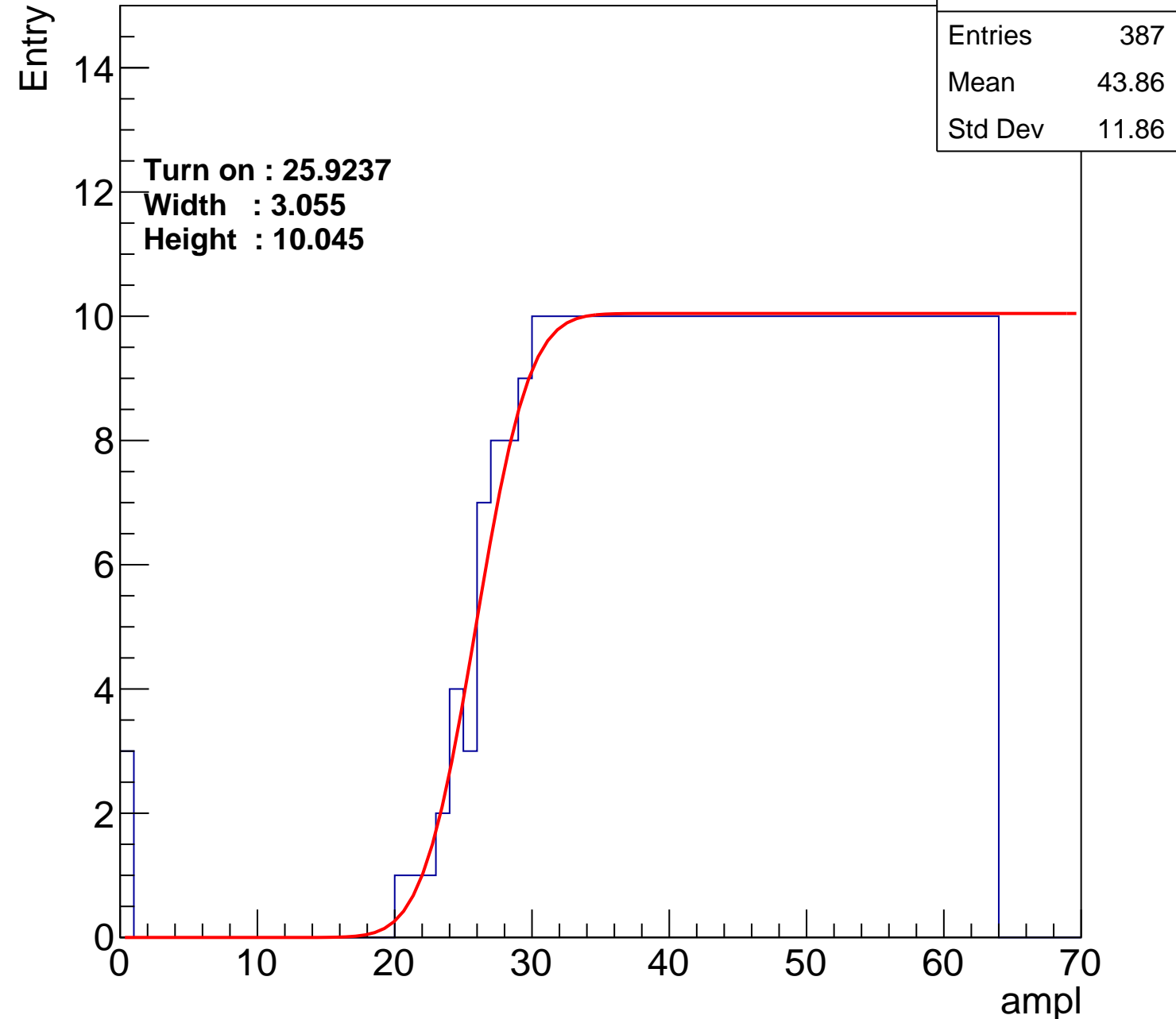
Width : 3.055

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.97
Std Dev	11.79

Turn on : 25.9880

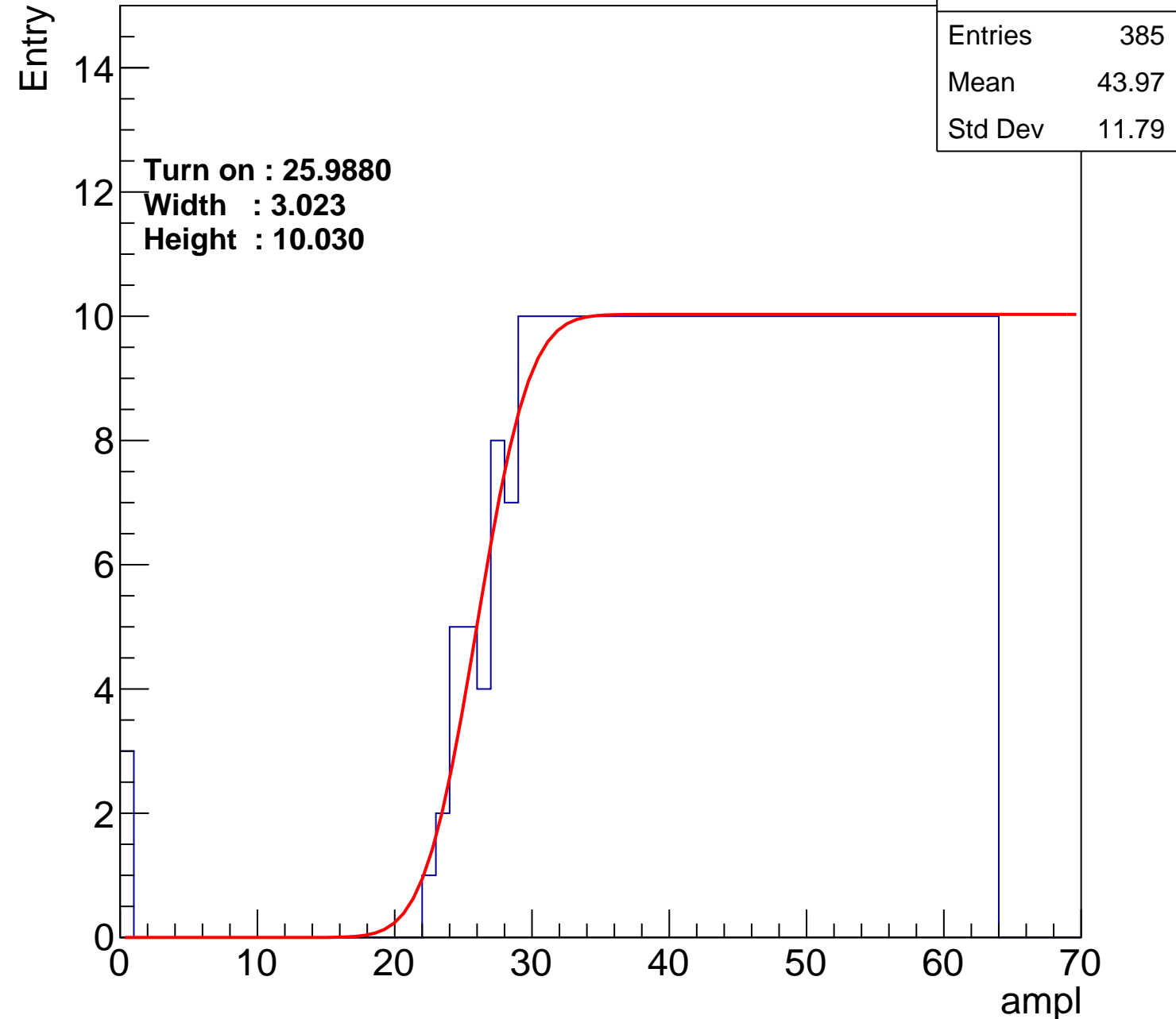
Width : 3.023

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch21

calib_packv5_042523_0143.root, FC#3, port B3

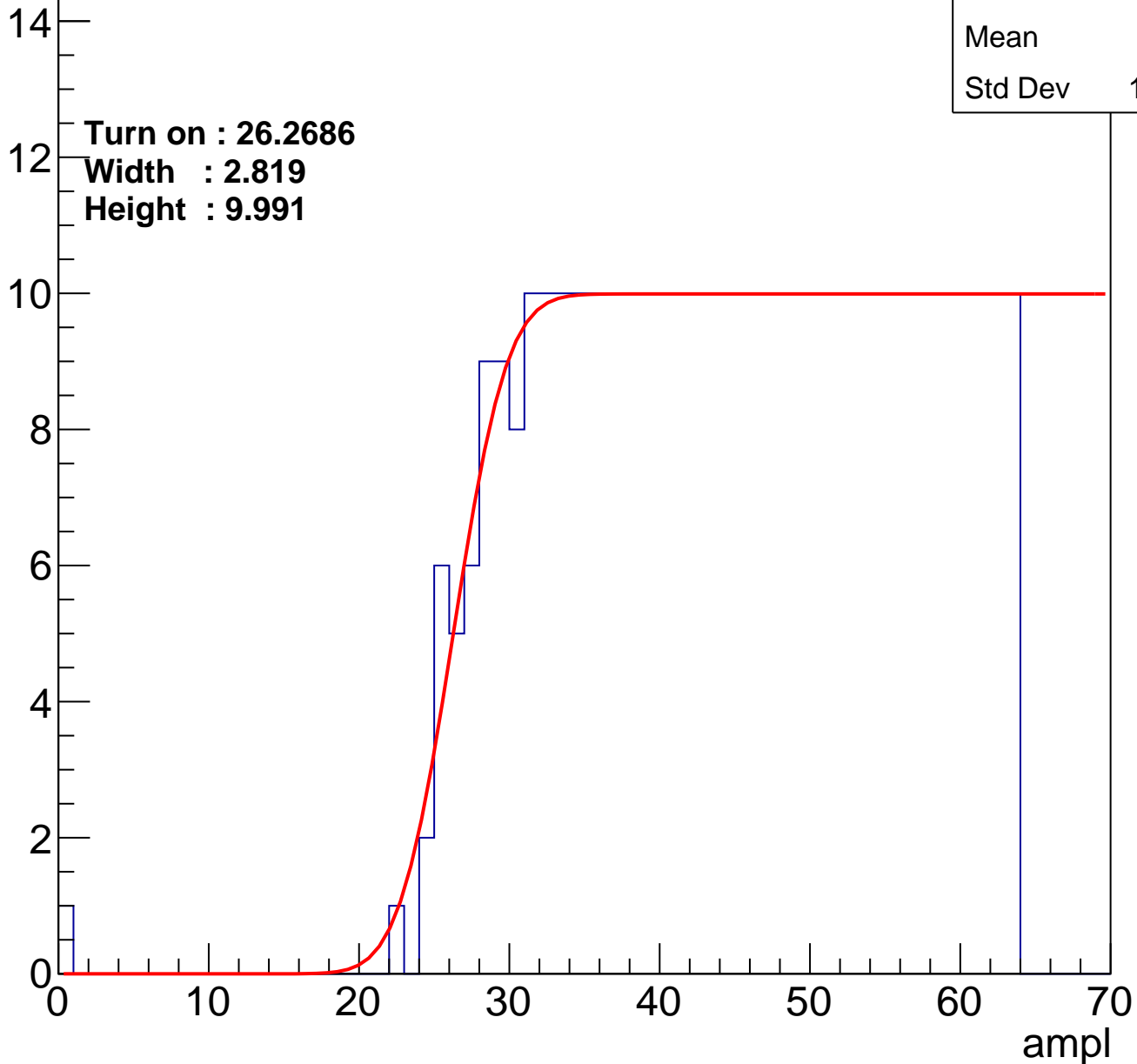
Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 26.2686

Width : 2.819

Height : 9.991

Entry



B1L000S, U18-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.57
Std Dev	11.88

Turn on : 25.4813

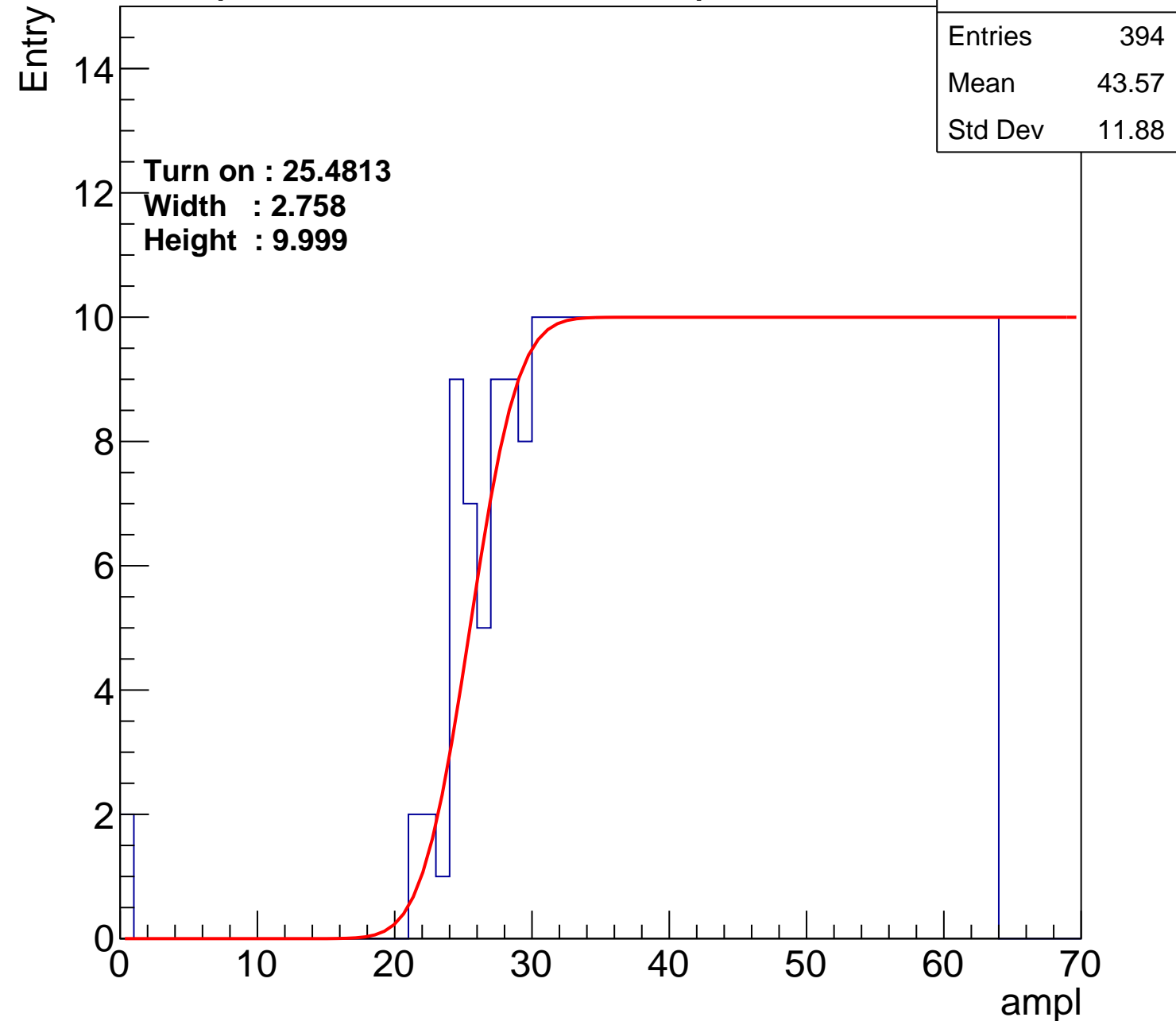
Width : 2.758

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch23

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.72
Std Dev	12.21

Turn on : 25.8494

Width : 3.692

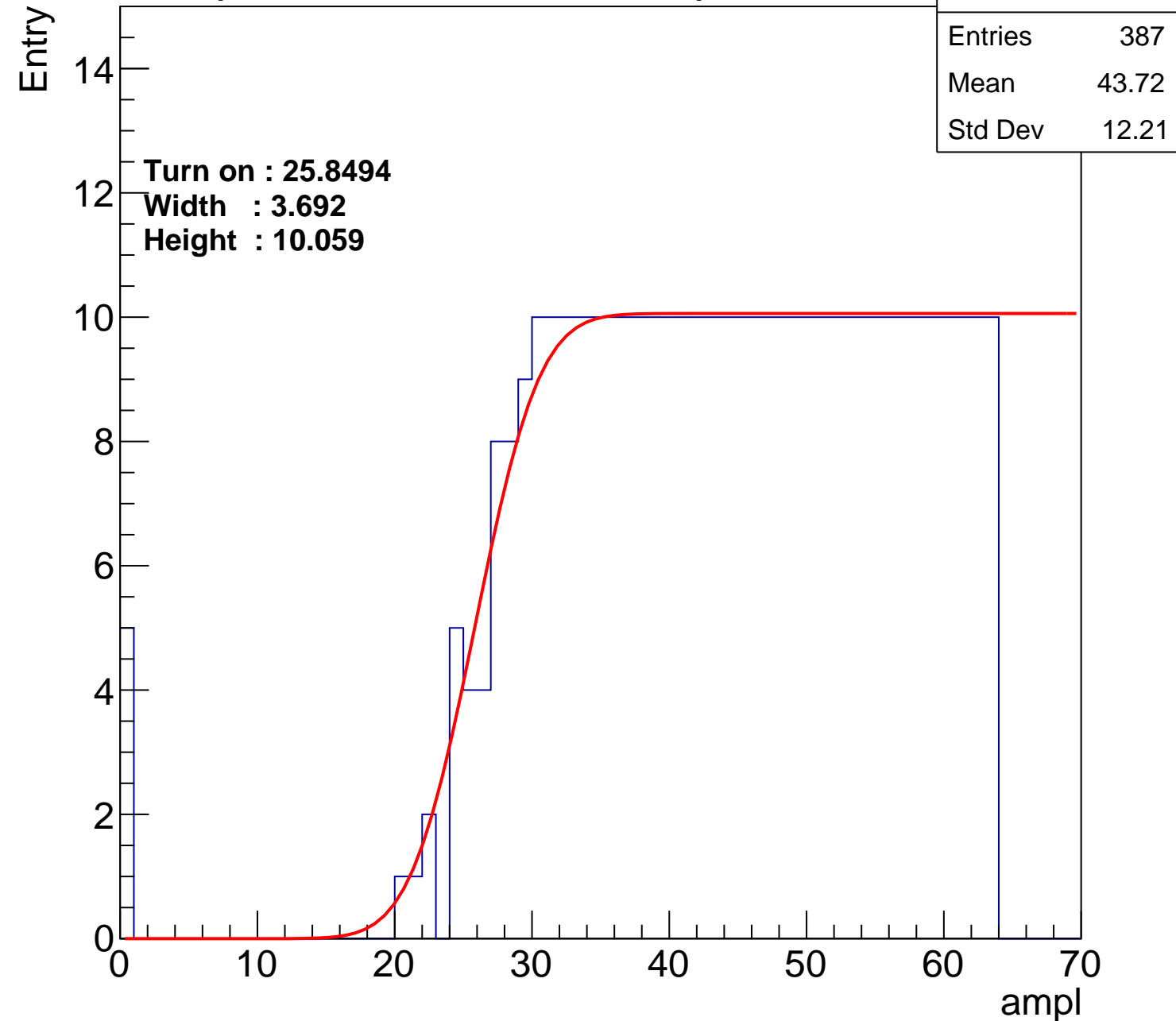
Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U18-ch24

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.06
Std Dev	12.05

Turn on : 26.9583

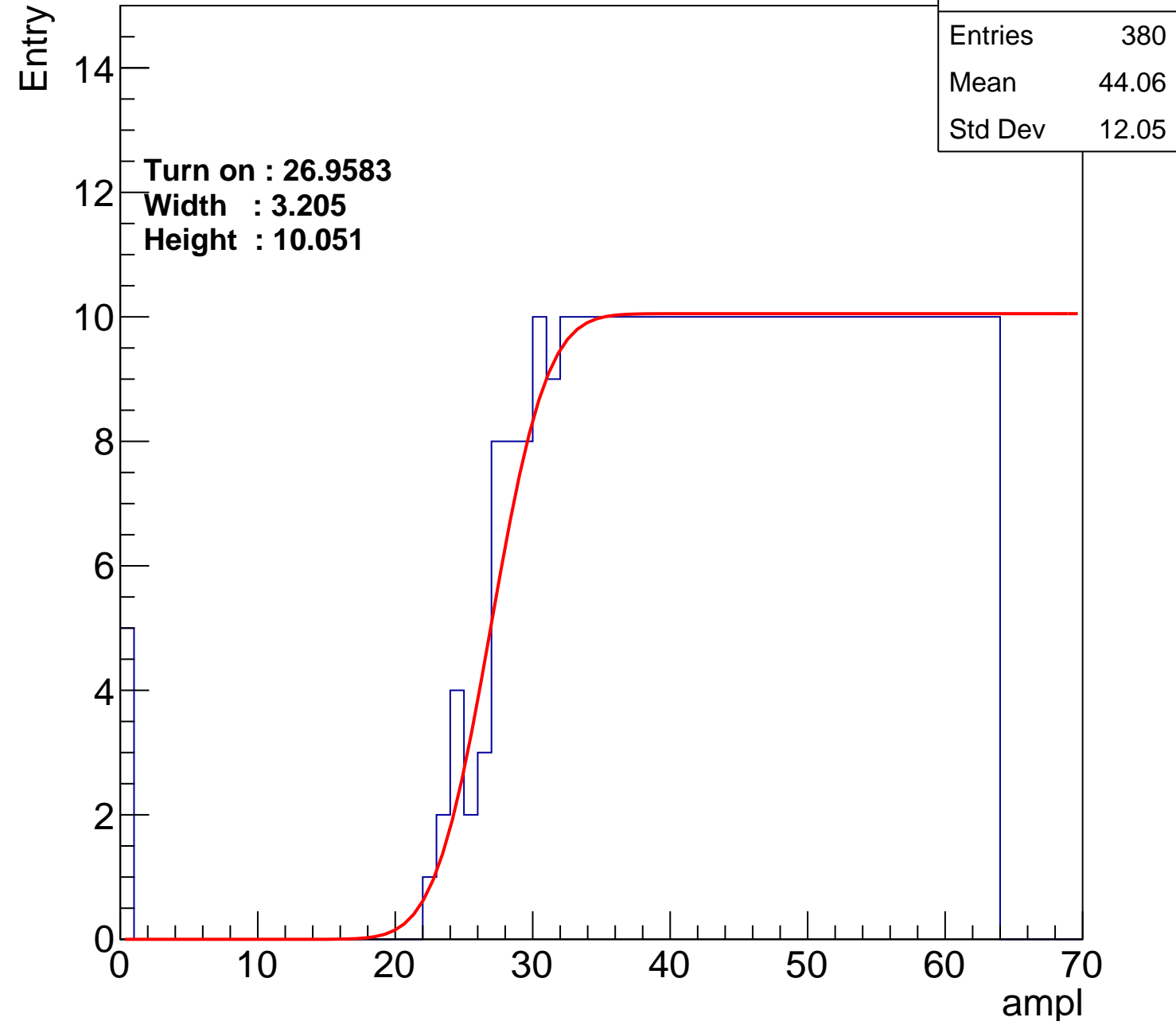
Width : 3.205

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch25

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.54
Std Dev	11.2

Turn on : 26.7724

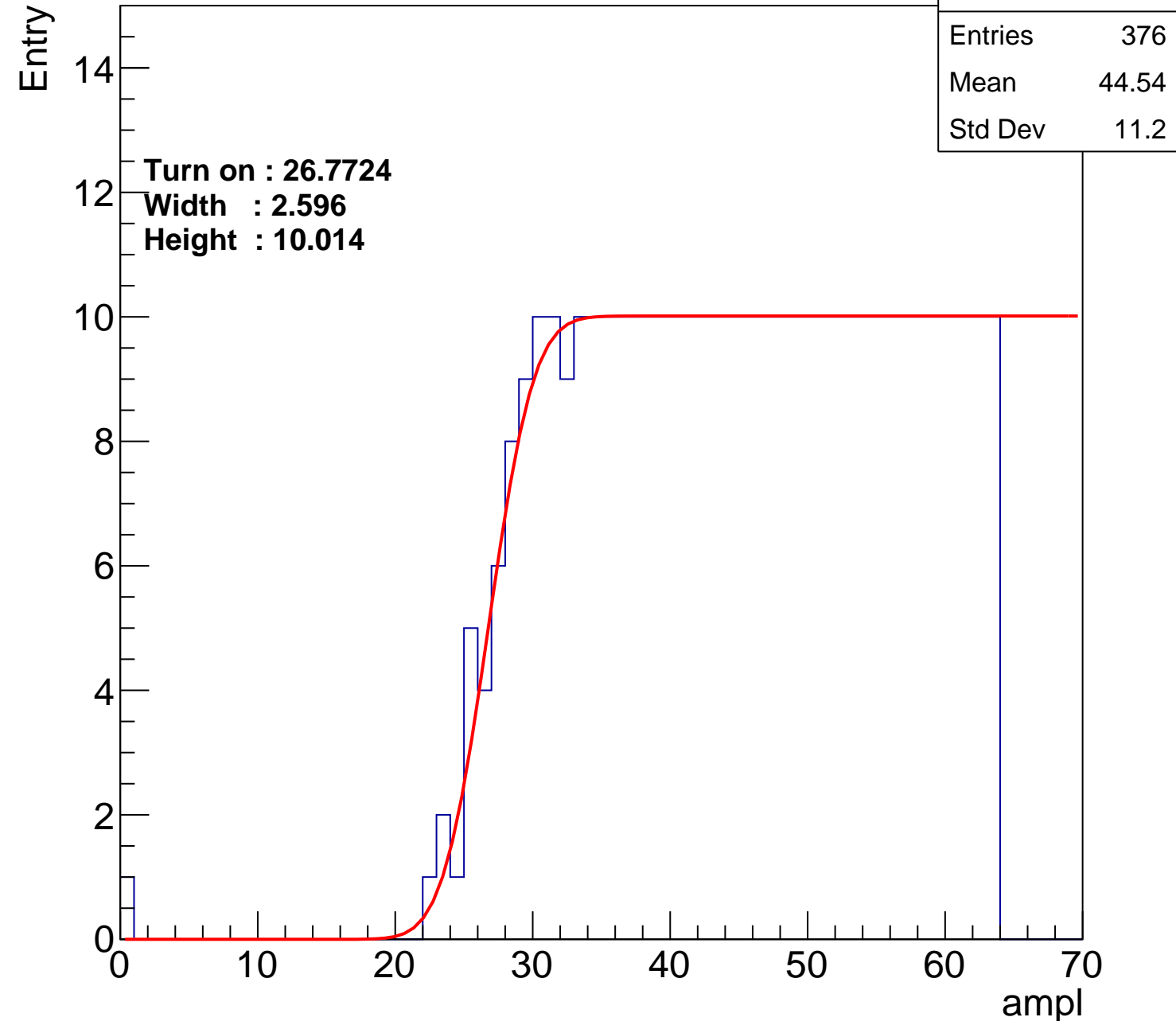
Width : 2.596

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.13
Std Dev	11.57

Turn on : 25.9620

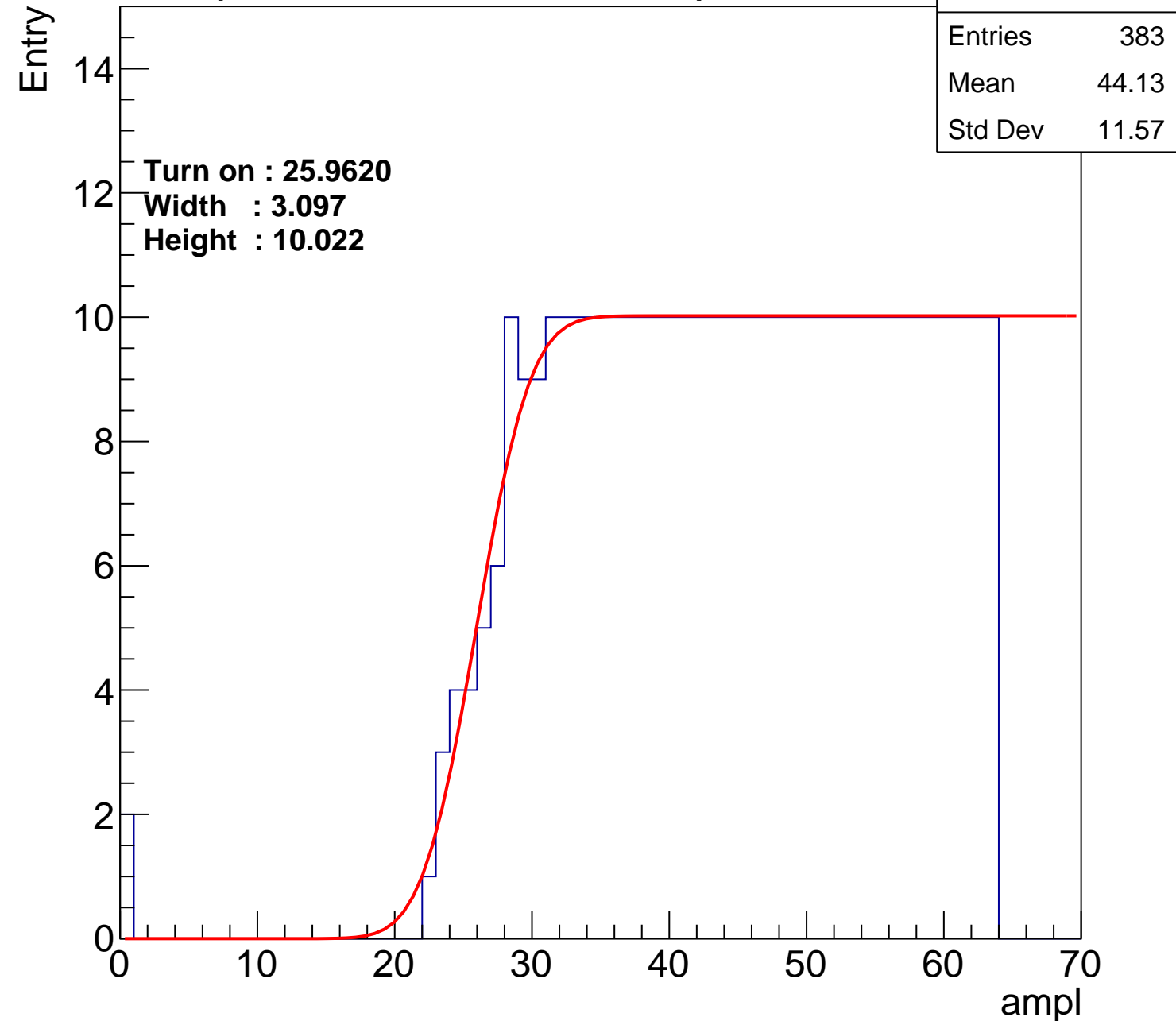
Width : 3.097

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch27

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.7272

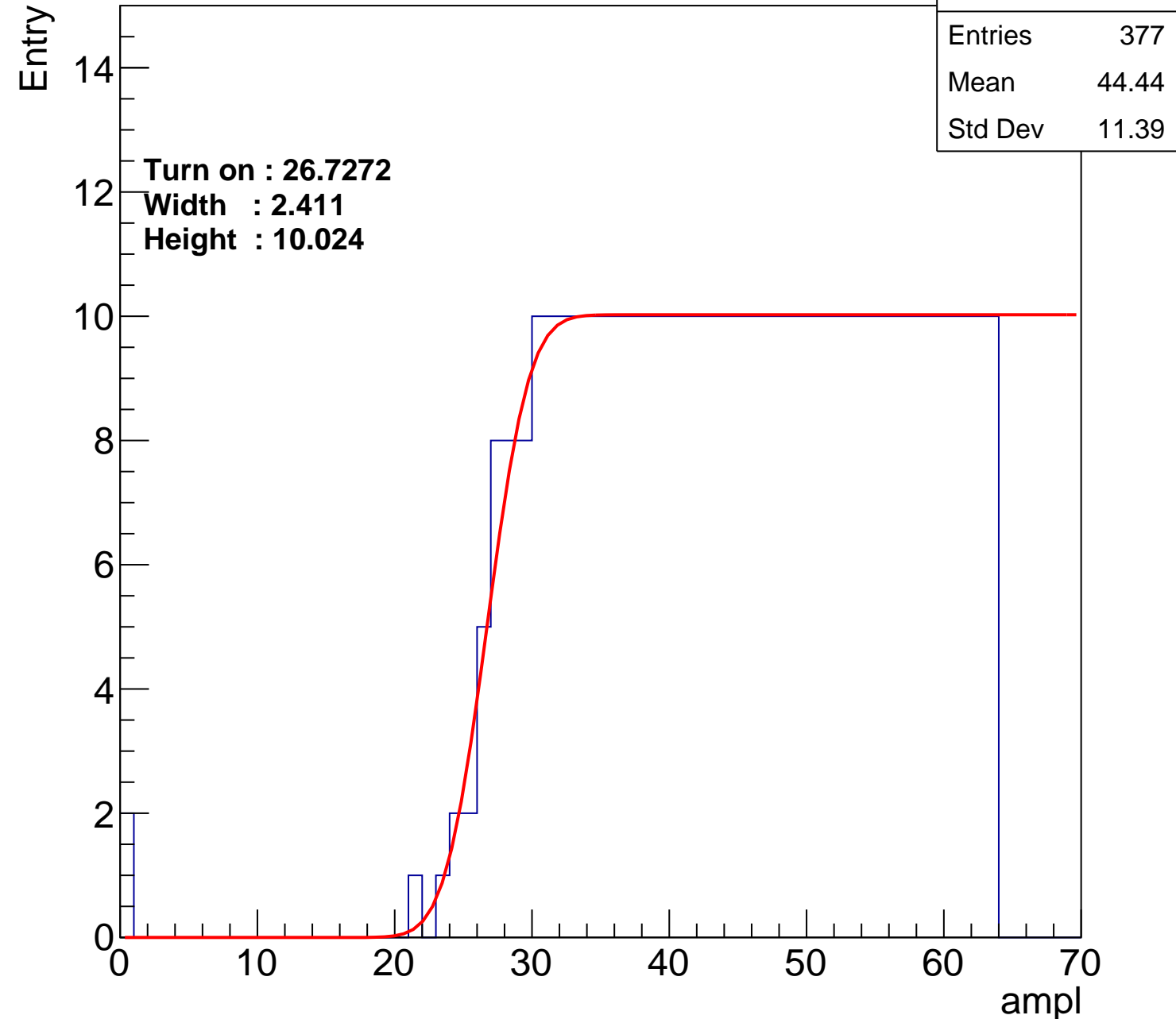
Width : 2.411

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.51
Std Dev	11.51

Turn on : 27.2834

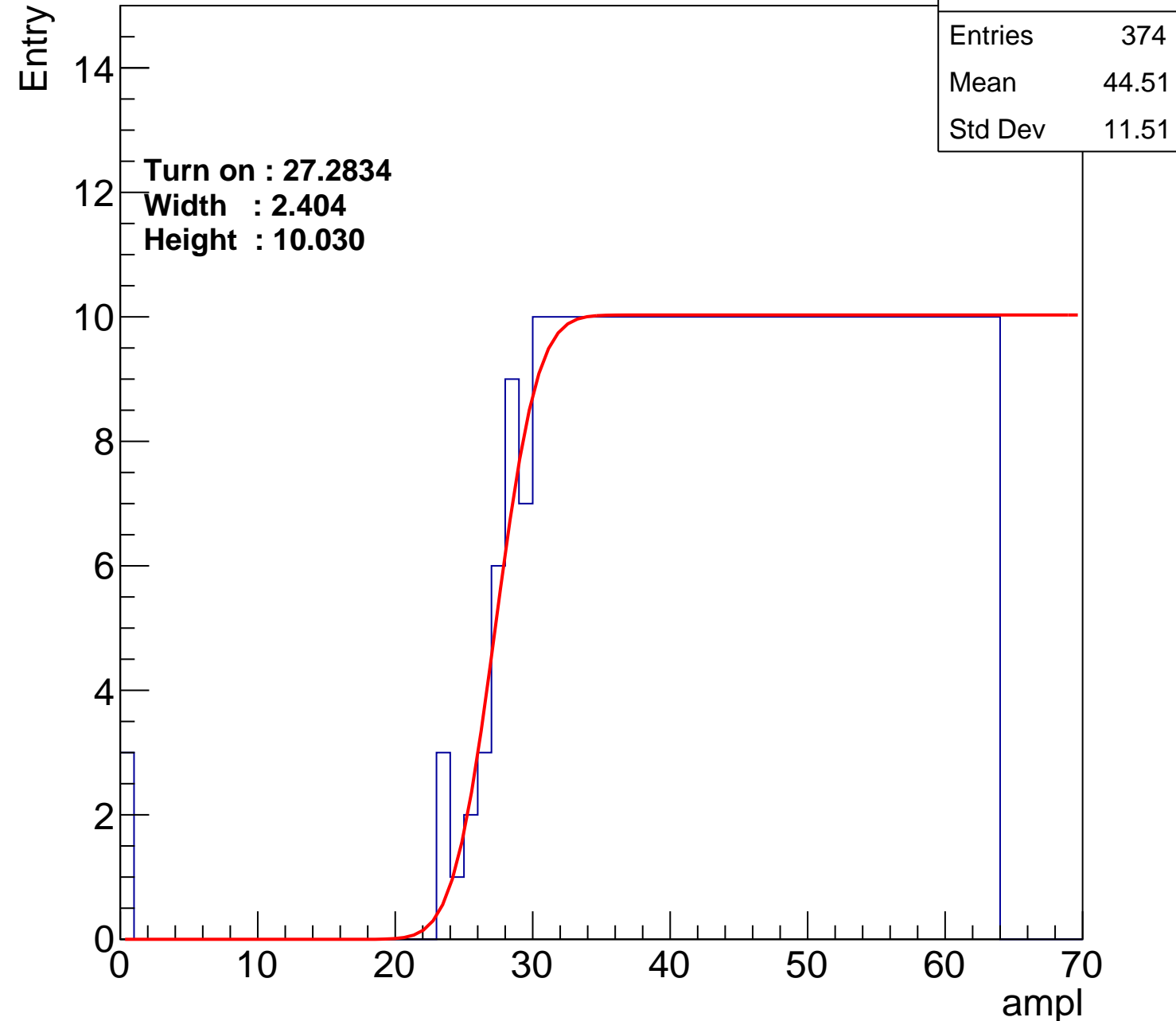
Width : 2.404

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch29

calib_packv5_042523_0143.root, FC#3, port B3

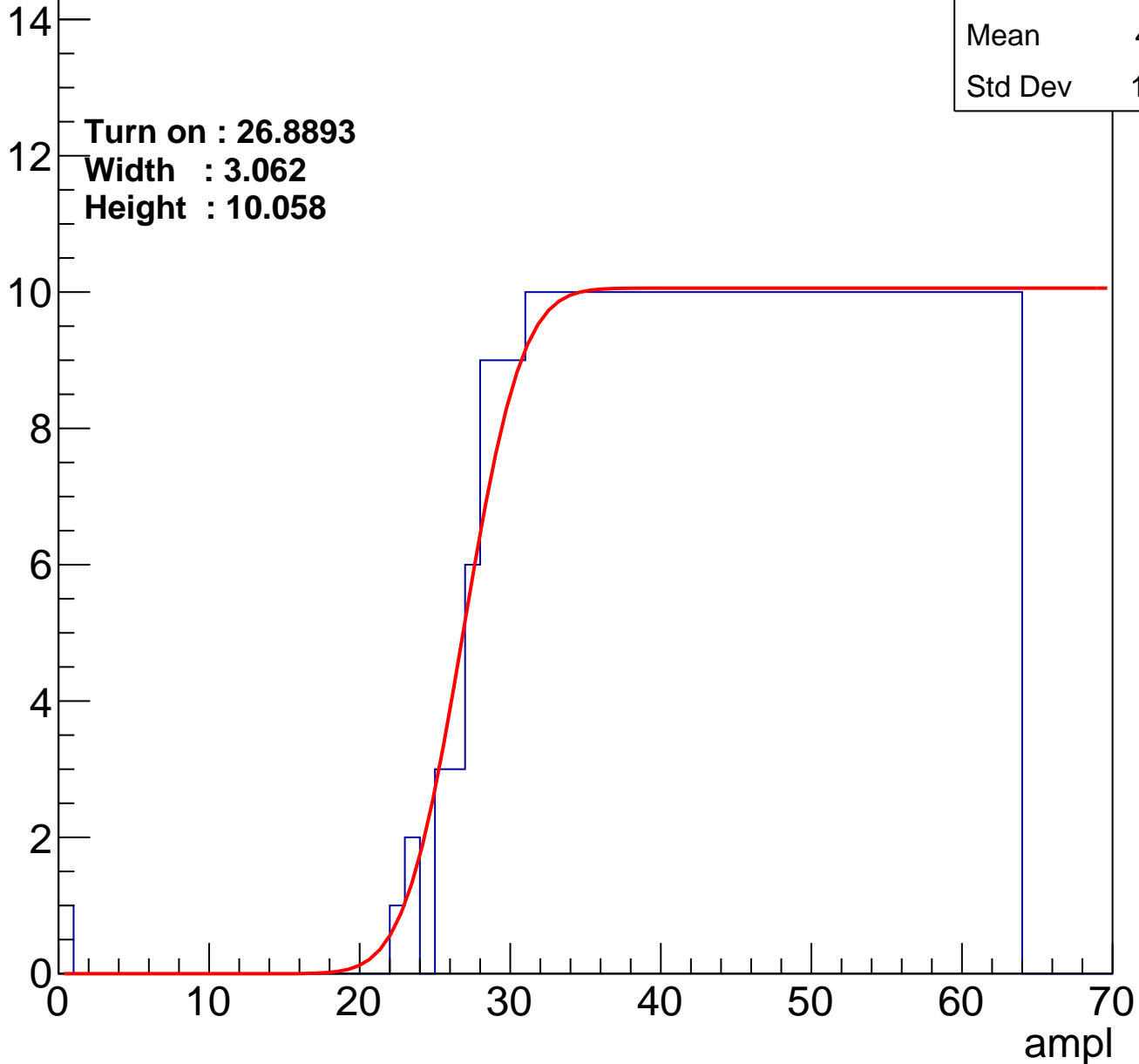
Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 26.8893

Width : 3.062

Height : 10.058

Entry



B1L000S, U18-ch30

calib_packv5_042523_0143.root, FC#3, port B3

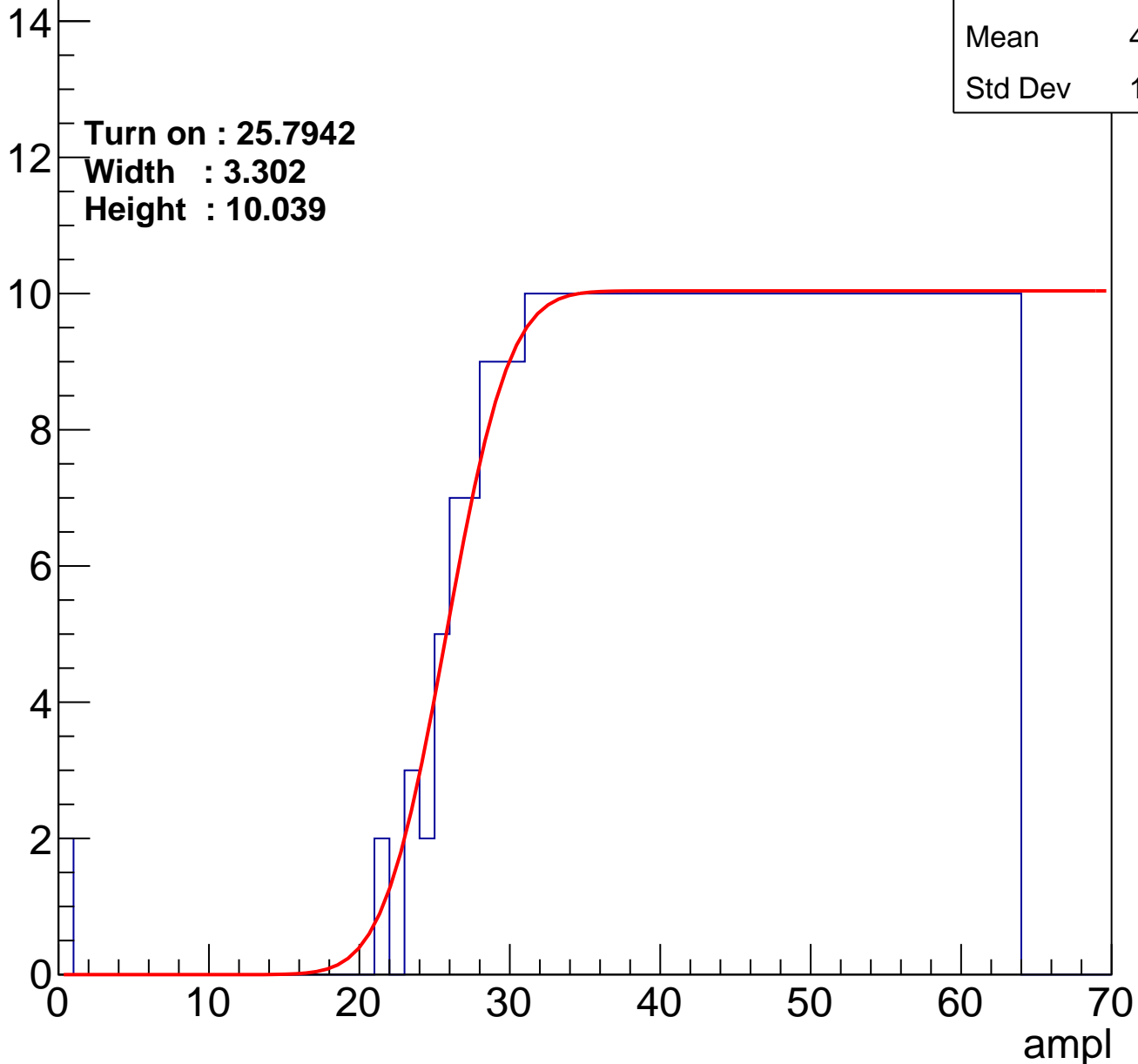
Entries	385
Mean	44.02
Std Dev	11.64

Turn on : 25.7942

Width : 3.302

Height : 10.039

Entry



B1L000S, U18-ch31

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	43.97
Std Dev	11.84

Turn on : 25.9435

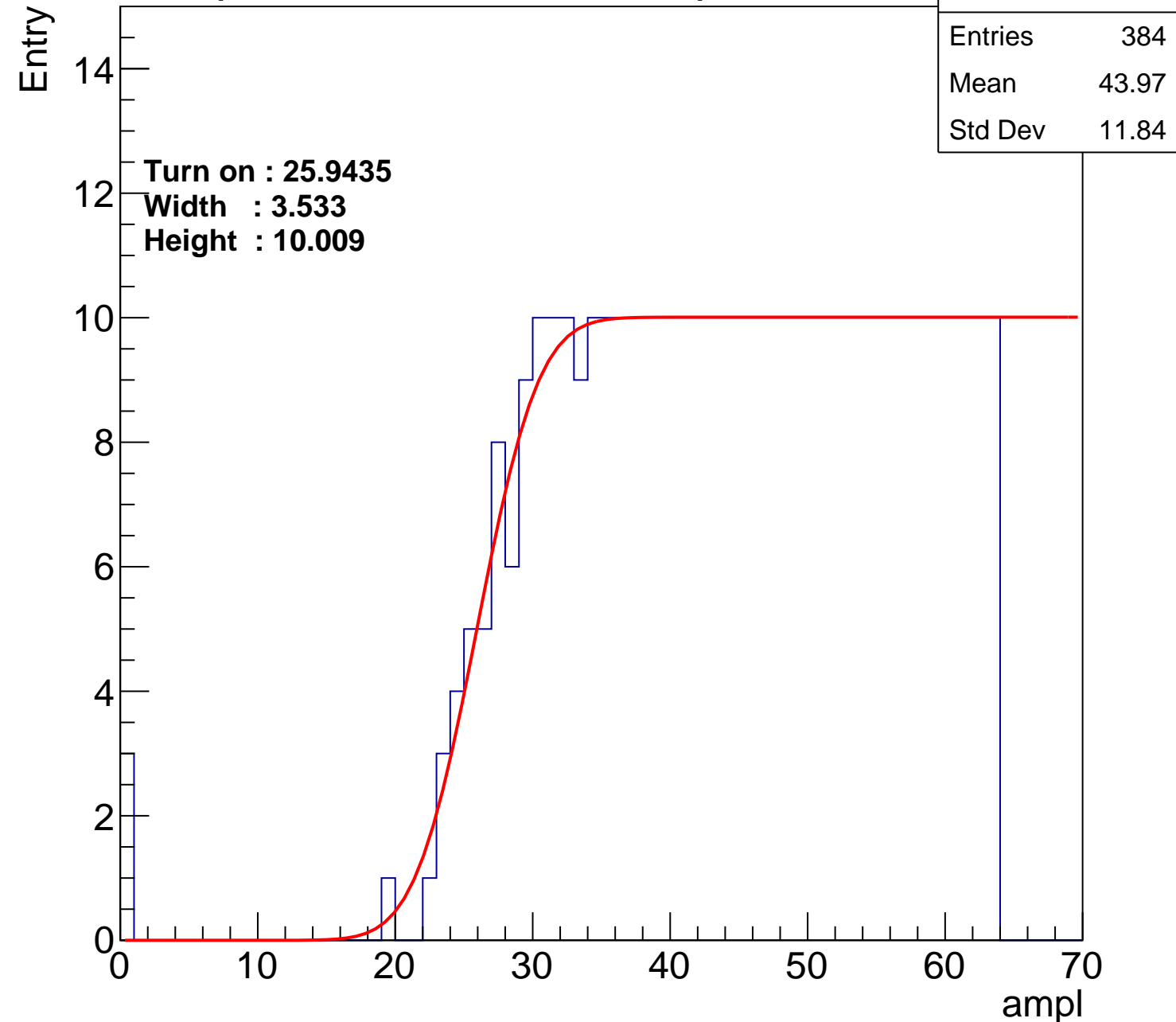
Width : 3.533

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch32

calib_packv5_042523_0143.root, FC#3, port B3

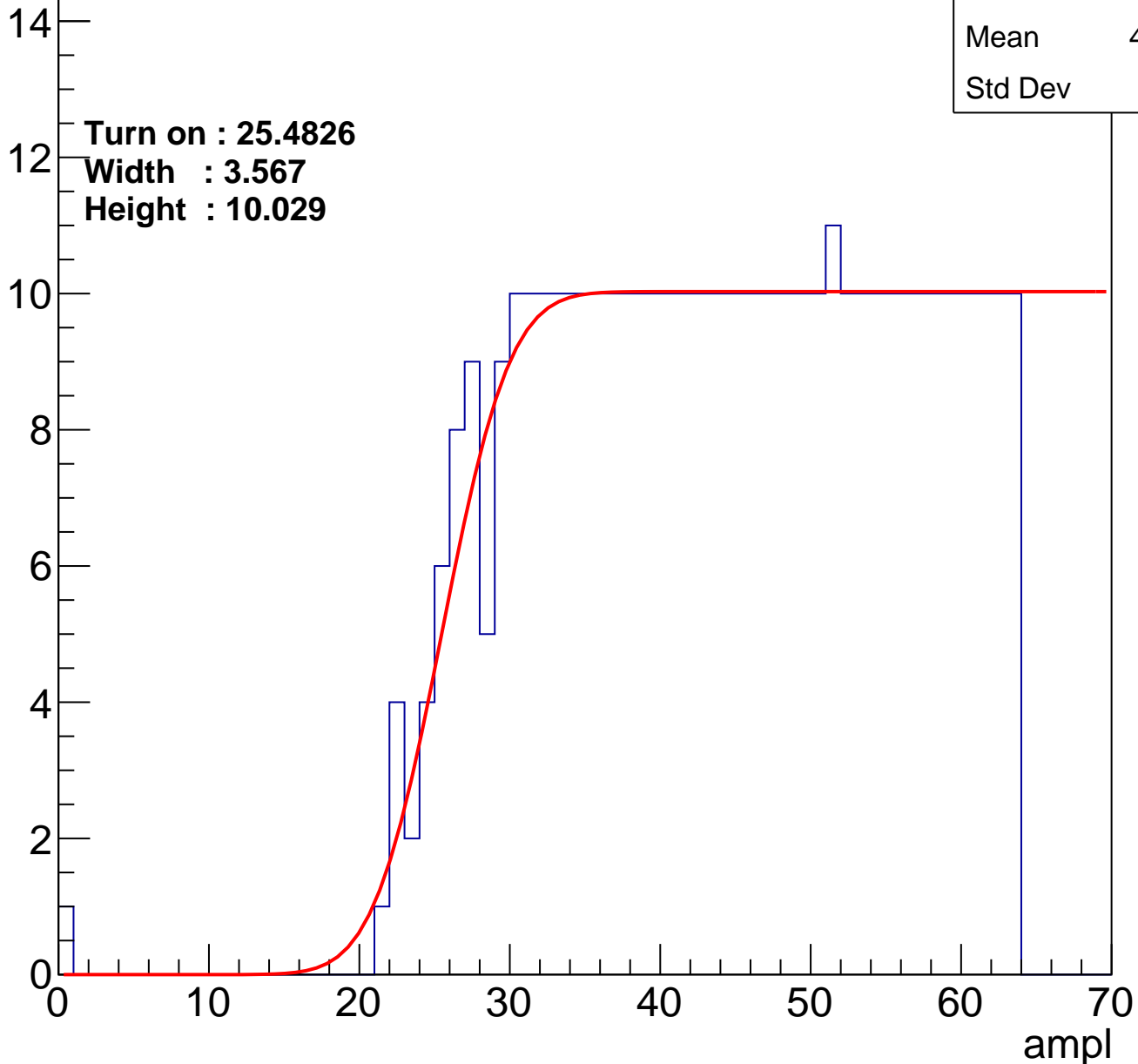
Entries	390
Mean	43.88
Std Dev	11.6

Turn on : 25.4826

Width : 3.567

Height : 10.029

Entry



B1L000S, U18-ch33

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 27.3307

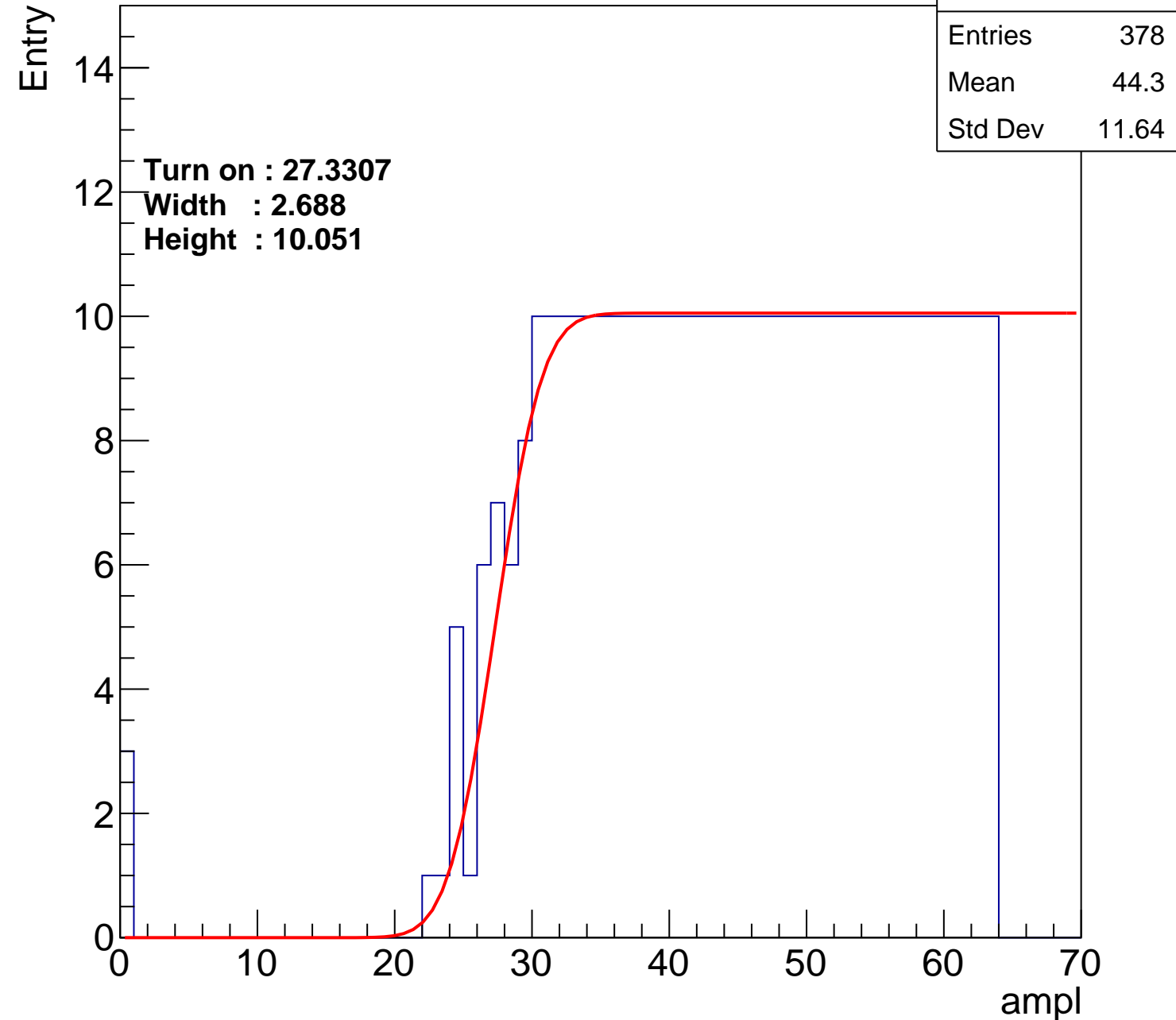
Width : 2.688

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.21
Std Dev	11.37

Turn on : 26.0178

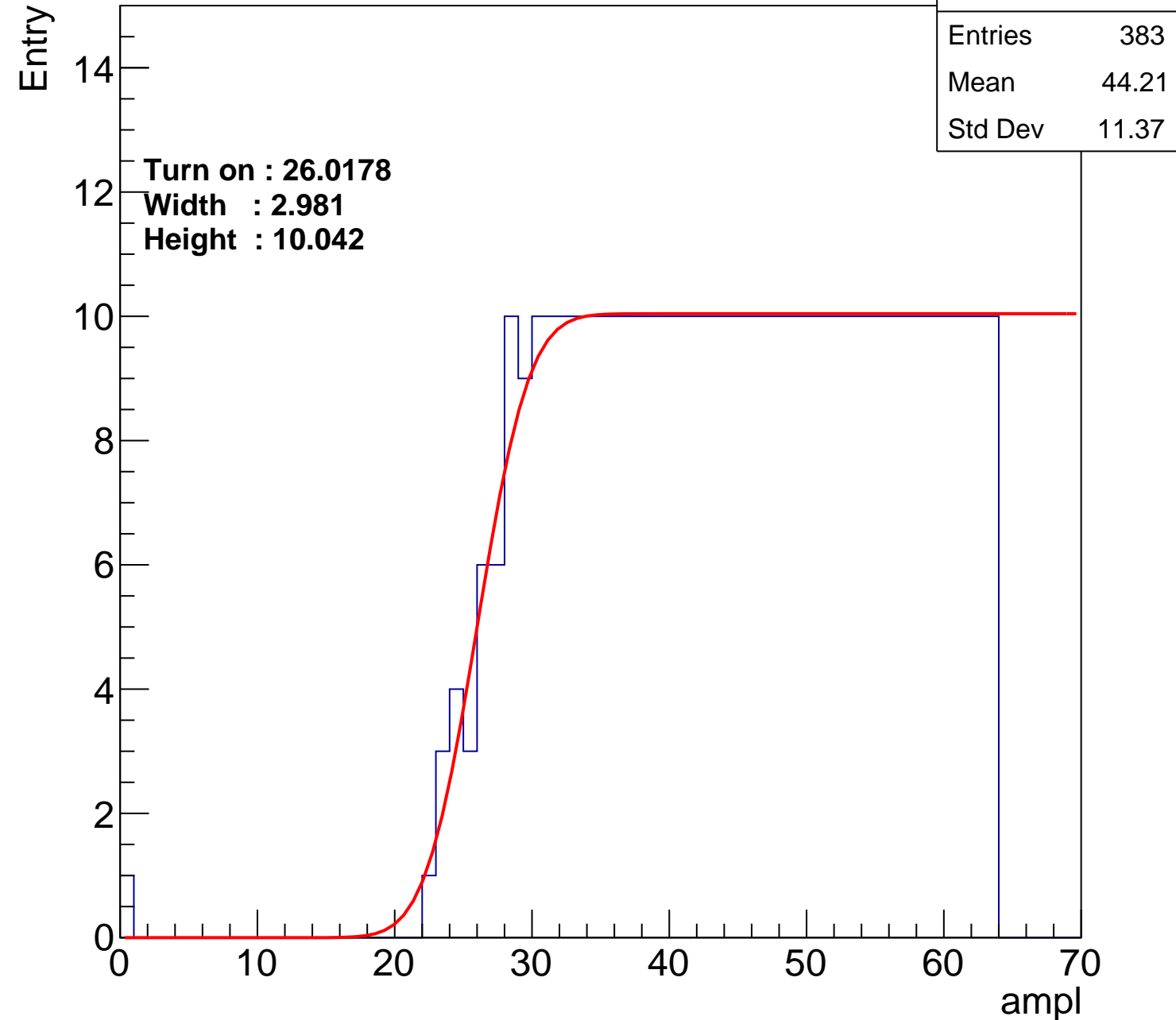
Width : 2.981

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch35

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.57
Std Dev	11.77

Turn on : 24.9673

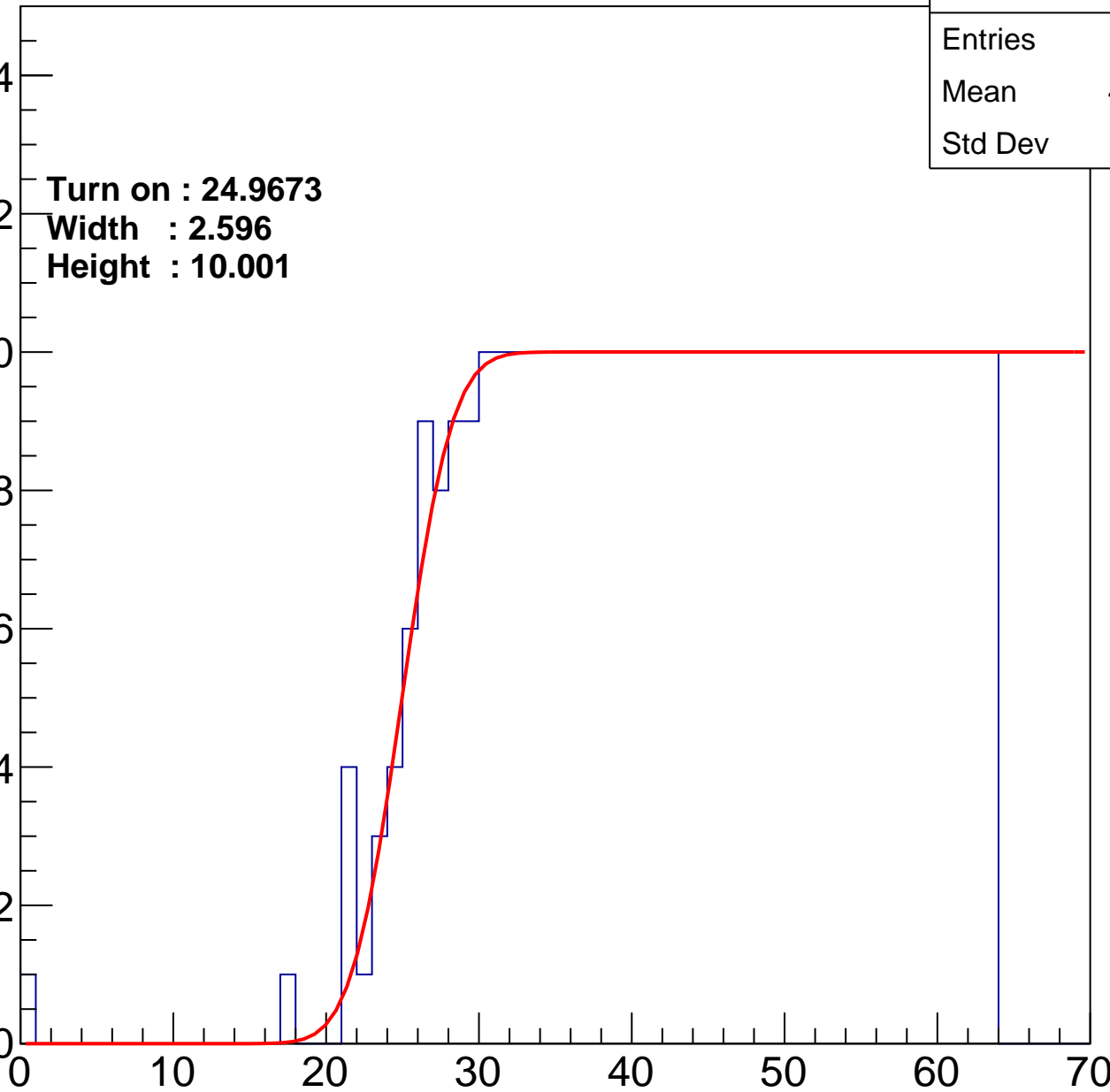
Width : 2.596

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch36

calib_packv5_042523_0143.root, FC#3, port B3

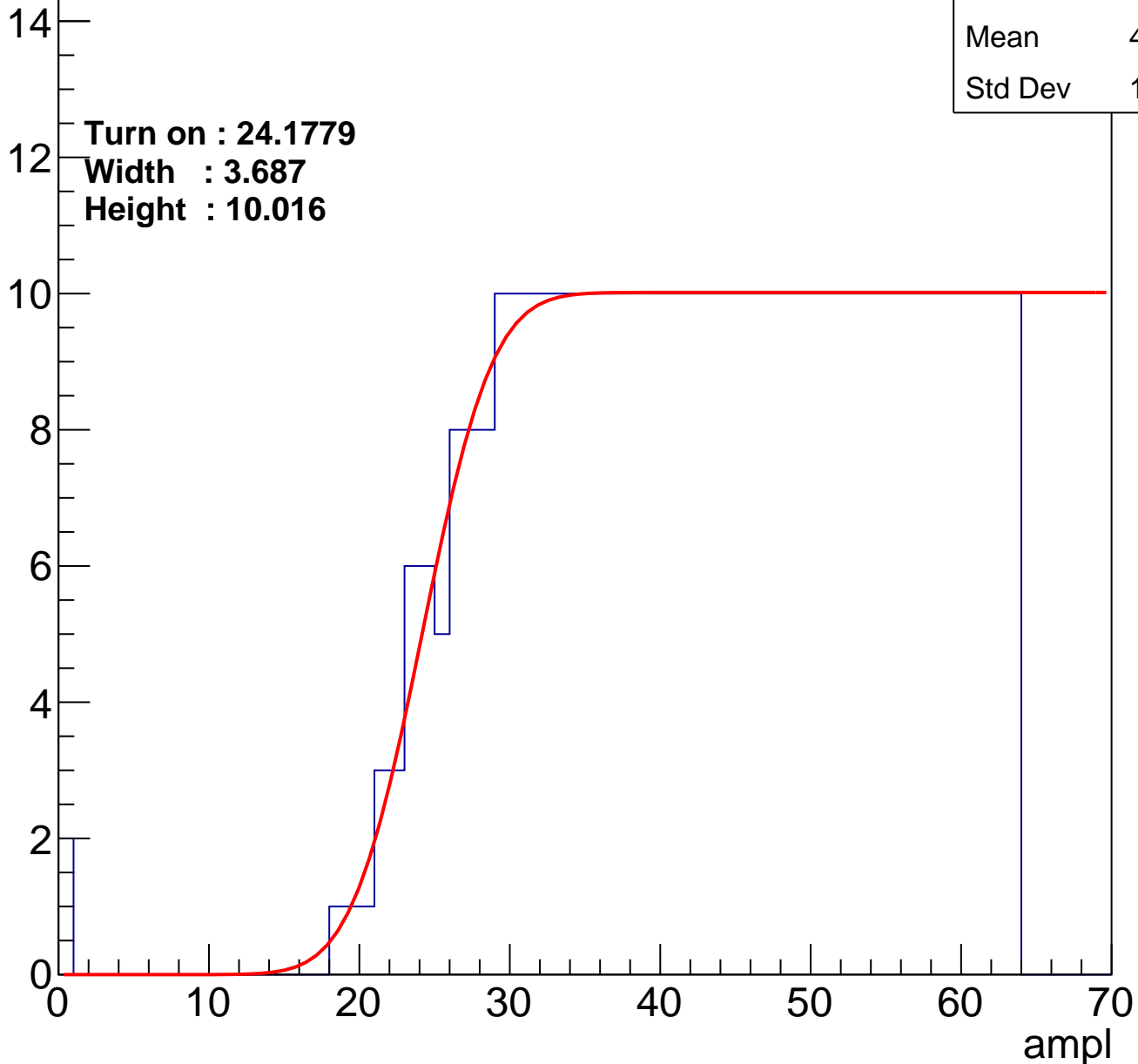
Entries	402
Mean	43.14
Std Dev	12.15

Turn on : 24.1779

Width : 3.687

Height : 10.016

Entry



B1L000S, U18-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.27
Std Dev	12.28

Turn on : 24.7936

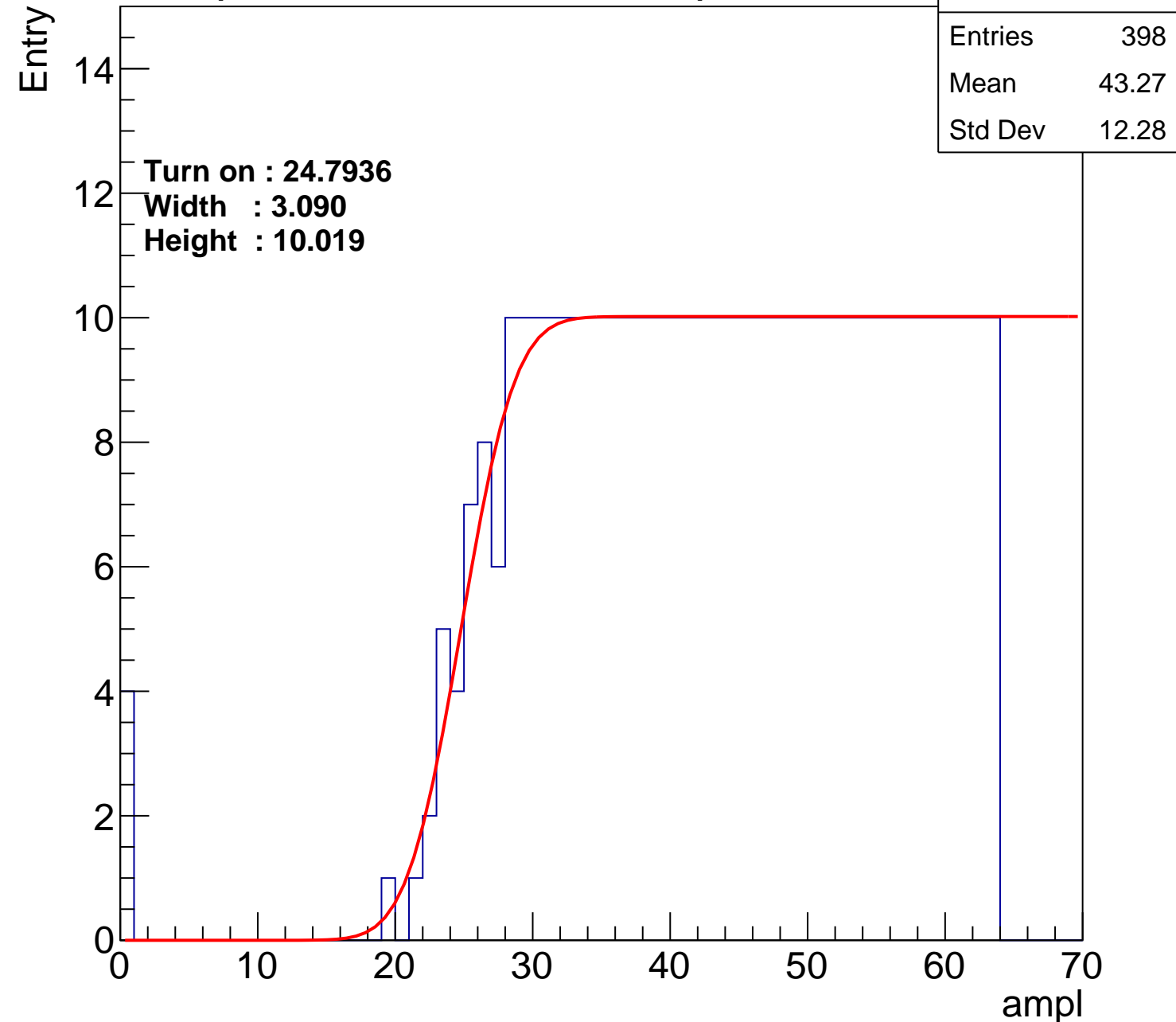
Width : 3.090

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.34
Std Dev	11.45

Turn on : 25.7499

Width : 2.932

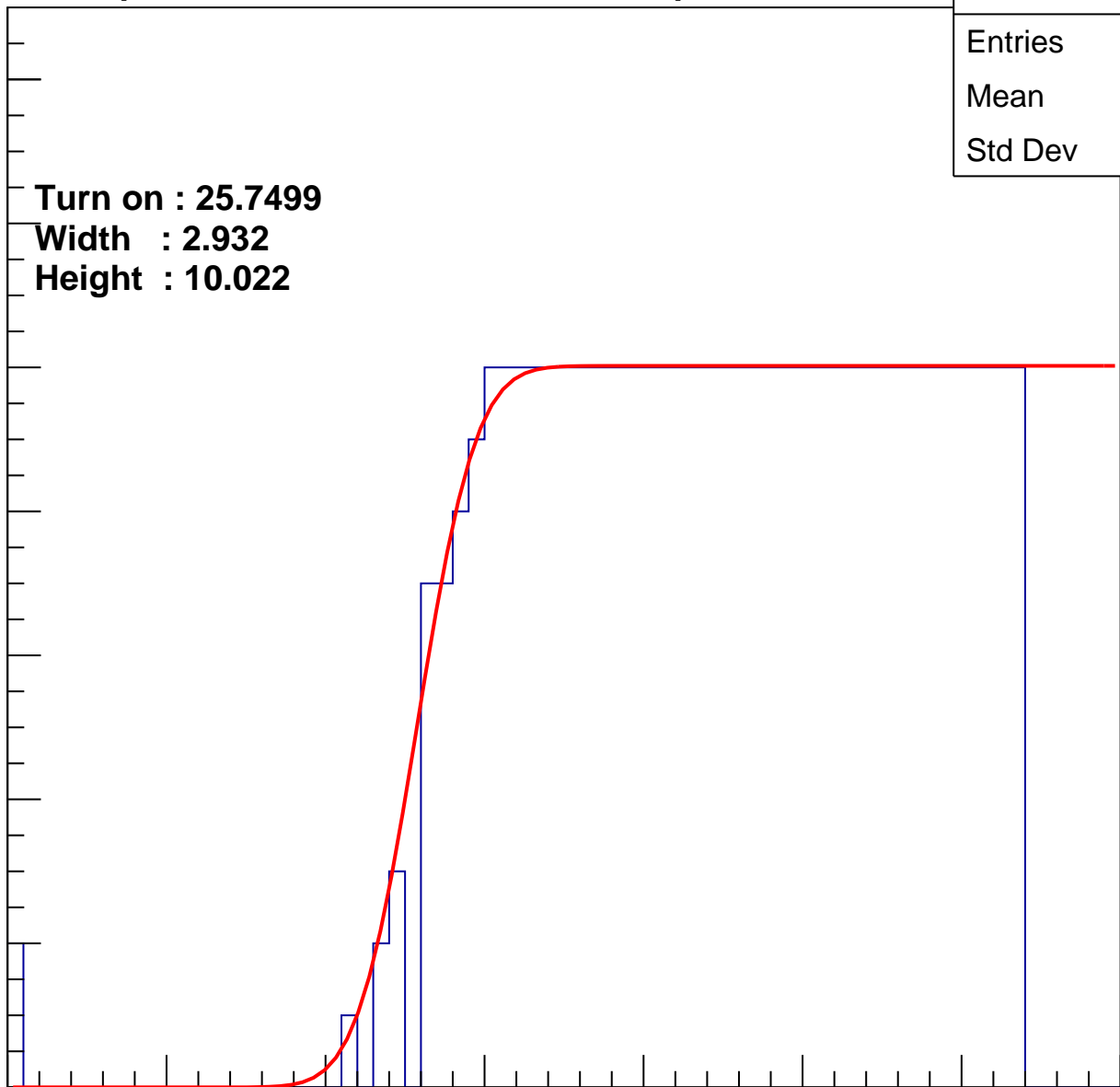
Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U18-ch39

calib_packv5_042523_0143.root, FC#3, port B3

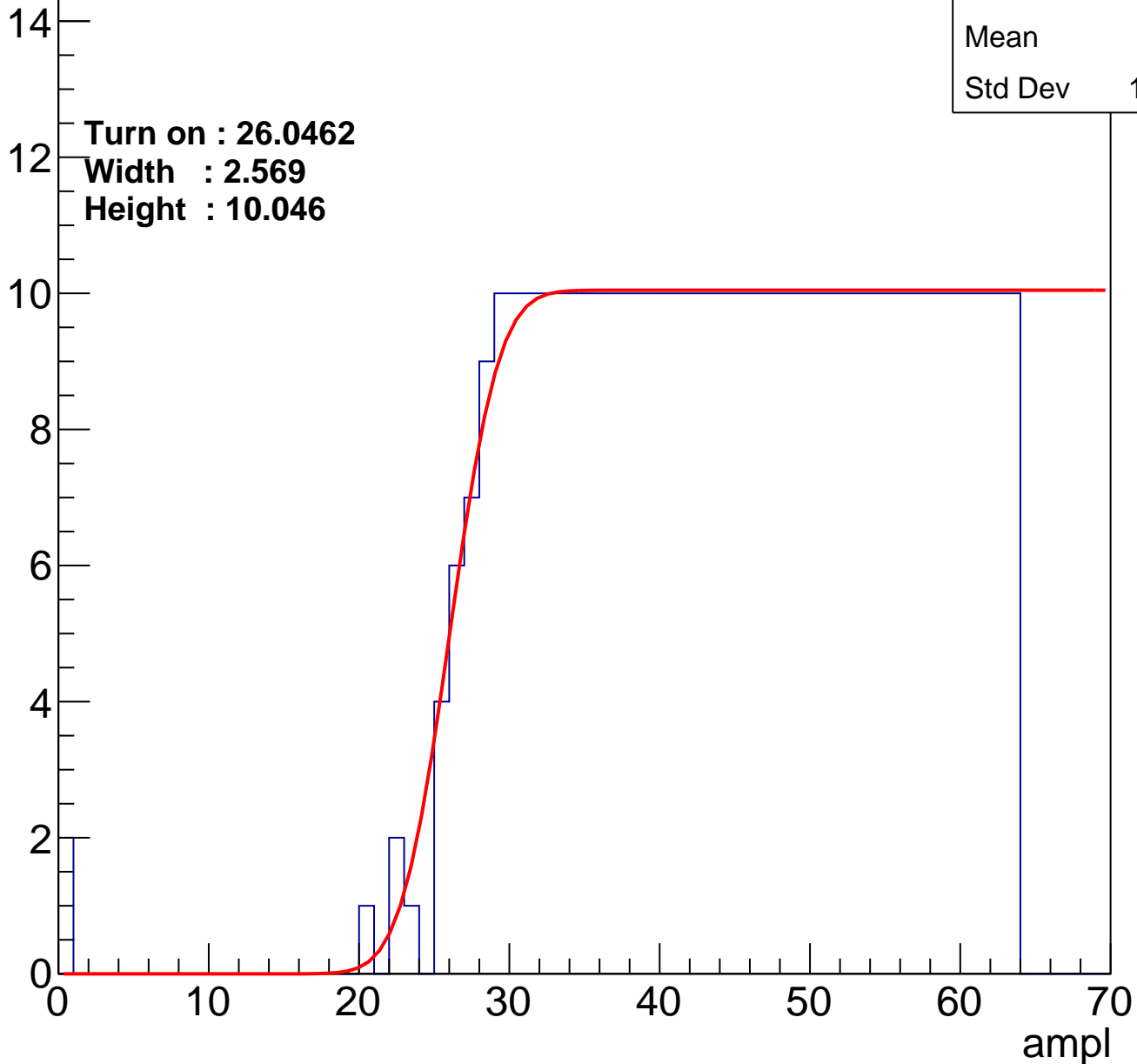
Entries	382
Mean	44.2
Std Dev	11.52

Turn on : 26.0462

Width : 2.569

Height : 10.046

Entry



B1L000S, U18-ch40

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.82
Std Dev	11.93

Turn on : 26.2026

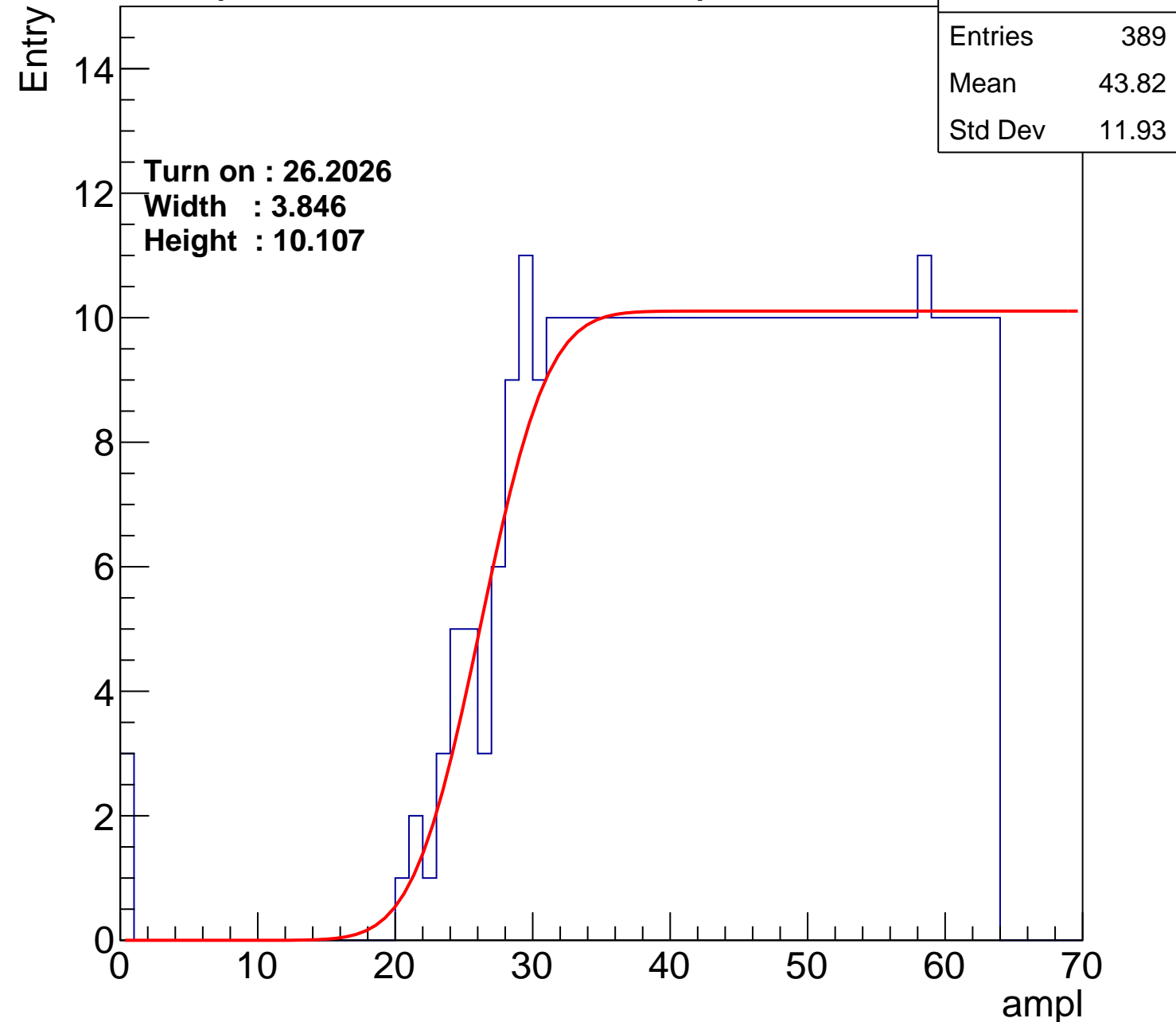
Width : 3.846

Height : 10.107

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.35

Turn on : 26.4453

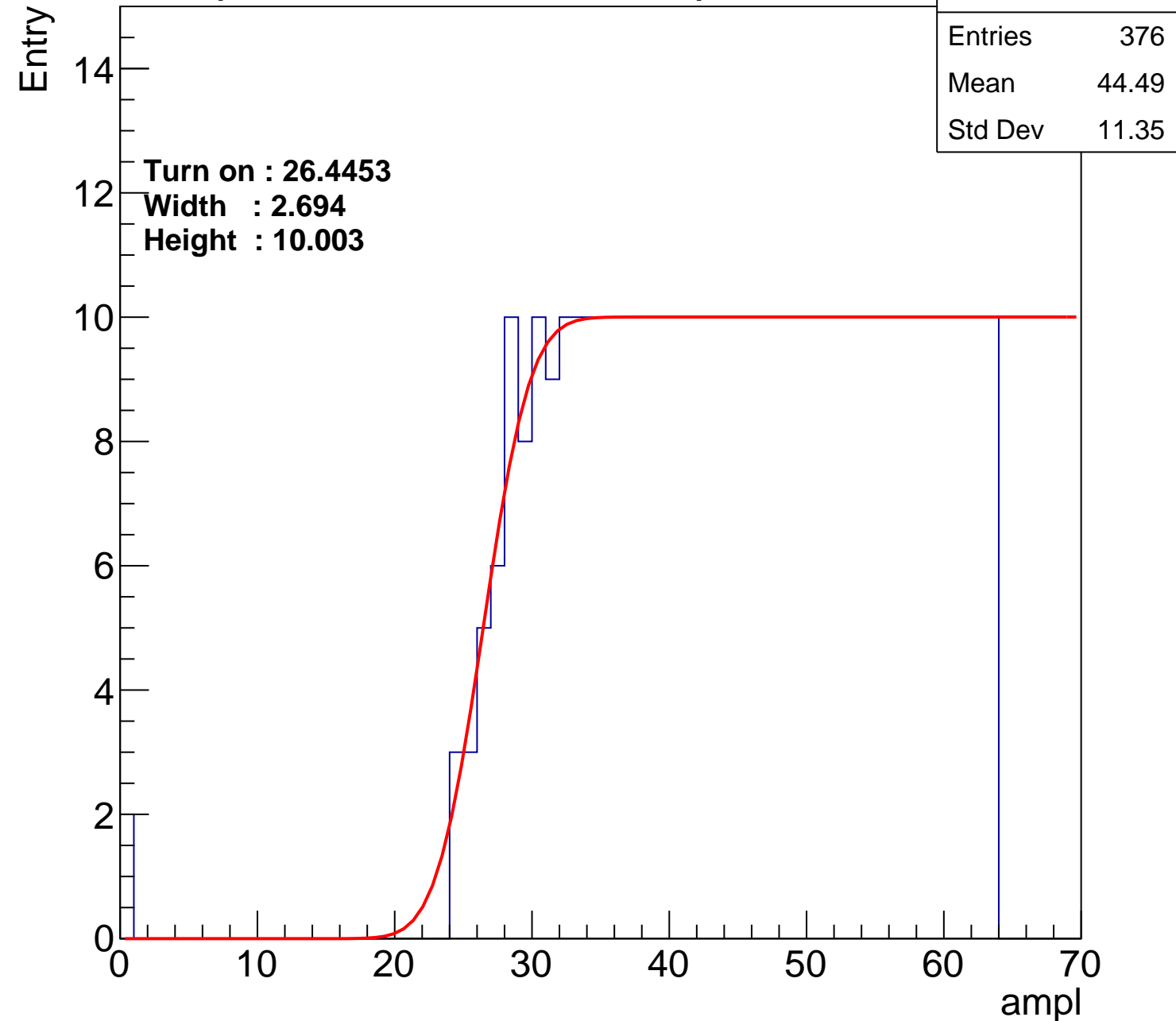
Width : 2.694

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch42

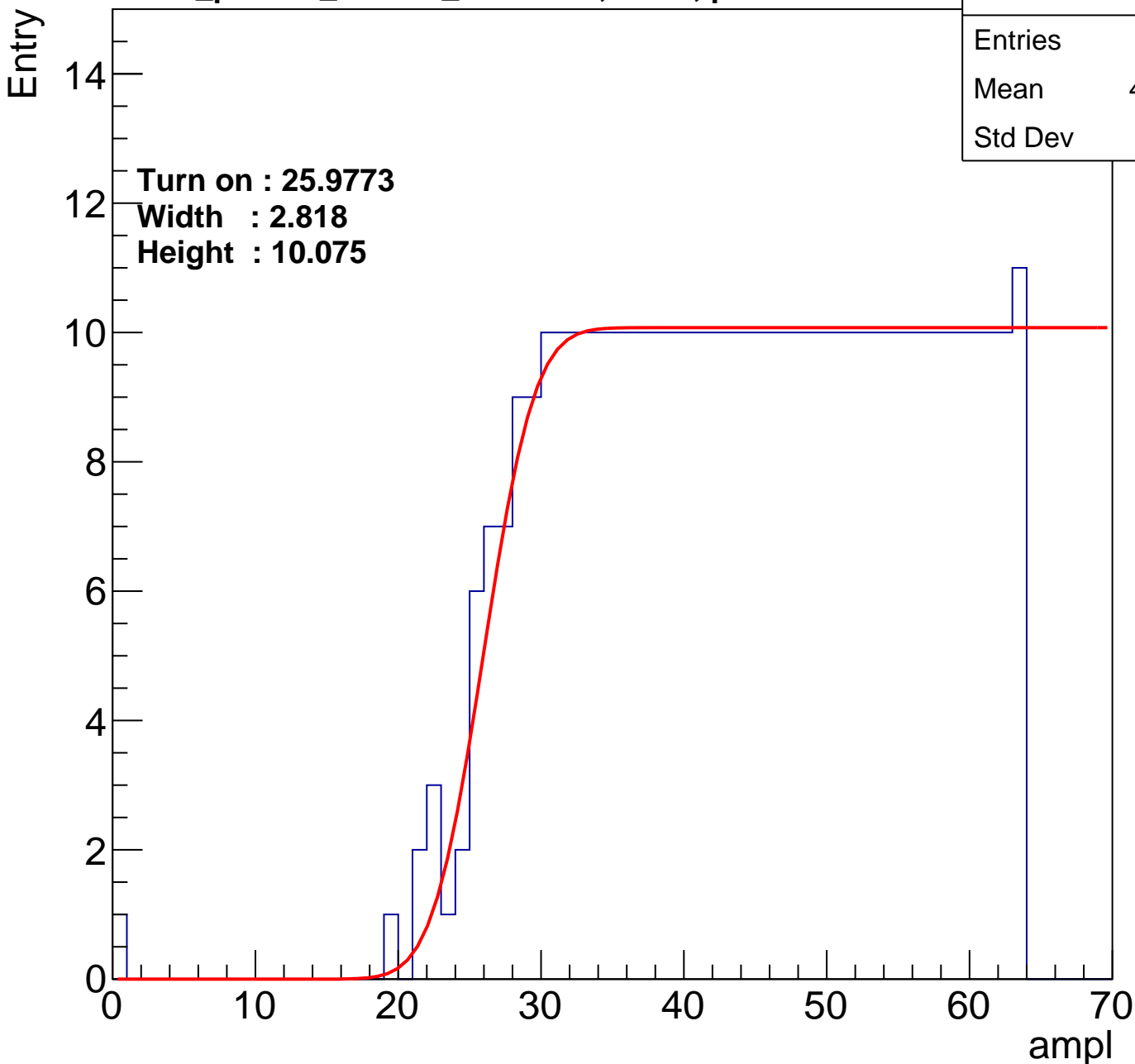
calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.97
Std Dev	11.6

Turn on : 25.9773

Width : 2.818

Height : 10.075



B1L000S, U18-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.66
Std Dev	11.25

Turn on : 26.8949

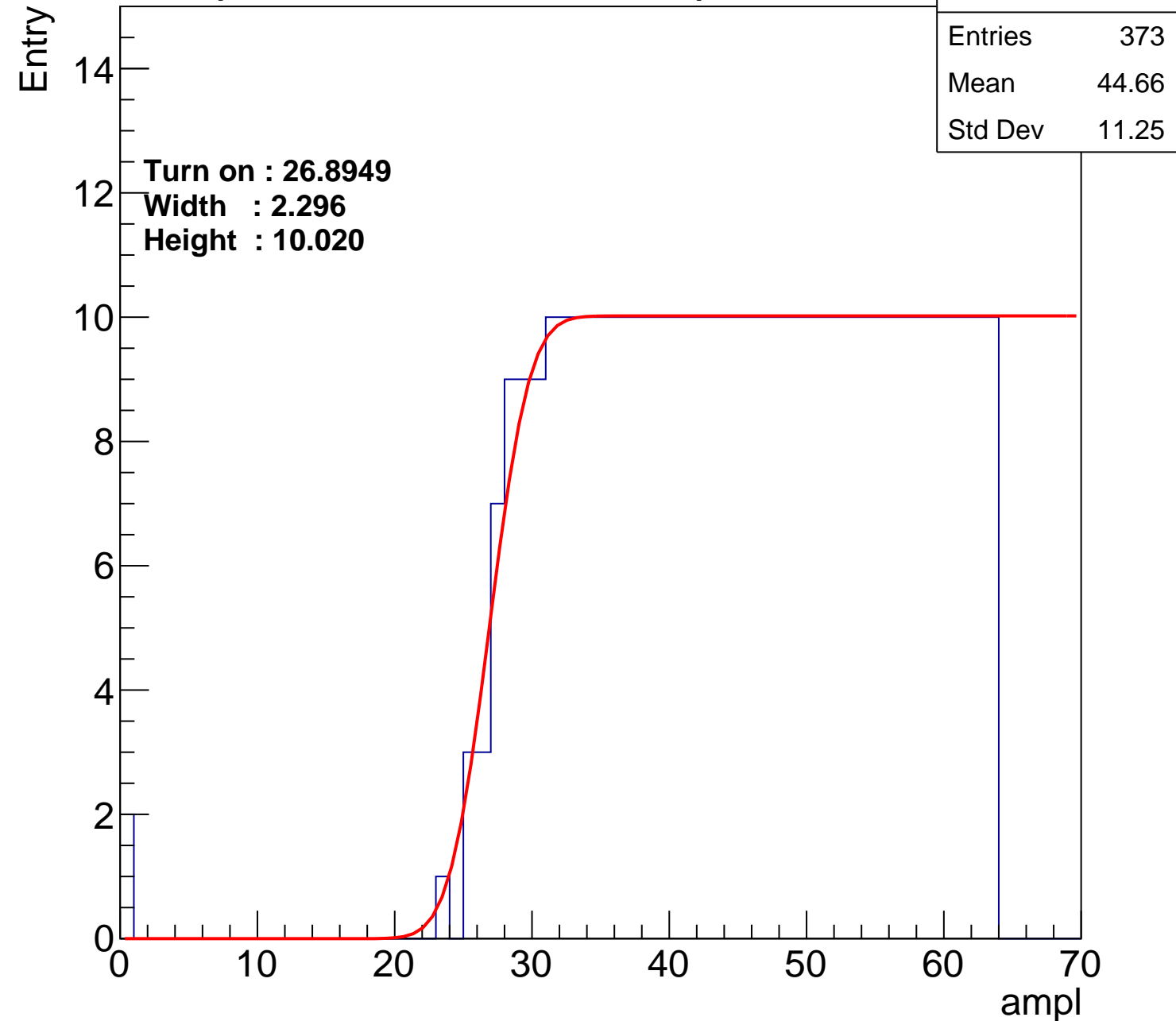
Width : 2.296

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch44

calib_packv5_042523_0143.root, FC#3, port B3

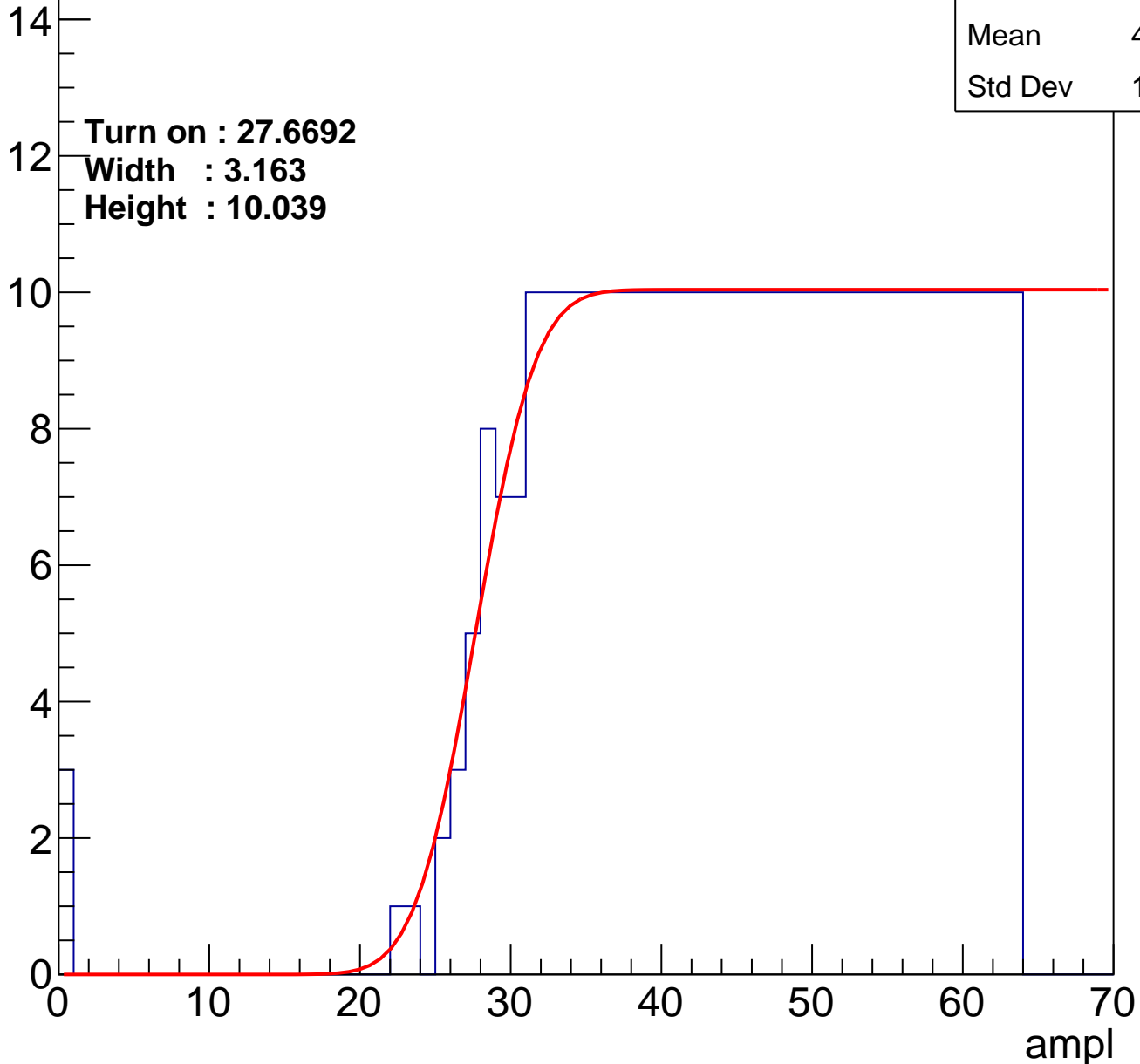
Entries	367
Mean	44.84
Std Dev	11.38

Turn on : 27.6692

Width : 3.163

Height : 10.039

Entry



B1L000S, U18-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.55
Std Dev	11.43

Turn on : 27.3585

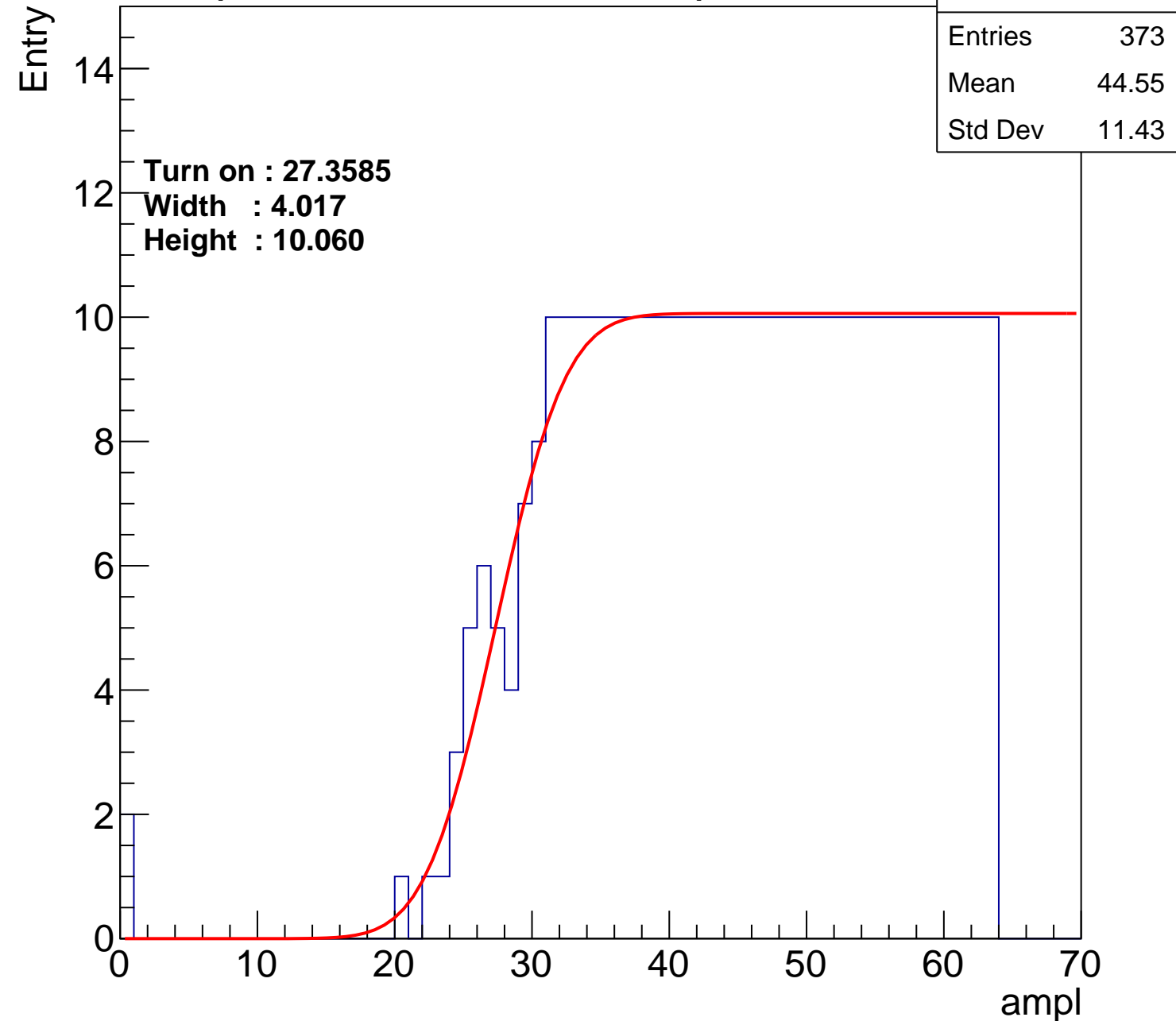
Width : 4.017

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.84
Std Dev	11.6

Turn on : 25.5619

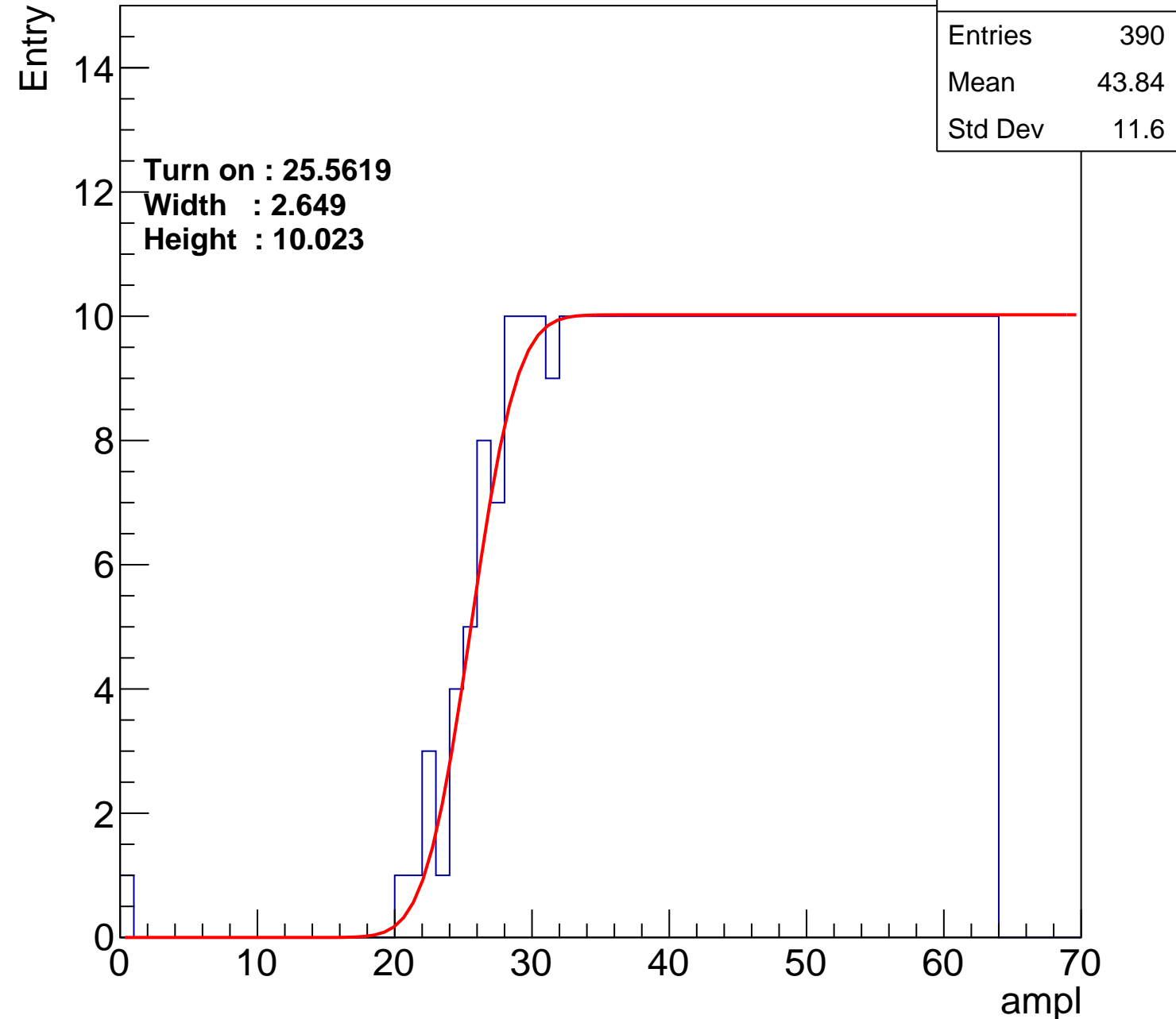
Width : 2.649

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch47

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.81
Std Dev	11.22

Turn on : 27.9925

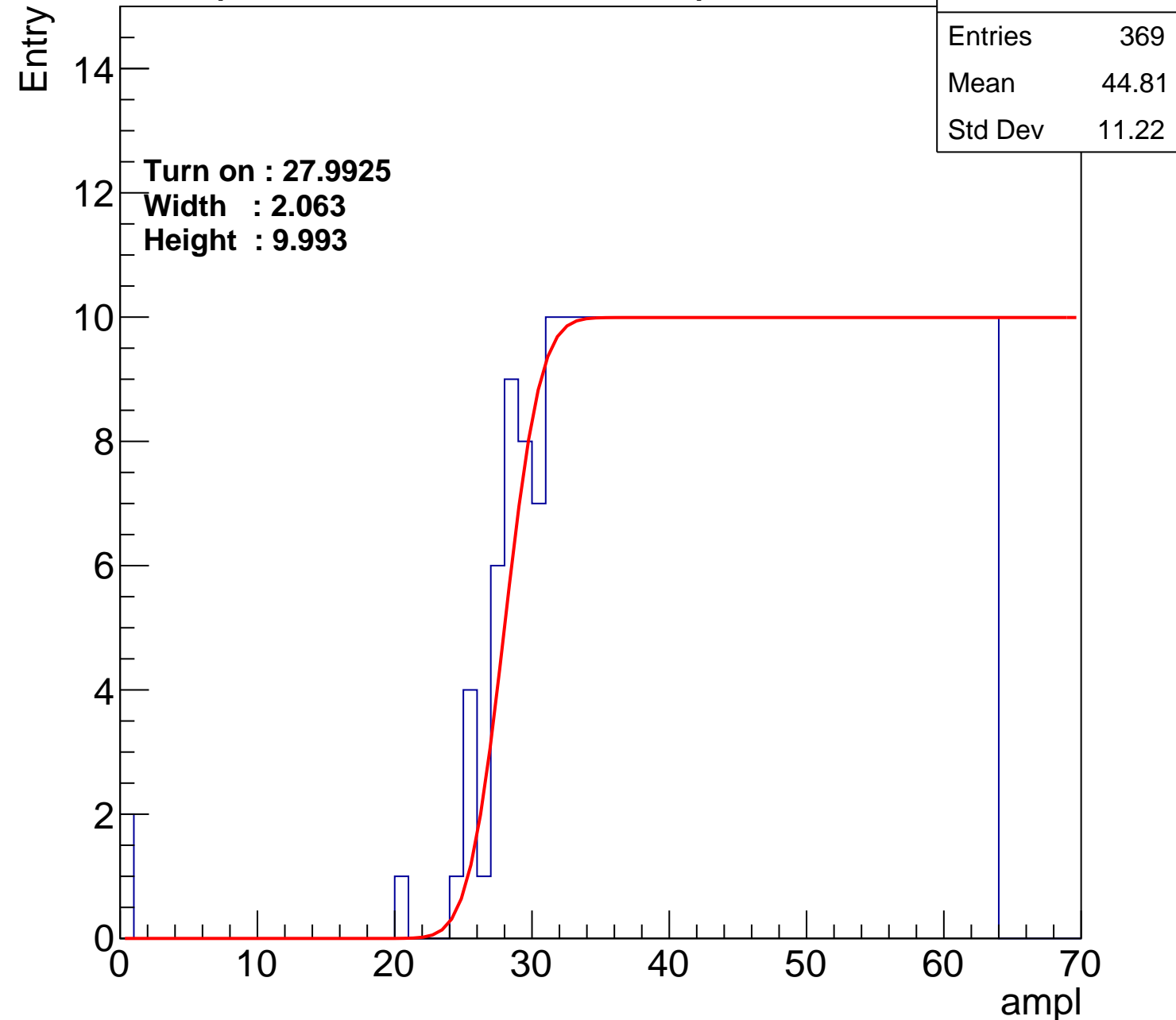
Width : 2.063

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch48

calib_packv5_042523_0143.root, FC#3, port B3

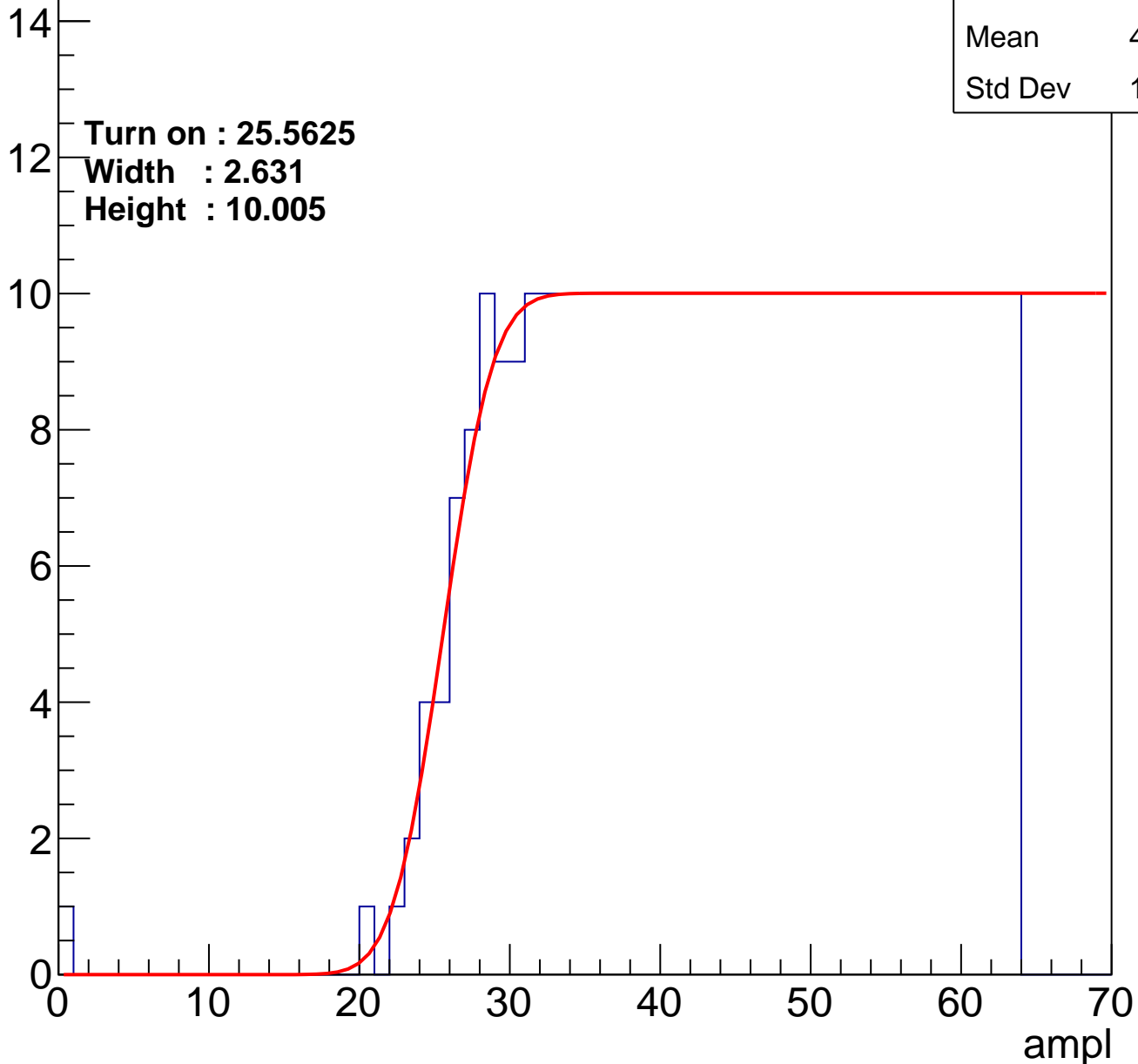
Entries	386
Mean	44.05
Std Dev	11.47

Turn on : 25.5625

Width : 2.631

Height : 10.005

Entry



B1L000S, U18-ch49

calib_packv5_042523_0143.root, FC#3, port B3

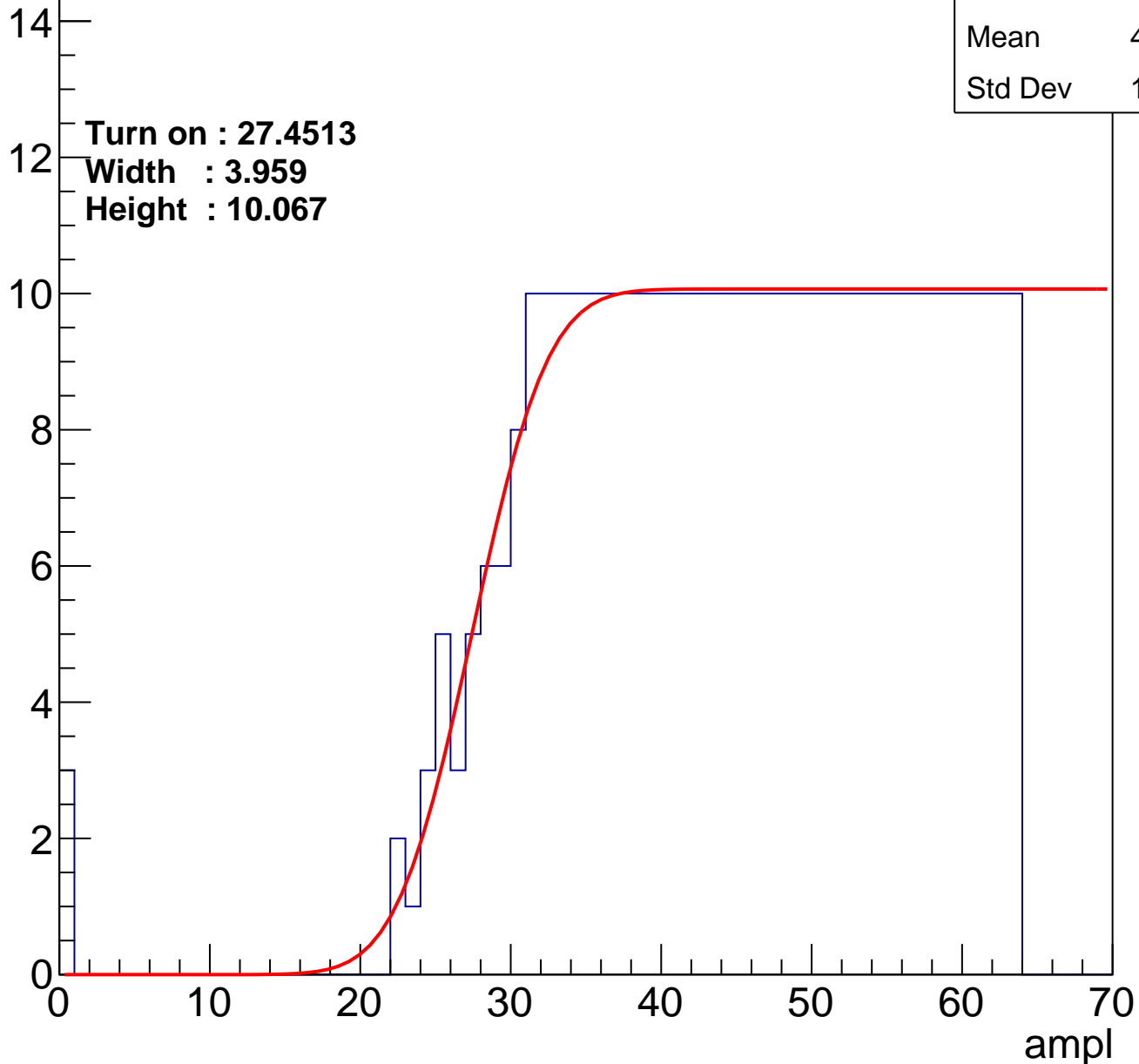
Entries	372
Mean	44.54
Std Dev	11.58

Turn on : 27.4513

Width : 3.959

Height : 10.067

Entry



B1L000S, U18-ch50

calib_packv5_042523_0143.root, FC#3, port B3

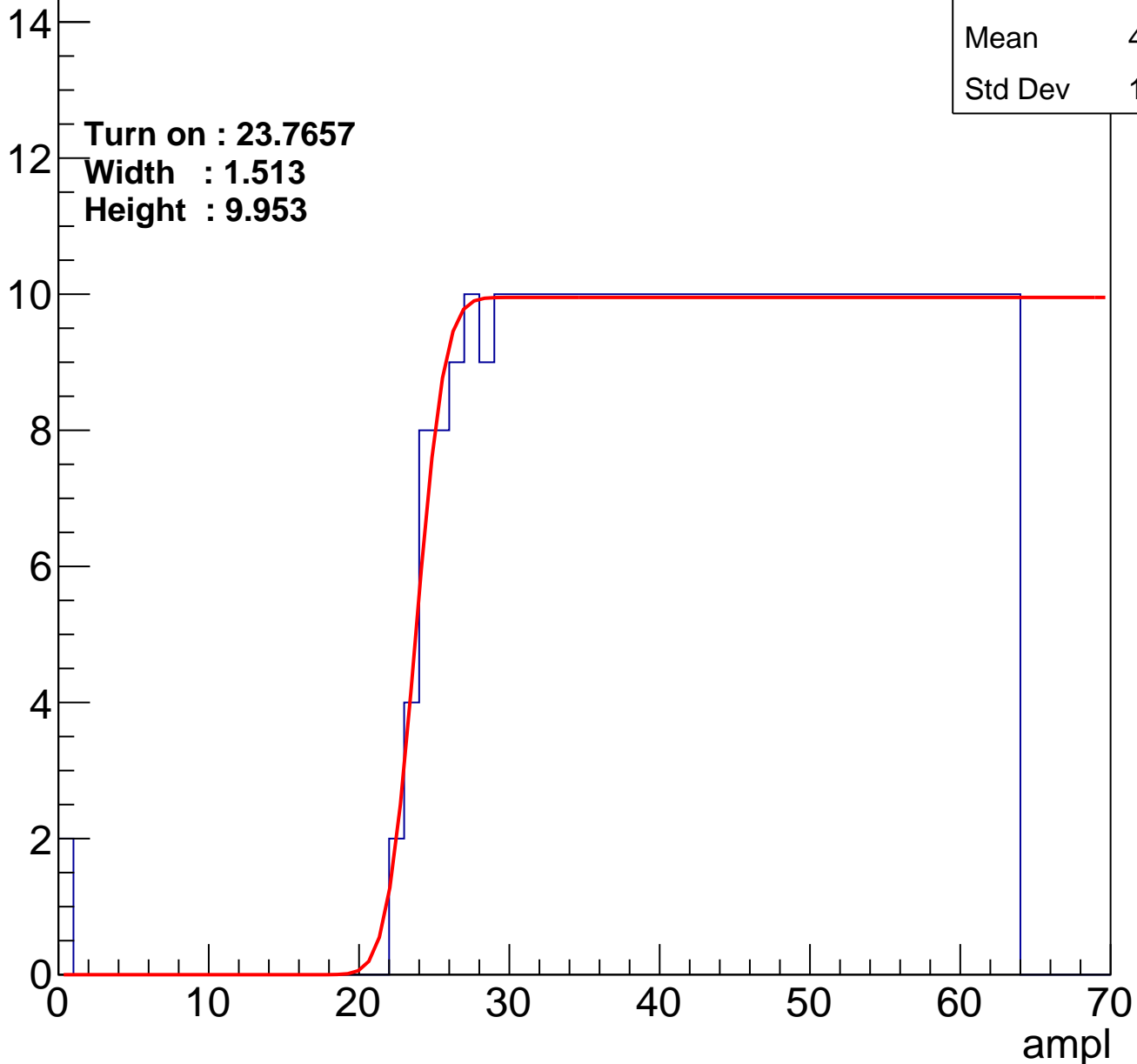
Entries	402
Mean	43.24
Std Dev	11.98

Turn on : 23.7657

Width : 1.513

Height : 9.953

Entry



B1L000S, U18-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.86
Std Dev	11.86

Turn on : 25.4482

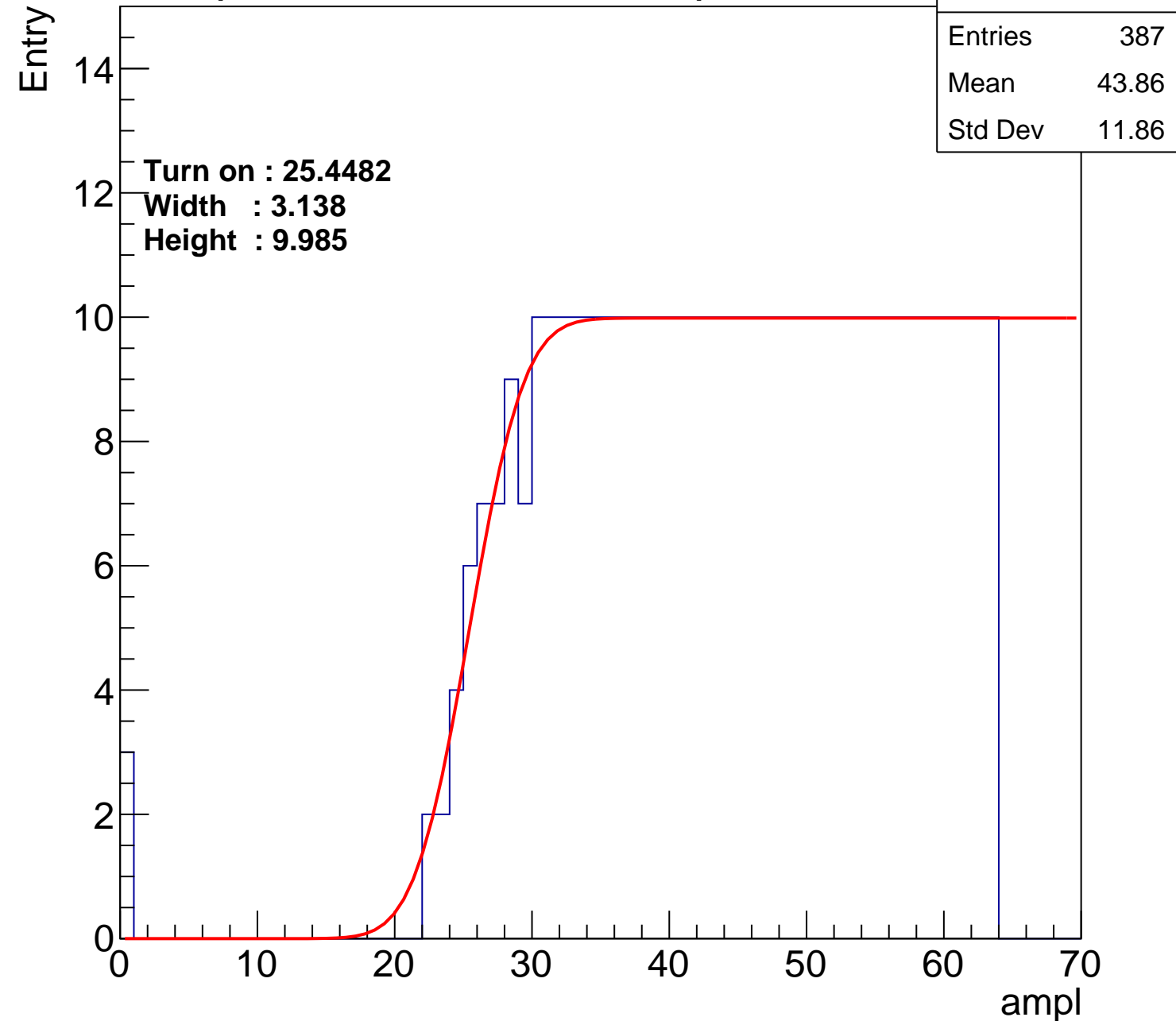
Width : 3.138

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.23
Std Dev	11.34

Turn on : 25.9248

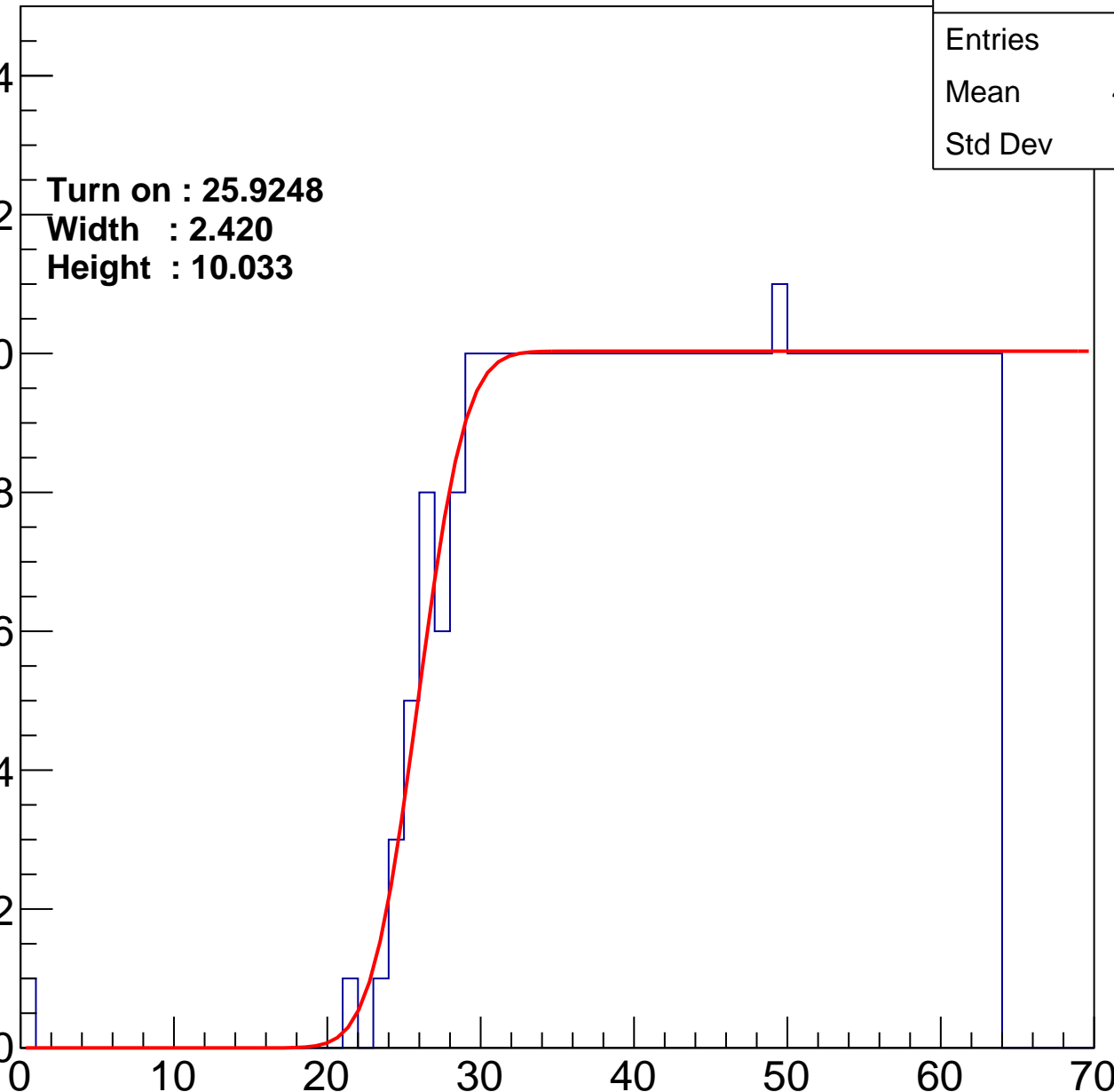
Width : 2.420

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch53

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.25
Std Dev	12.19

Turn on : 24.5561

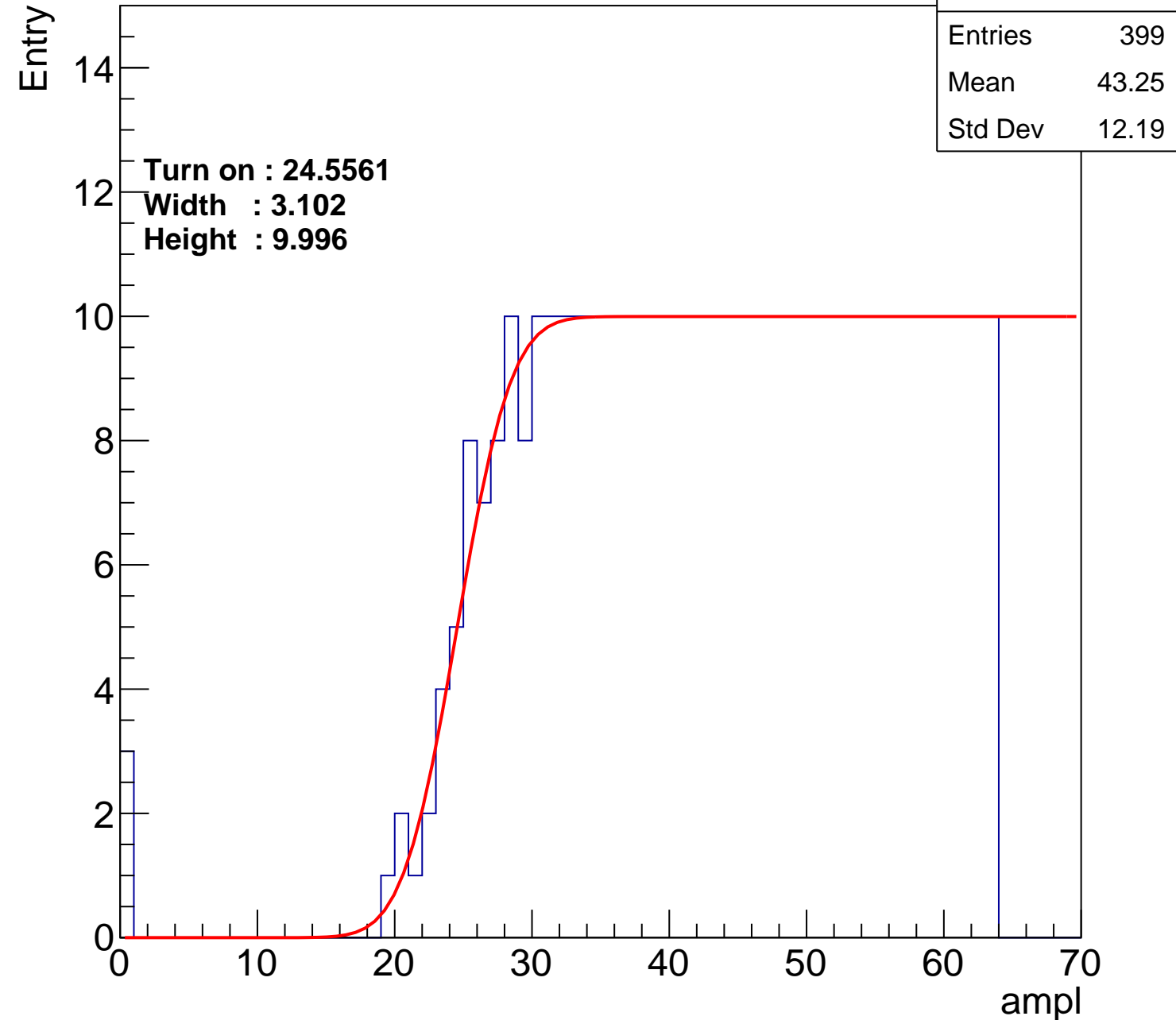
Width : 3.102

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.84
Std Dev	11.84

Turn on : 25.6424

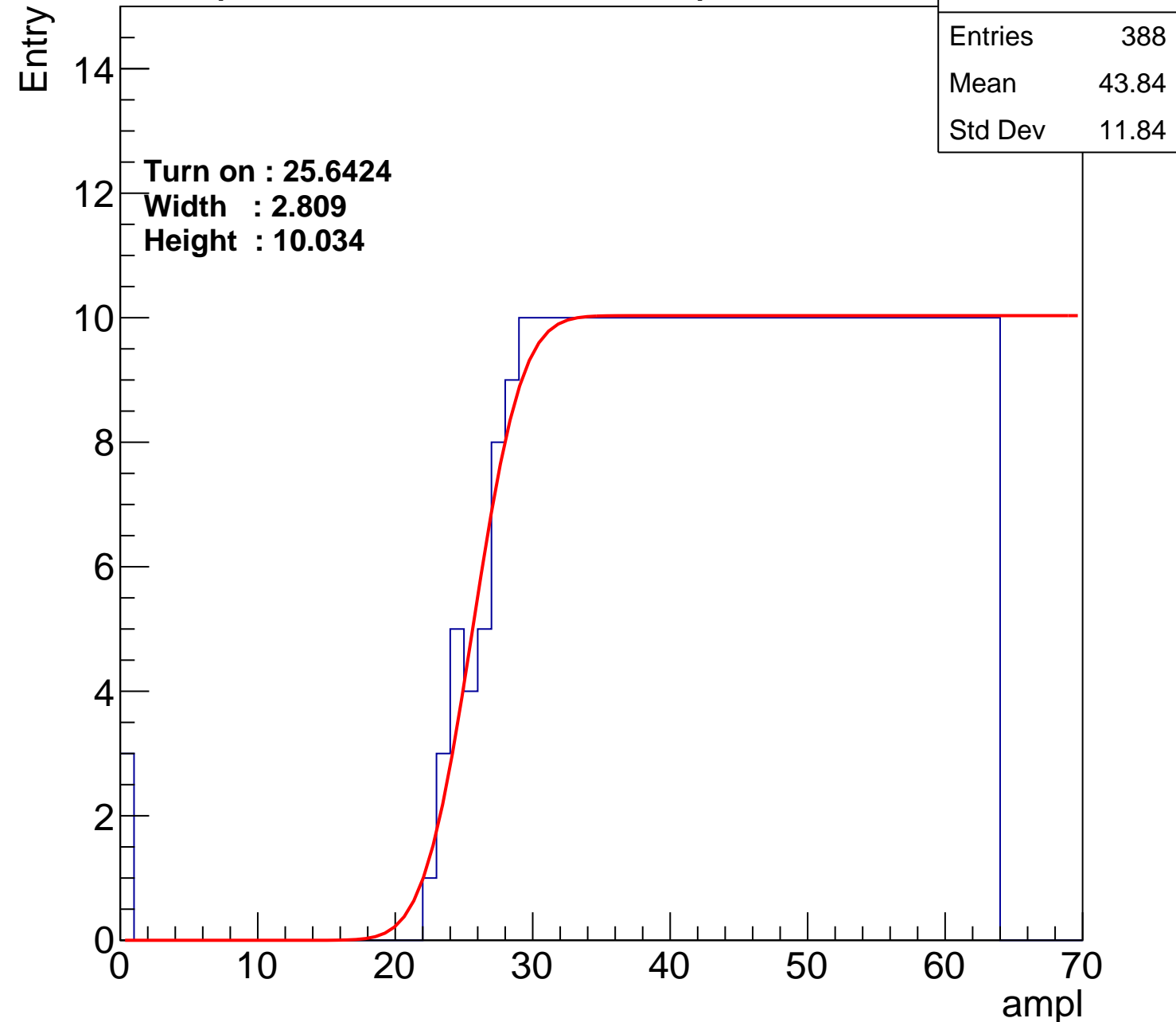
Width : 2.809

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch55

calib_packv5_042523_0143.root, FC#3, port B3

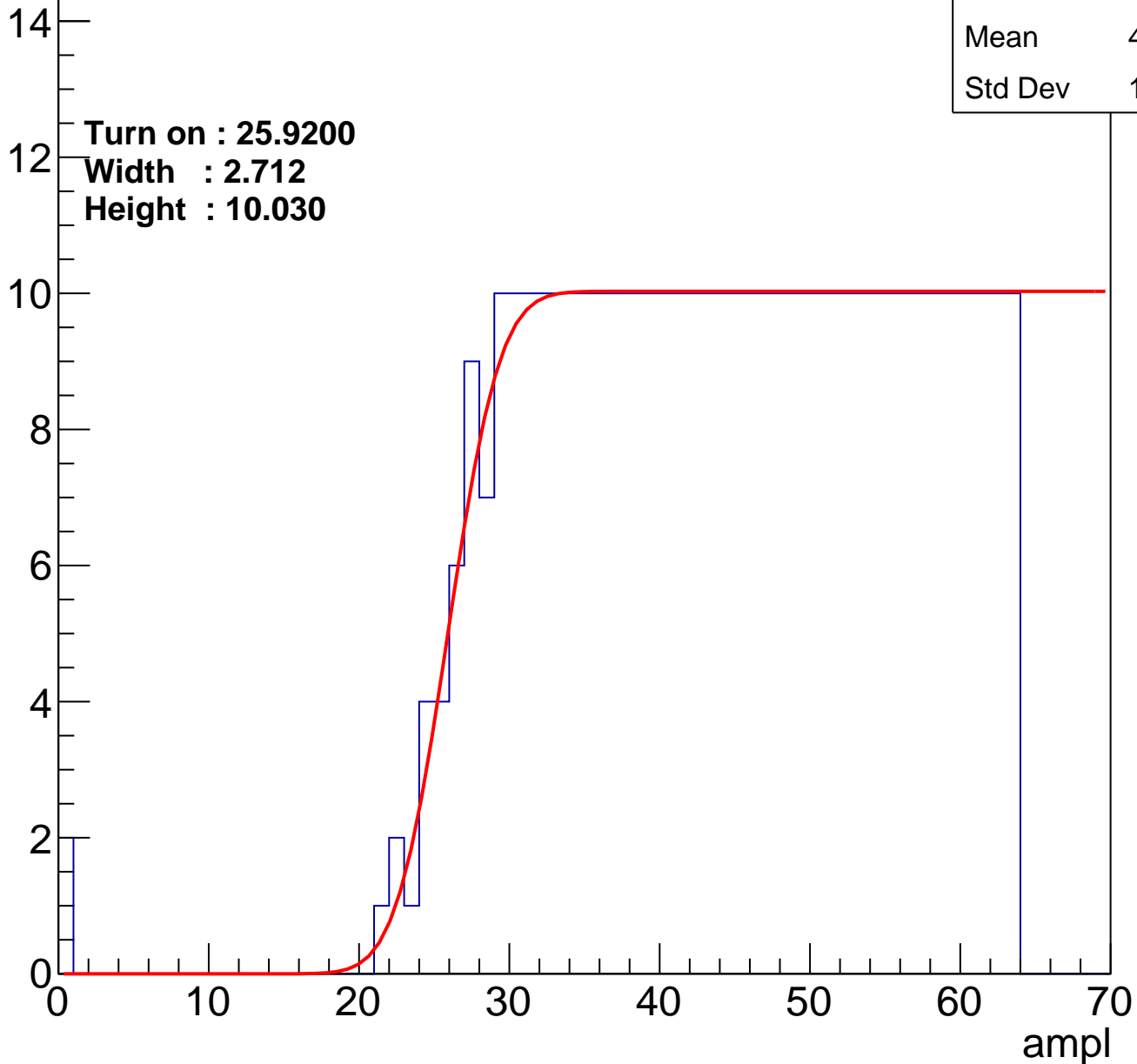
Entries	386
Mean	43.99
Std Dev	11.64

Turn on : 25.9200

Width : 2.712

Height : 10.030

Entry



B1L000S, U18-ch56

calib_packv5_042523_0143.root, FC#3, port B3

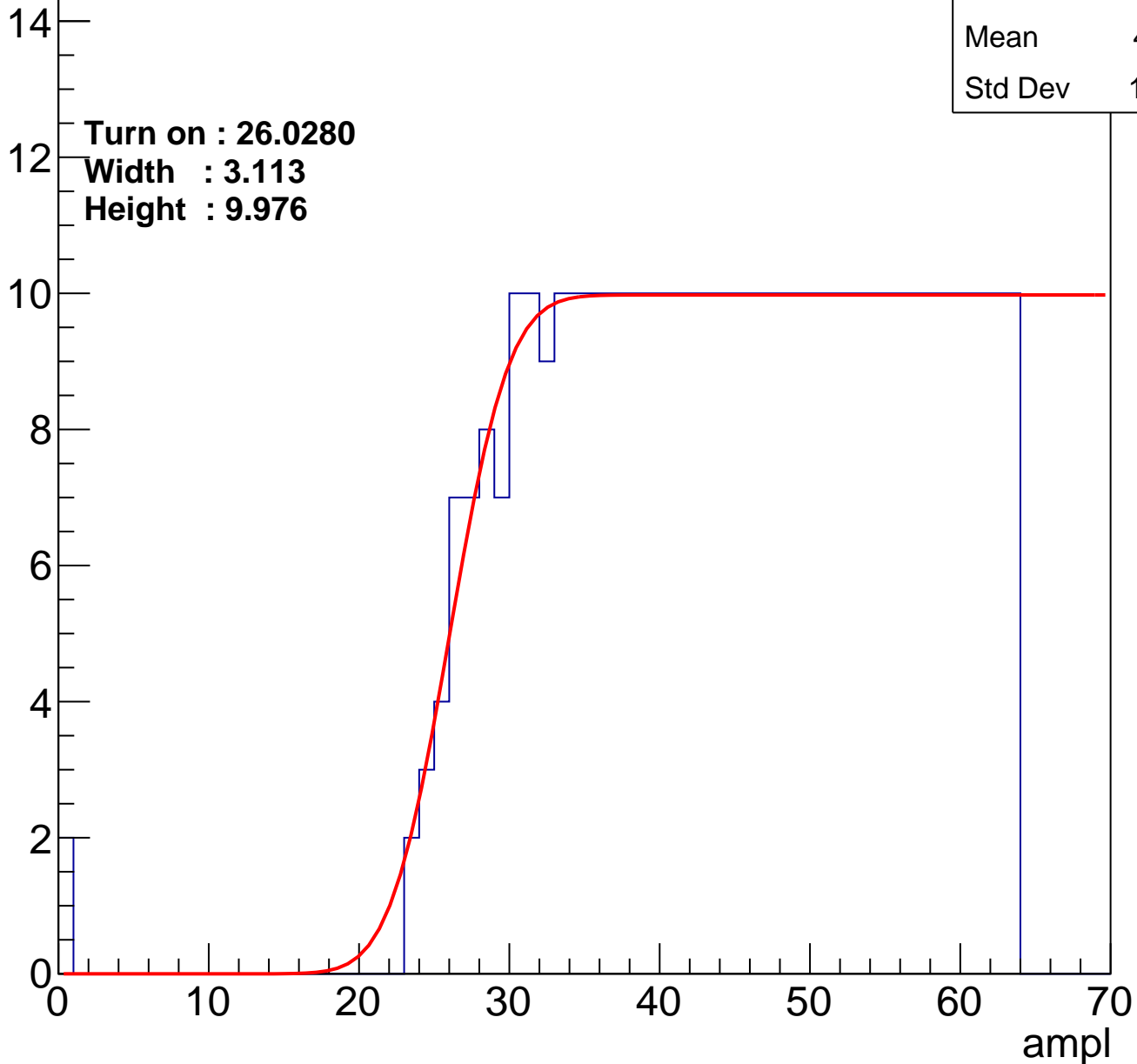
Entries	379
Mean	44.31
Std Dev	11.48

Turn on : 26.0280

Width : 3.113

Height : 9.976

Entry



B1L000S, U18-ch57

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.53
Std Dev	11.49

Turn on : 26.8816

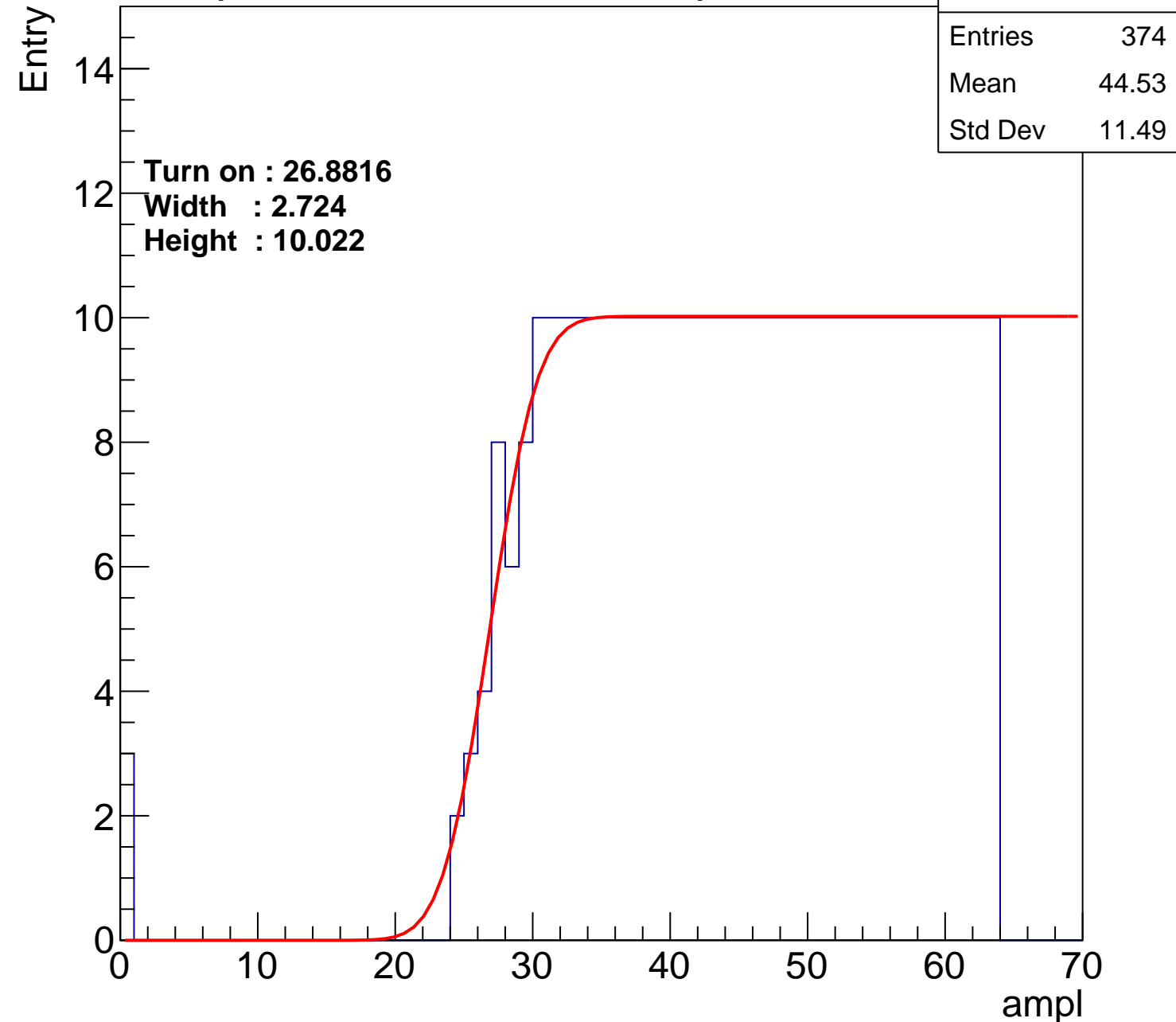
Width : 2.724

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch58

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	45.05
Std Dev	10.96

Turn on : 28.2539

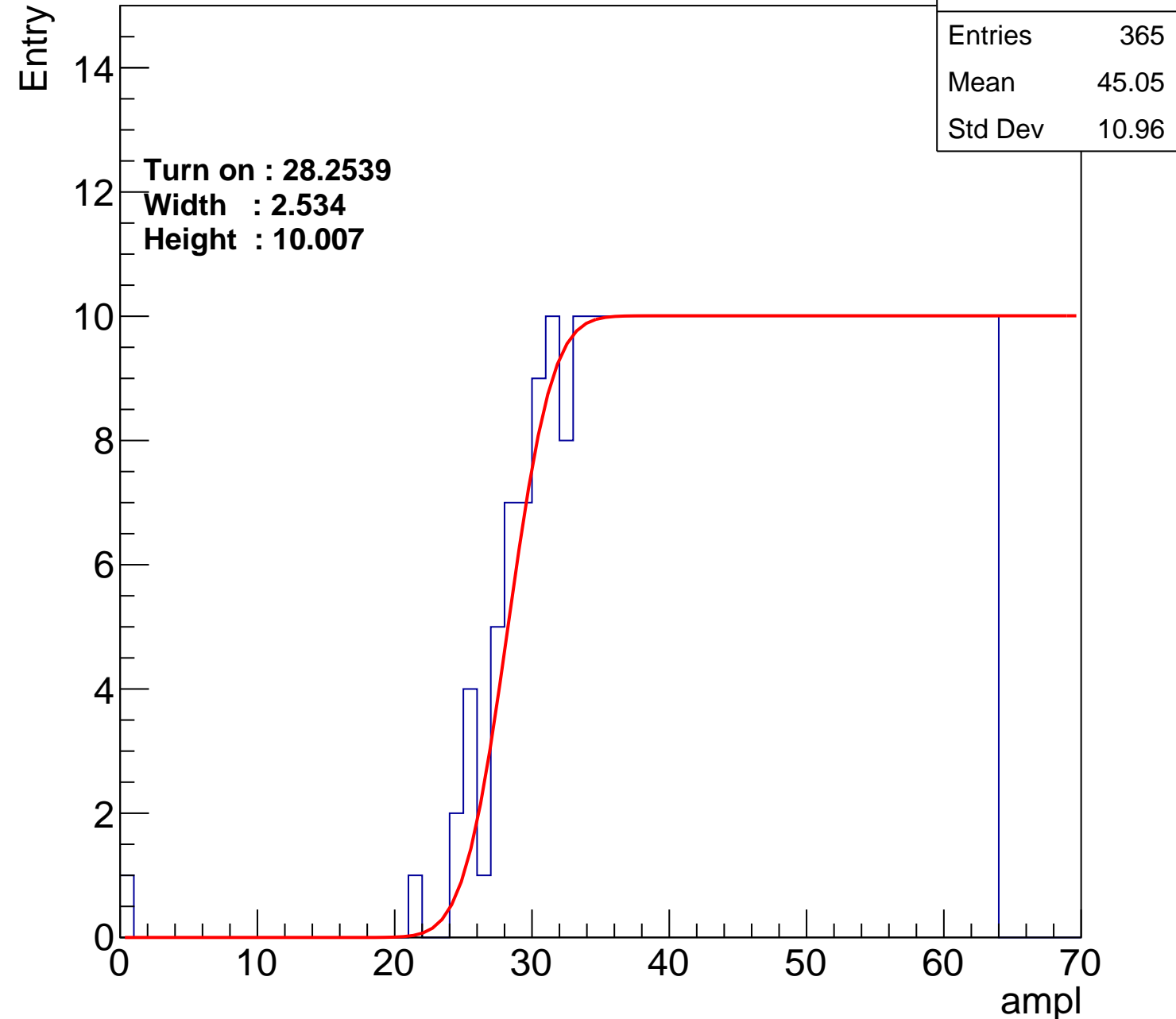
Width : 2.534

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 26.1603

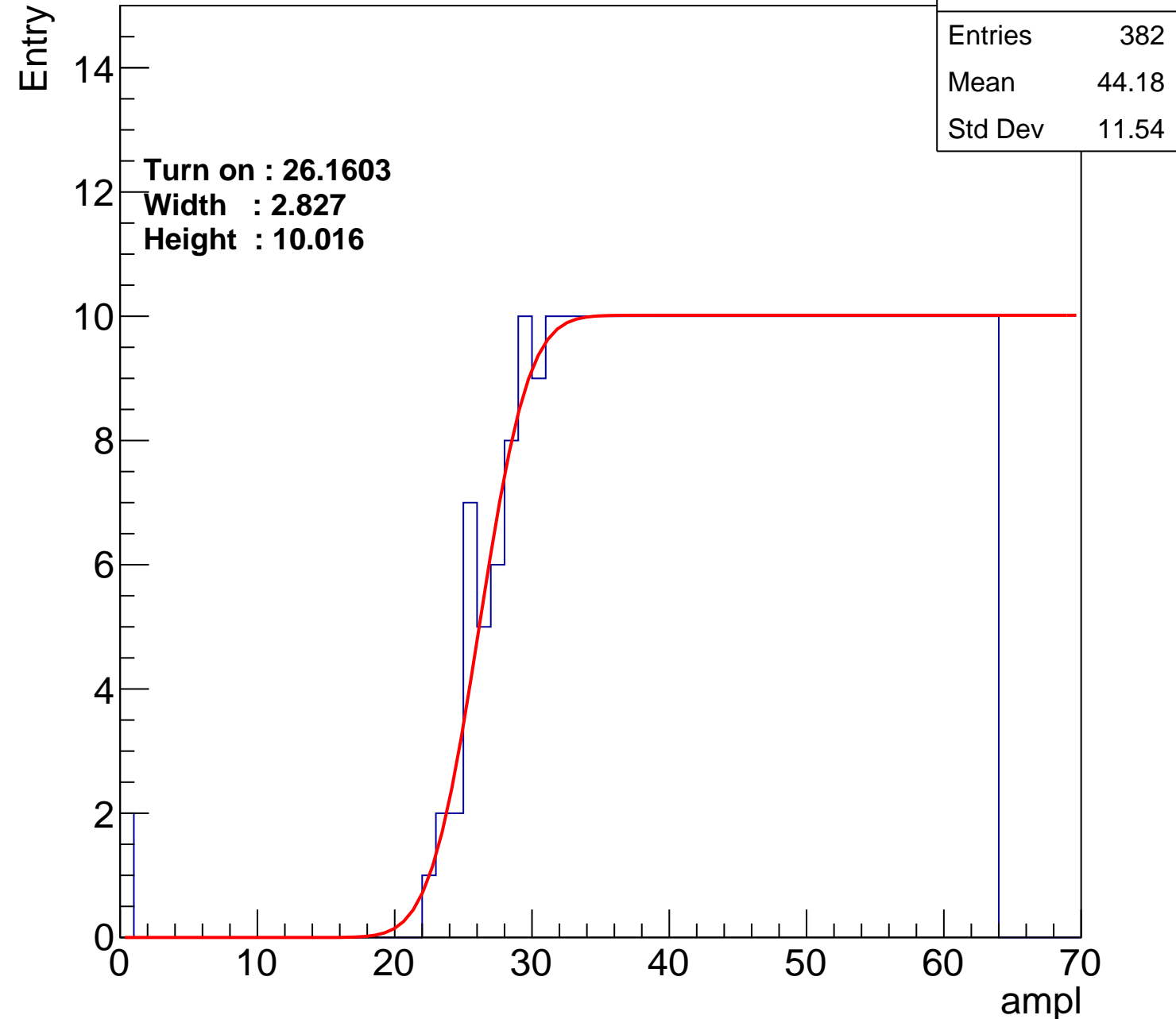
Width : 2.827

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch60

calib_packv5_042523_0143.root, FC#3, port B3

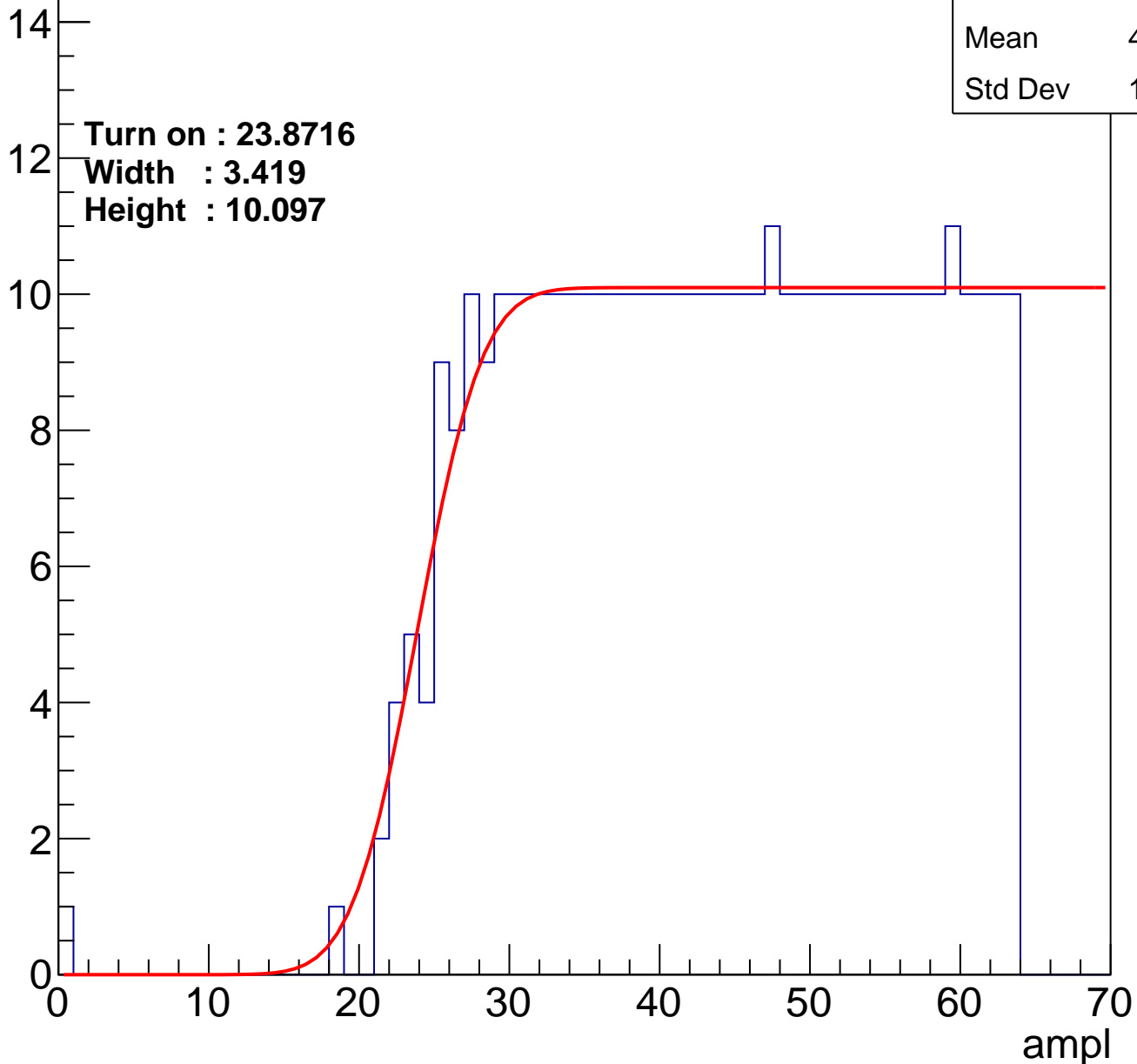
Entries	405
Mean	43.26
Std Dev	11.92

Turn on : 23.8716

Width : 3.419

Height : 10.097

Entry



B1L000S, U18-ch61

calib_packv5_042523_0143.root, FC#3, port B3

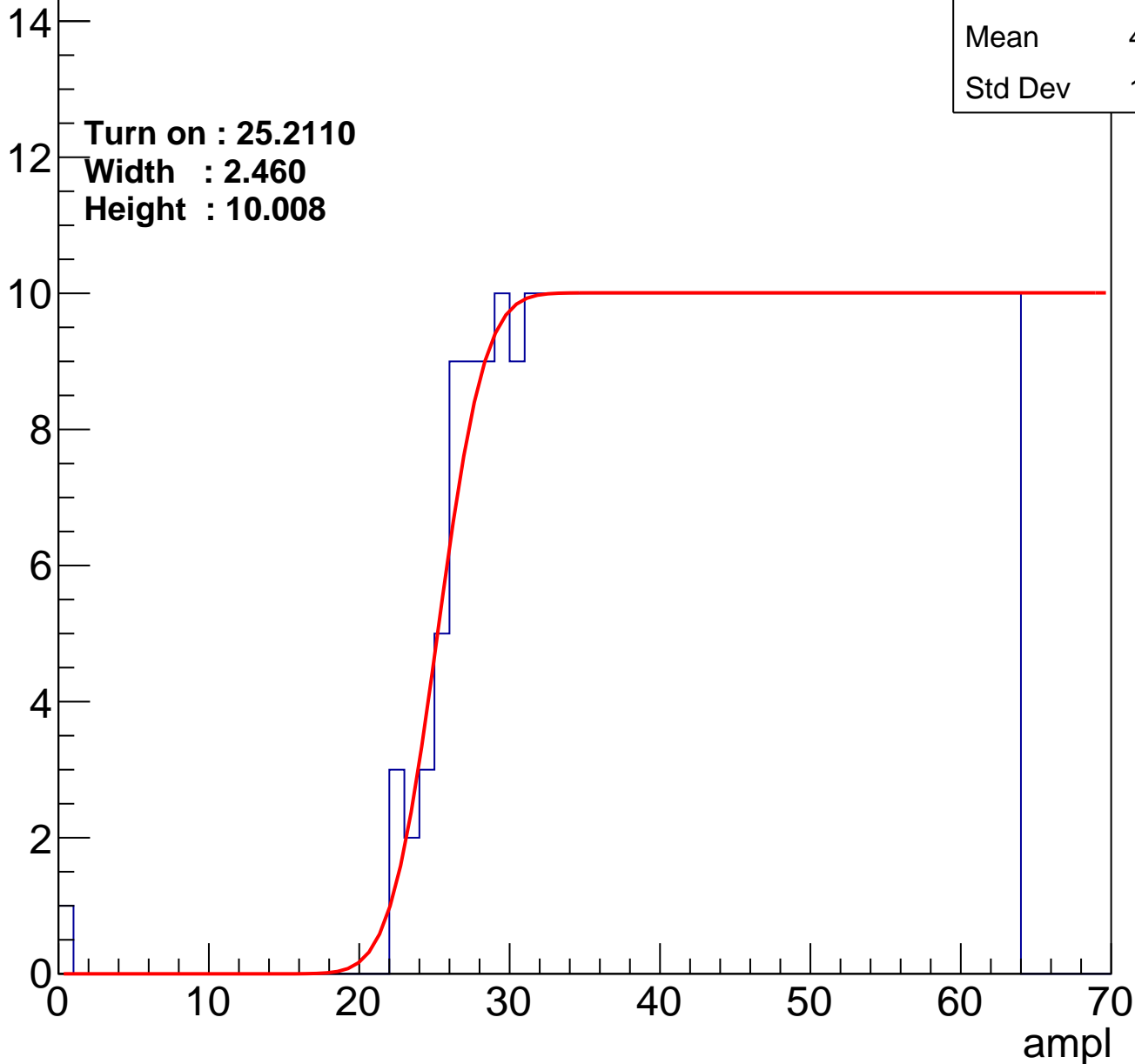
Entries	390
Mean	43.87
Std Dev	11.55

Turn on : 25.2110

Width : 2.460

Height : 10.008

Entry



B1L000S, U18-ch62

calib_packv5_042523_0143.root, FC#3, port B3

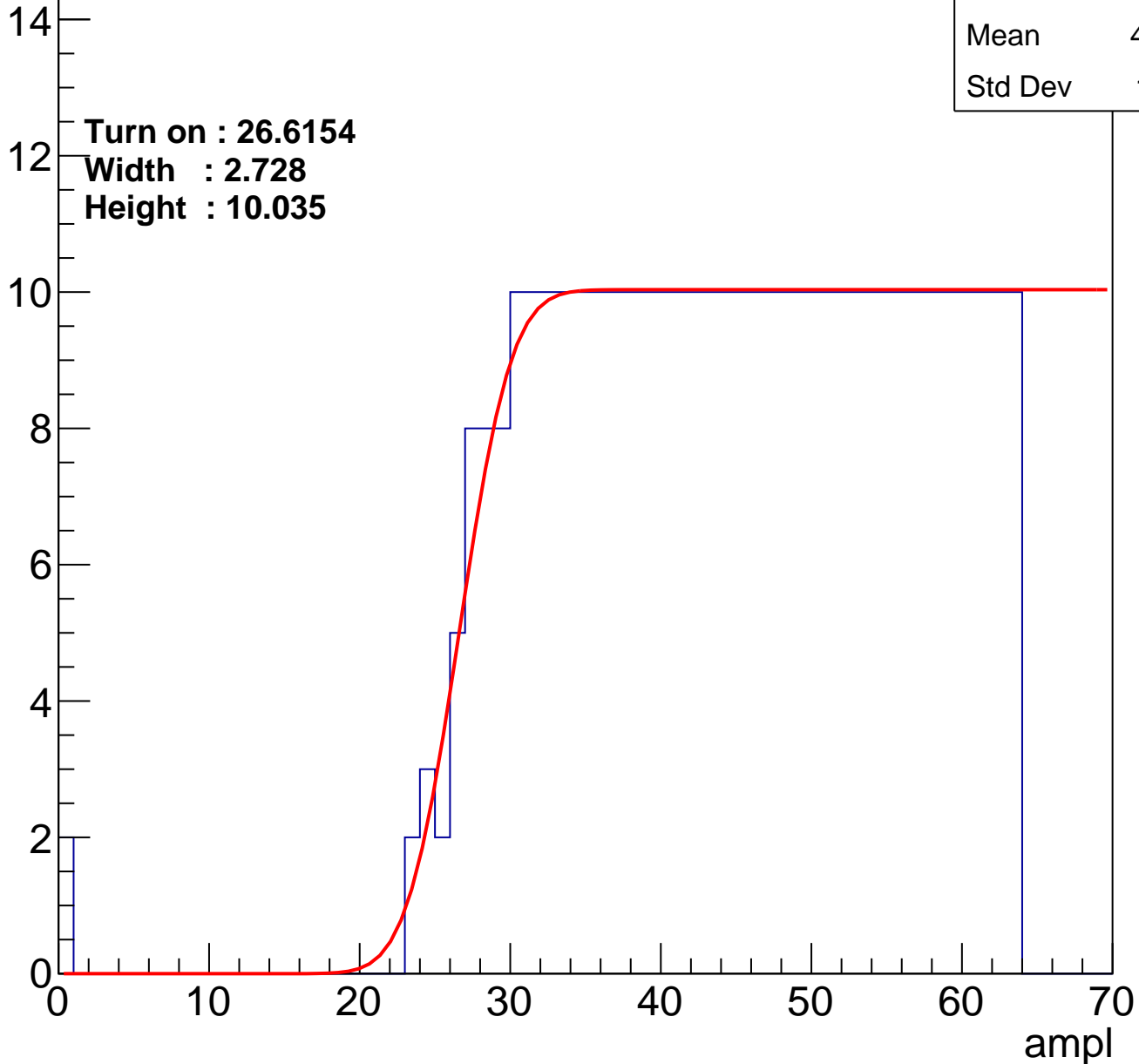
Entries	378
Mean	44.39
Std Dev	11.41

Turn on : 26.6154

Width : 2.728

Height : 10.035

Entry



B1L000S, U18-ch63

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.81
Std Dev	11.41

Turn on : 28.1283

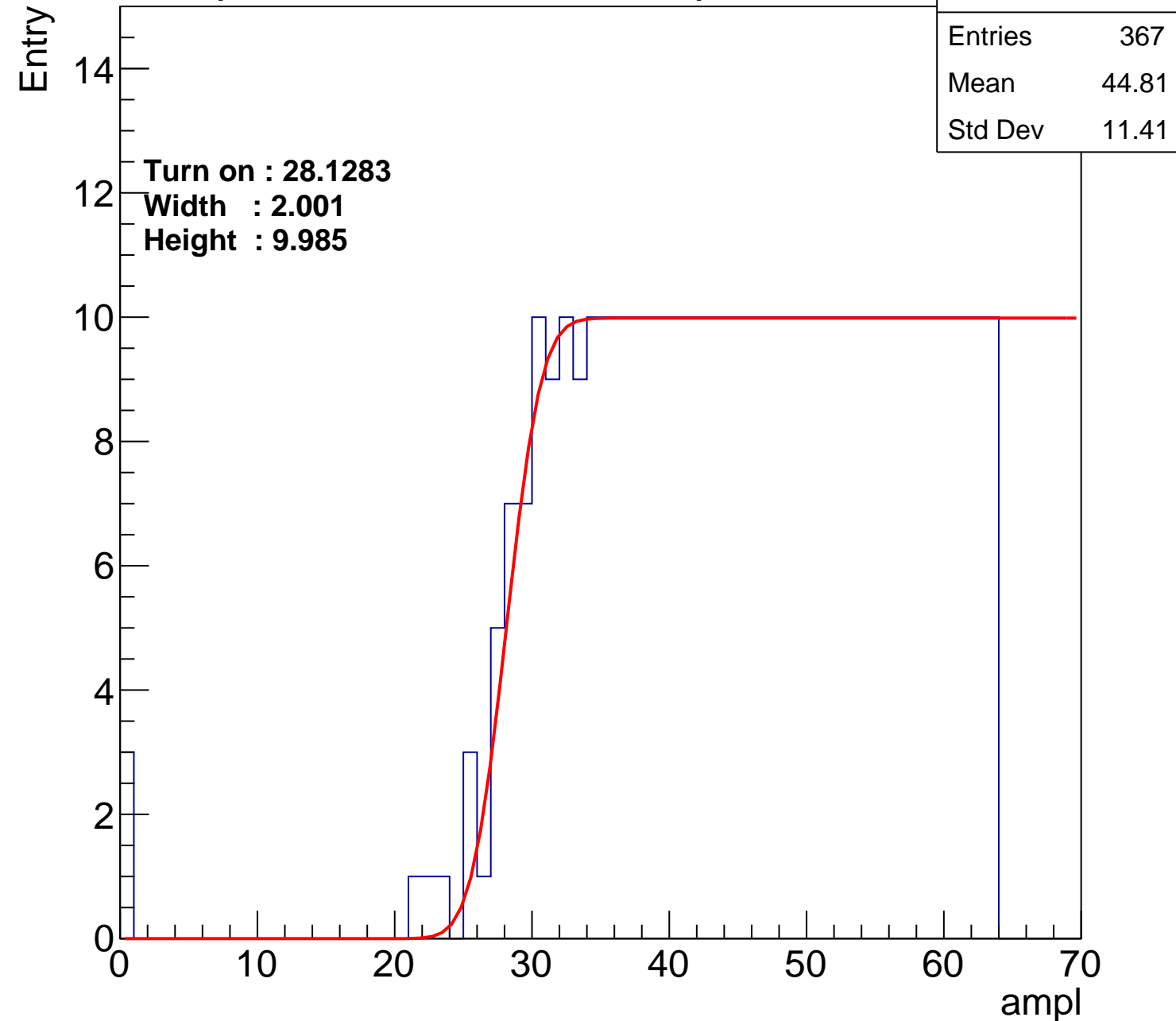
Width : 2.001

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch64

calib_packv5_042523_0143.root, FC#3, port B3

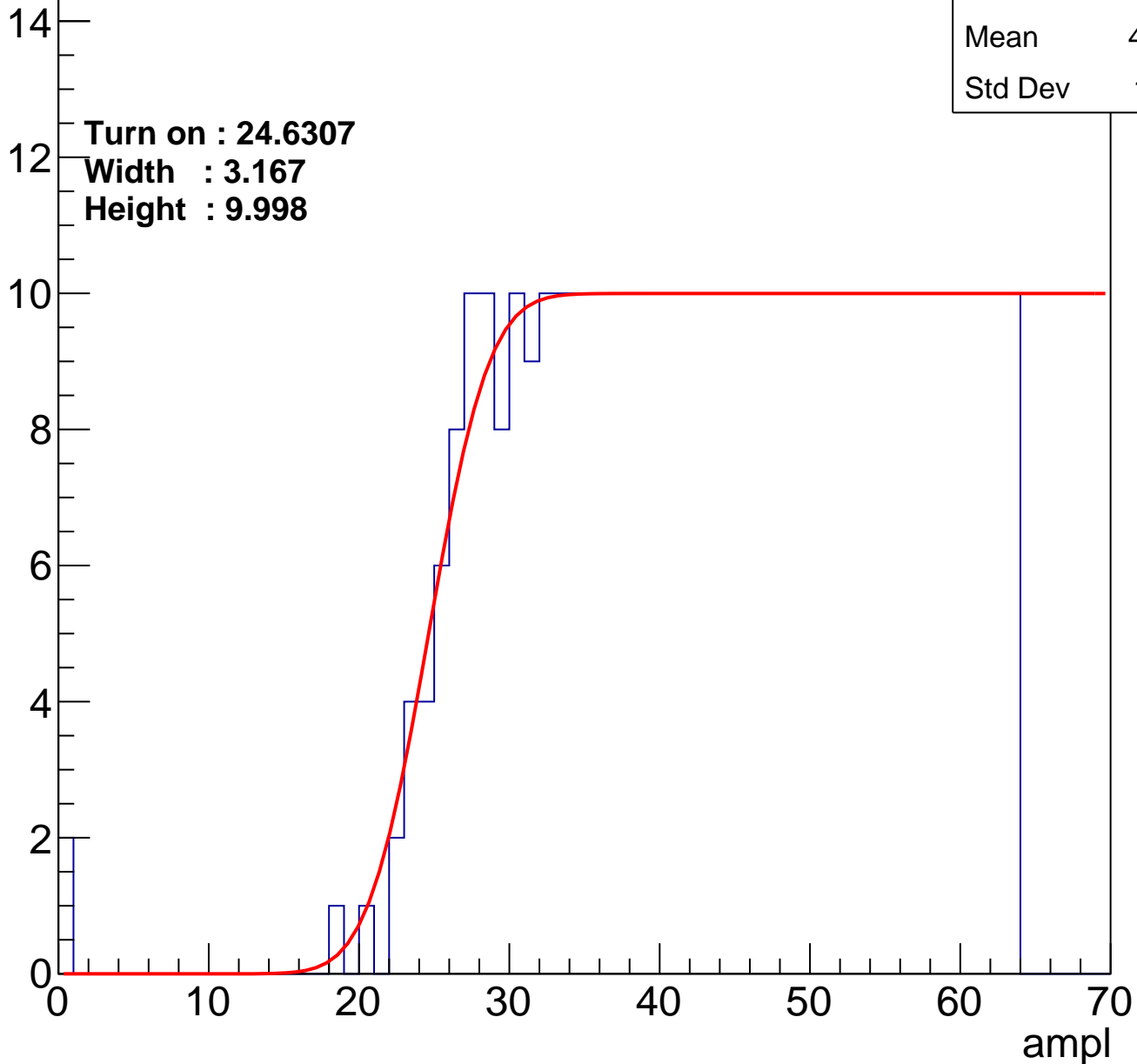
Entries	395
Mean	43.52
Std Dev	11.91

Turn on : 24.6307

Width : 3.167

Height : 9.998

Entry



B1L000S, U18-ch65

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.67
Std Dev	12.06

Turn on : 25.5658

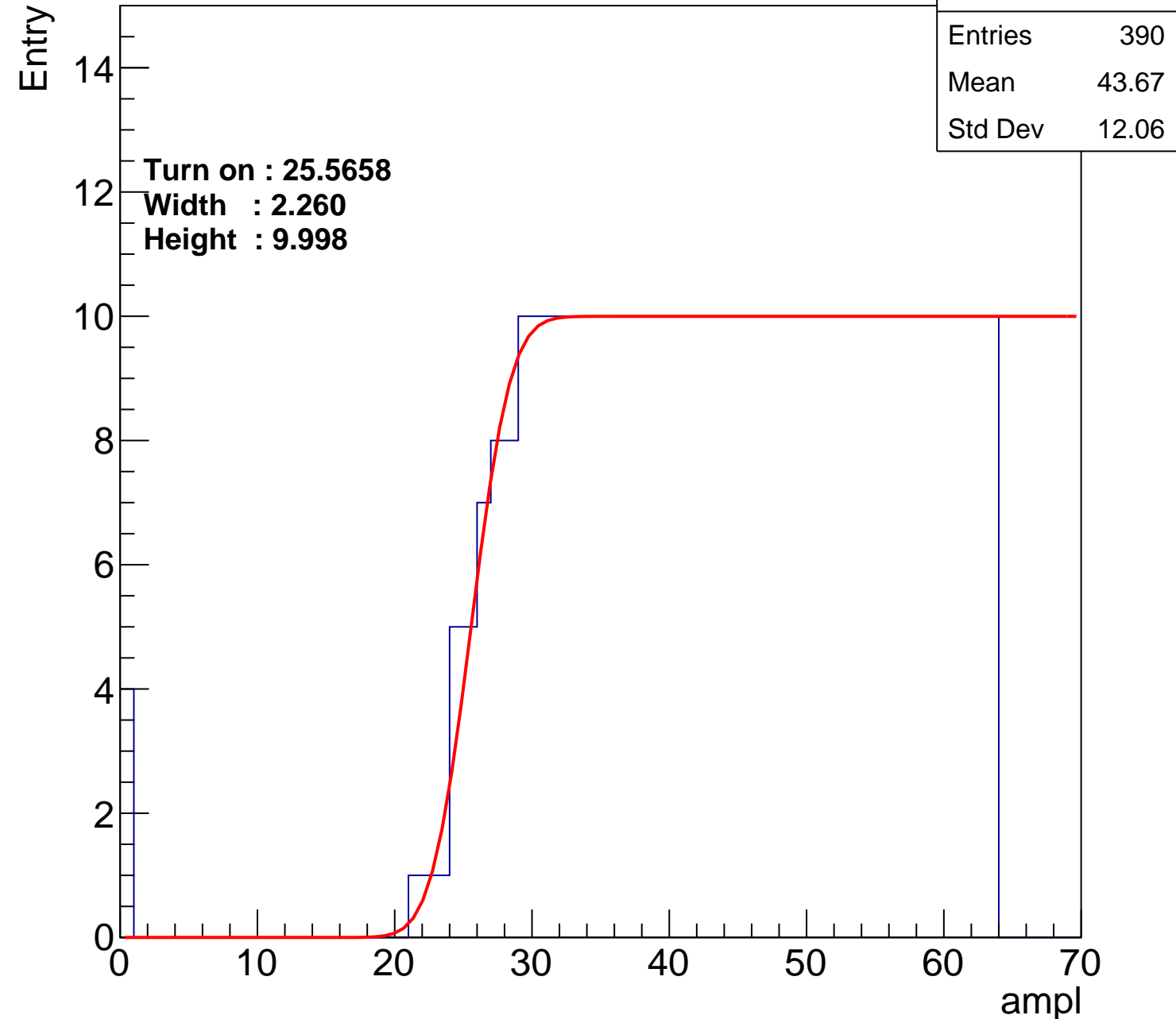
Width : 2.260

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.23
Std Dev	11.53

Turn on : 26.3476

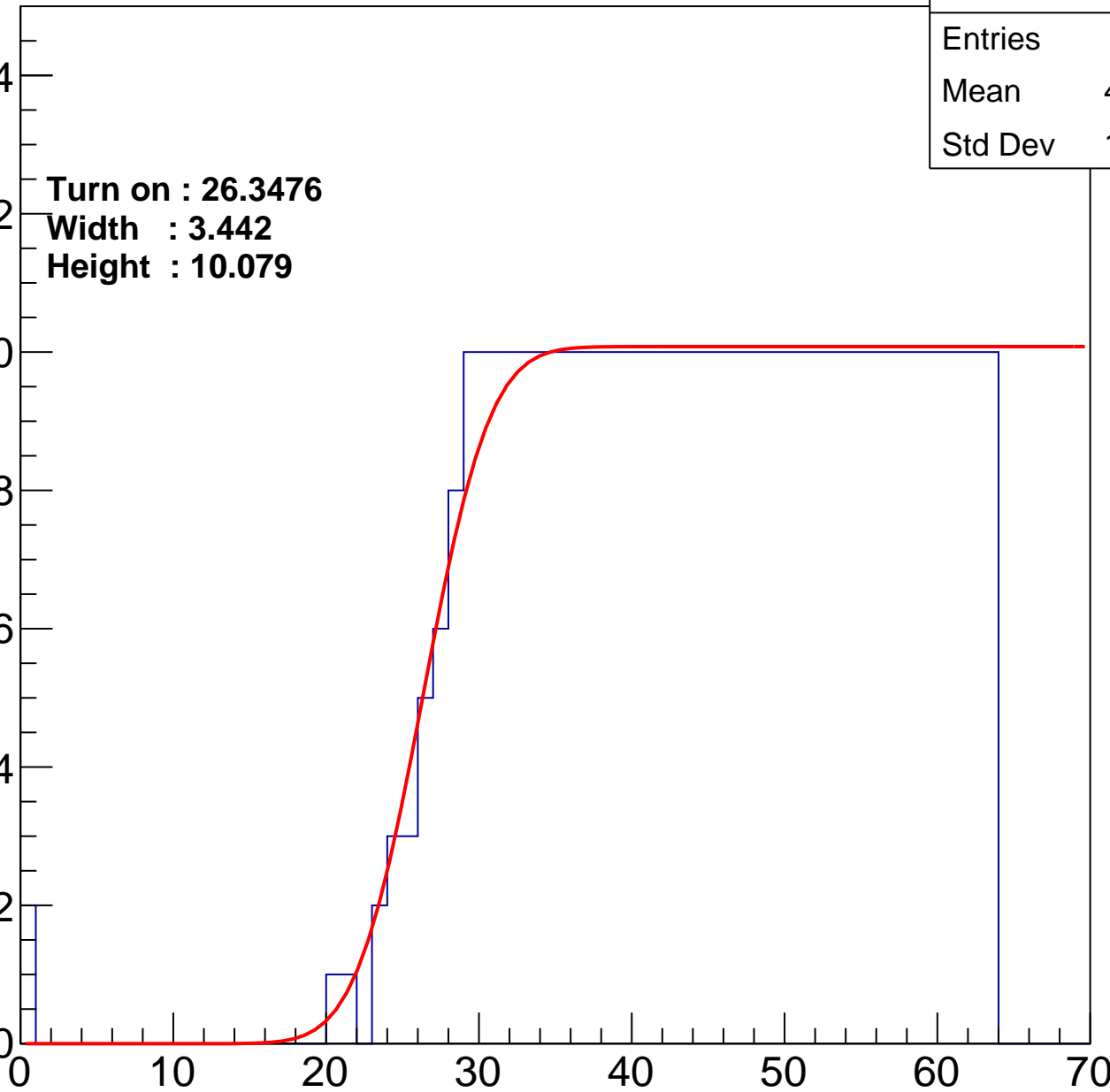
Width : 3.442

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.58
Std Dev	11.7

Turn on : 24.5184

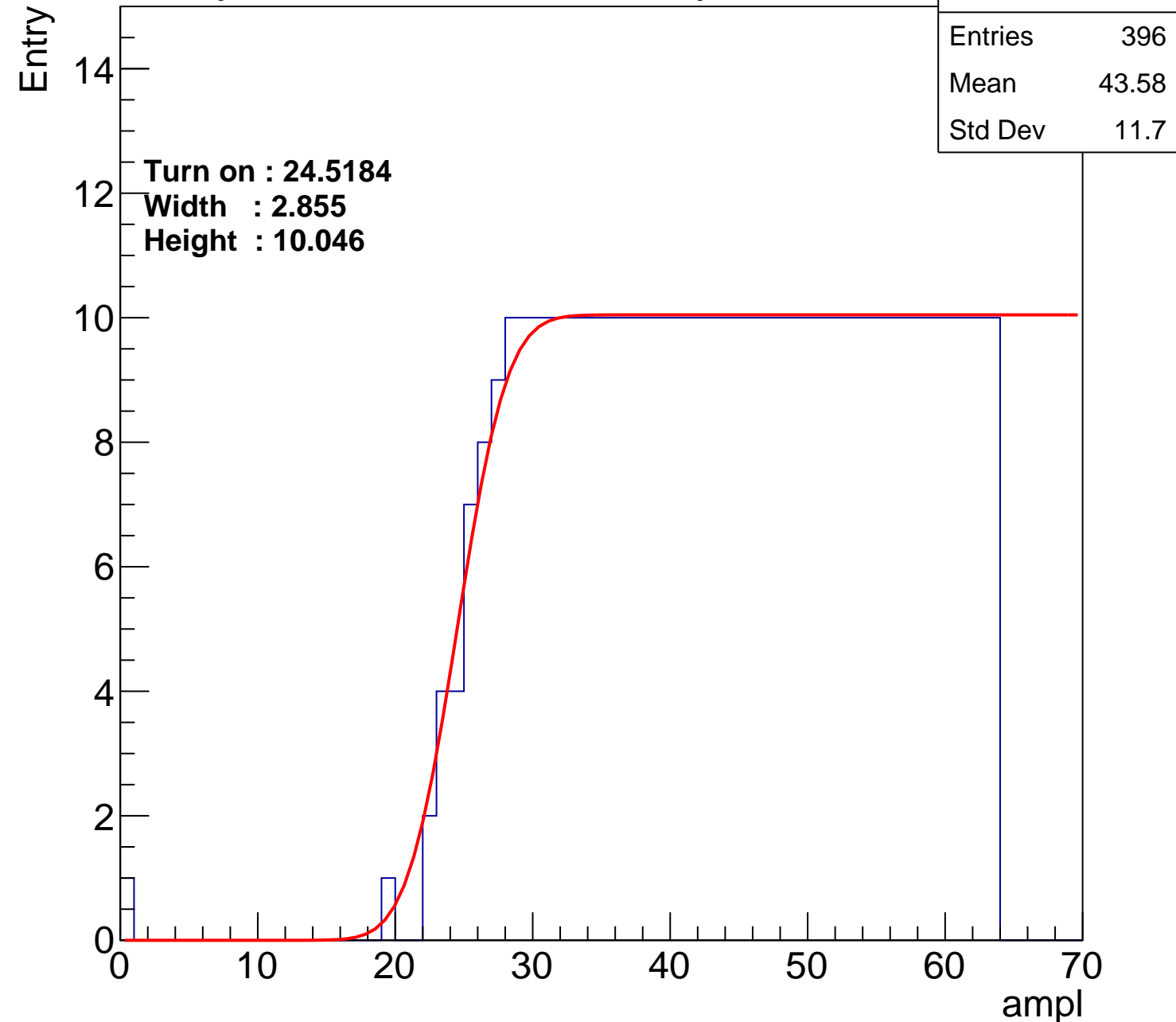
Width : 2.855

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.67
Std Dev	11.29

Turn on : 26.7930

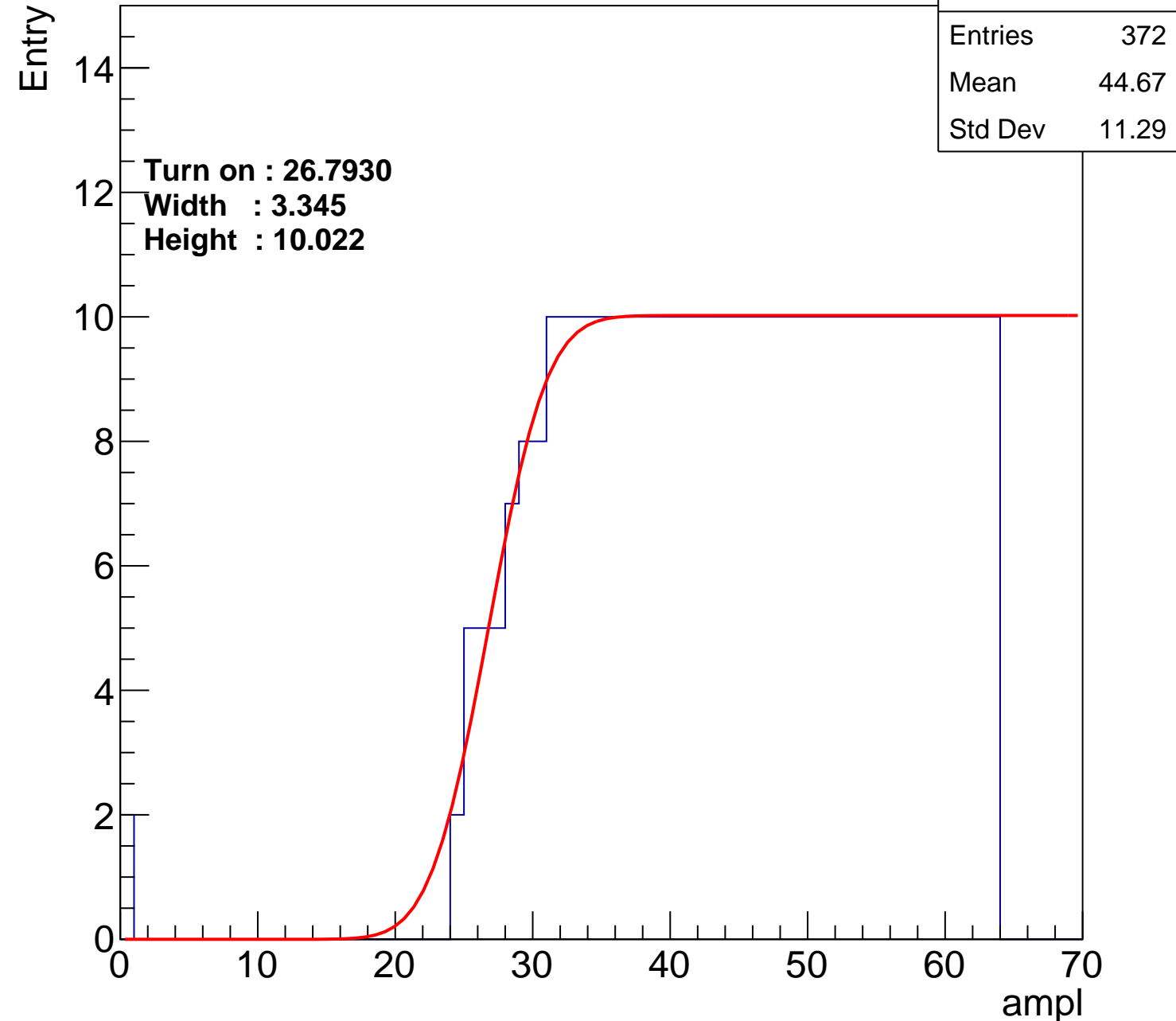
Width : 3.345

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch69

calib_packv5_042523_0143.root, FC#3, port B3

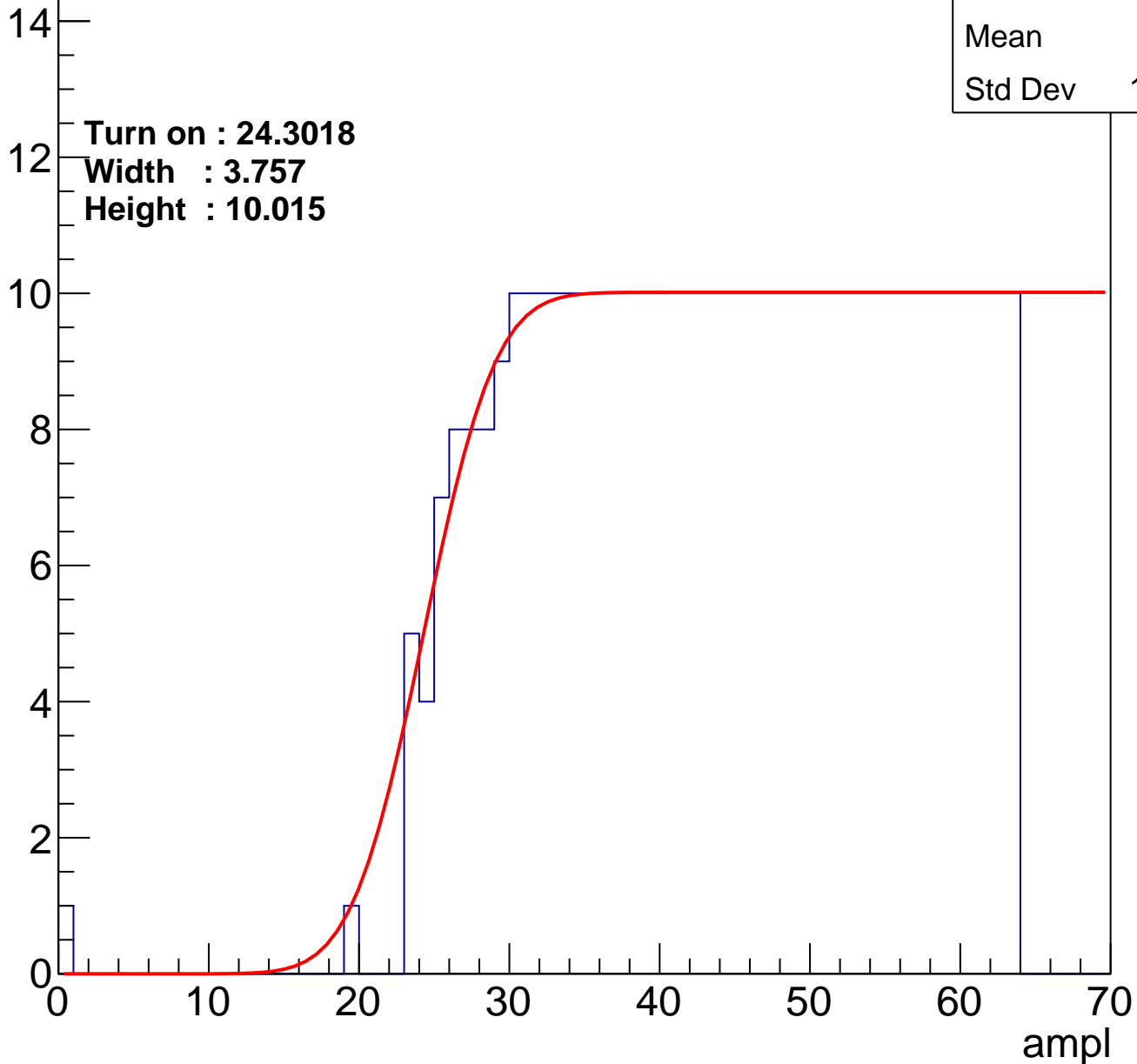
Entries	391
Mean	43.8
Std Dev	11.61

Turn on : 24.3018

Width : 3.757

Height : 10.015

Entry



B1L000S, U18-ch70

calib_packv5_042523_0143.root, FC#3, port B3

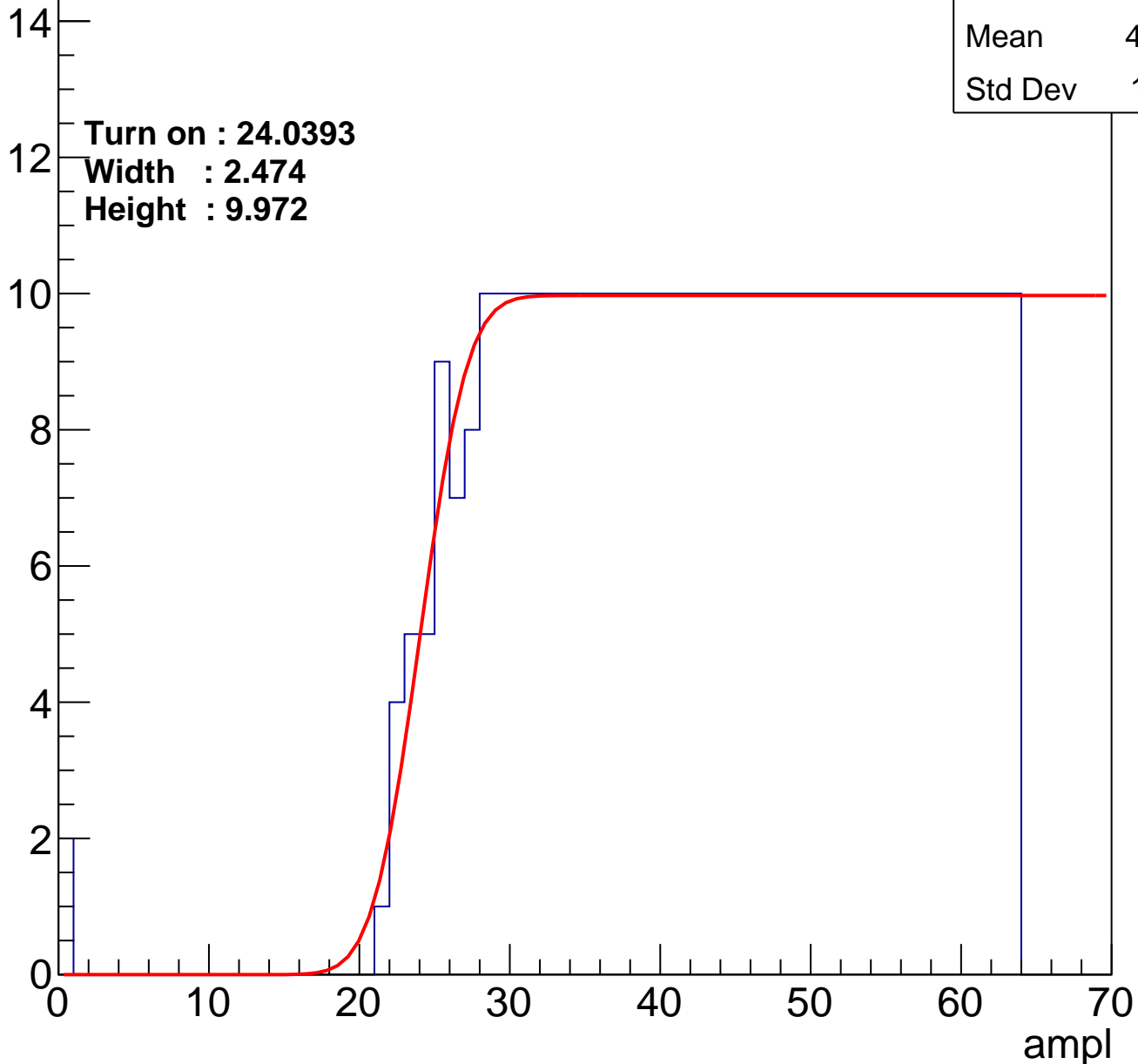
Entries	401
Mean	43.26
Std Dev	12.01

Turn on : 24.0393

Width : 2.474

Height : 9.972

Entry



B1L000S, U18-ch71

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.61
Std Dev	11.88

Turn on : 25.2724

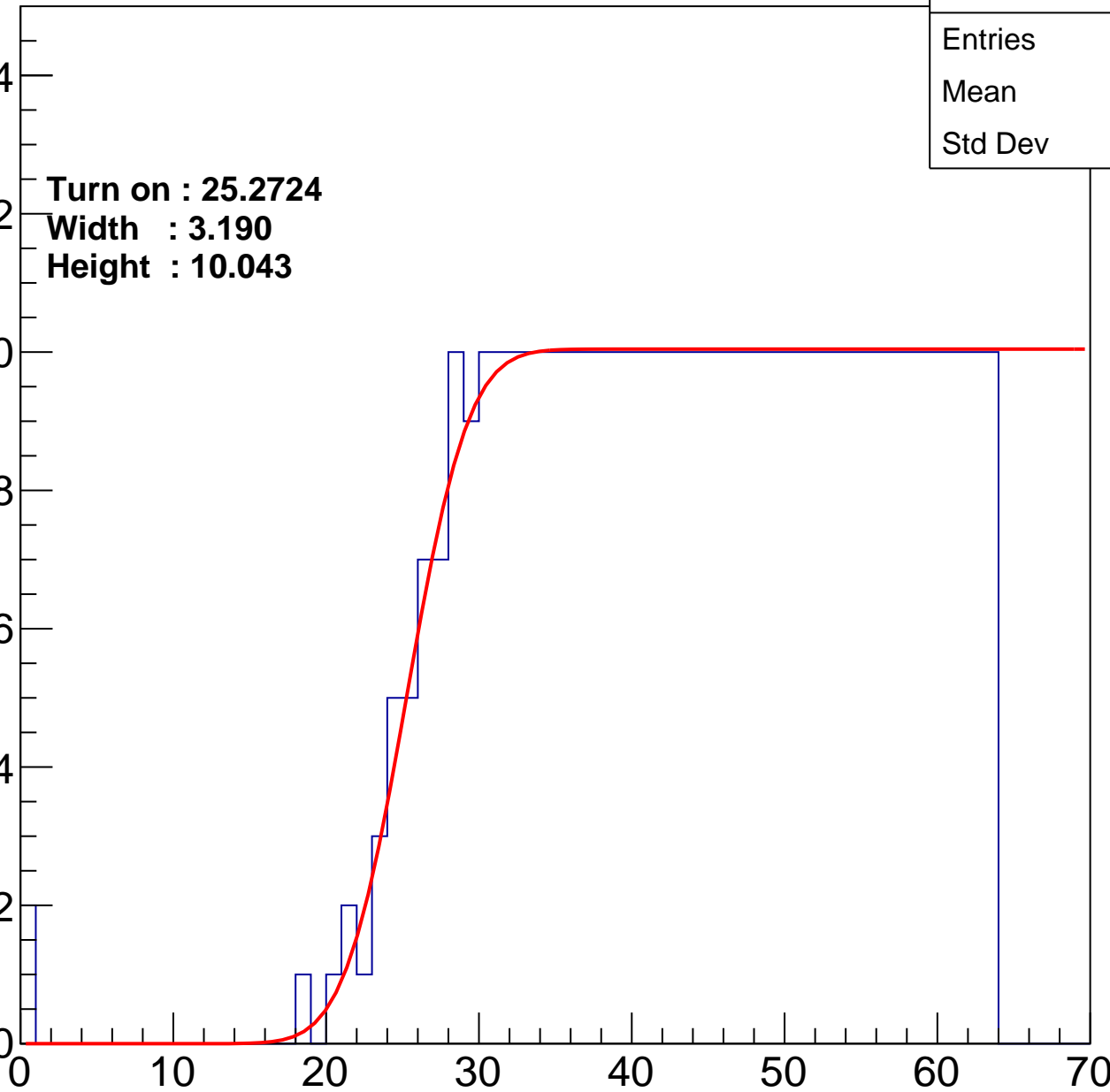
Width : 3.190

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch72

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.35
Std Dev	11.3

Turn on : 26.3280

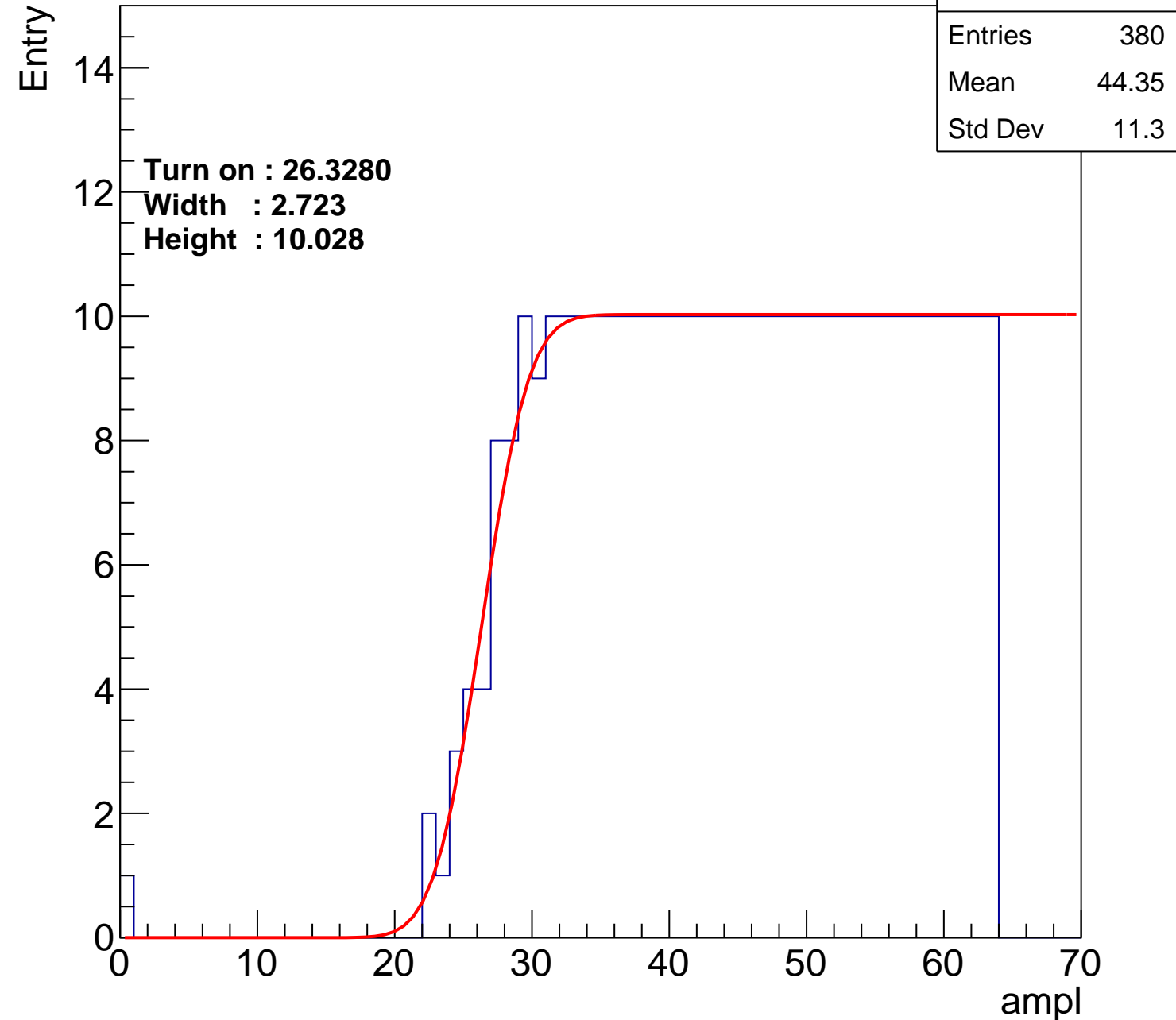
Width : 2.723

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.81
Std Dev	11.36

Turn on : 27.4256

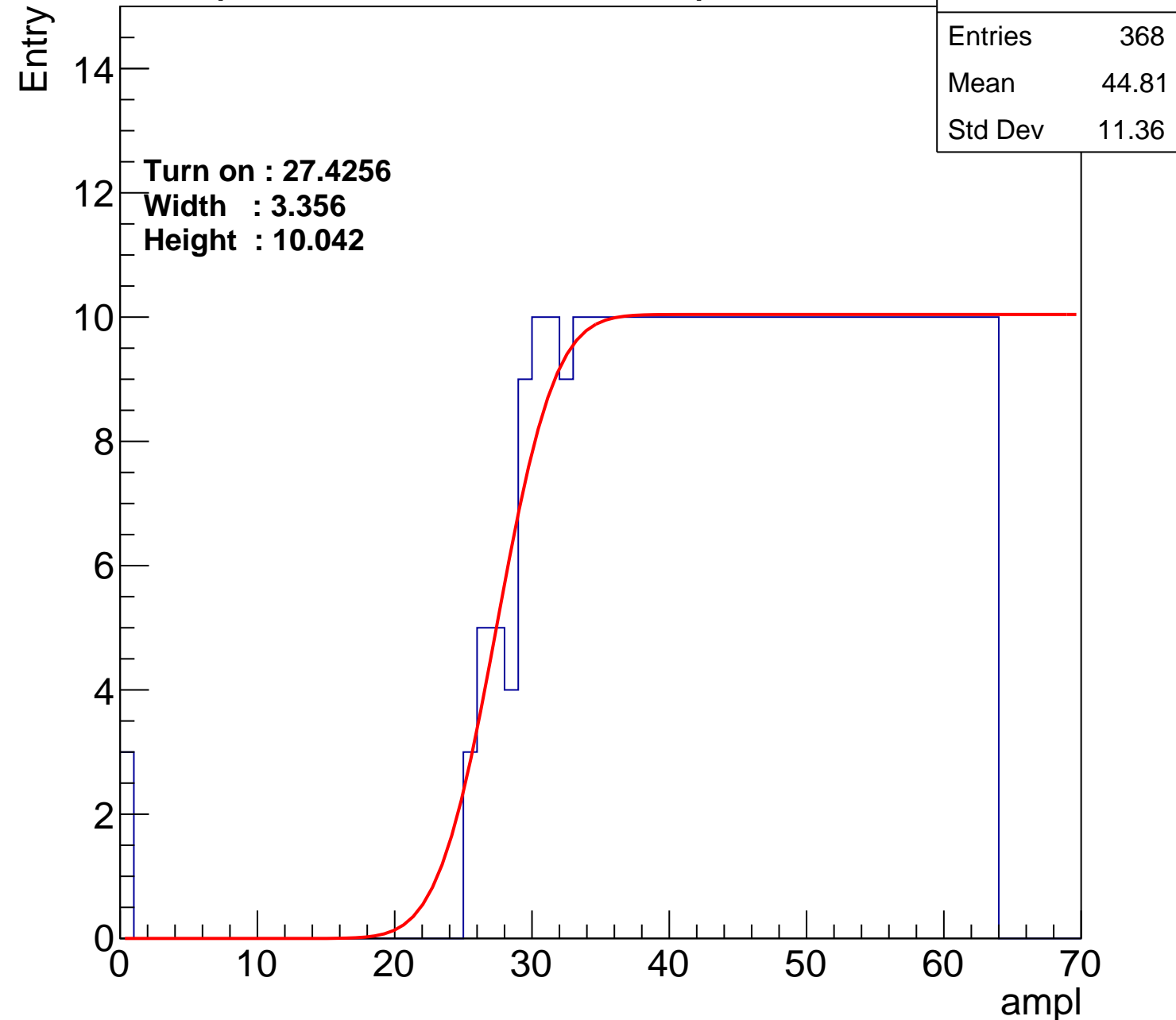
Width : 3.356

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch74

calib_packv5_042523_0143.root, FC#3, port B3

Entries	370
Mean	44.86
Std Dev	11.19

Turn on : 28.2252

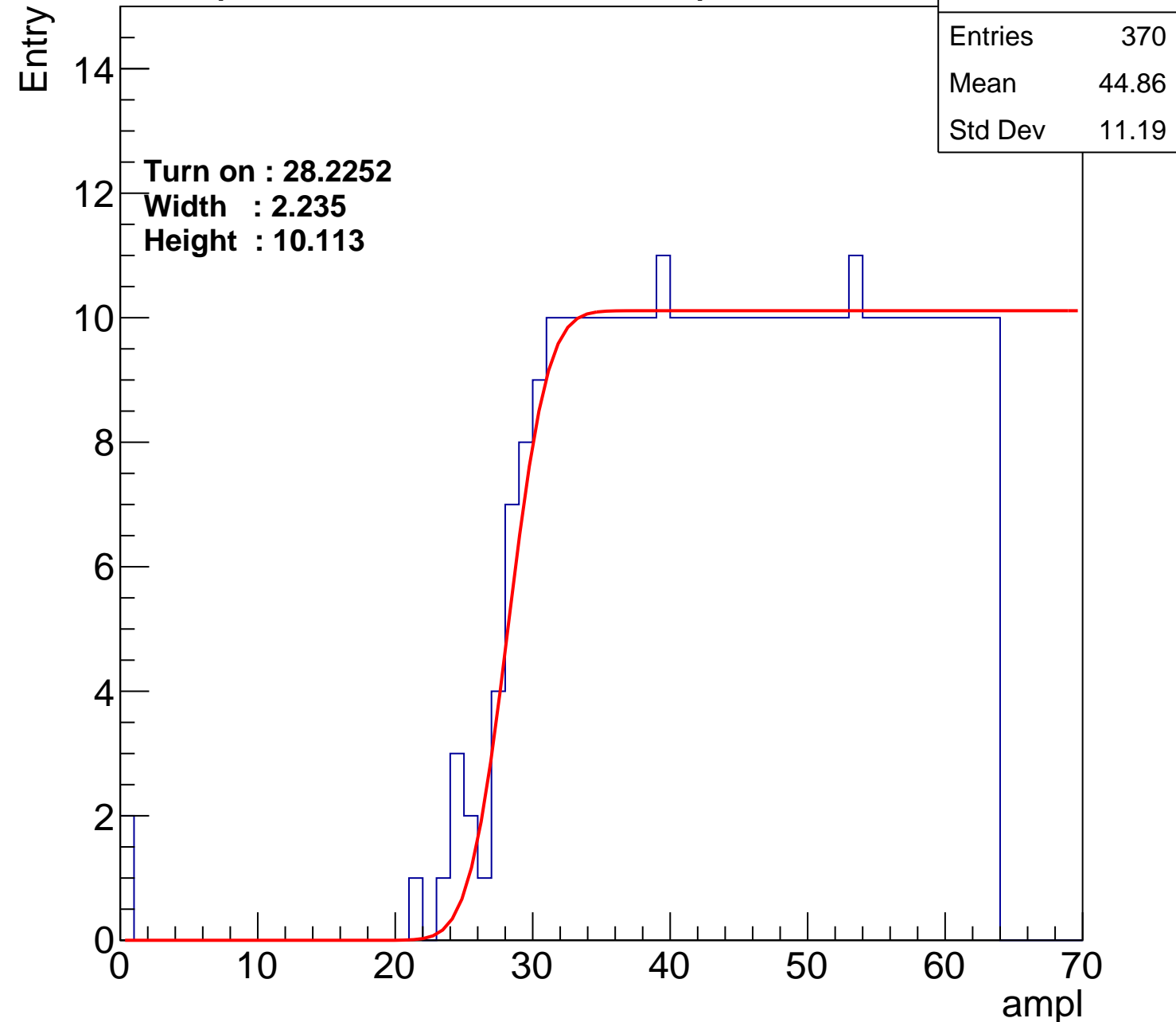
Width : 2.235

Height : 10.113

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 26.5218

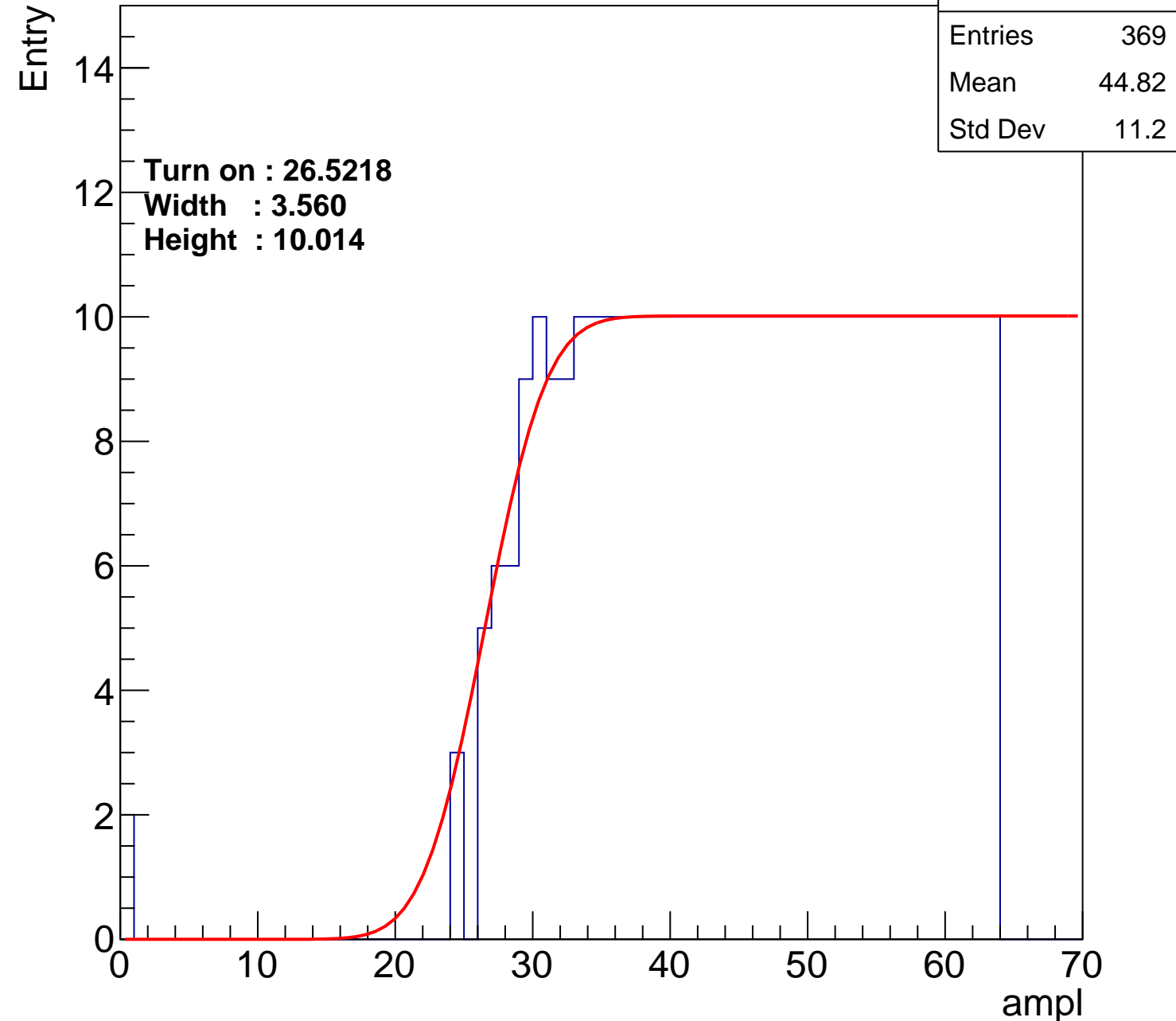
Width : 3.560

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch76

calib_packv5_042523_0143.root, FC#3, port B3

Entries	351
Mean	45.75
Std Dev	10.58

Turn on : 29.2819

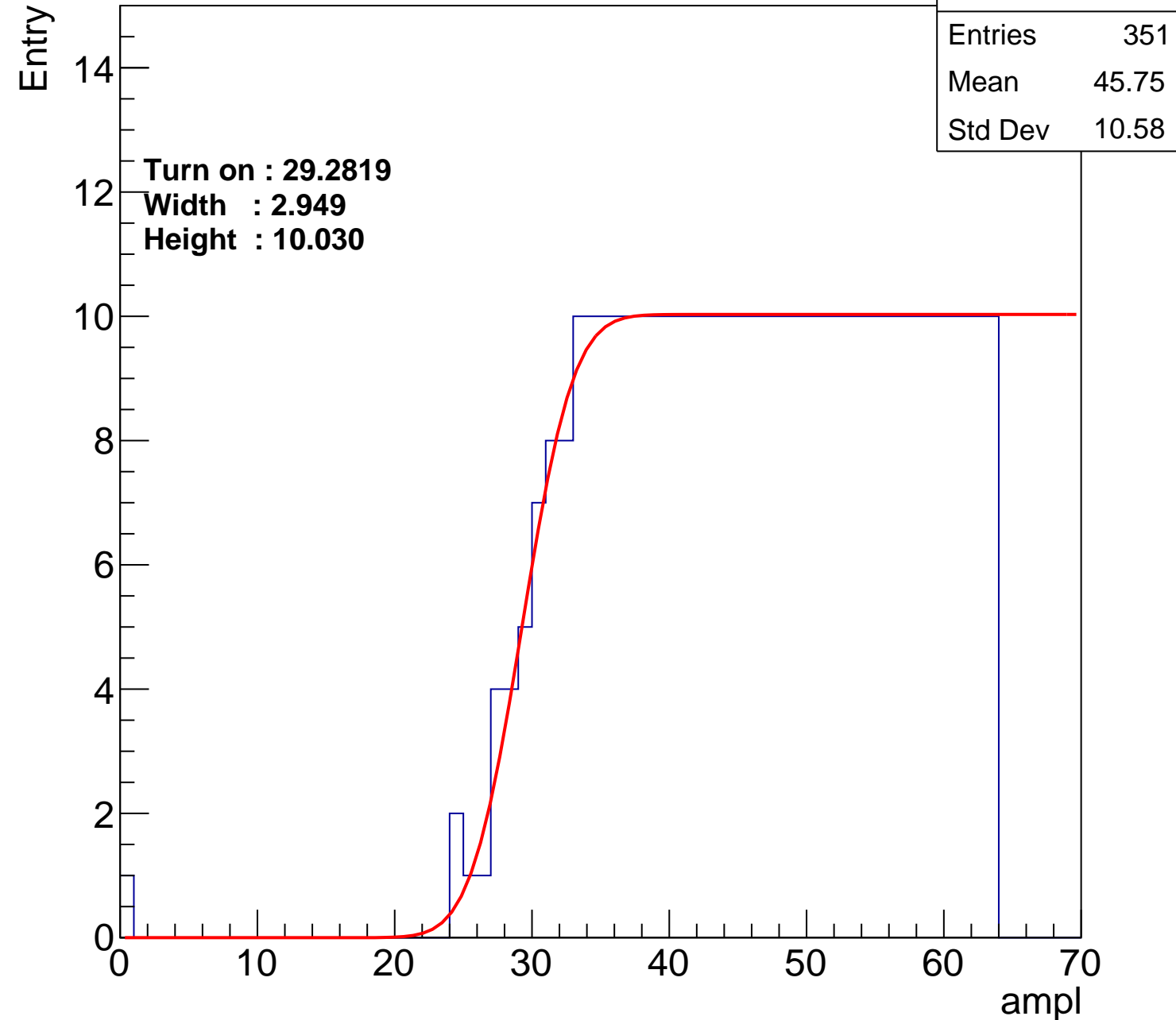
Width : 2.949

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch77

calib_packv5_042523_0143.root, FC#3, port B3

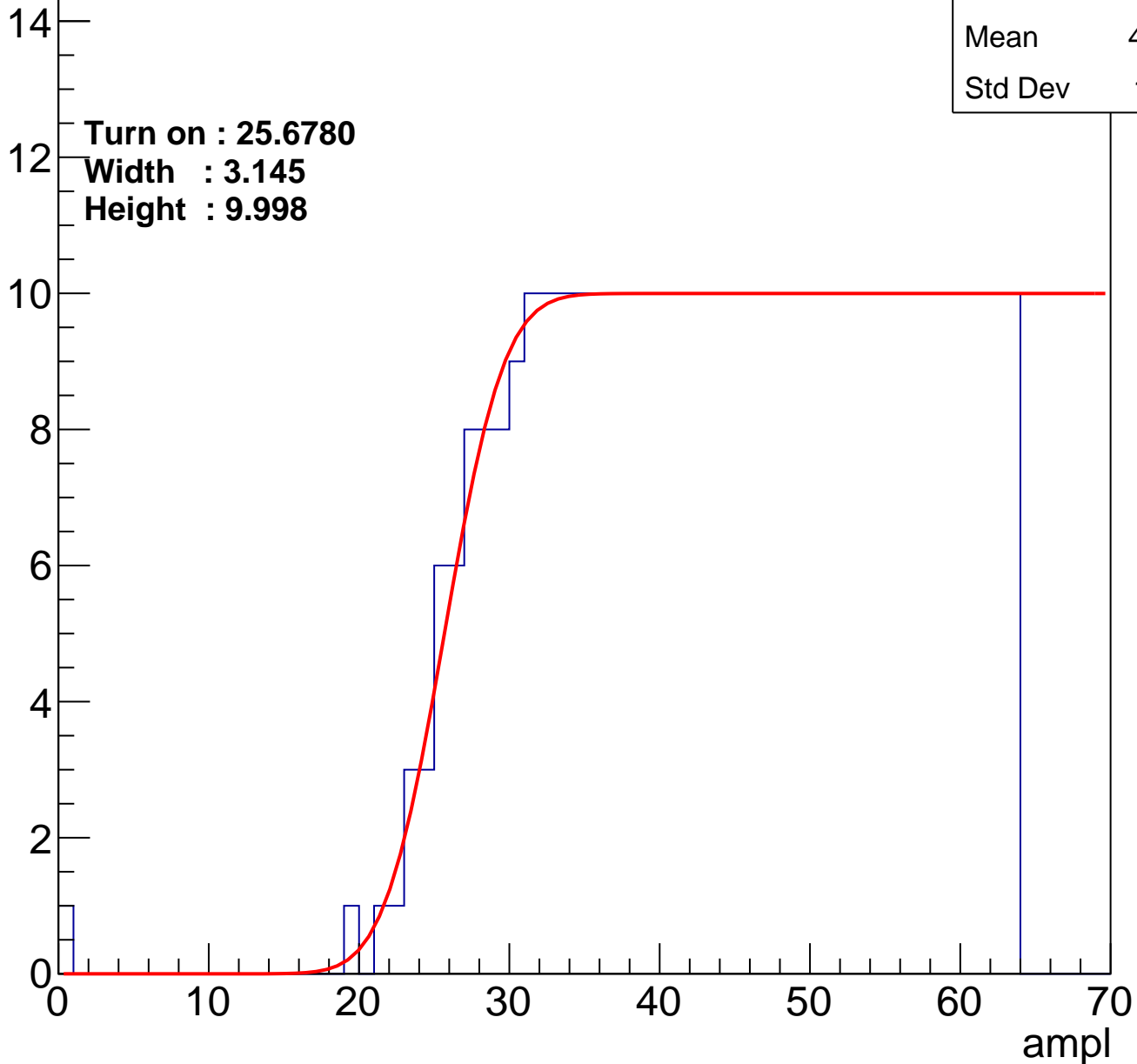
Entries	385
Mean	44.05
Std Dev	11.51

Turn on : 25.6780

Width : 3.145

Height : 9.998

Entry



B1L000S, U18-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.61
Std Dev	11.81

Turn on : 24.8106

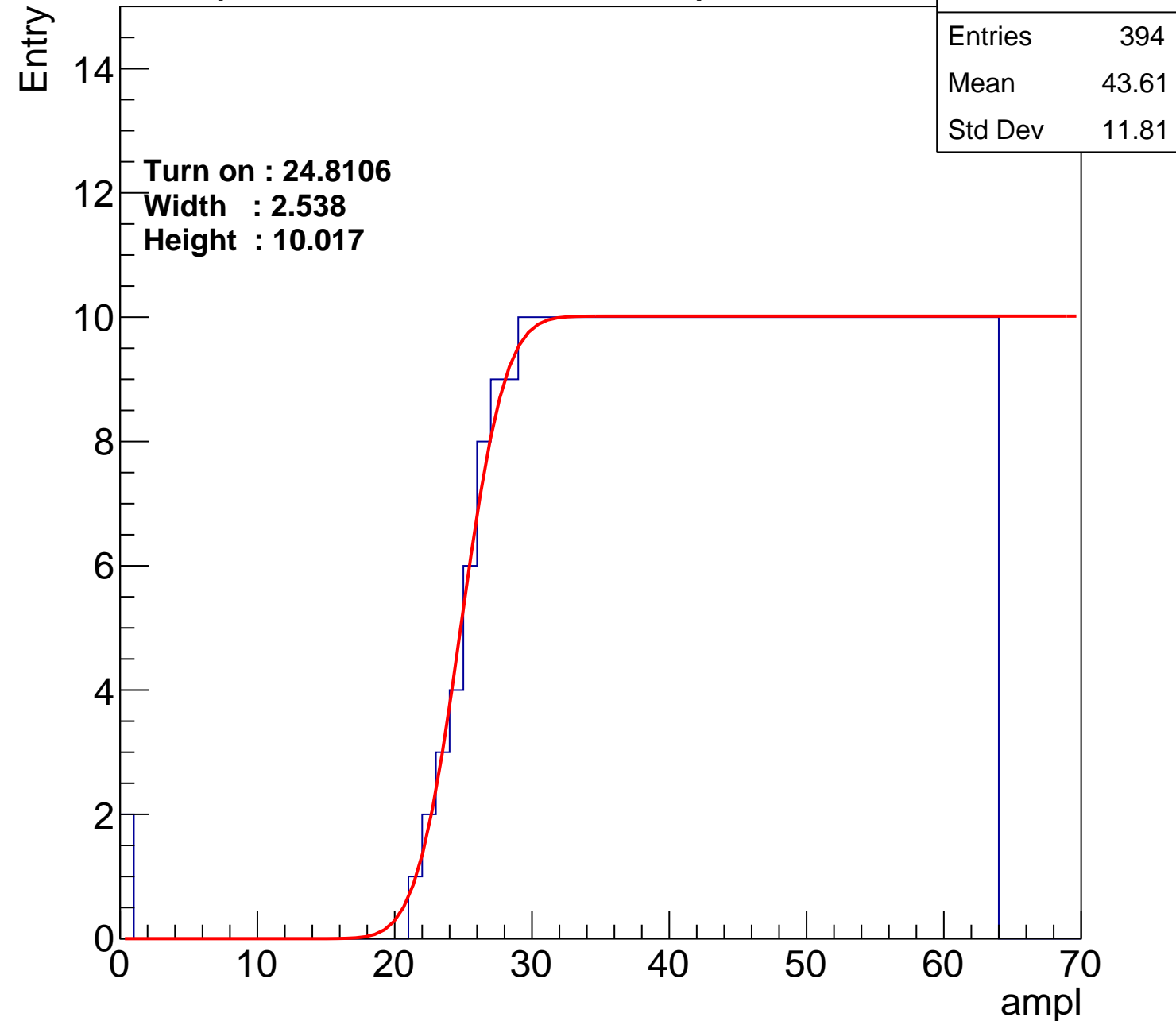
Width : 2.538

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.23
Std Dev	11.38

Turn on : 26.3160

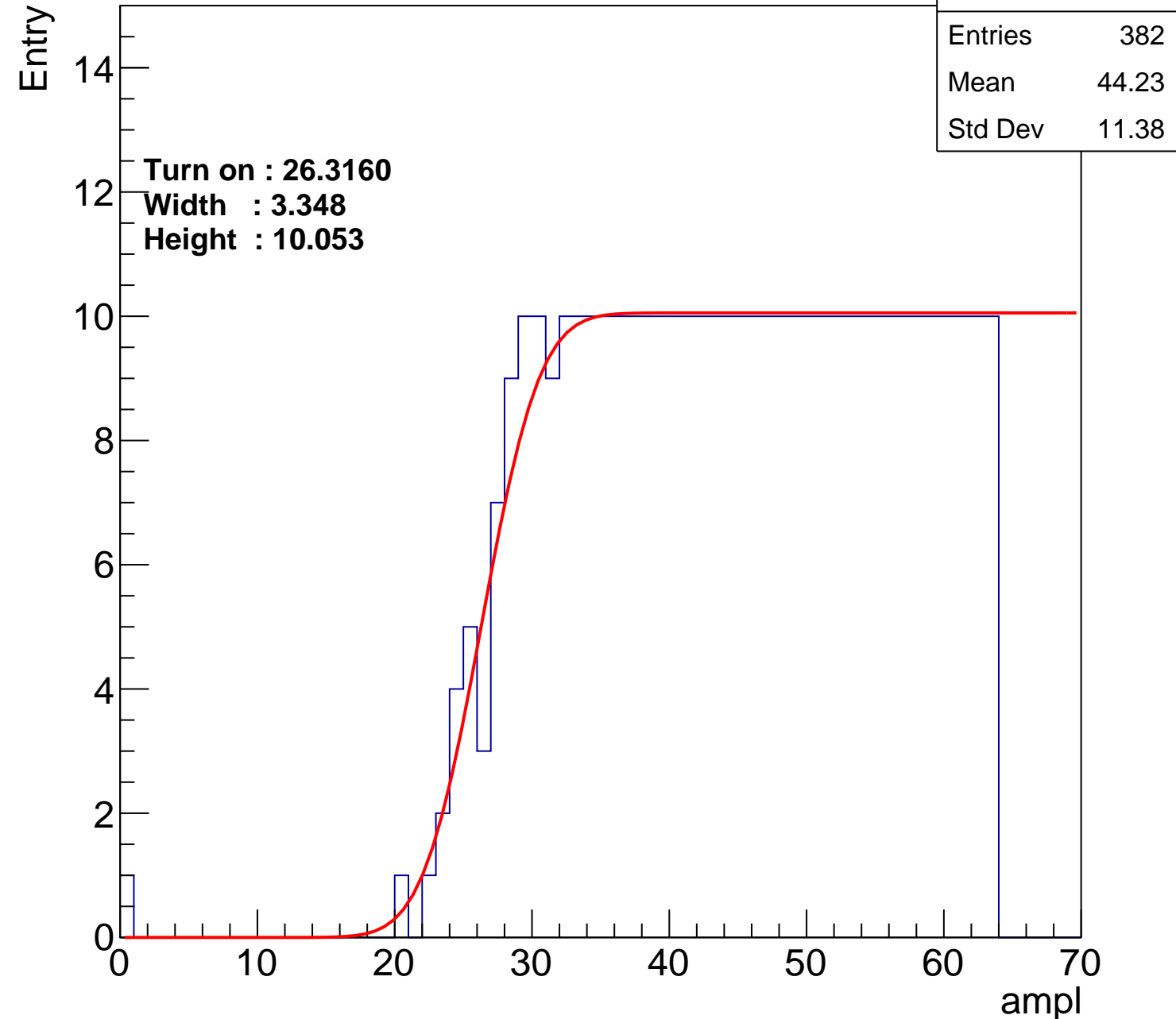
Width : 3.348

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch80

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.57
Std Dev	11.89

Turn on : 25.1313

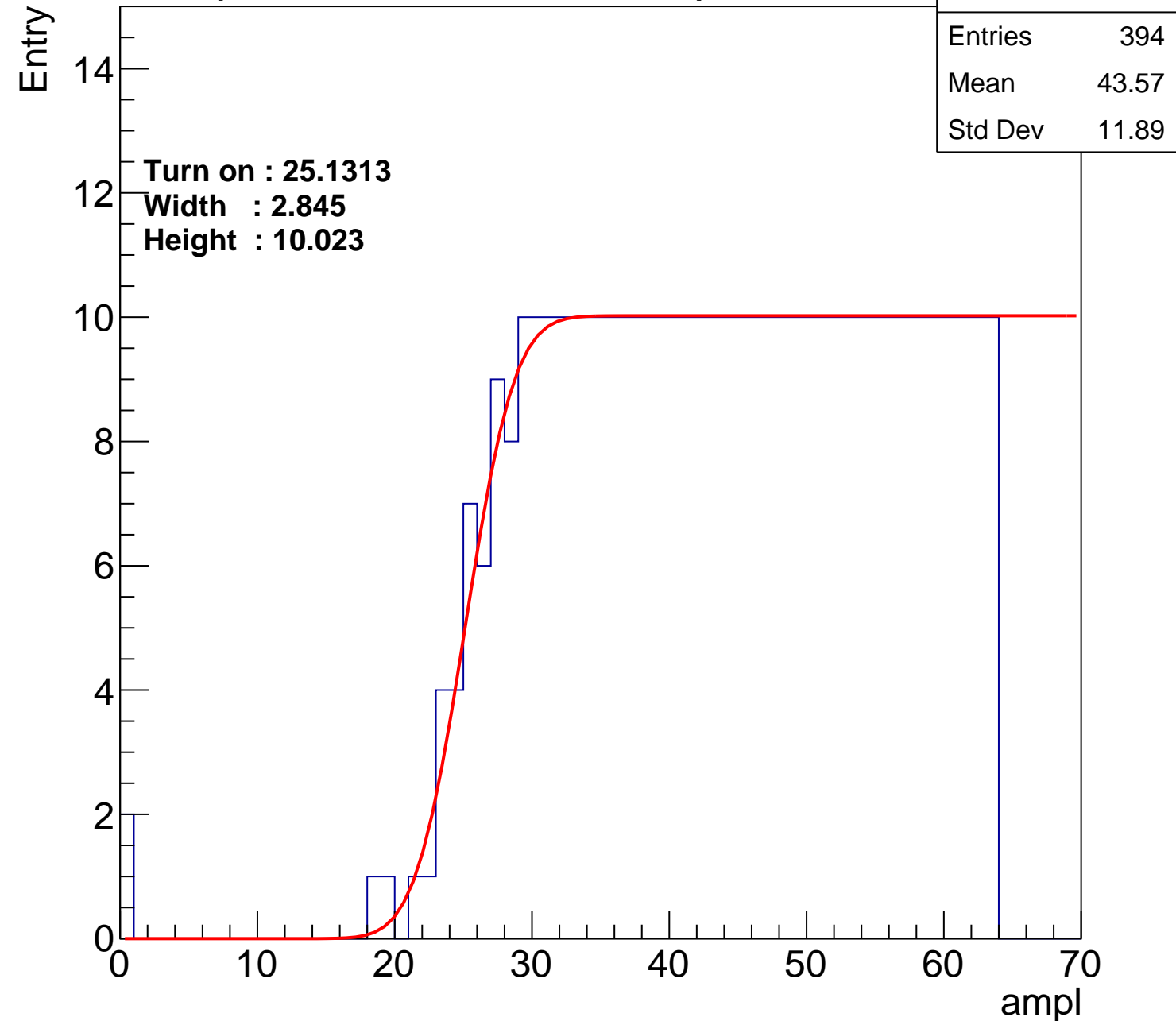
Width : 2.845

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	43.98
Std Dev	11.78

Turn on : 26.1044

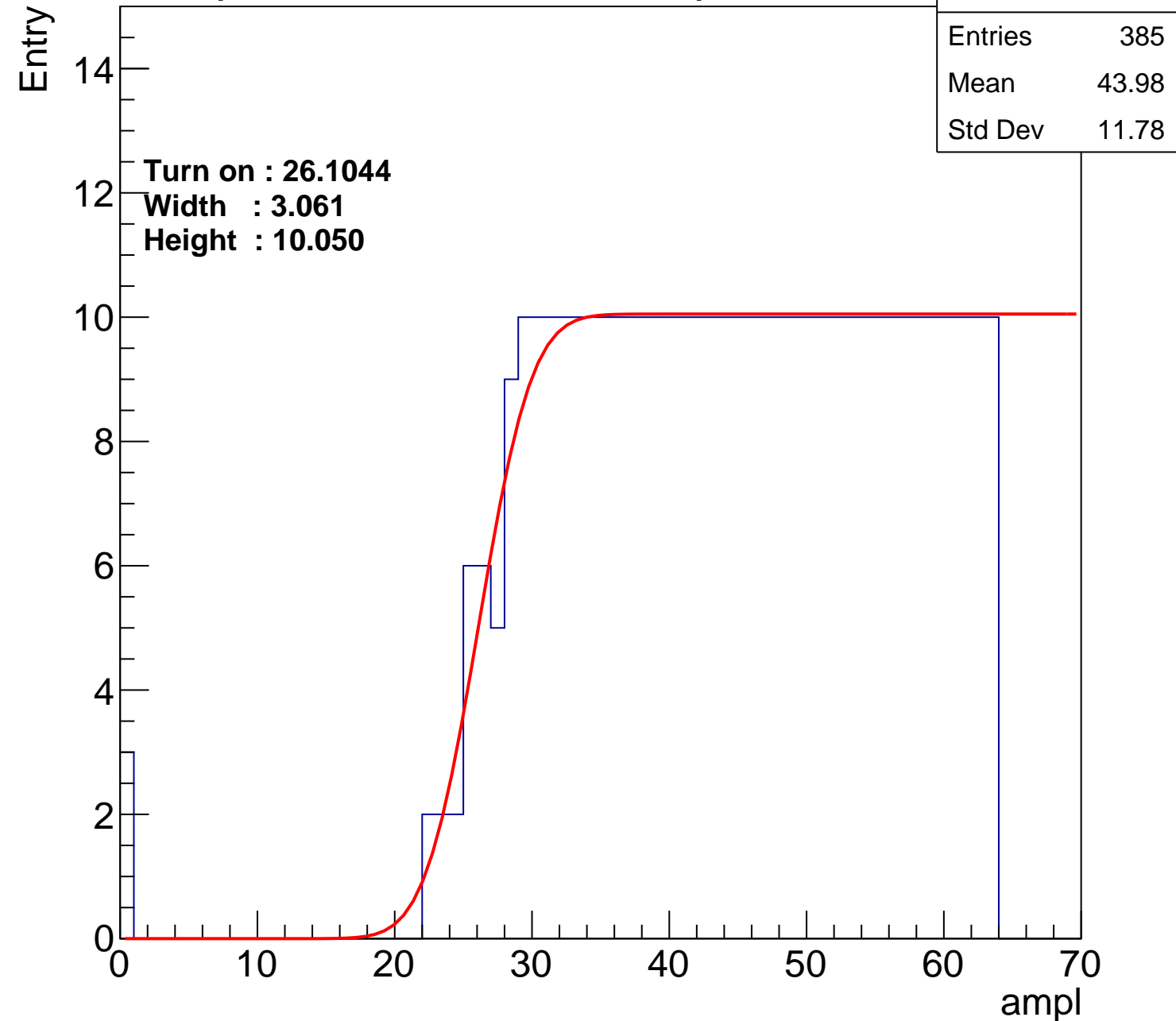
Width : 3.061

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.21
Std Dev	11.67

Turn on : 26.3083

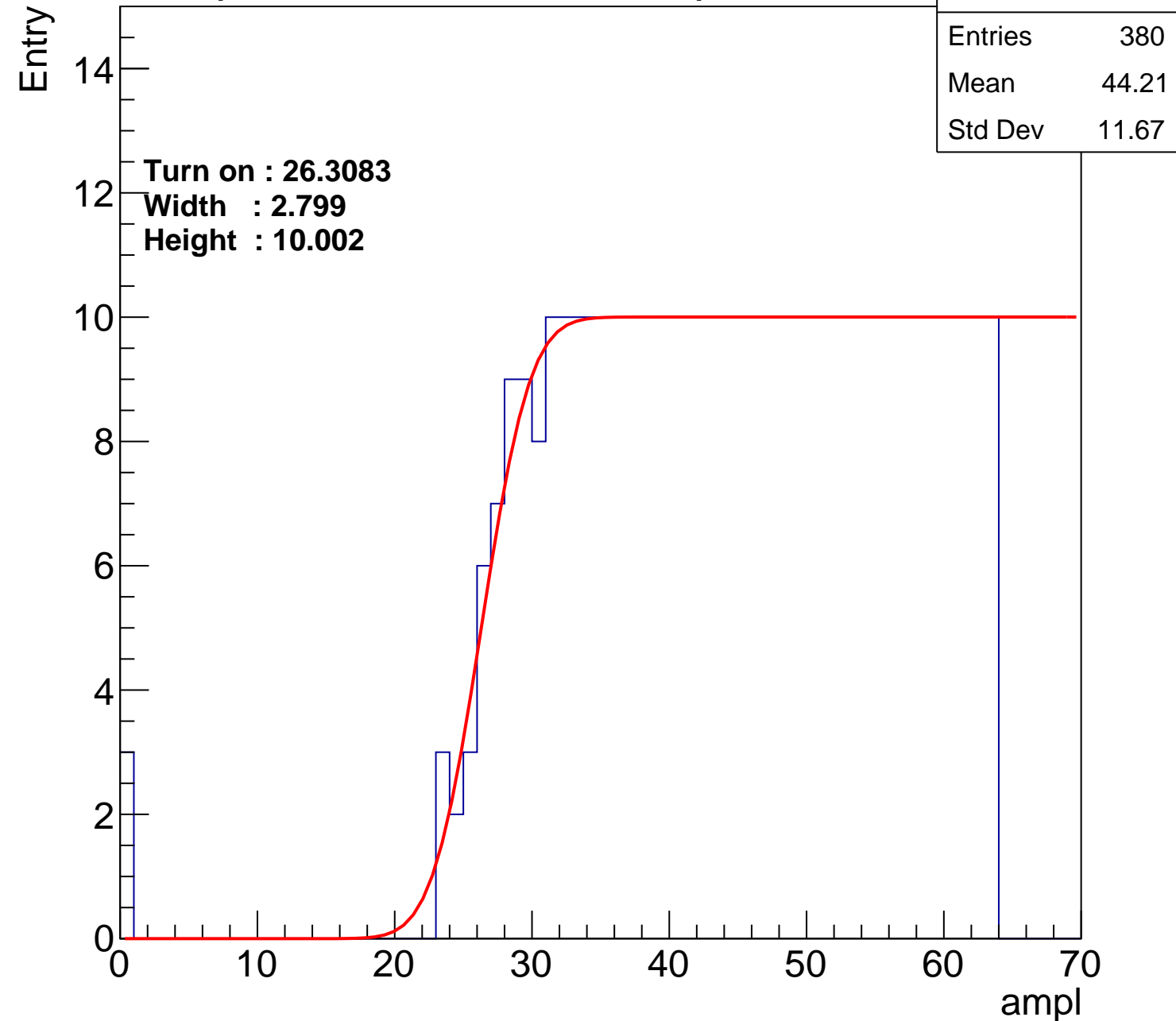
Width : 2.799

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch83

calib_packv5_042523_0143.root, FC#3, port B3

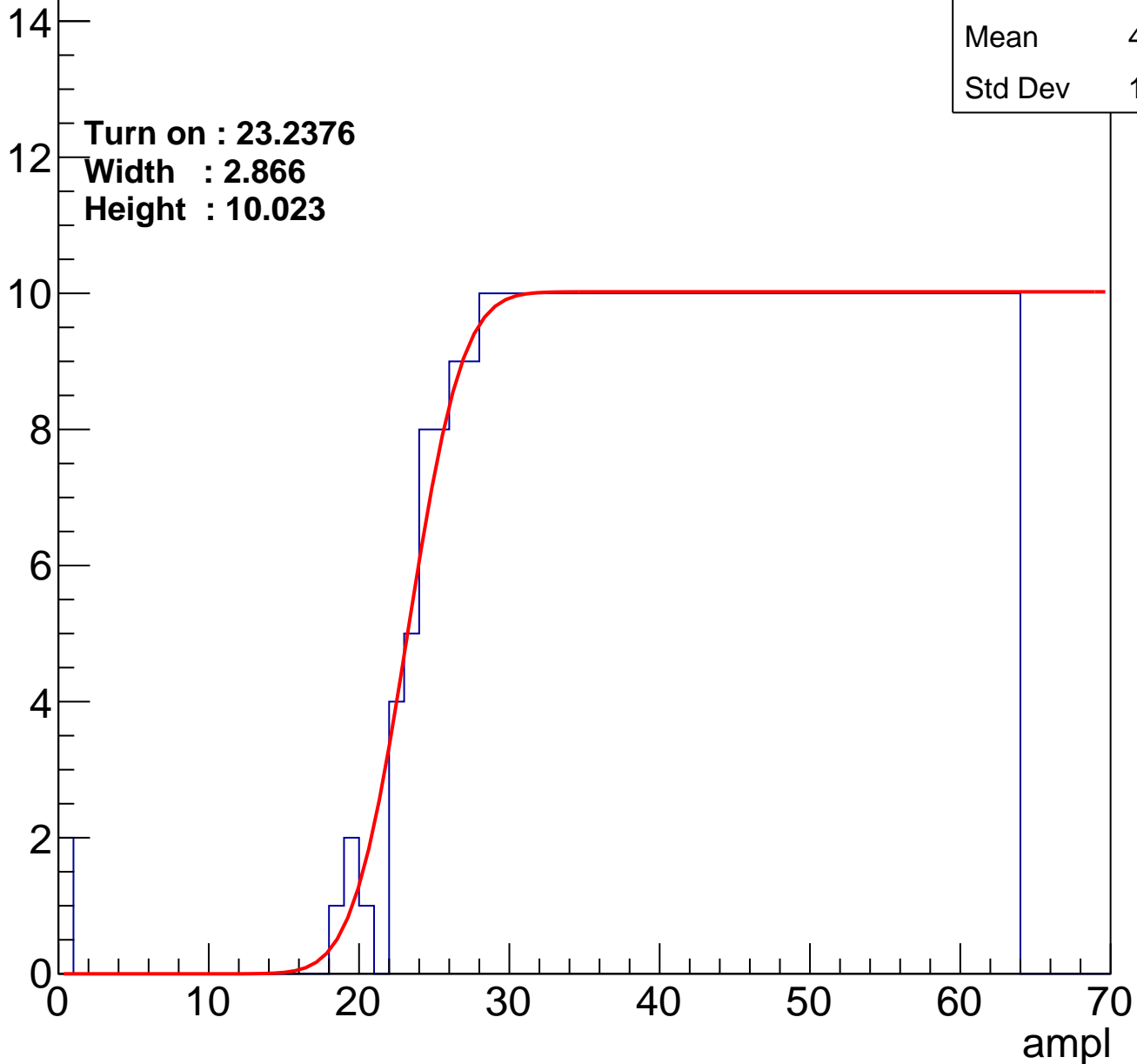
Entries	409
Mean	42.86
Std Dev	12.24

Turn on : 23.2376

Width : 2.866

Height : 10.023

Entry



B1L000S, U18-ch84

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.84

Turn on : 25.1042

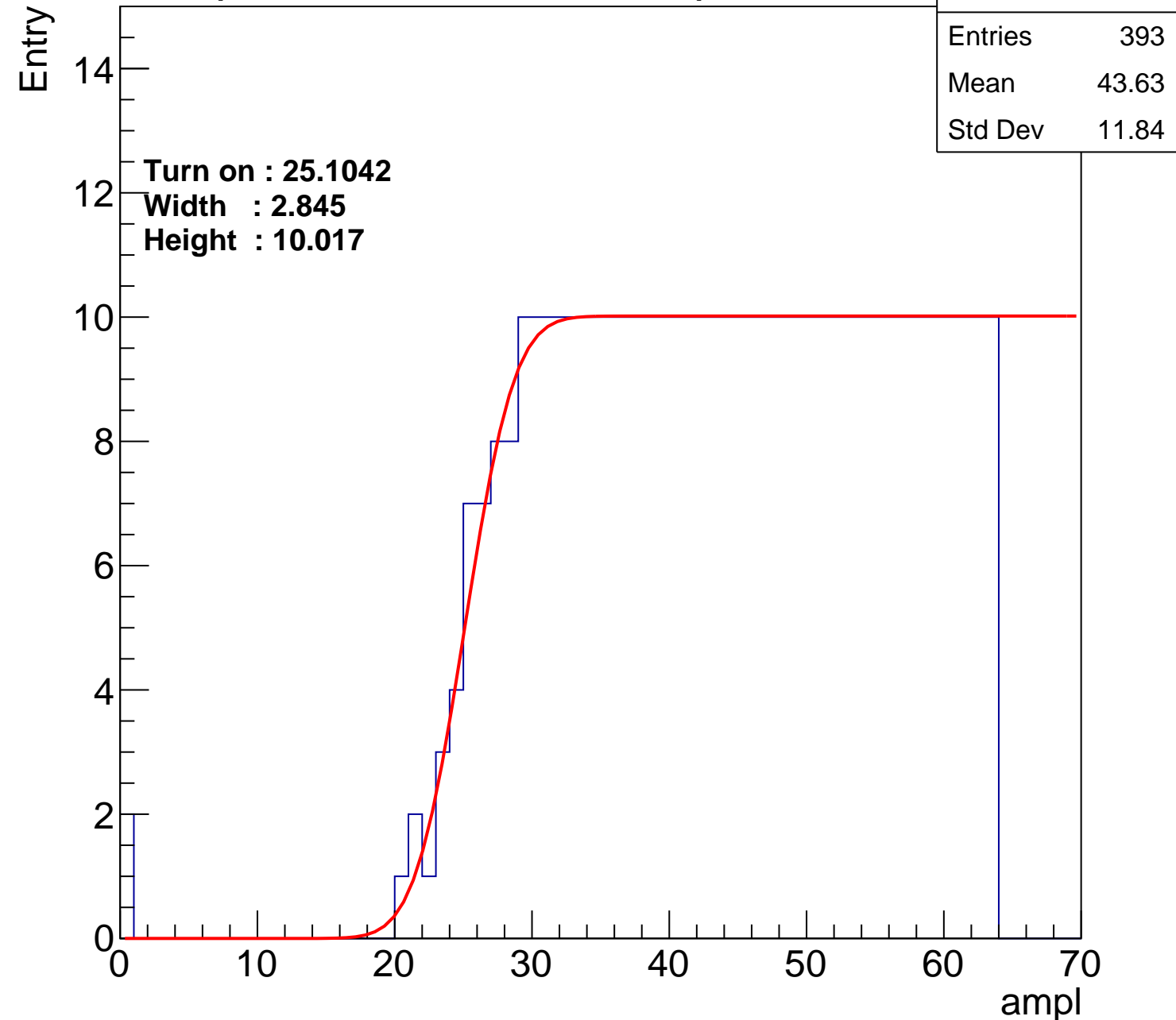
Width : 2.845

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.7978

Width : 2.178

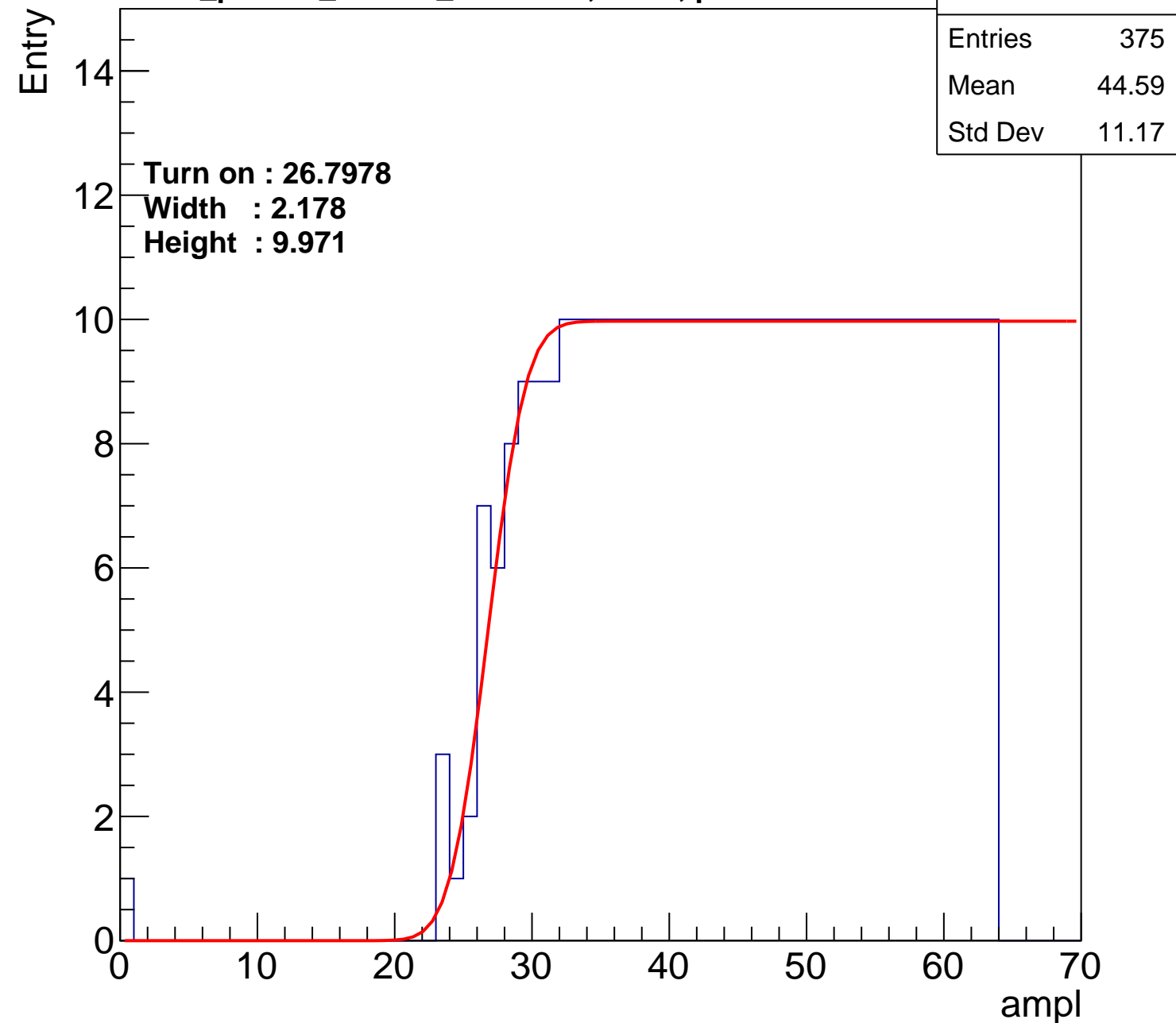
Height : 9.971

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U18-ch86

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.26
Std Dev	11.39

Turn on : 26.5465

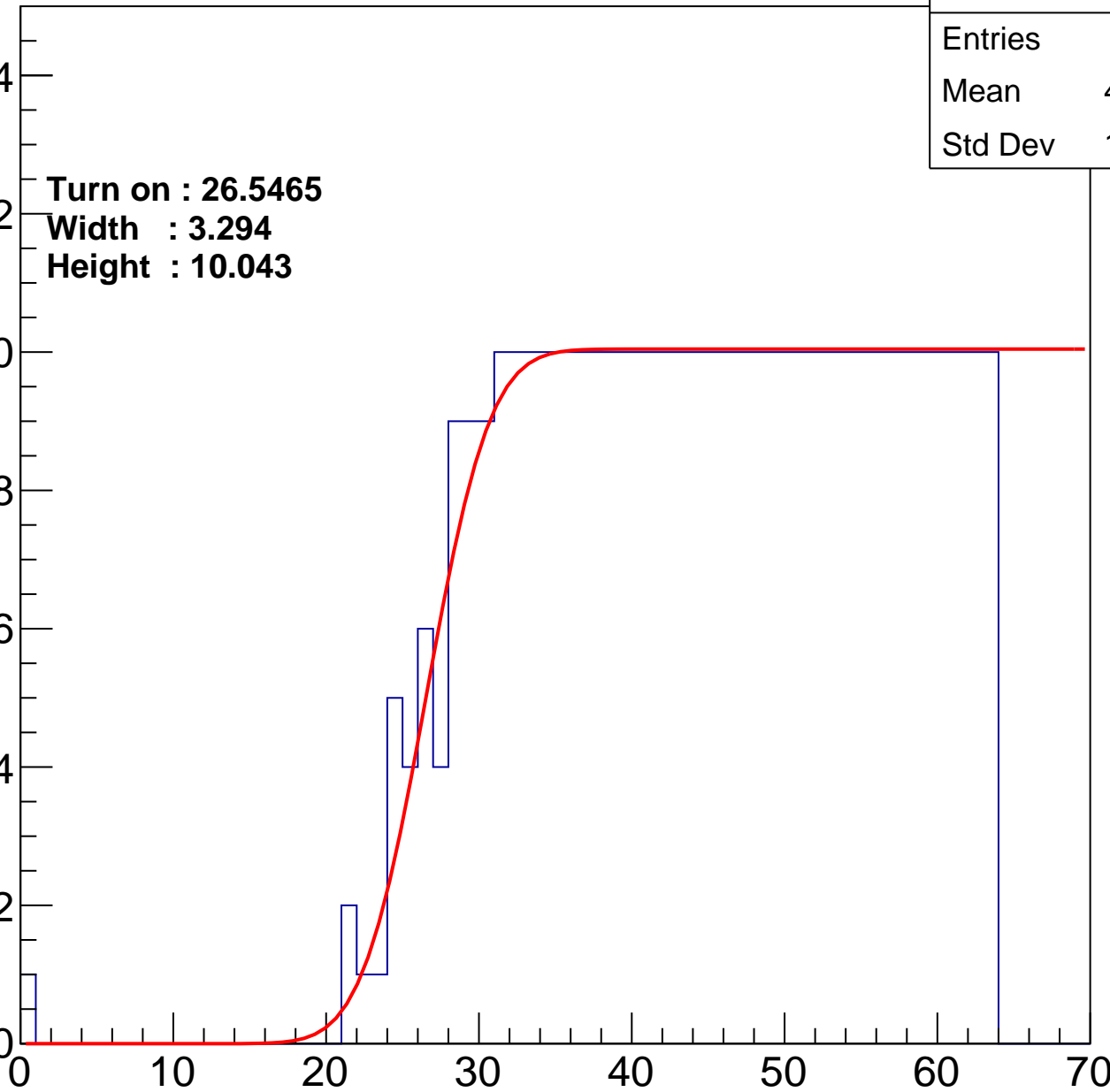
Width : 3.294

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.93
Std Dev	11.68

Turn on : 25.7769

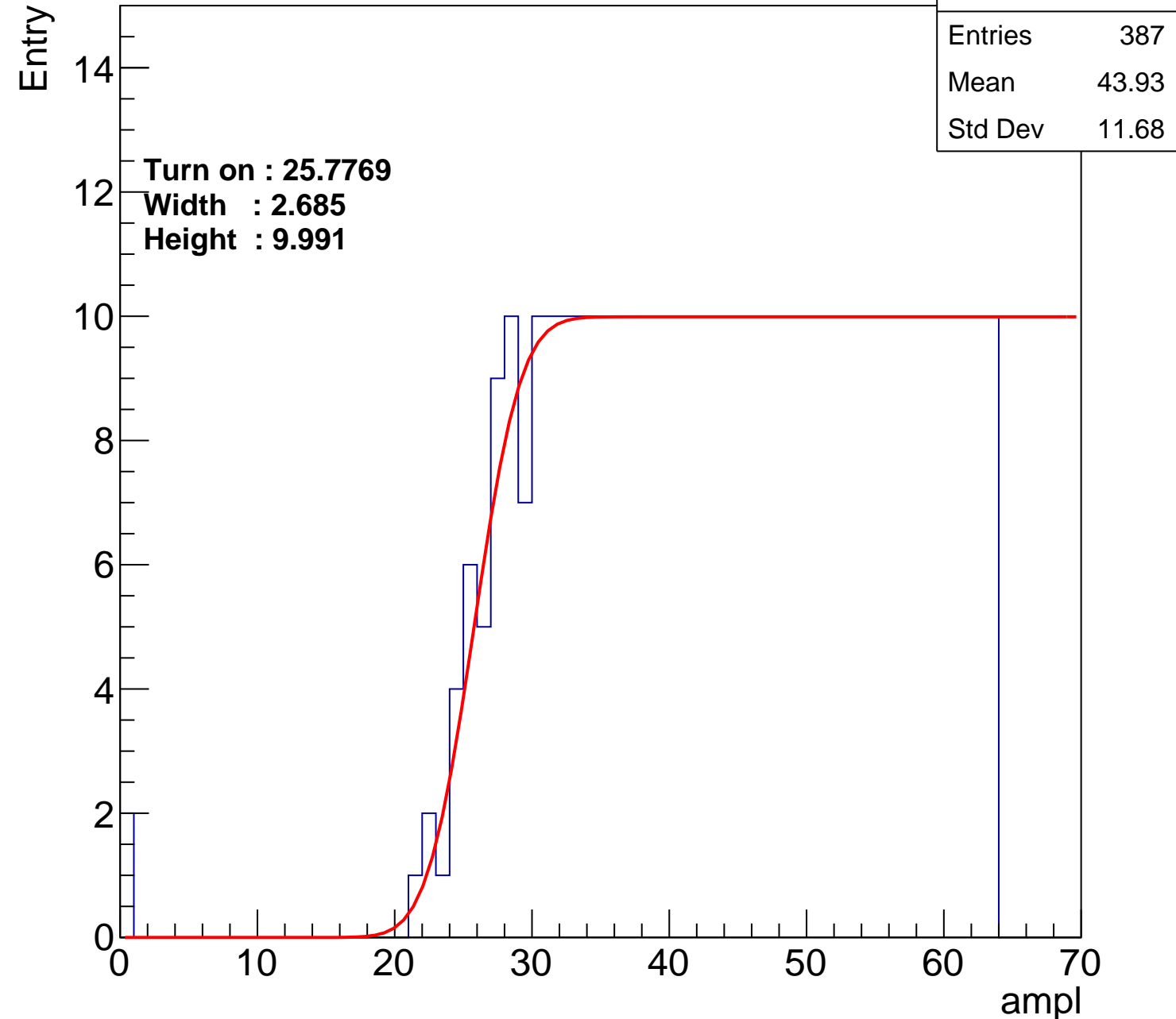
Width : 2.685

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch88

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.08
Std Dev	11.9

Turn on : 26.6099

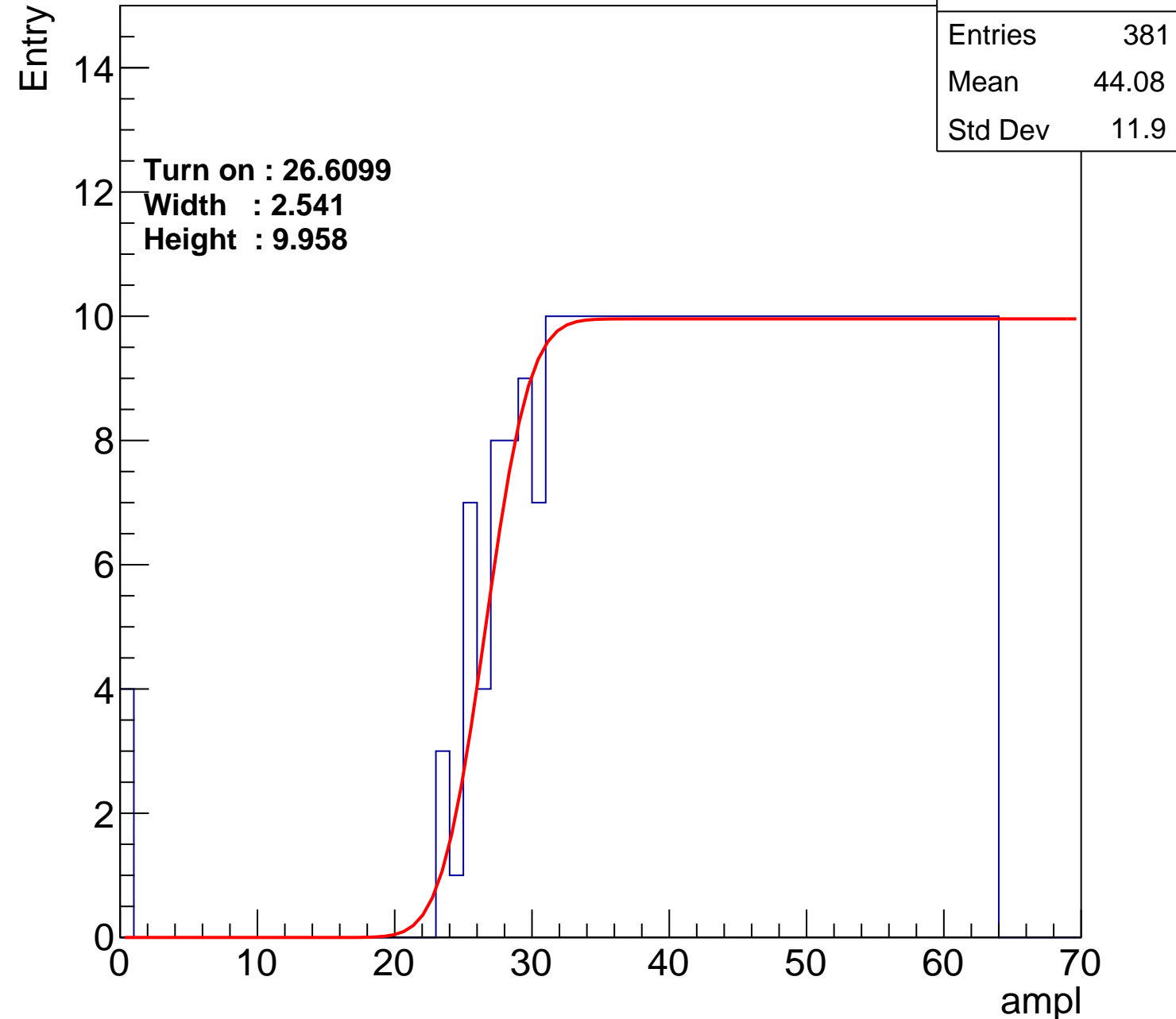
Width : 2.541

Height : 9.958

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.29
Std Dev	11.3

Turn on : 26.1165

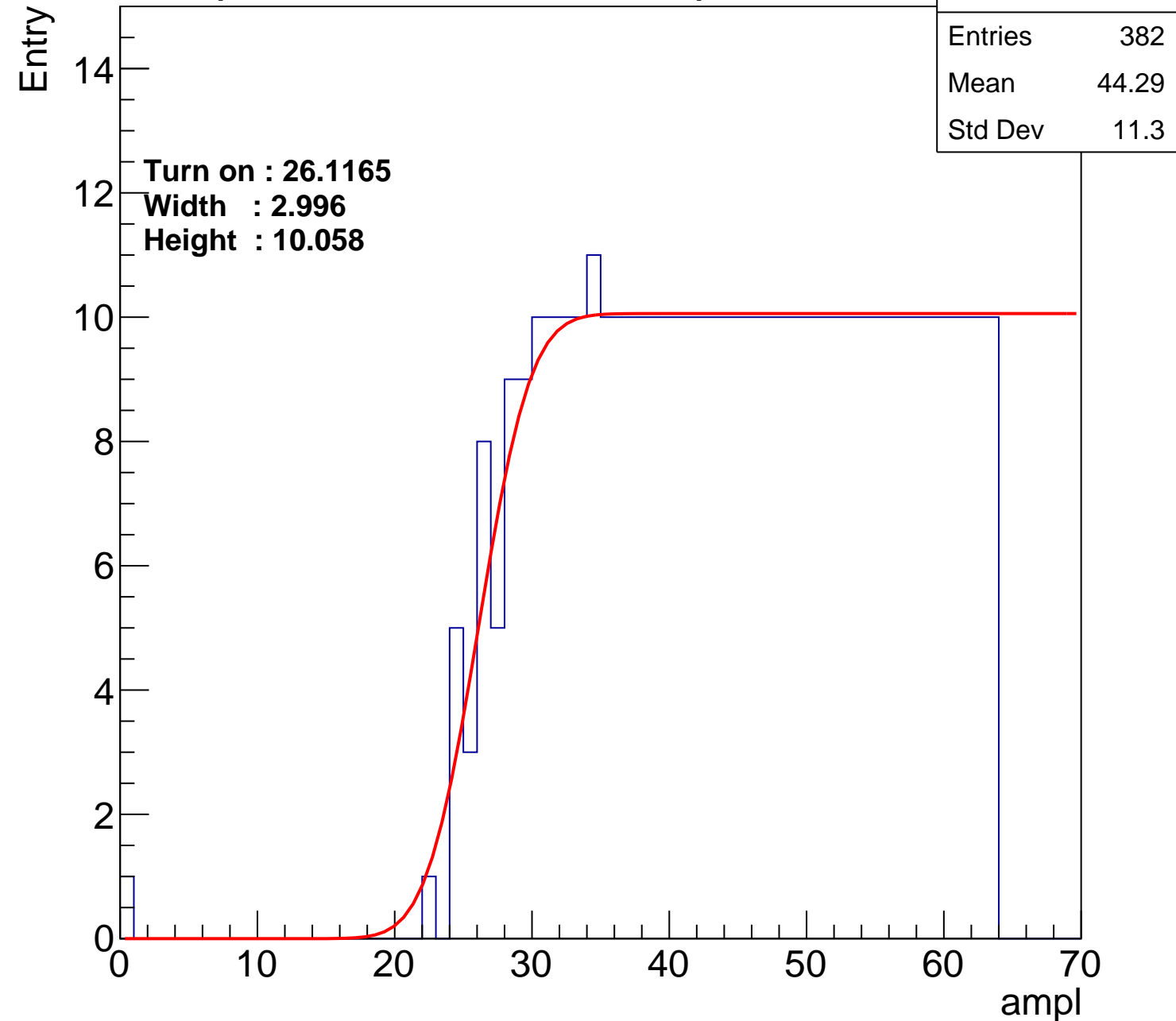
Width : 2.996

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch90

calib_packv5_042523_0143.root, FC#3, port B3

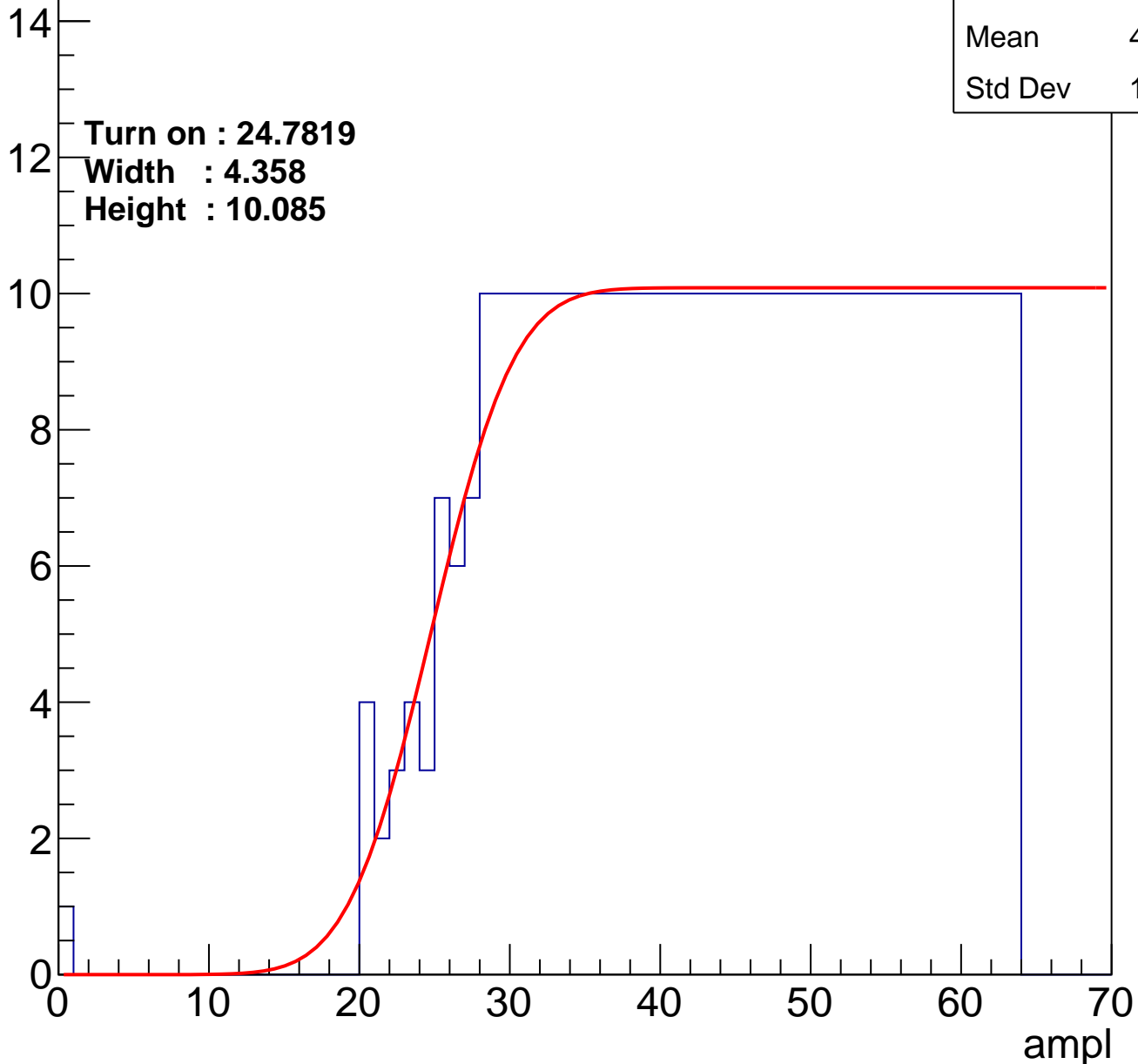
Entries	397
Mean	43.46
Std Dev	11.85

Turn on : 24.7819

Width : 4.358

Height : 10.085

Entry



B1L000S, U18-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
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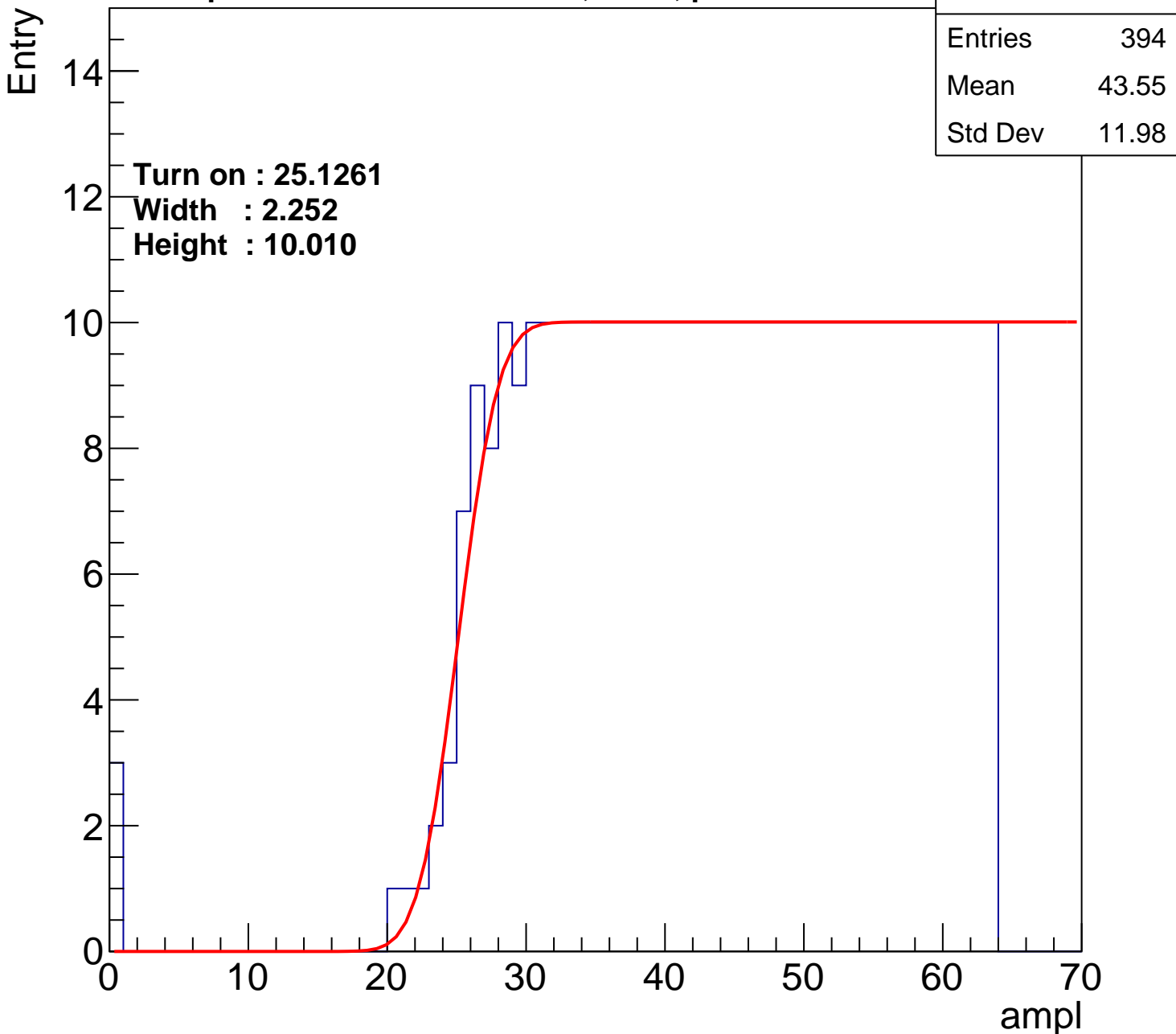
Mean	43.55
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Std Dev	11.98
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Turn on : 25.1261

Width : 2.252

Height : 10.010



B1L000S, U18-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.87
Std Dev	11.83

Turn on : 25.4160

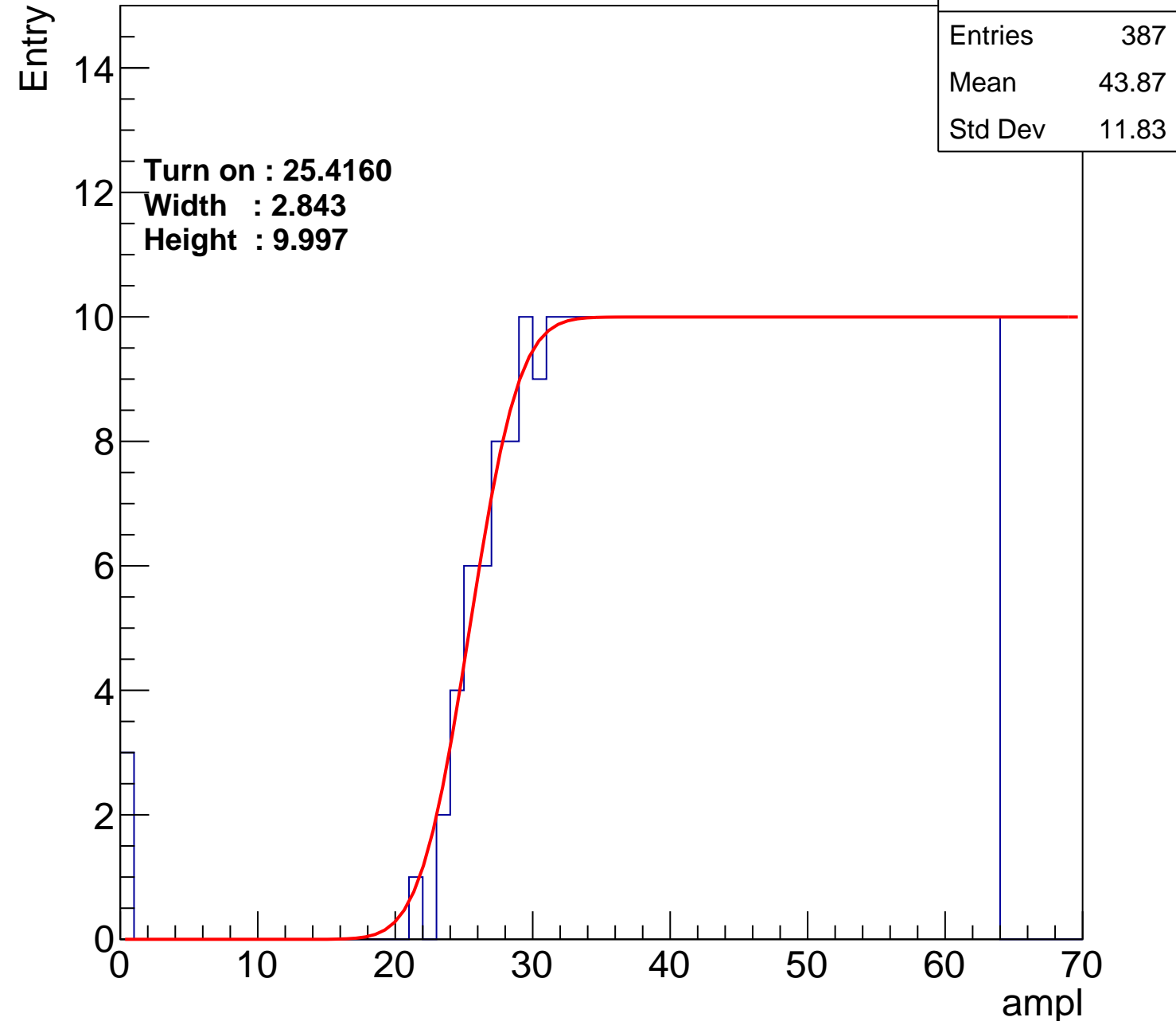
Width : 2.843

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch93

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.79
Std Dev	11.74

Turn on : 25.3980

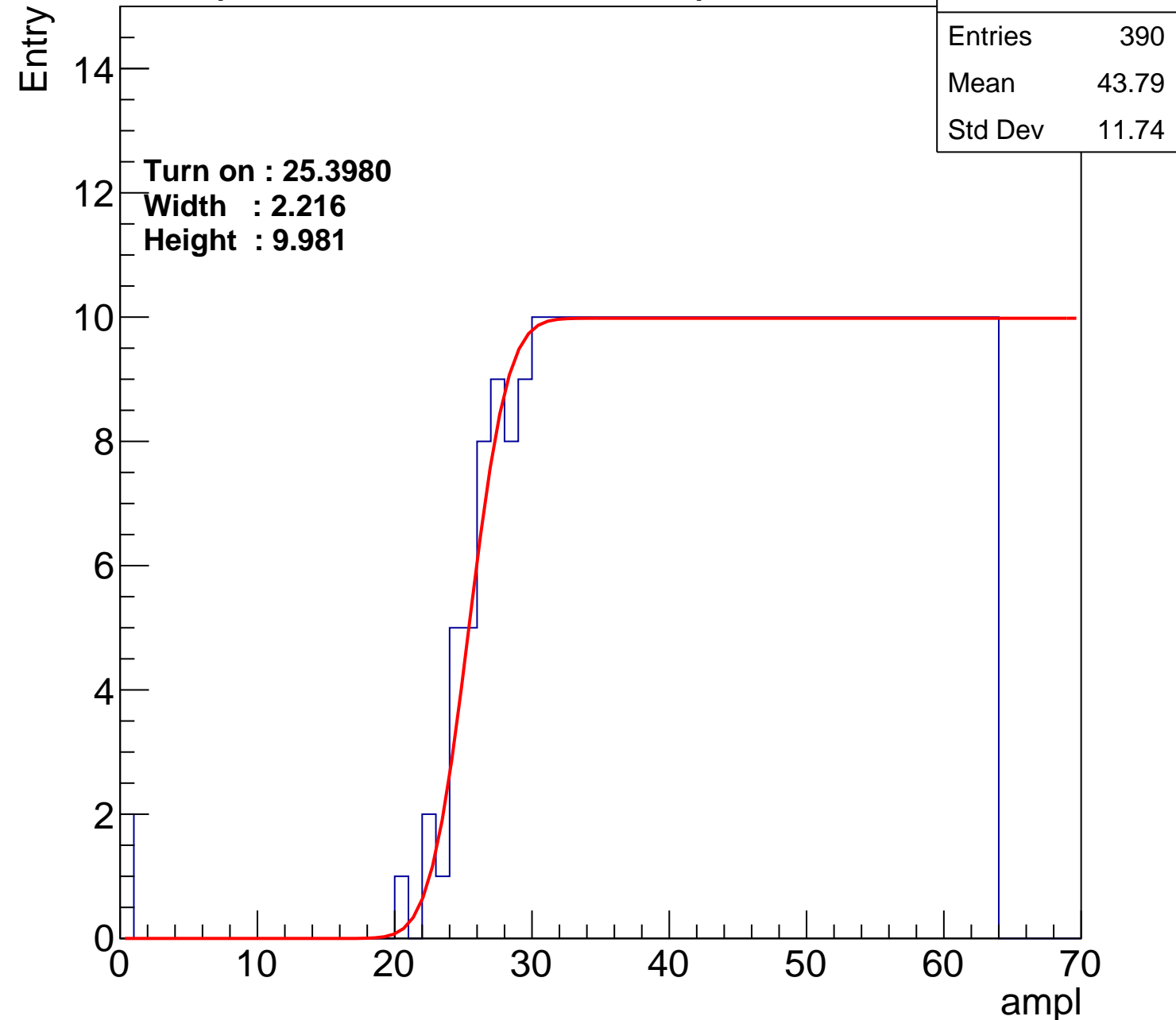
Width : 2.216

Height : 9.981

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch94

calib_packv5_042523_0143.root, FC#3, port B3

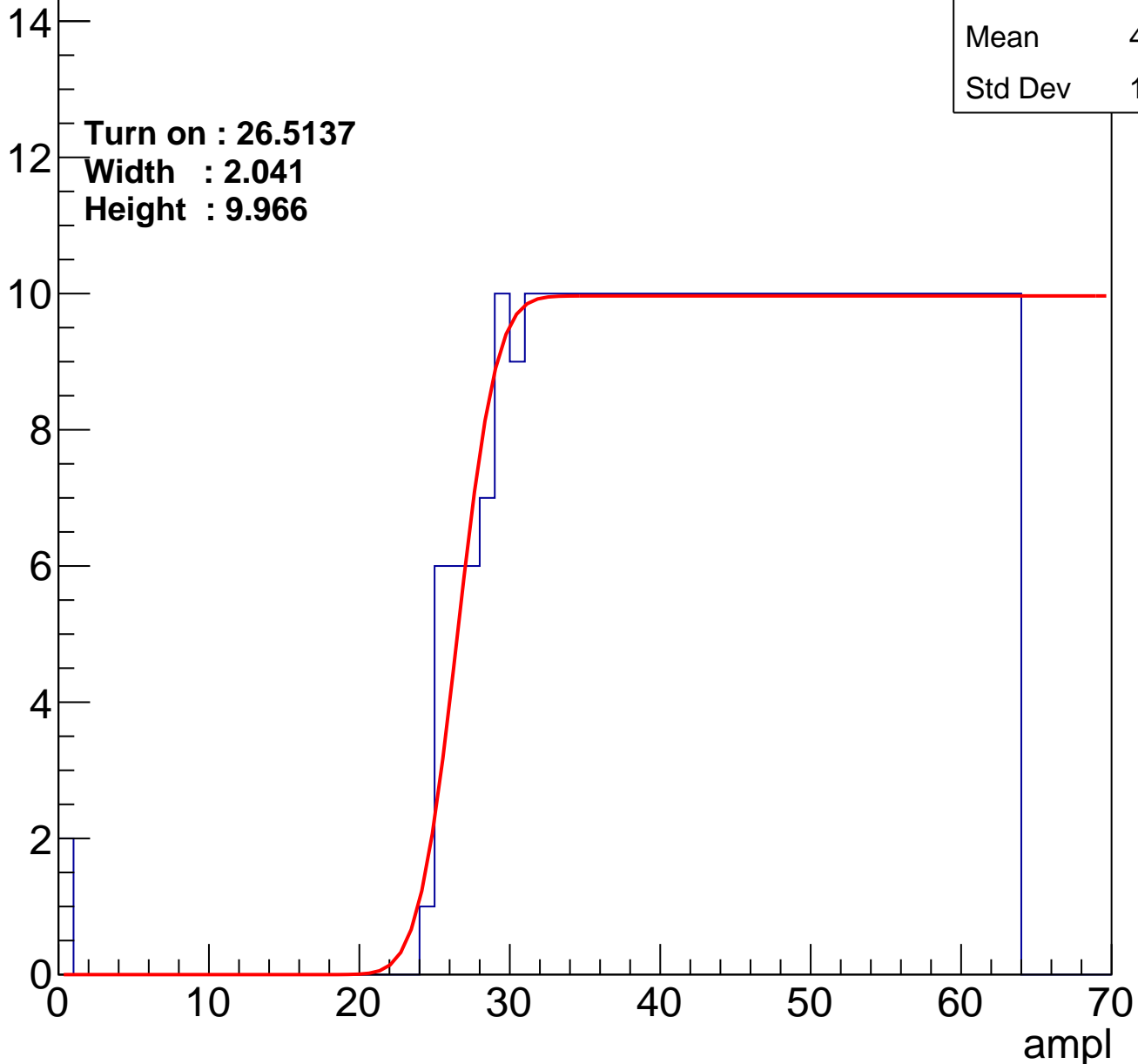
Entries	377
Mean	44.45
Std Dev	11.37

Turn on : 26.5137

Width : 2.041

Height : 9.966

Entry



B1L000S, U18-ch95

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.01
Std Dev	11.73

Turn on : 26.3166

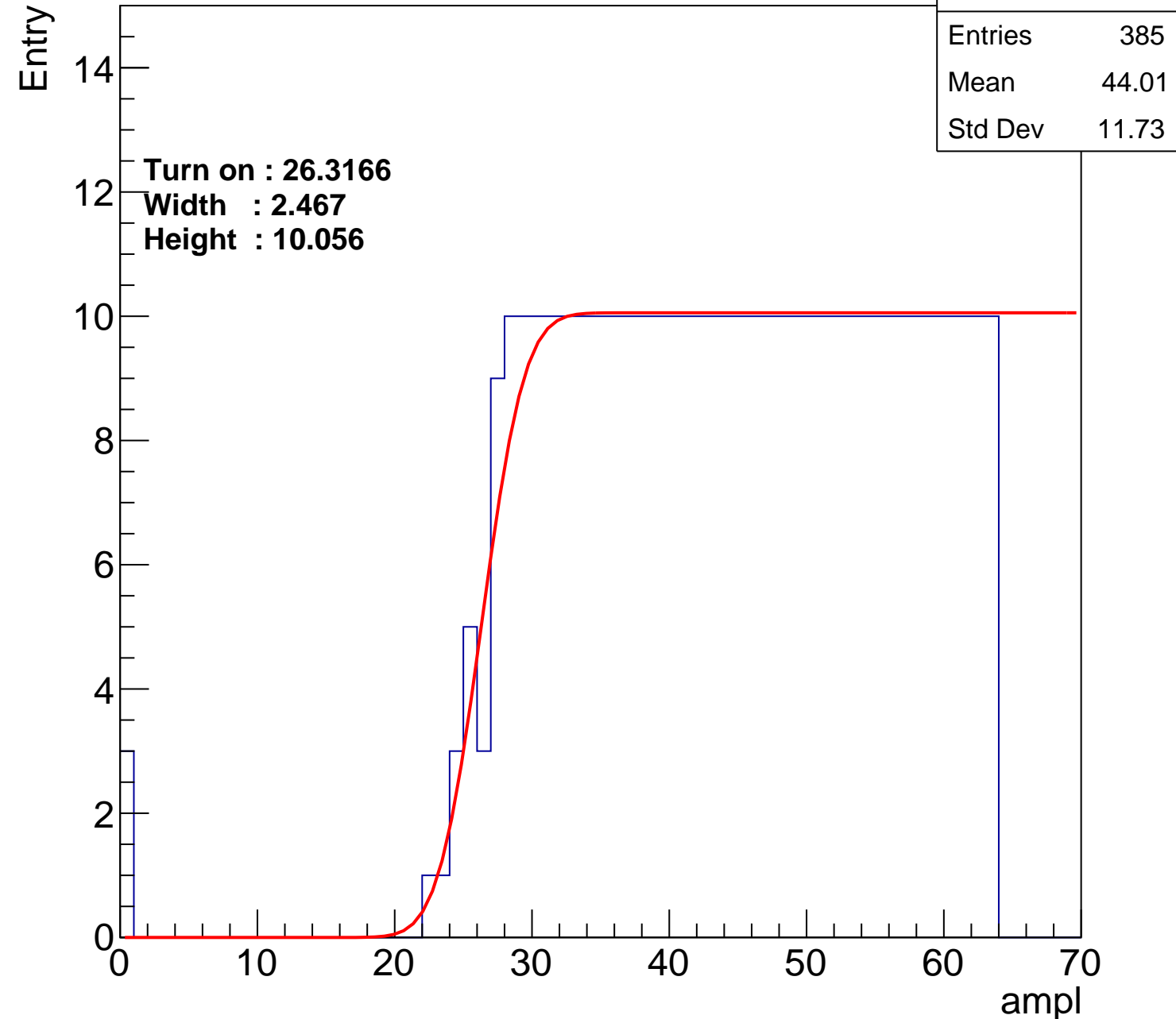
Width : 2.467

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch96

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.25
Std Dev	11.49

Turn on : 26.4403

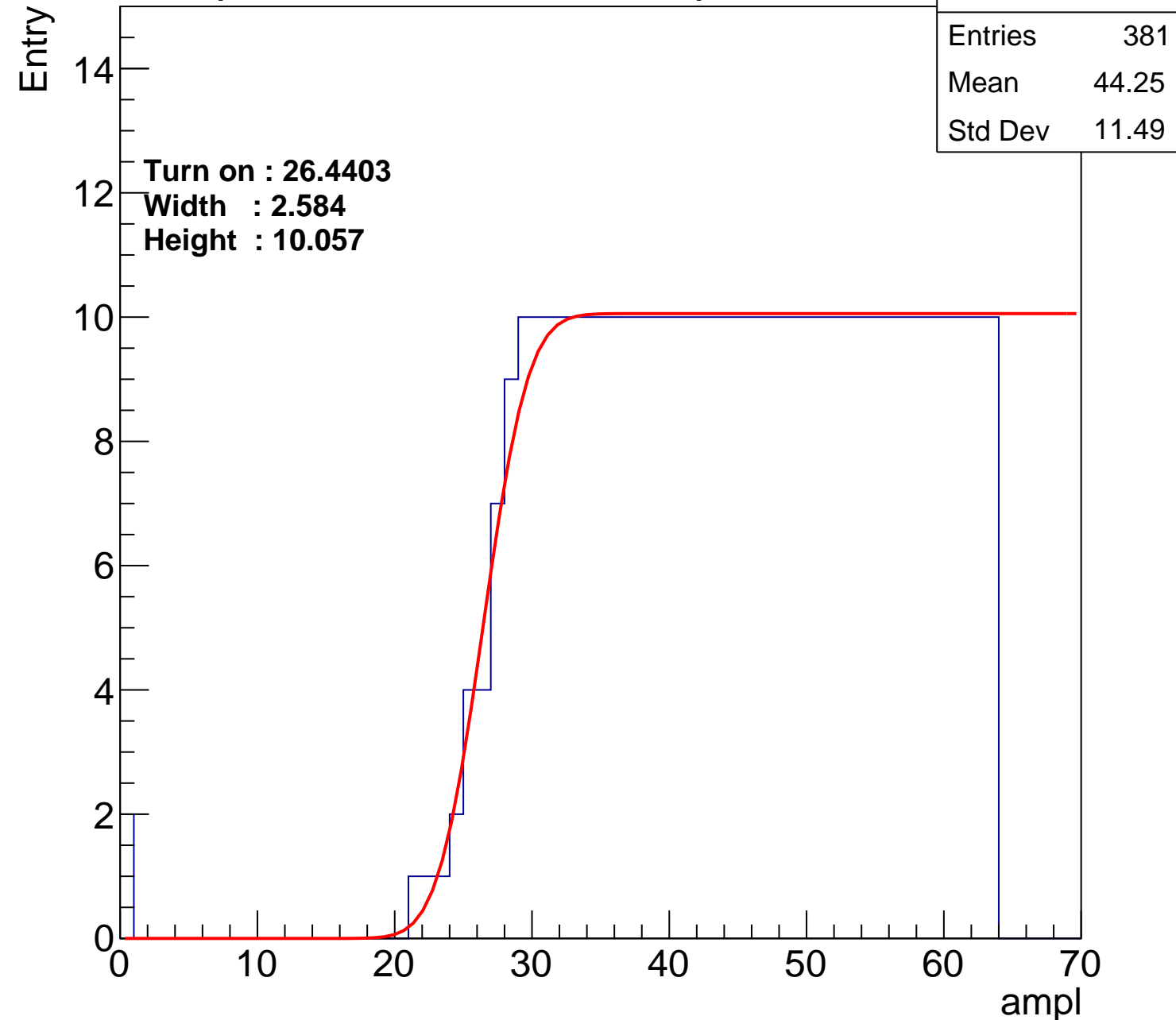
Width : 2.584

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch97

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.77
Std Dev	11.78

Turn on : 25.5288

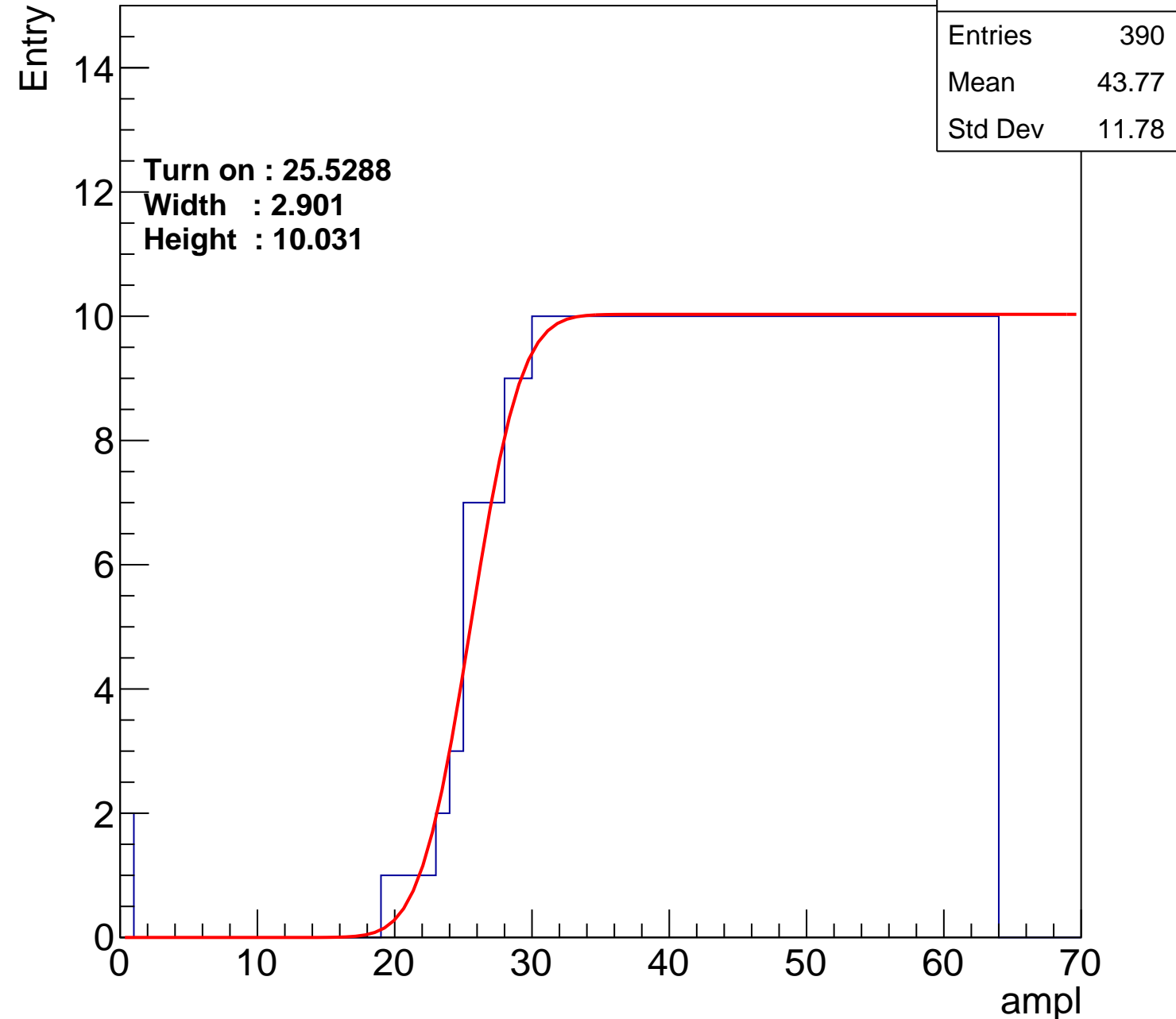
Width : 2.901

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch98

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.2
Std Dev	12.33

Turn on : 24.7910

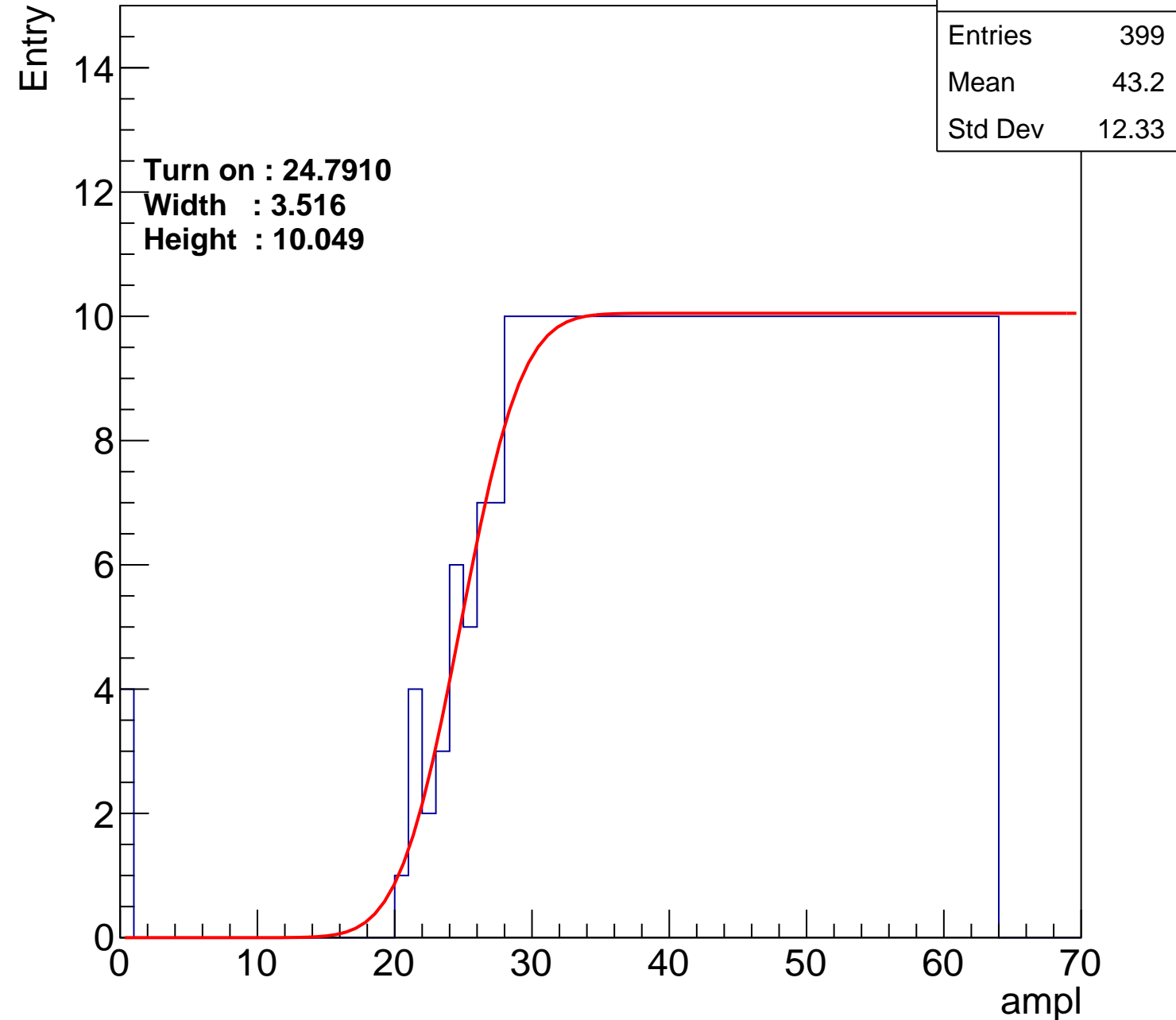
Width : 3.516

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch99

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 26.0026

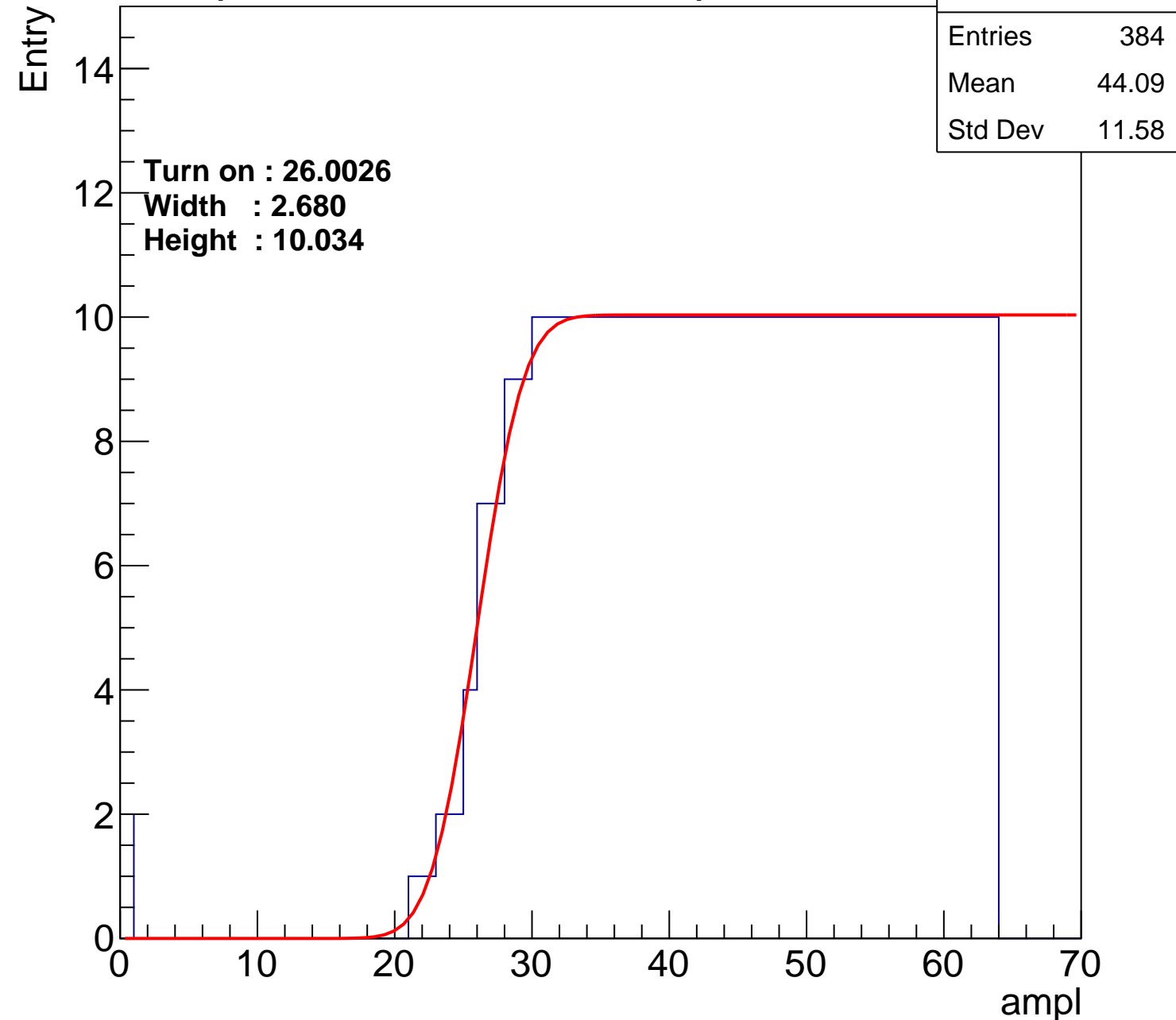
Width : 2.680

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch100

calib_packv5_042523_0143.root, FC#3, port B3

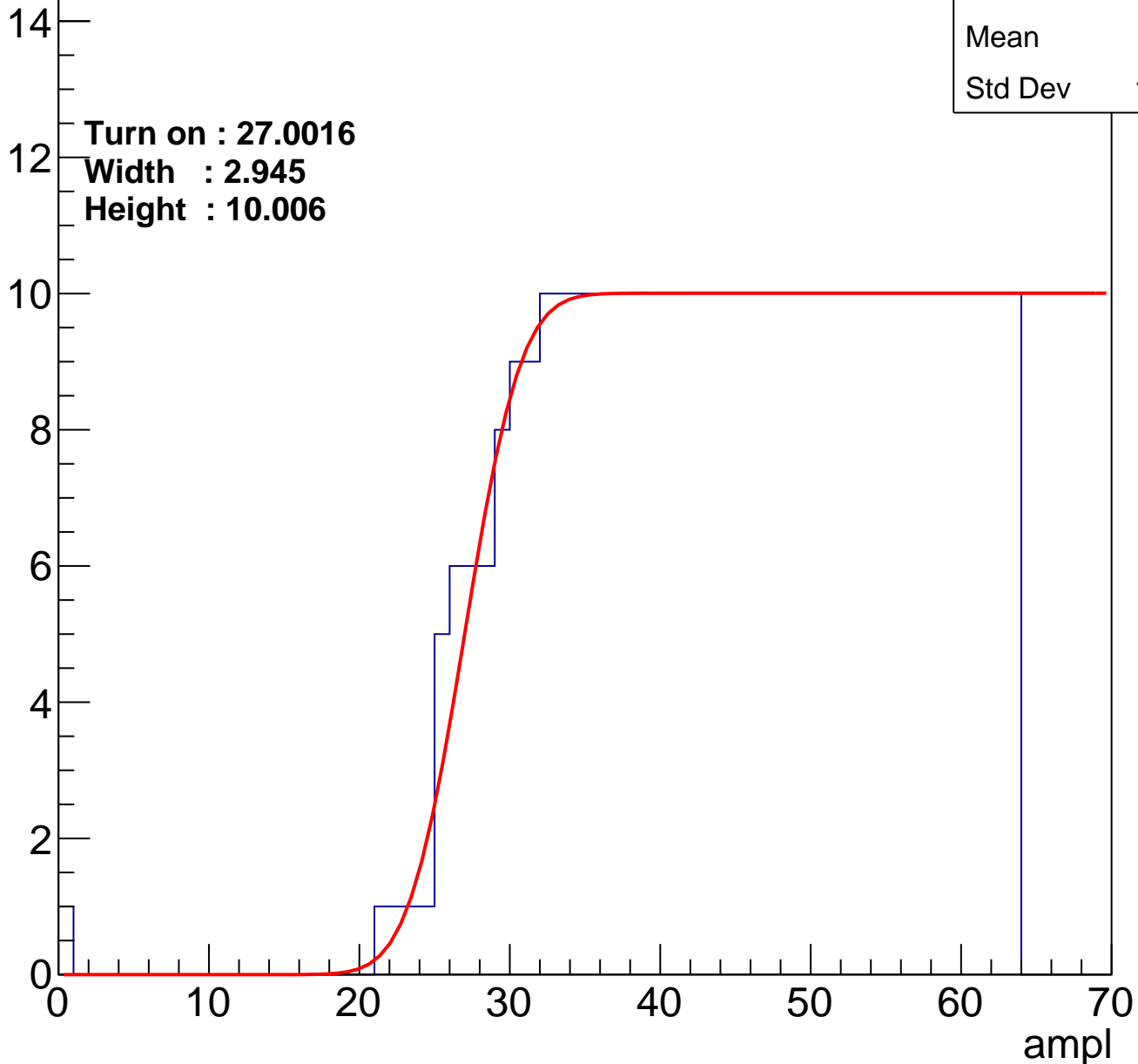
Entries	374
Mean	44.6
Std Dev	11.21

Turn on : 27.0016

Width : 2.945

Height : 10.006

Entry



B1L000S, U18-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.01
Std Dev	11.65

Turn on : 25.9288

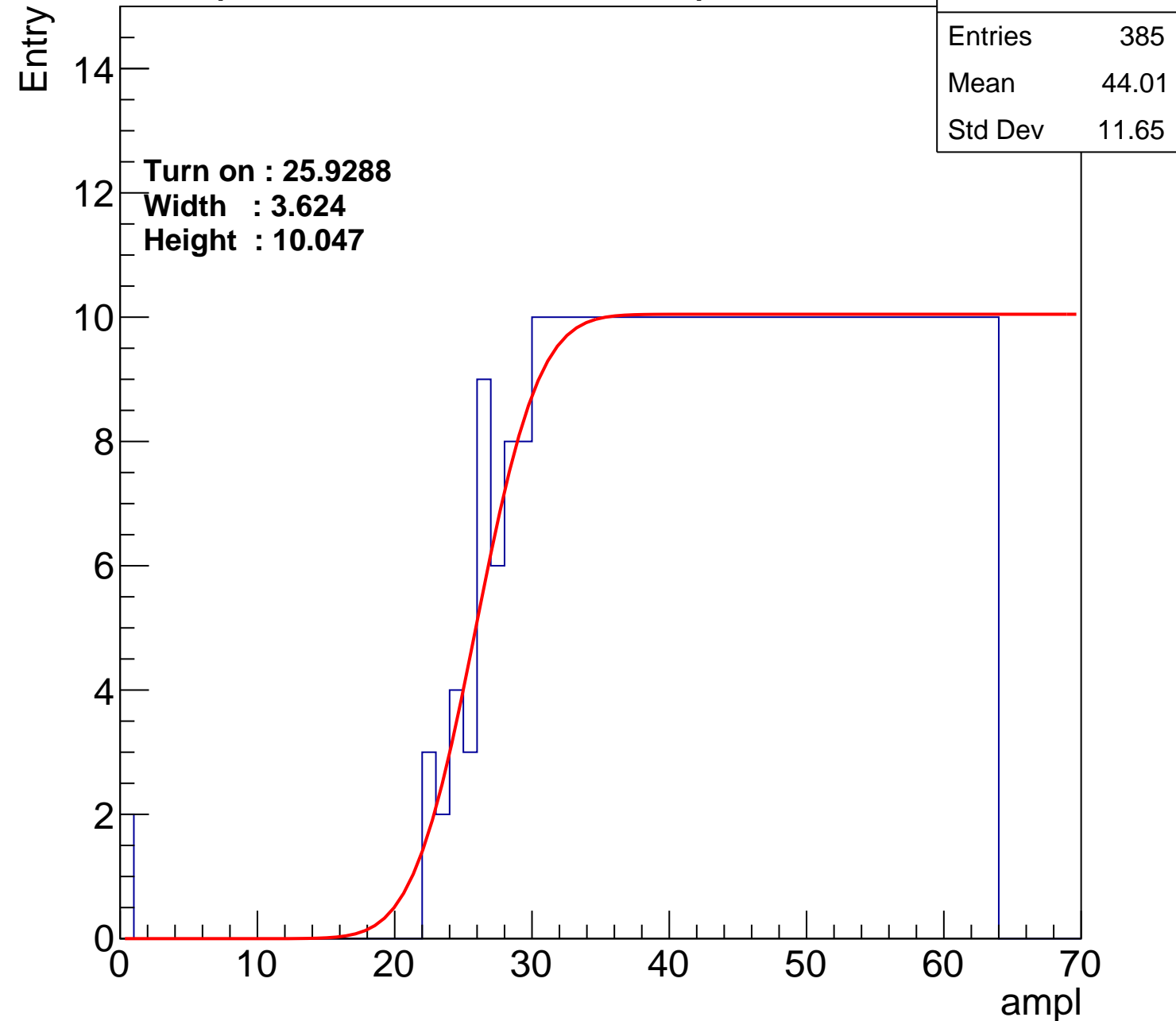
Width : 3.624

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch102

calib_packv5_042523_0143.root, FC#3, port B3

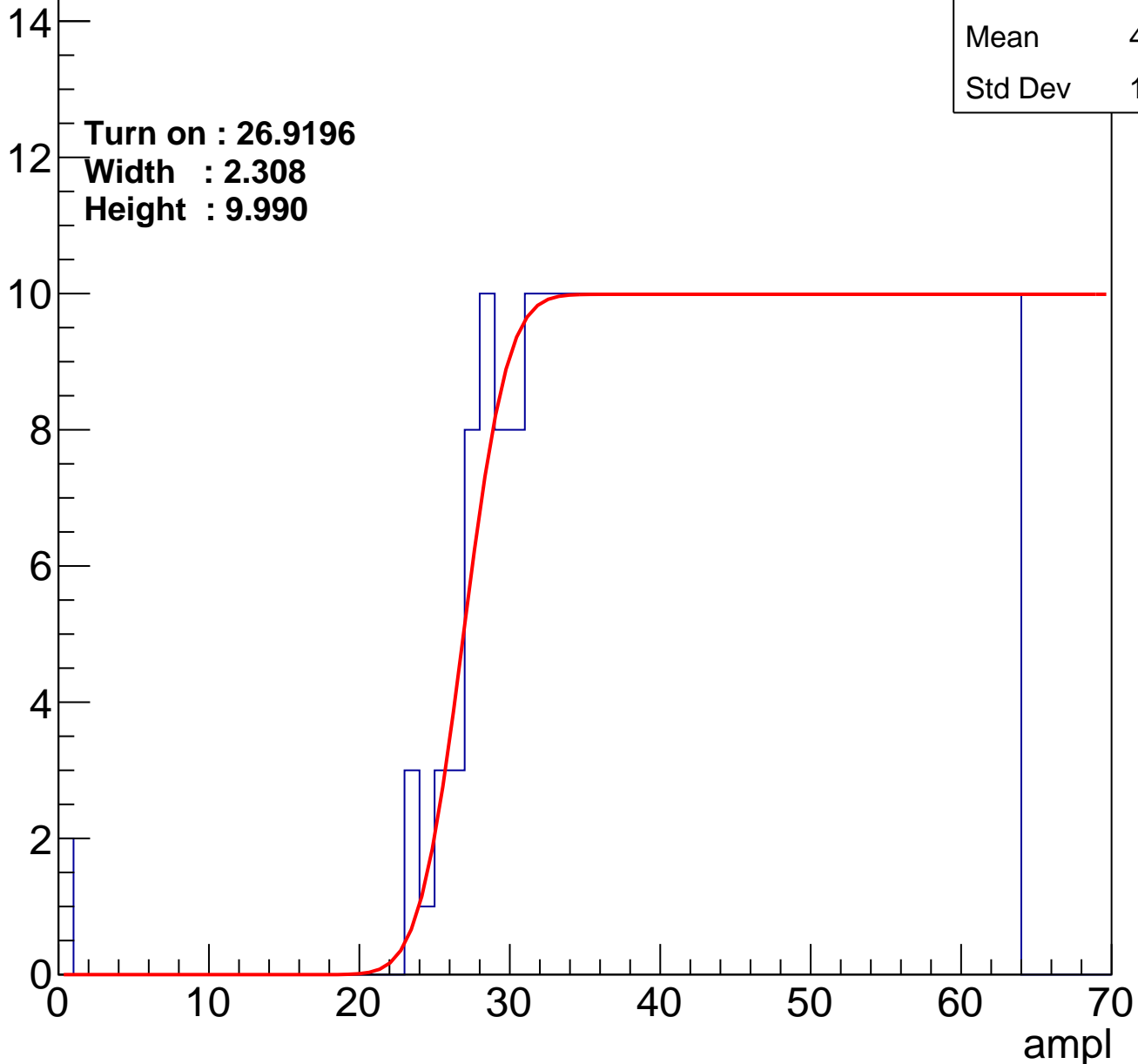
Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.9196

Width : 2.308

Height : 9.990

Entry



B1L000S, U18-ch103

calib_packv5_042523_0143.root, FC#3, port B3

Entries	374
Mean	44.43
Std Dev	11.73

Turn on : 27.4410

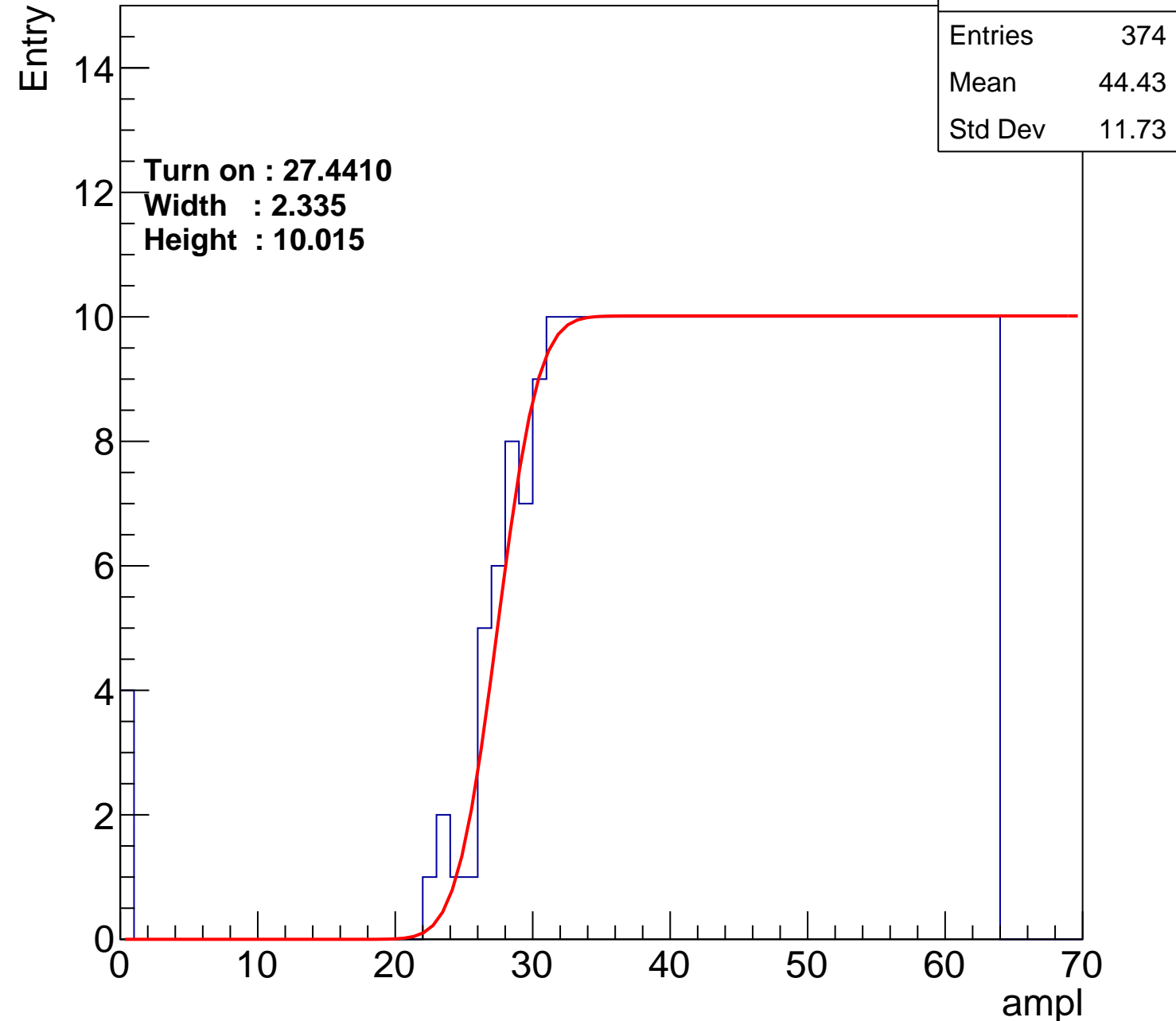
Width : 2.335

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.32
Std Dev	11.47

Turn on : 26.3570

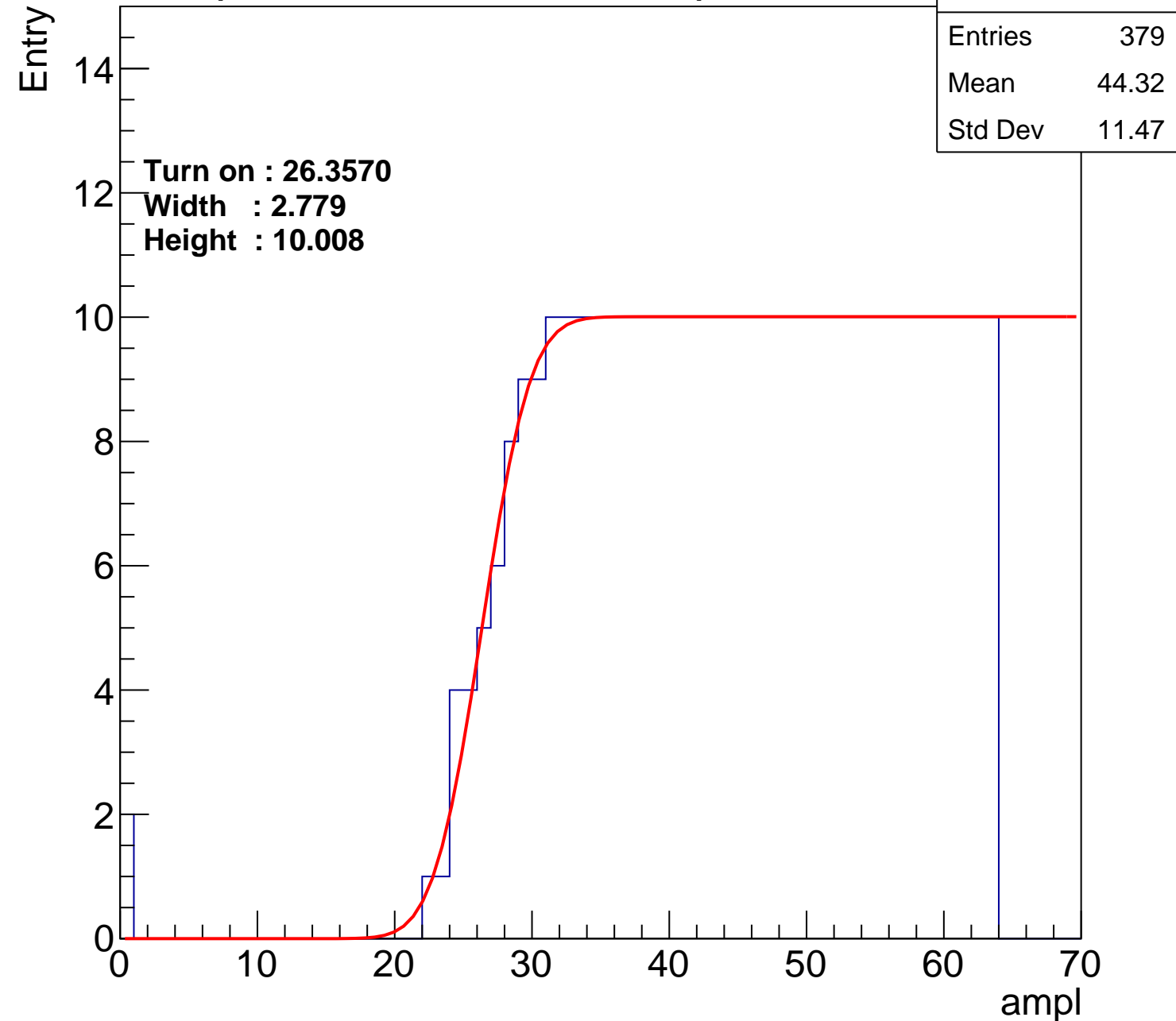
Width : 2.779

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.42
Std Dev	11.23

Turn on : 26.1853

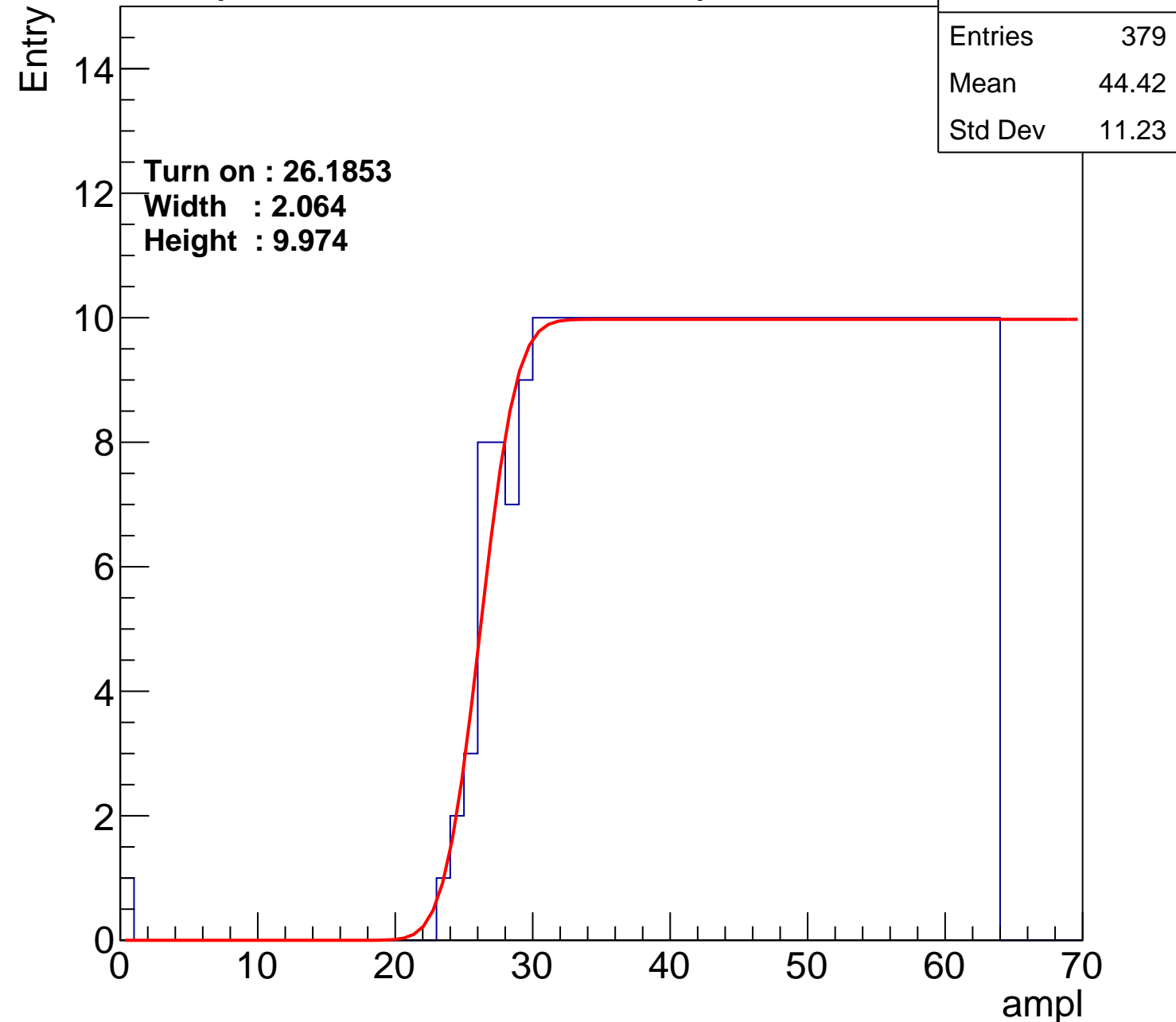
Width : 2.064

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.3
Std Dev	12.11

Turn on : 24.4000

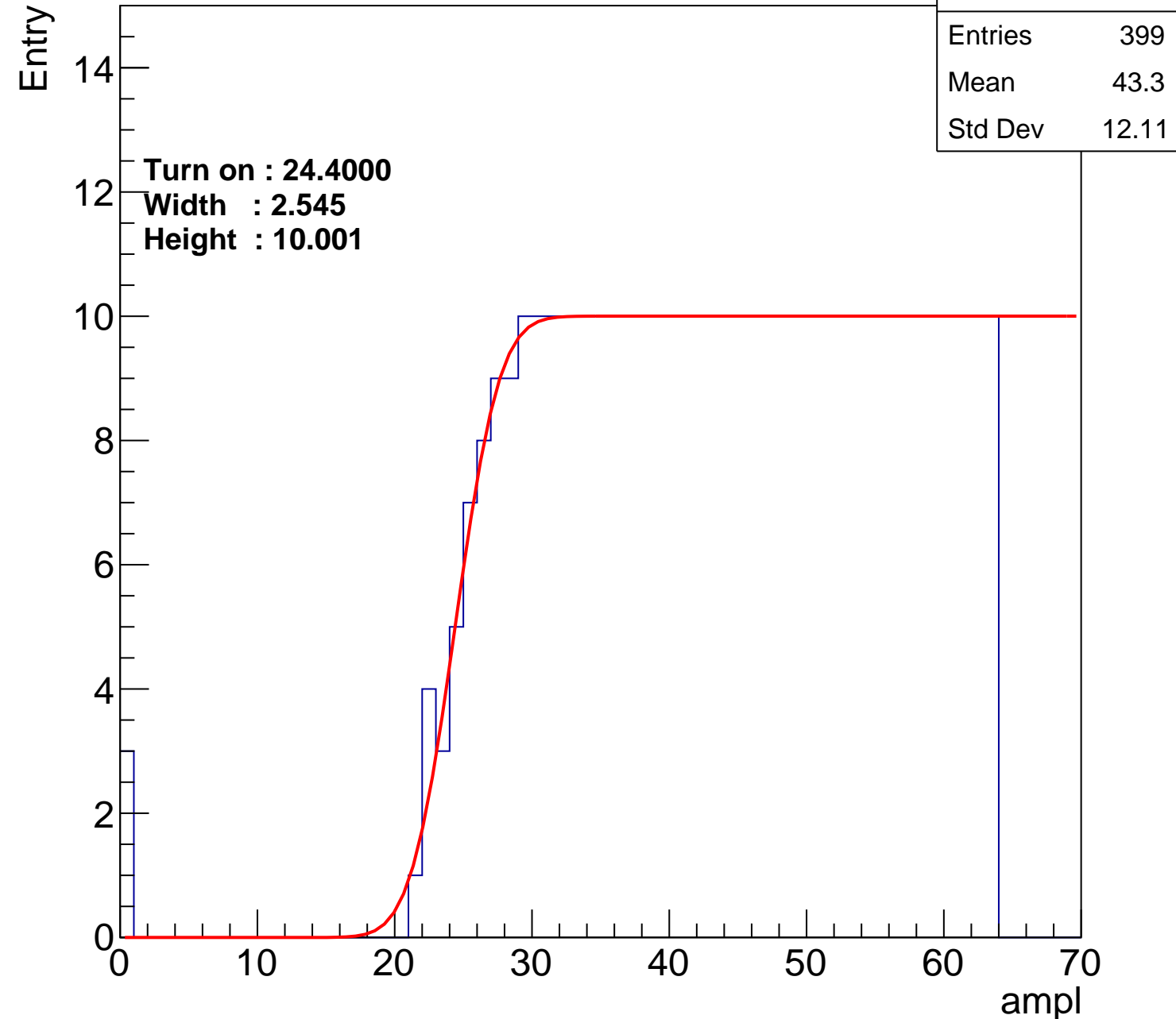
Width : 2.545

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.36
Std Dev	12.4

Turn on : 25.1980

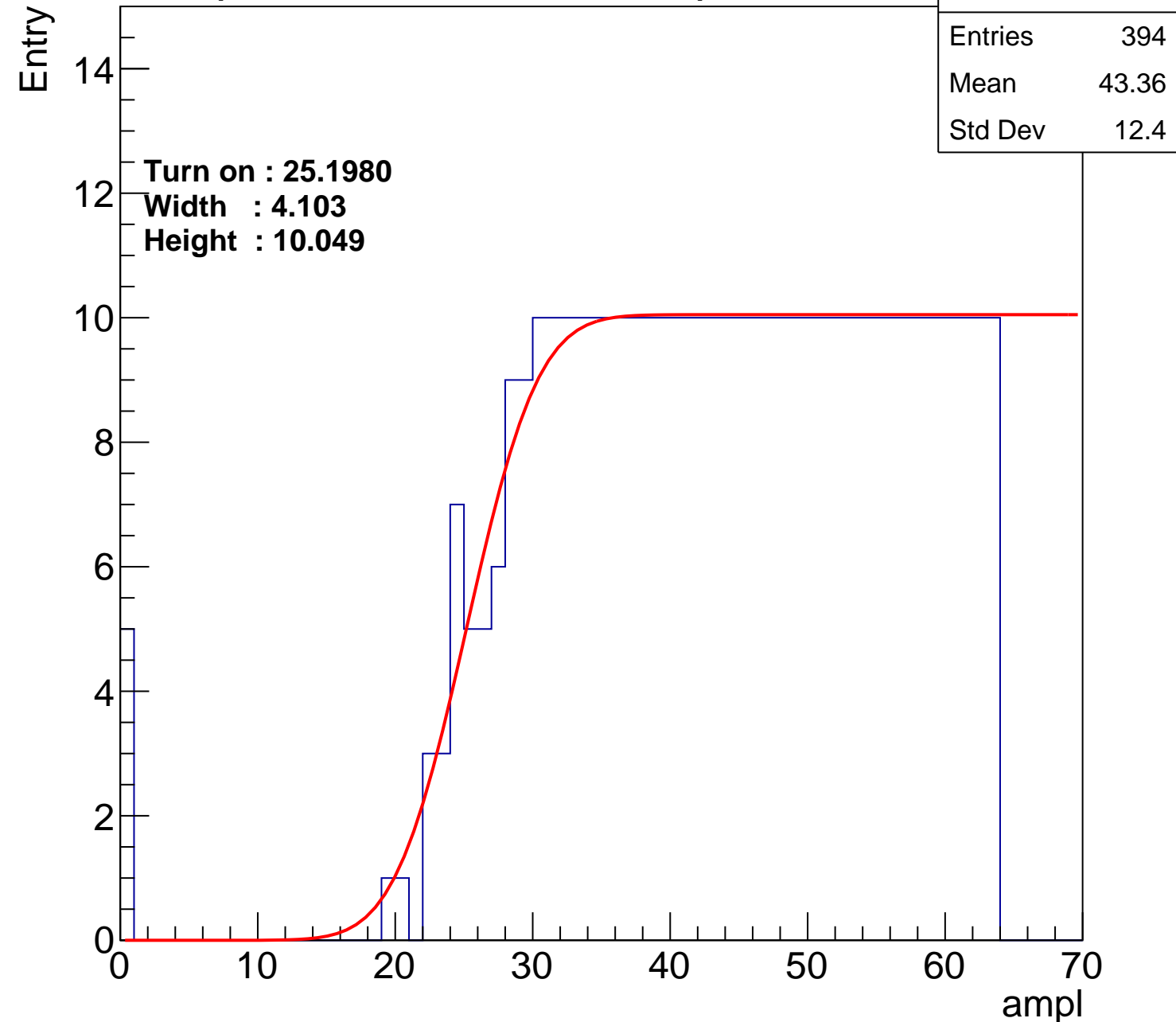
Width : 4.103

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch108

calib_packv5_042523_0143.root, FC#3, port B3

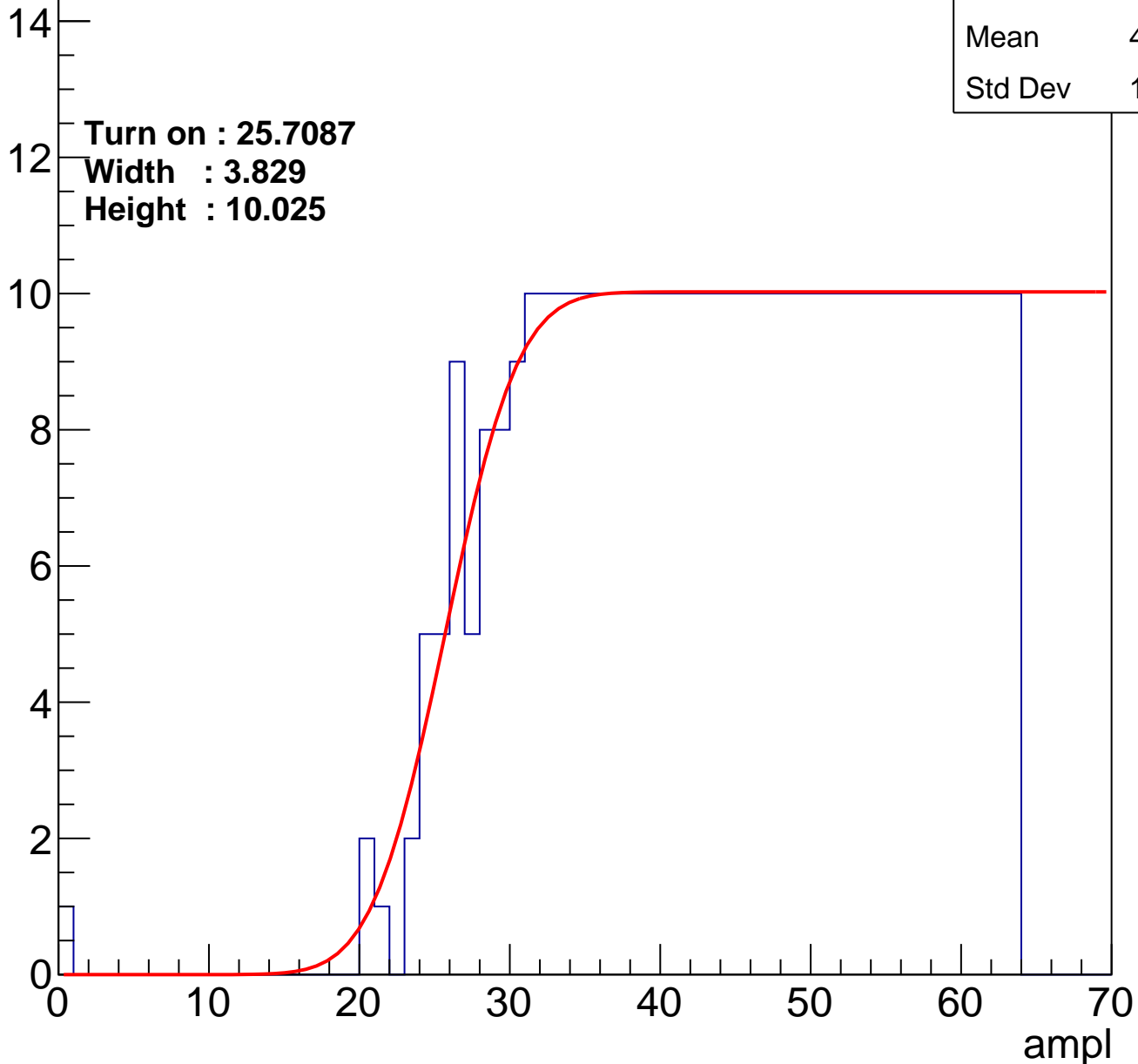
Entries	385
Mean	44.04
Std Dev	11.53

Turn on : 25.7087

Width : 3.829

Height : 10.025

Entry



B1L000S, U18-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	361
Mean	45.21
Std Dev	11.02

Turn on : 27.9128

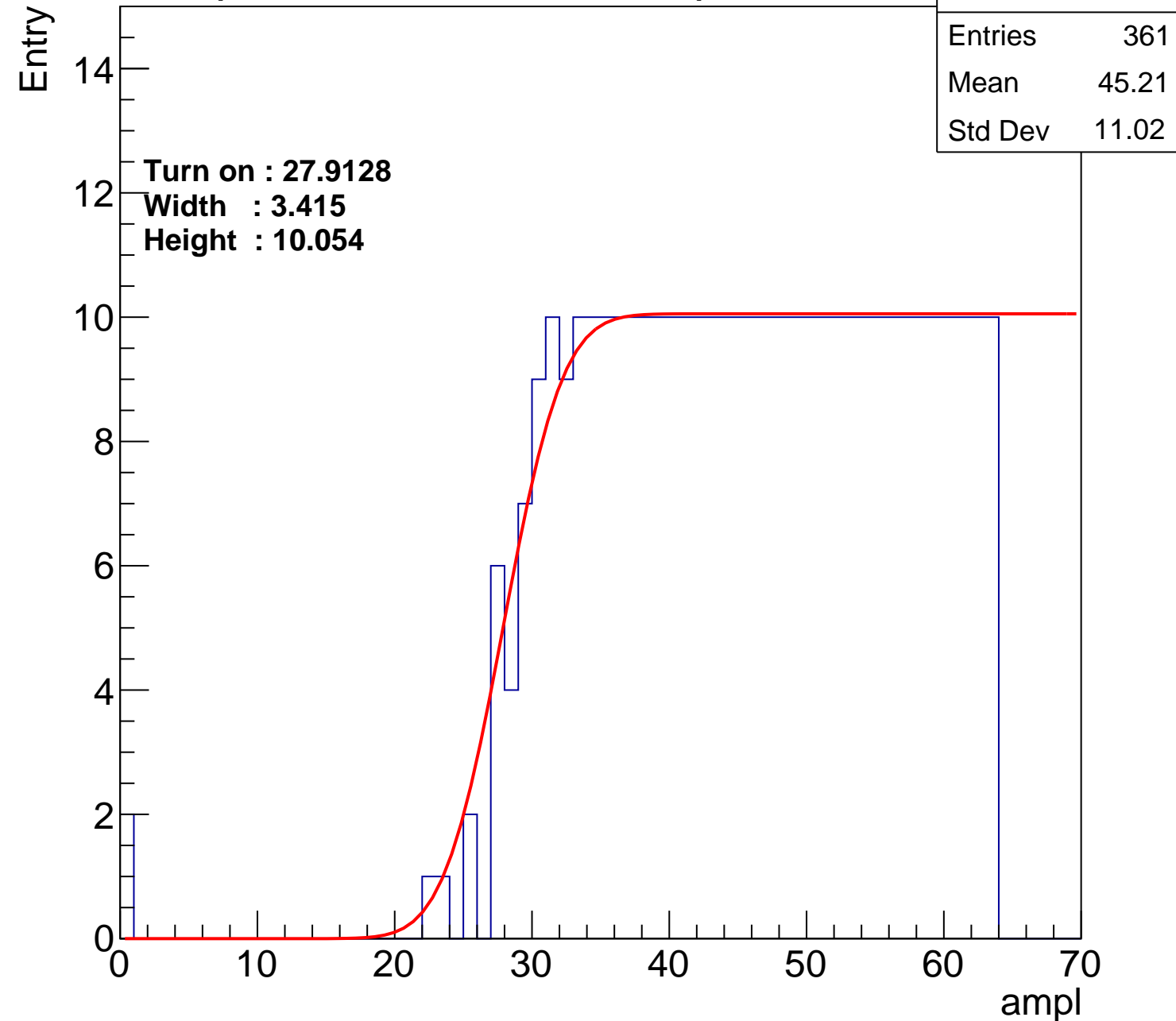
Width : 3.415

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch110

calib_packv5_042523_0143.root, FC#3, port B3

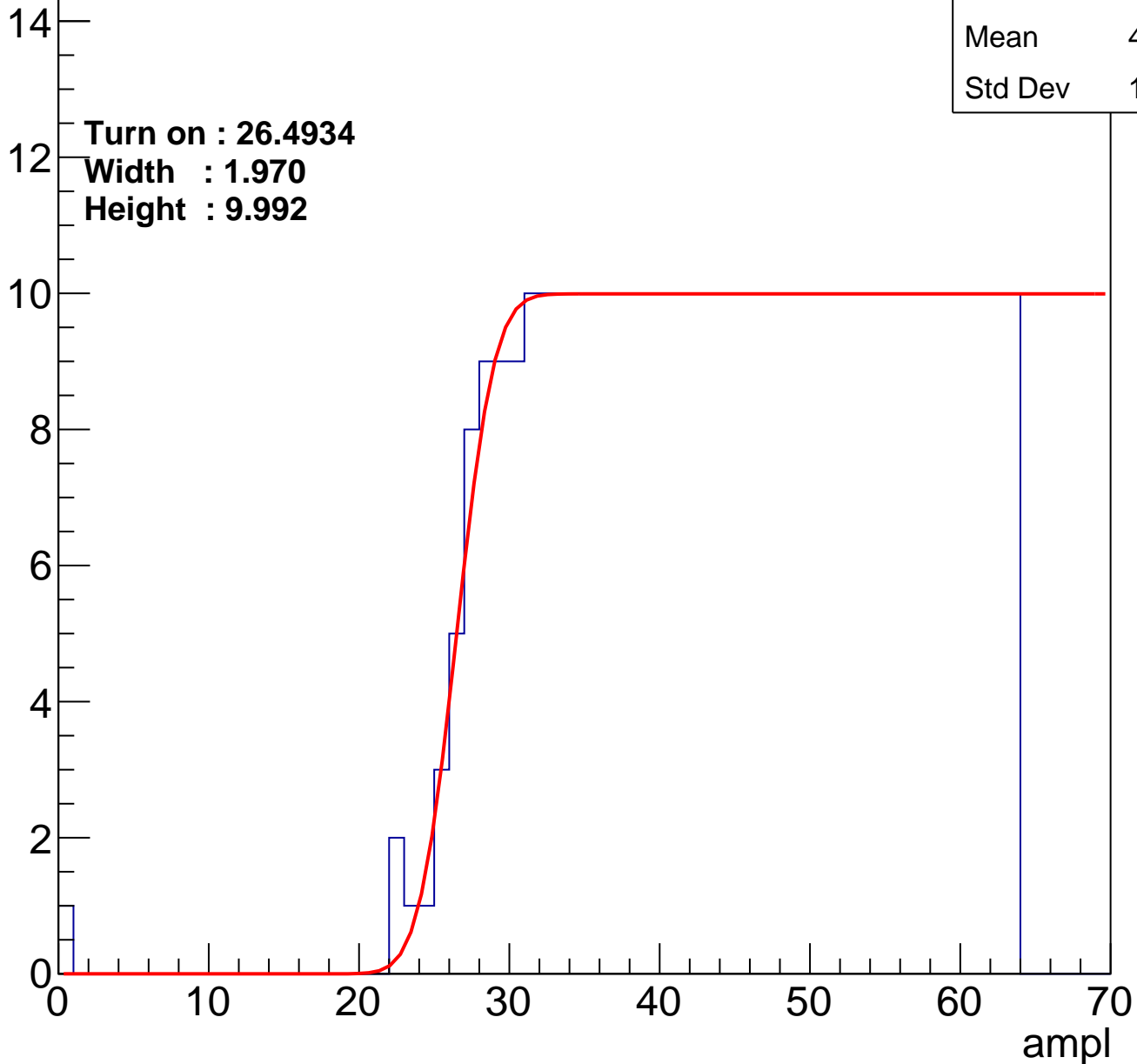
Entries	378
Mean	44.46
Std Dev	11.23

Turn on : 26.4934

Width : 1.970

Height : 9.992

Entry



B1L000S, U18-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.83

Turn on : 24.3087

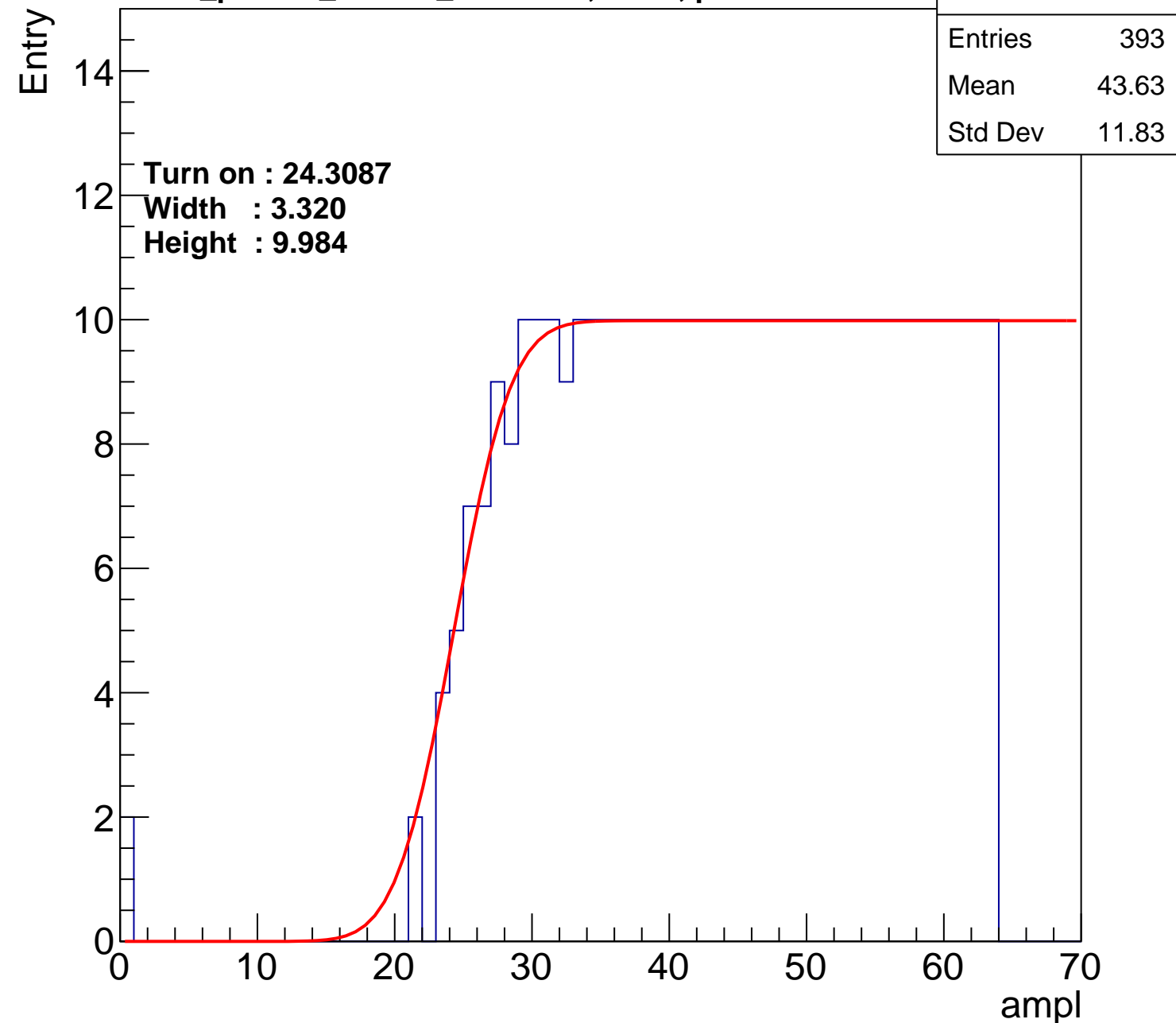
Width : 3.320

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch112

calib_packv5_042523_0143.root, FC#3, port B3

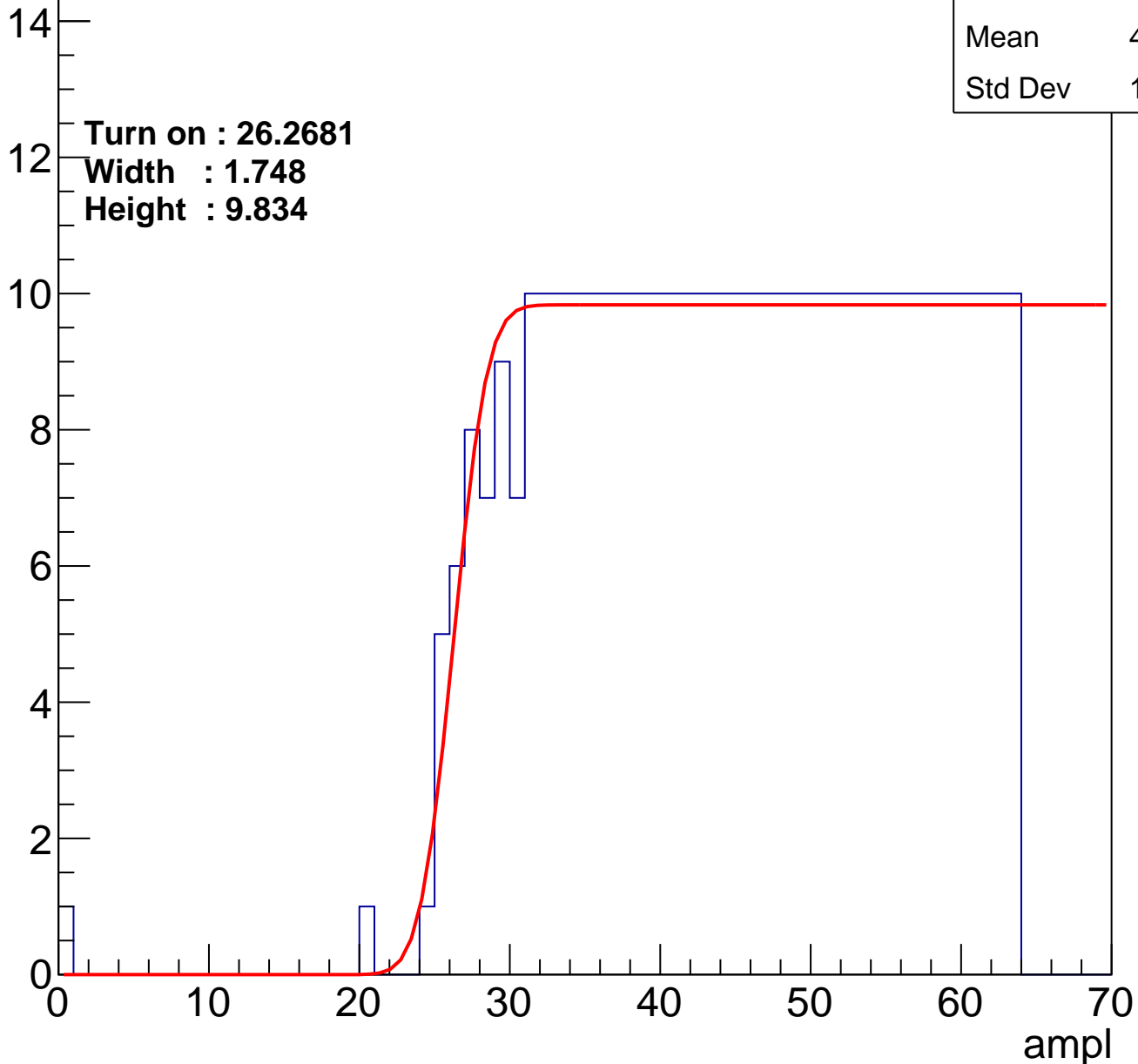
Entries	375
Mean	44.58
Std Dev	11.19

Turn on : 26.2681

Width : 1.748

Height : 9.834

Entry



B1L000S, U18-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.39
Std Dev	11.58

Turn on : 26.8888

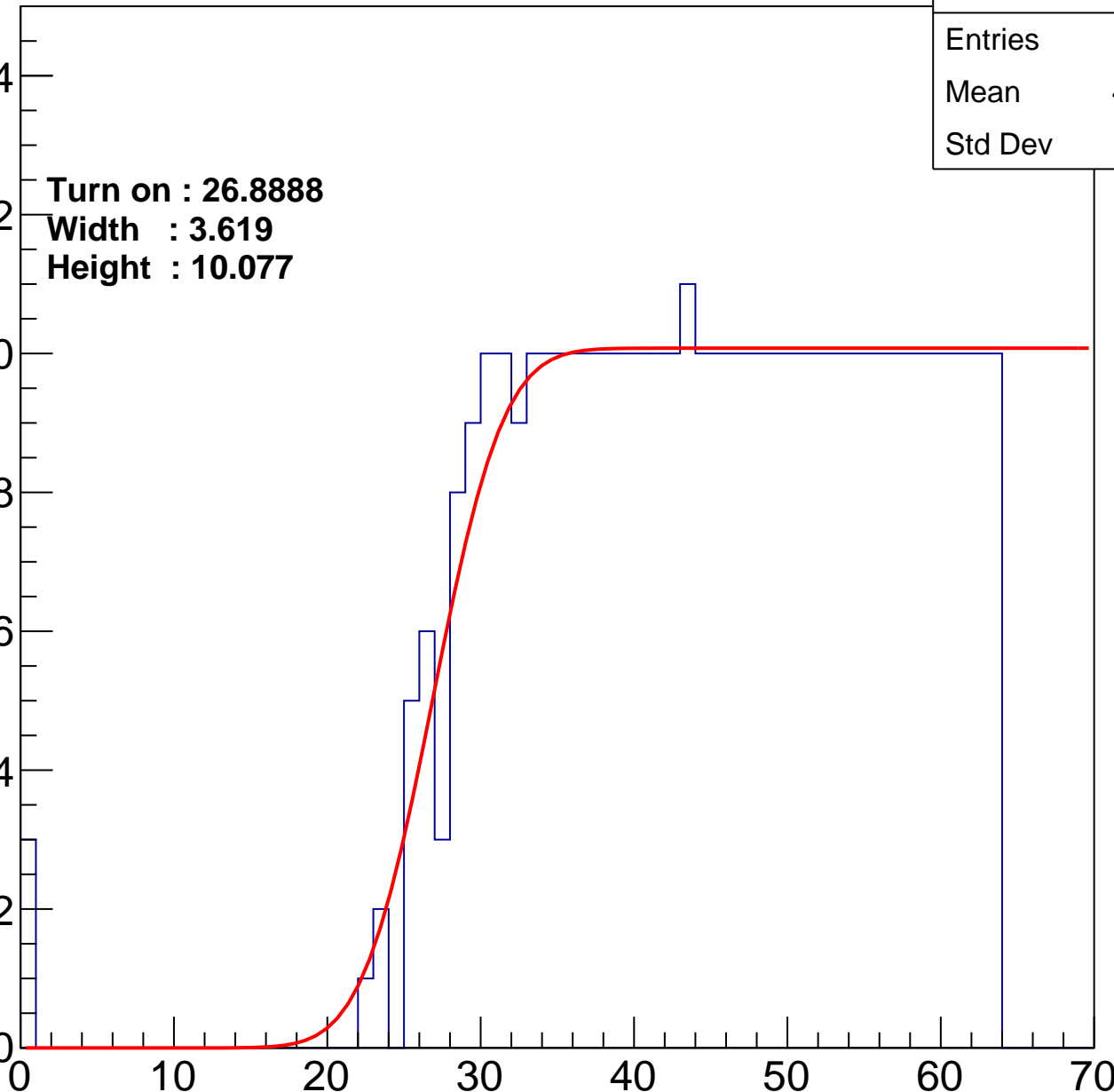
Width : 3.619

Height : 10.077

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.51
Std Dev	11.34

Turn on : 26.7911

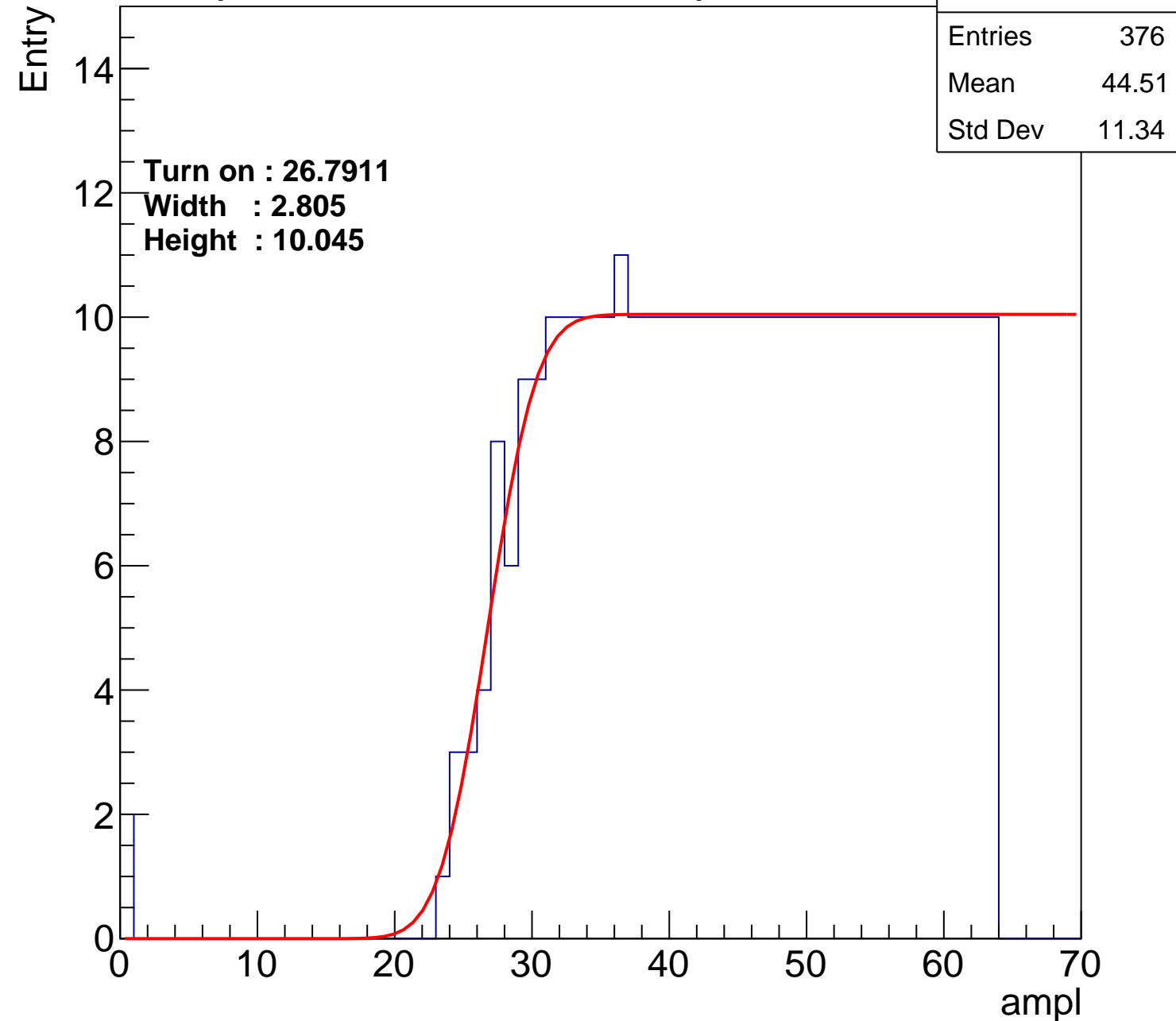
Width : 2.805

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch115

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.17
Std Dev	12.05

Turn on : 24.2952

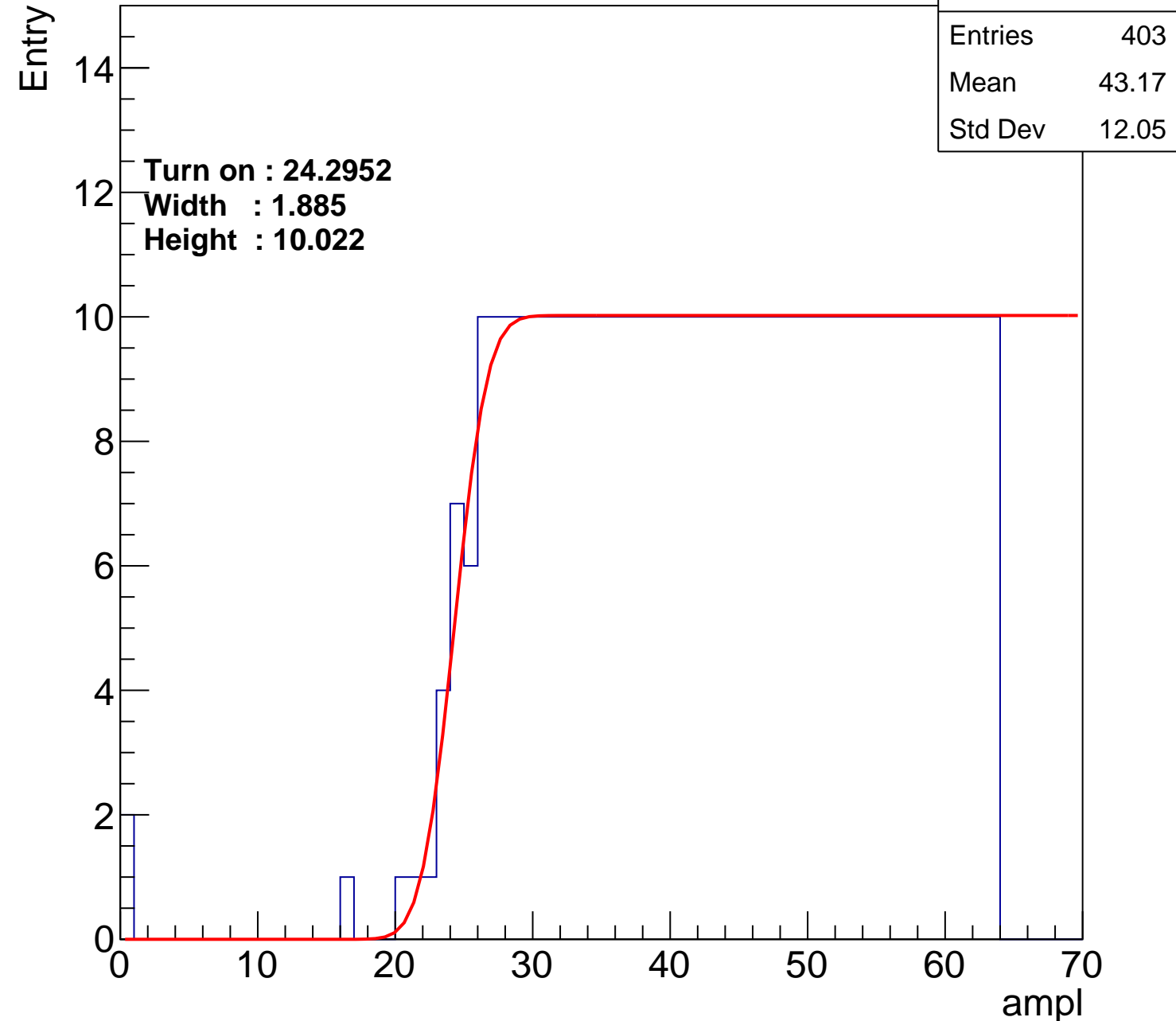
Width : 1.885

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.49
Std Dev	11.39

Turn on : 27.1895

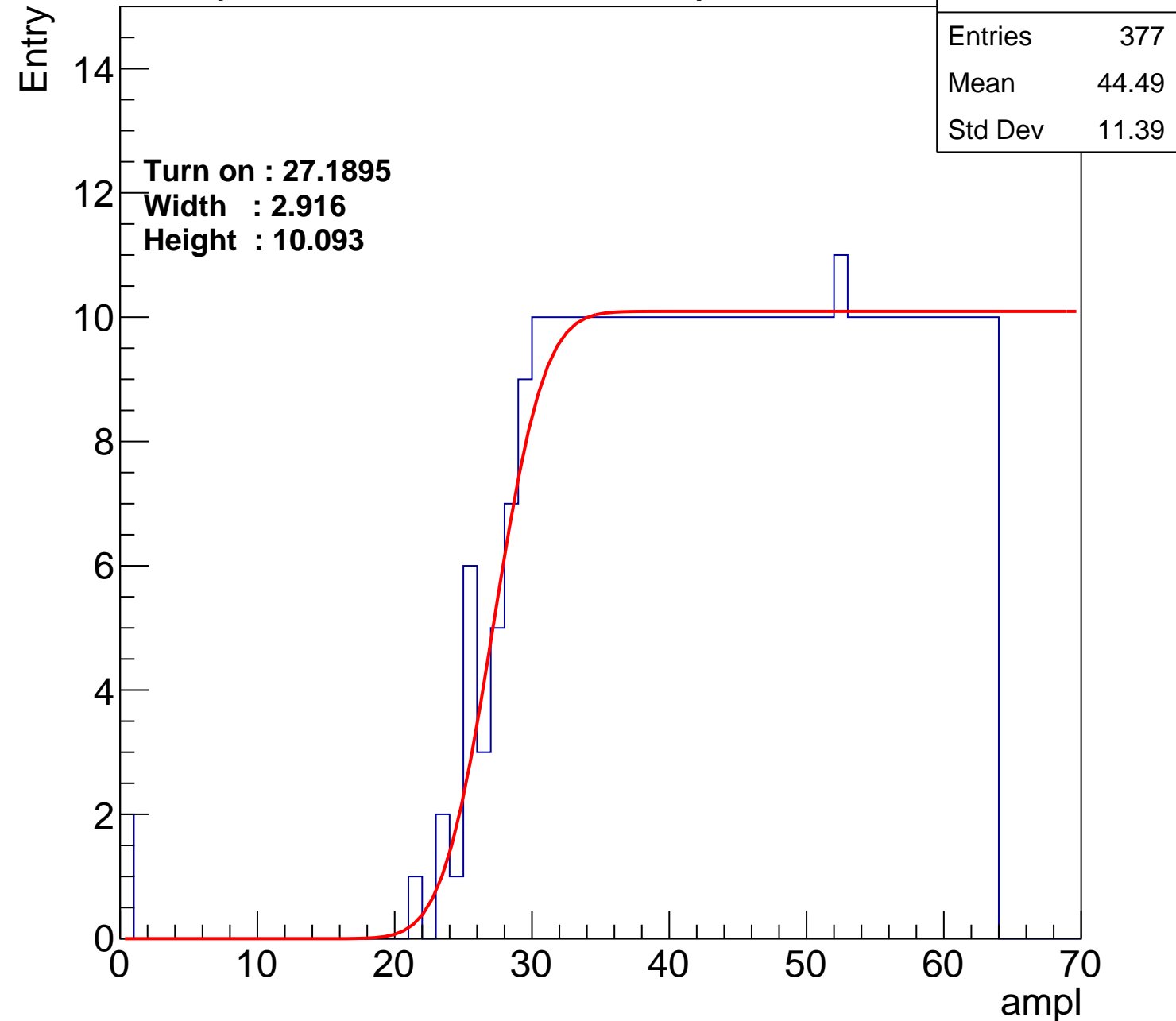
Width : 2.916

Height : 10.093

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.75
Std Dev	11.93

Turn on : 25.5600

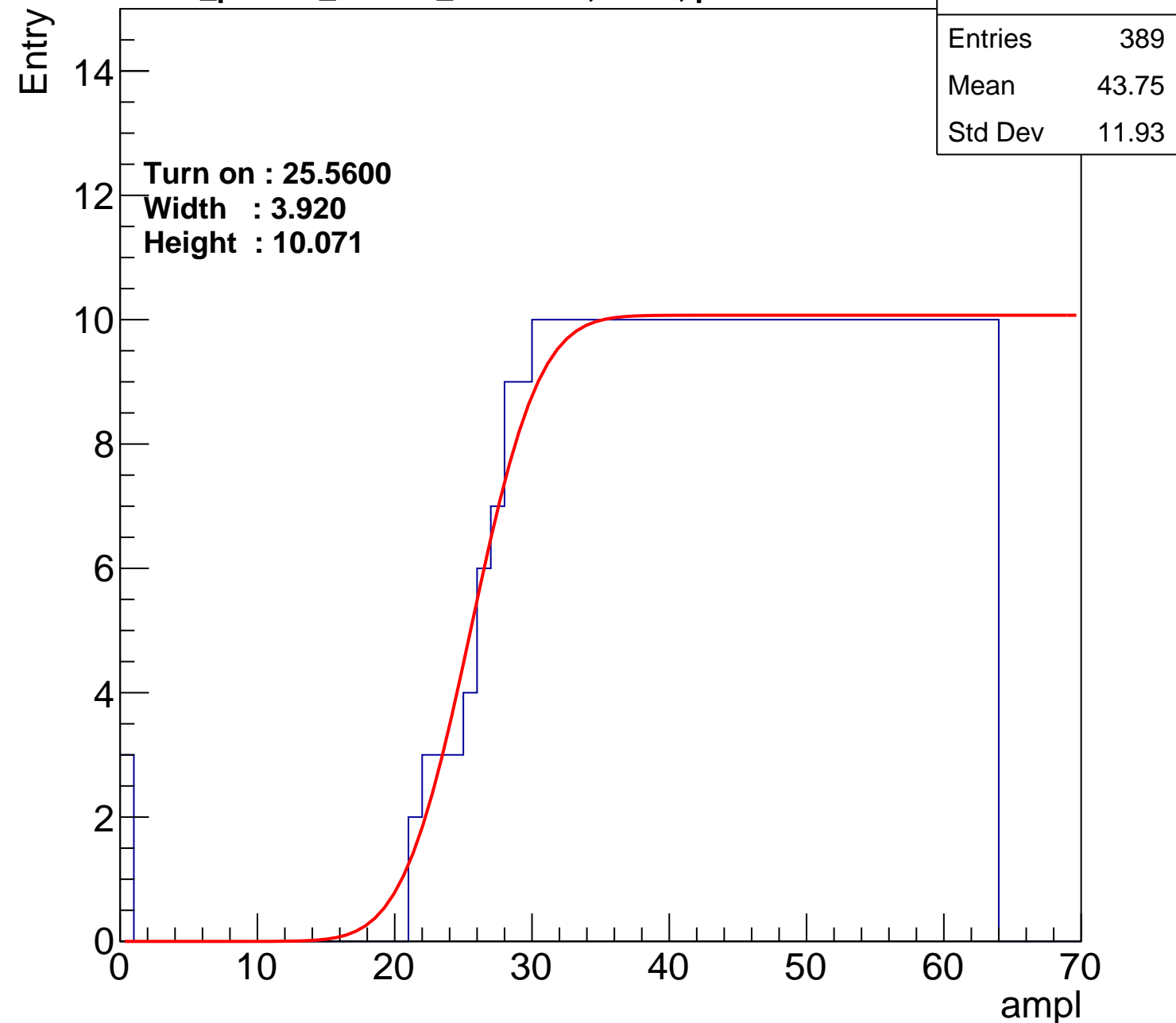
Width : 3.920

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	421
Mean	42.33
Std Dev	12.48

Turn on : 22.3937

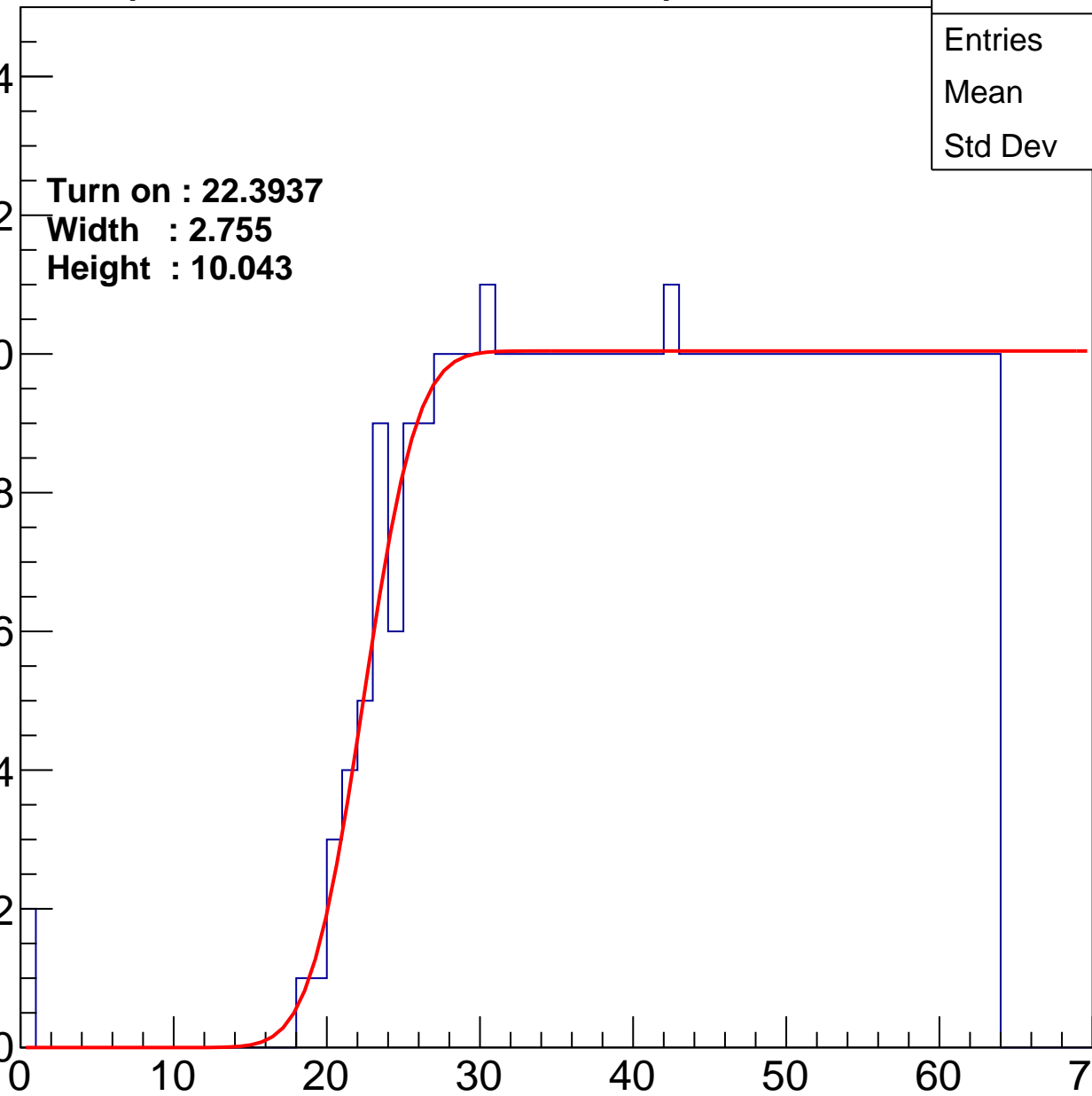
Width : 2.755

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch119

calib_packv5_042523_0143.root, FC#3, port B3

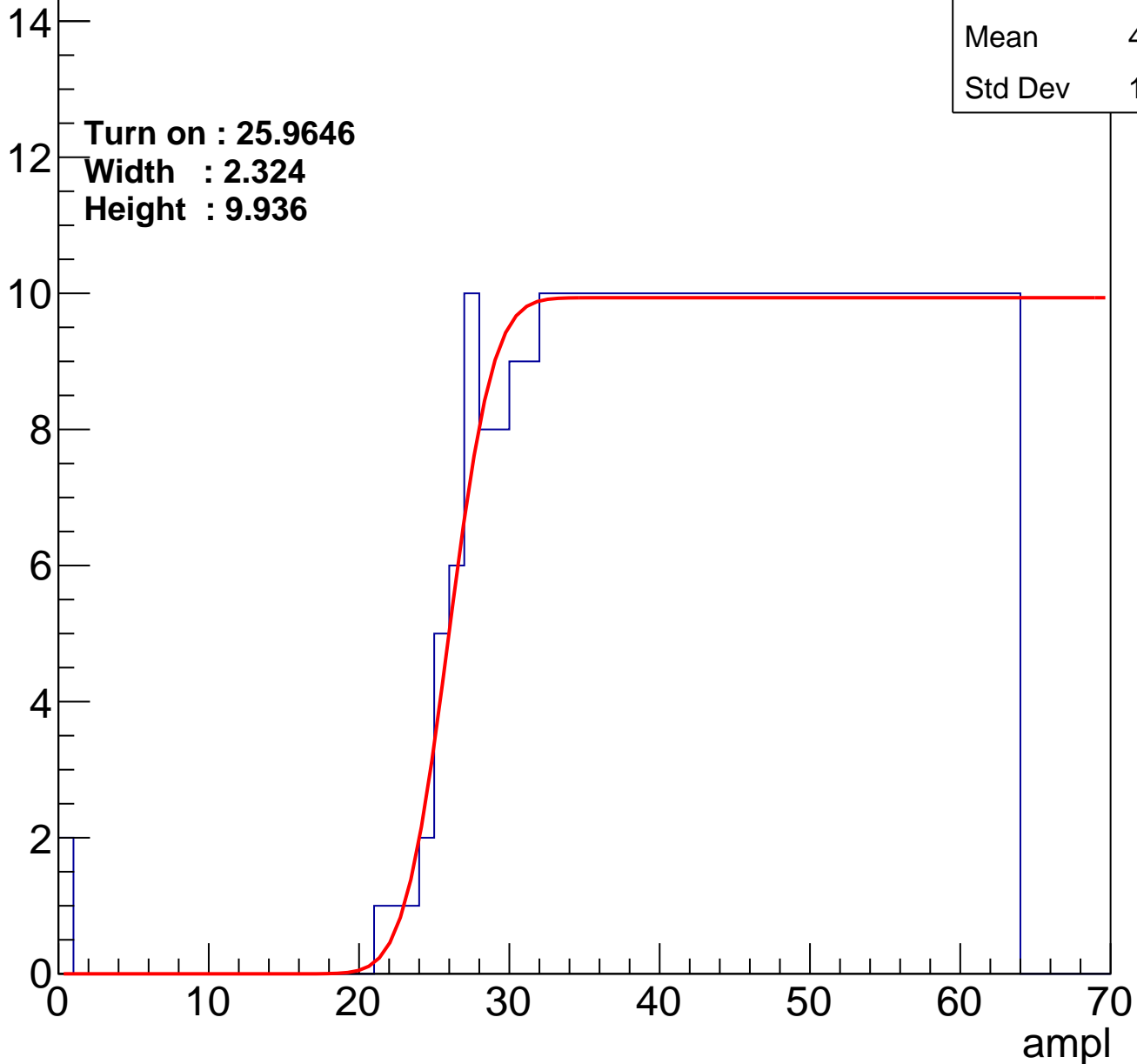
Entries	382
Mean	44.16
Std Dev	11.56

Turn on : 25.9646

Width : 2.324

Height : 9.936

Entry



B1L000S, U18-ch120

calib_packv5_042523_0143.root, FC#3, port B3

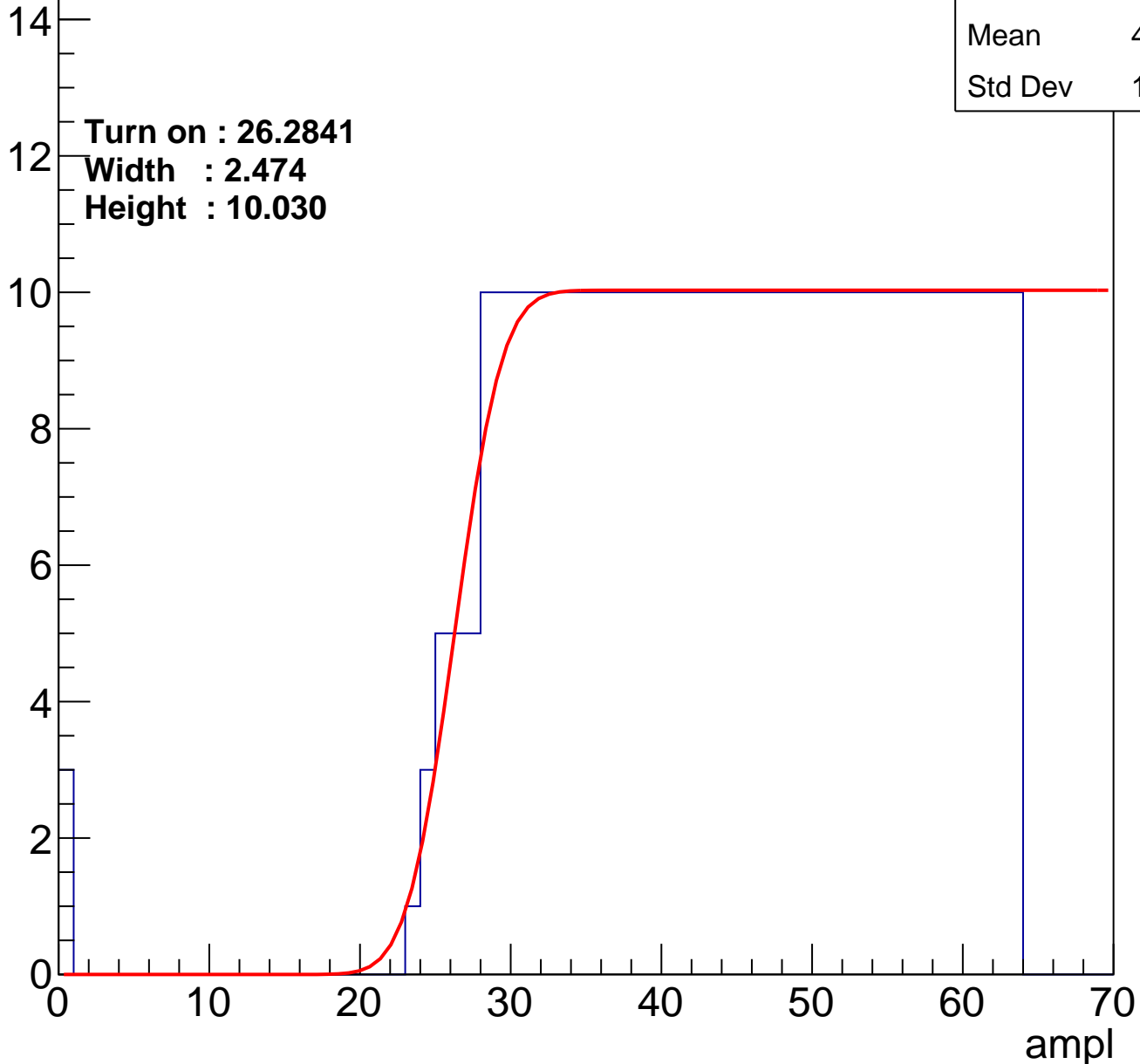
Entries	382
Mean	44.15
Std Dev	11.66

Turn on : 26.2841

Width : 2.474

Height : 10.030

Entry



B1L000S, U18-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.63
Std Dev	11.83

Turn on : 24.7511

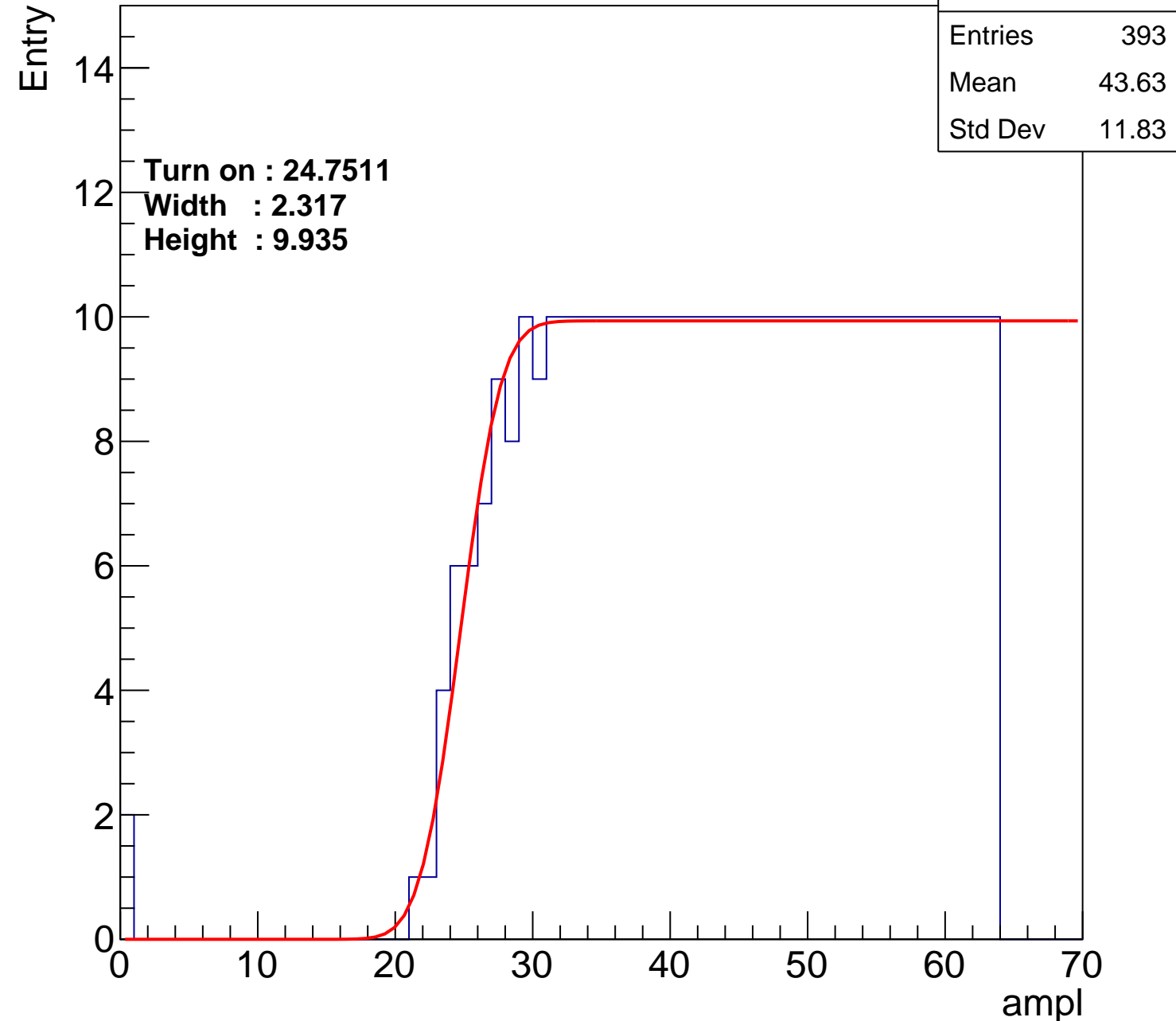
Width : 2.317

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch122

calib_packv5_042523_0143.root, FC#3, port B3

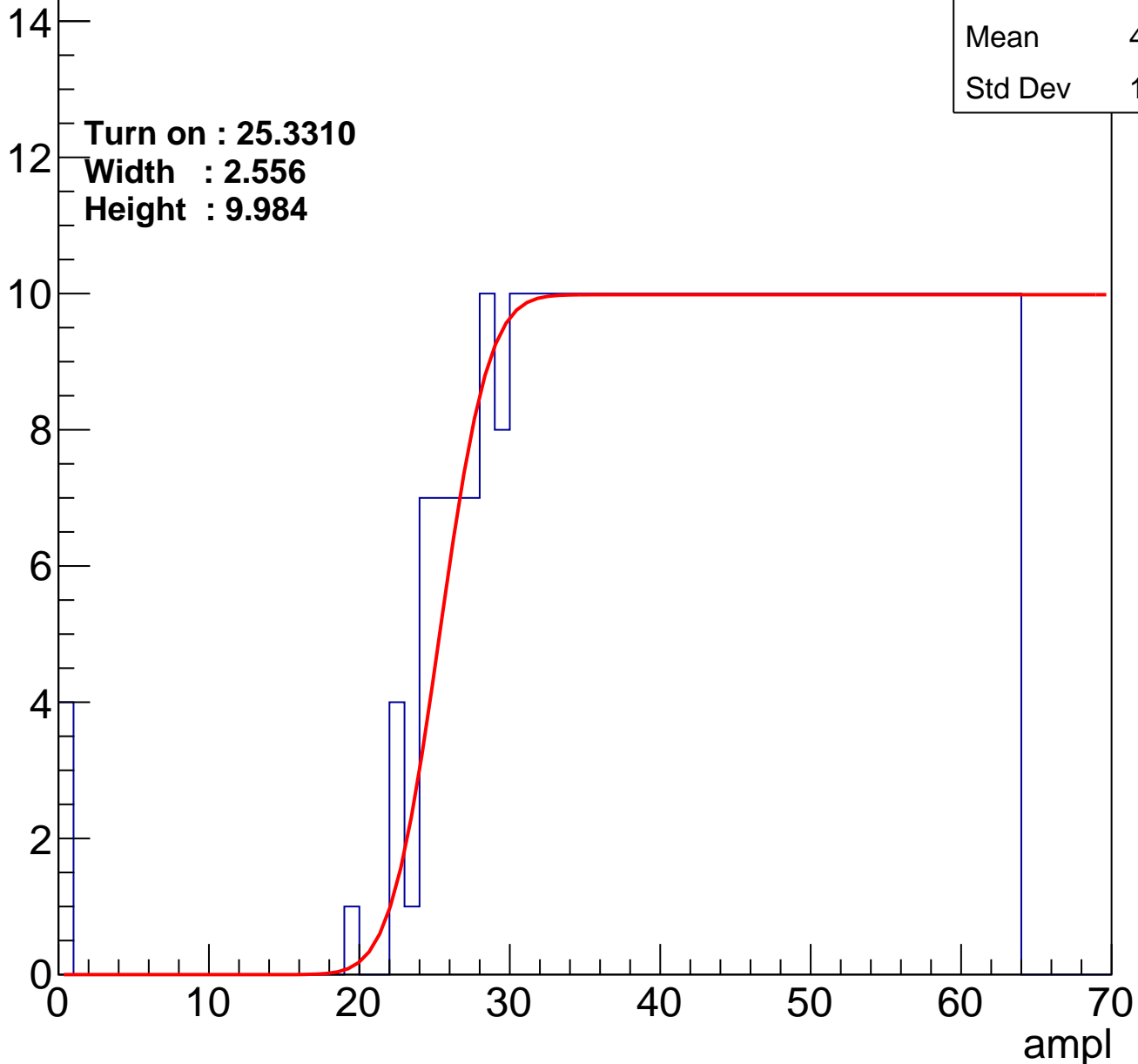
Entries	396
Mean	43.35
Std Dev	12.25

Turn on : 25.3310

Width : 2.556

Height : 9.984

Entry



B1L000S, U18-ch123

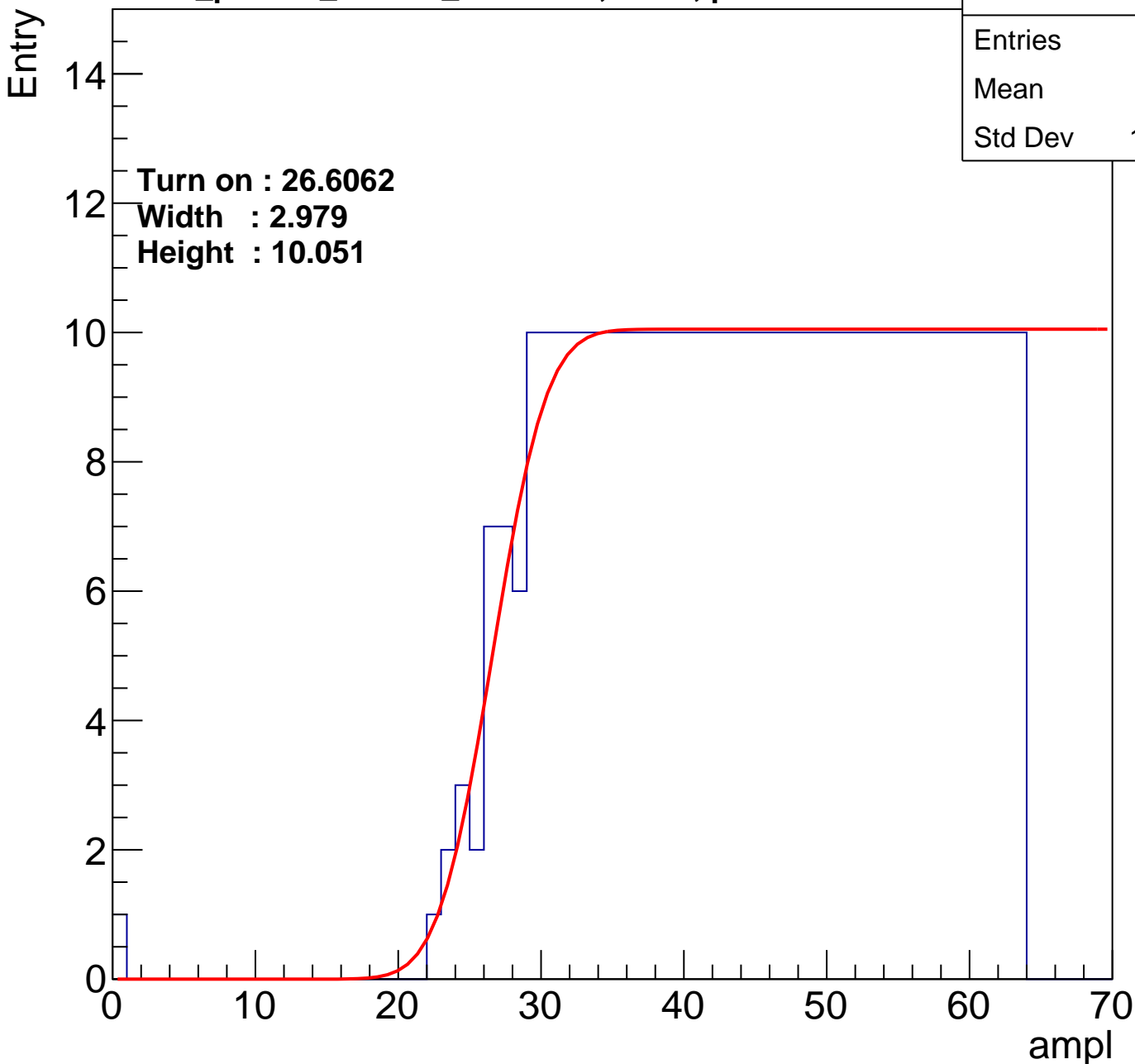
calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.4
Std Dev	11.26

Turn on : 26.6062

Width : 2.979

Height : 10.051



B1L000S, U18-ch124

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.41
Std Dev	11.25

Turn on : 25.9050

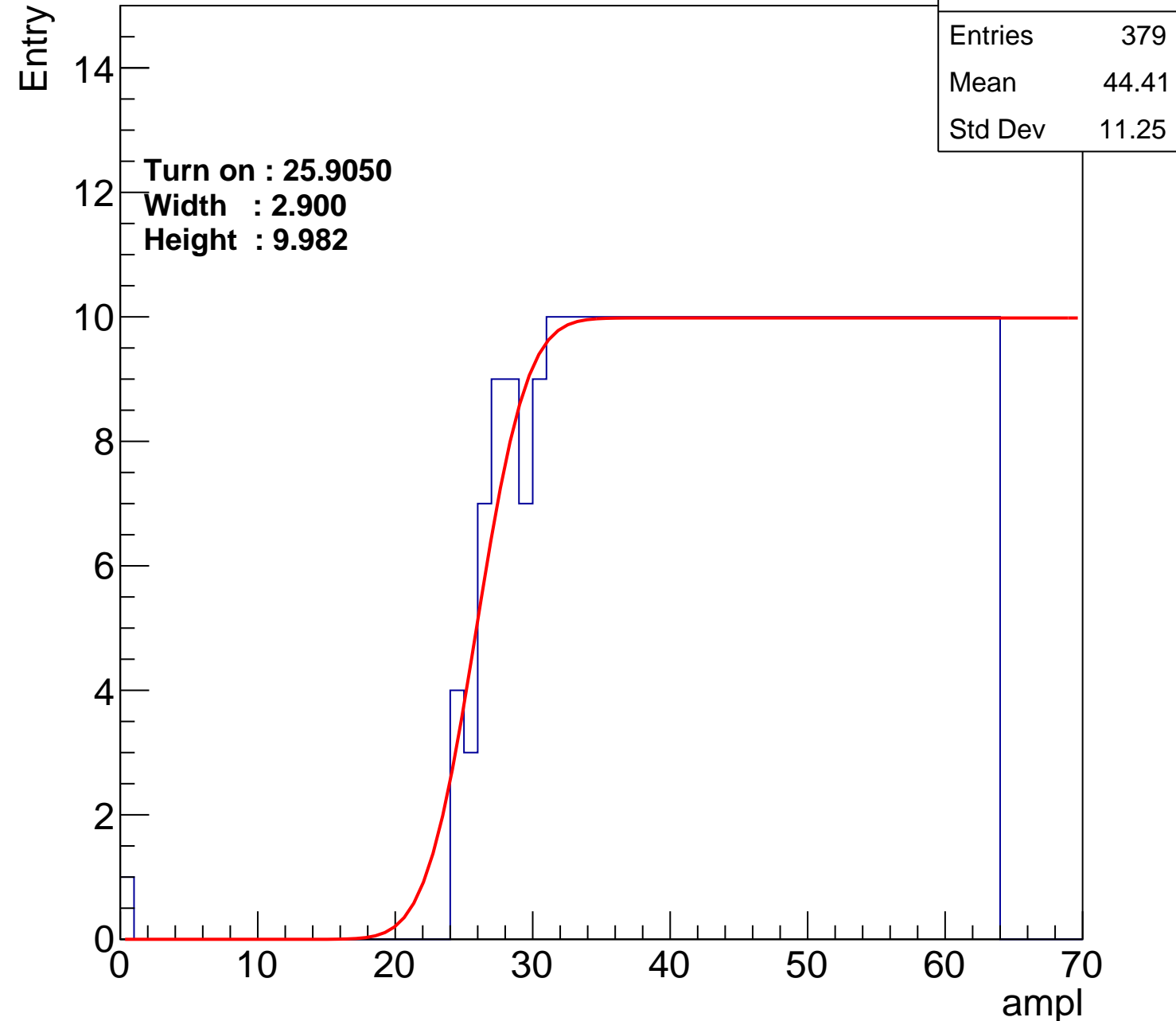
Width : 2.900

Height : 9.982

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch125

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.18
Std Dev	12.09

Turn on : 24.5417

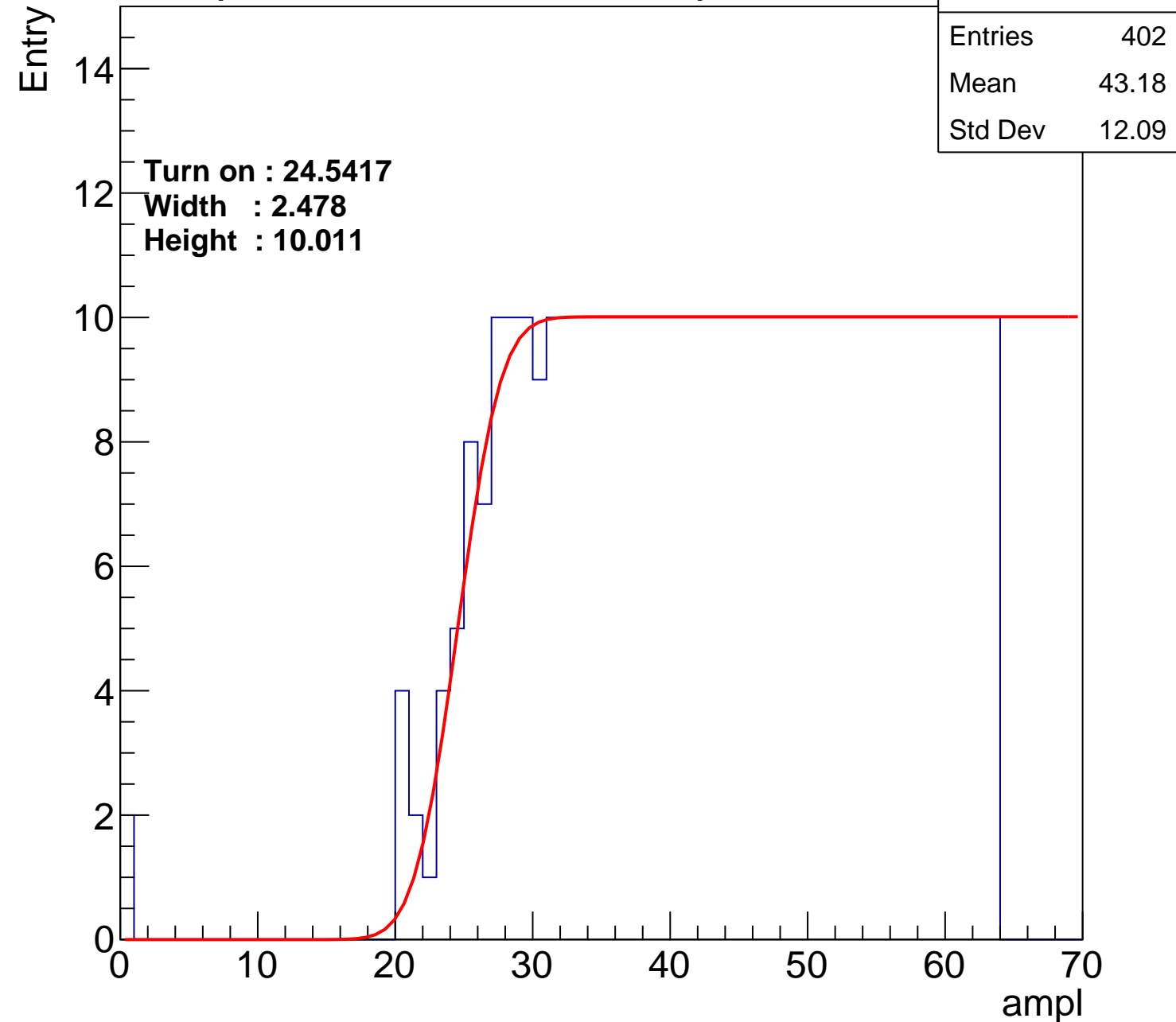
Width : 2.478

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch126

calib_packv5_042523_0143.root, FC#3, port B3

Entries	395
Mean	43.2
Std Dev	12.71

Turn on : 25.6213

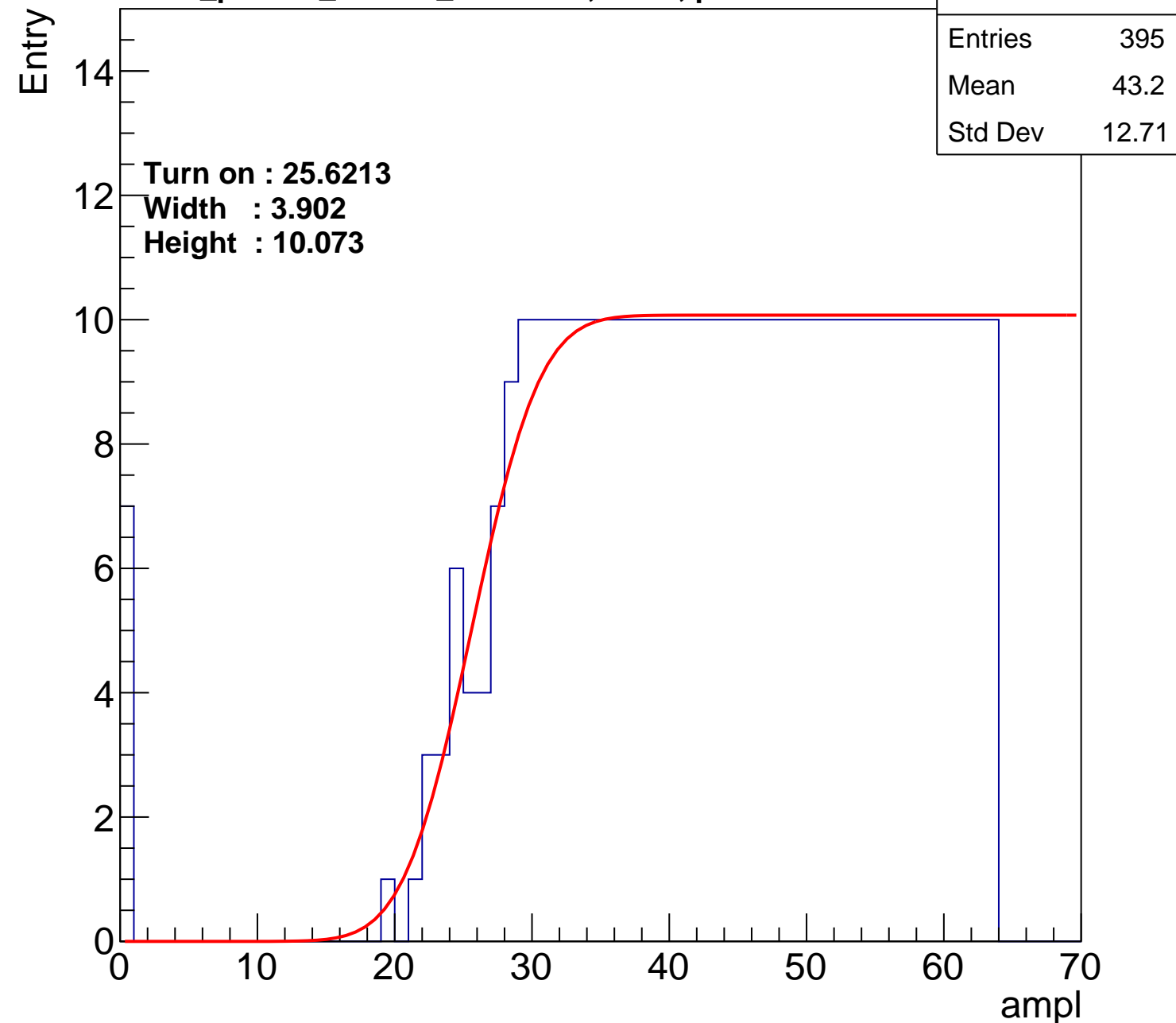
Width : 3.902

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U18-ch127

calib_packv5_042523_0143.root, FC#3, port B3

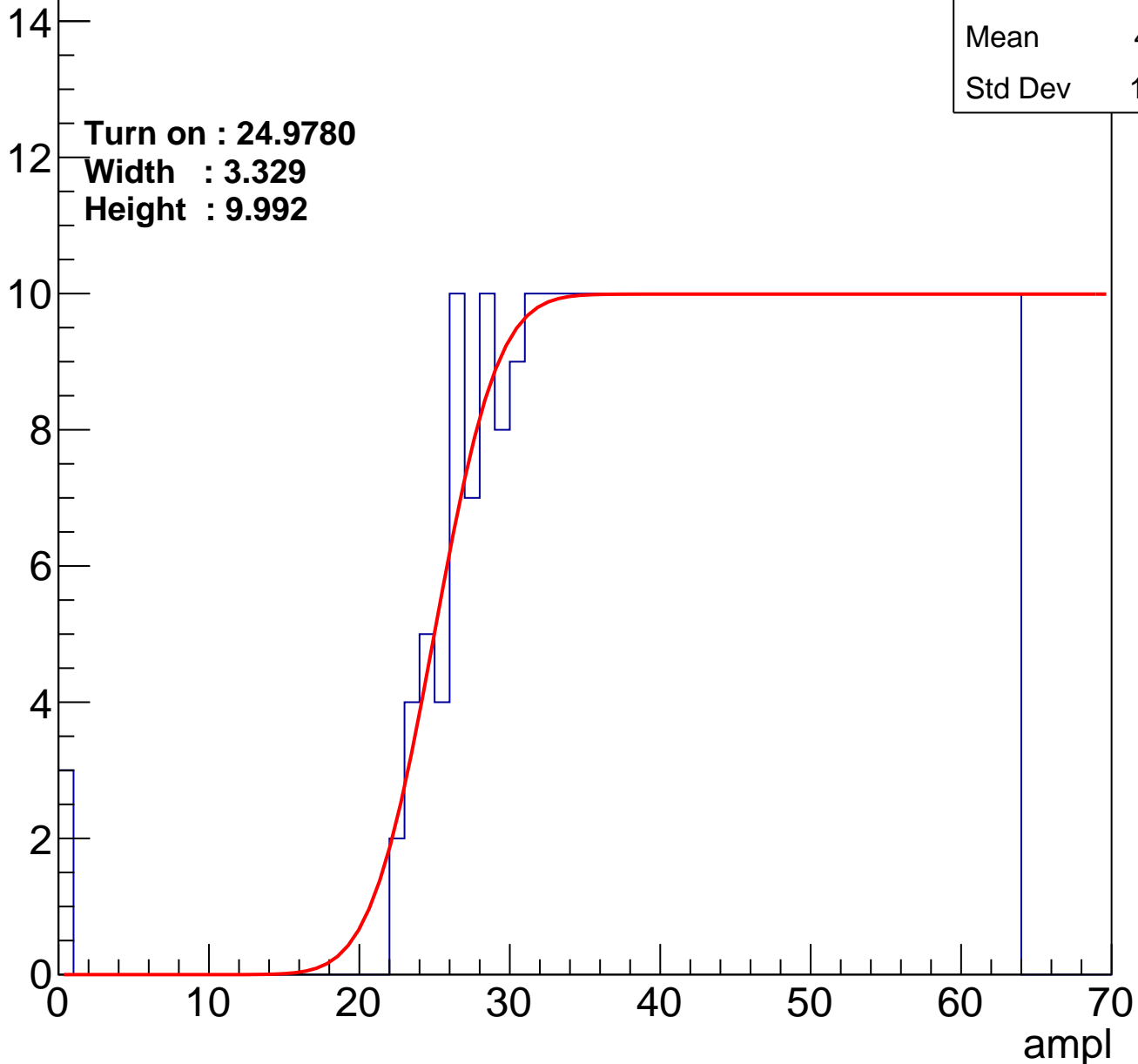
Entries	392
Mean	43.61
Std Dev	11.97

Turn on : 24.9780

Width : 3.329

Height : 9.992

Entry



B1L000S, U18-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.61
Std Dev	11.97

Turn on : 24.9780

Width : 3.329

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl

