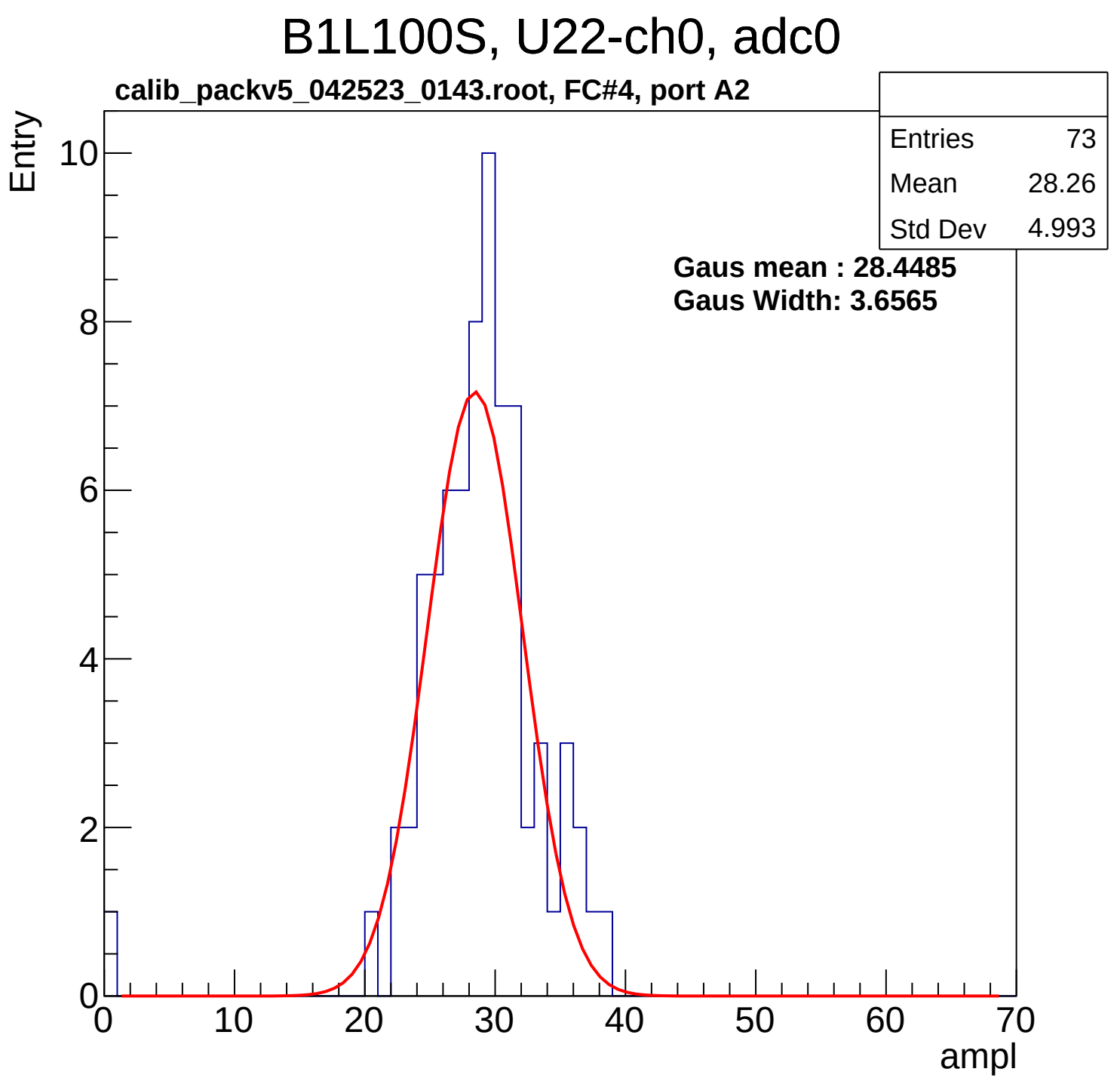
B1L100S, U22-ch0, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 28.26
Std Dev 4.993
Gaus mean : 28.4485
Gaus Width: 3.6565
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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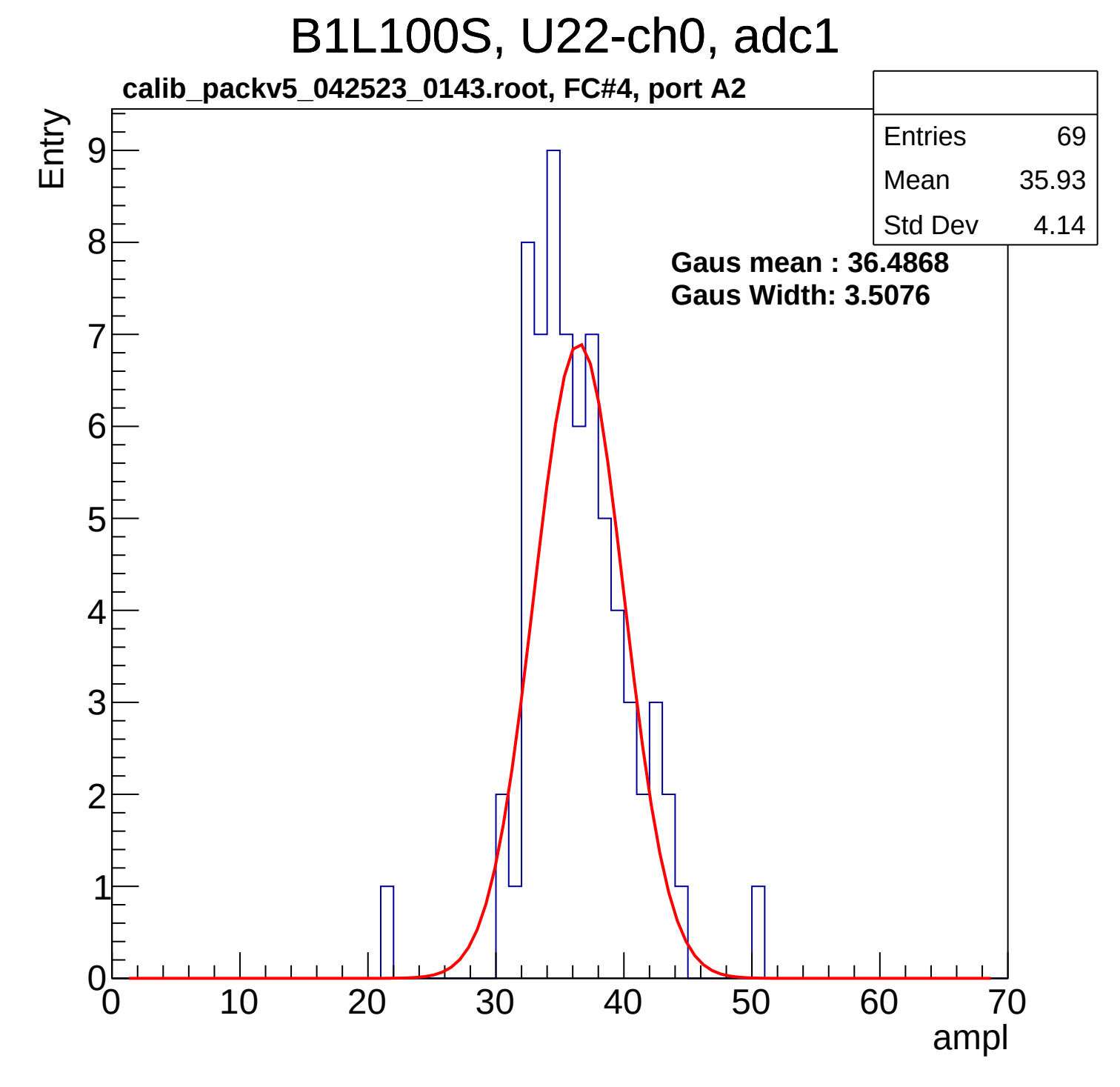
B1L100S, U22-ch0, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 35.93
Std Dev 4.14
Gaus mean : 36.4868
Gaus Width: 3.5076
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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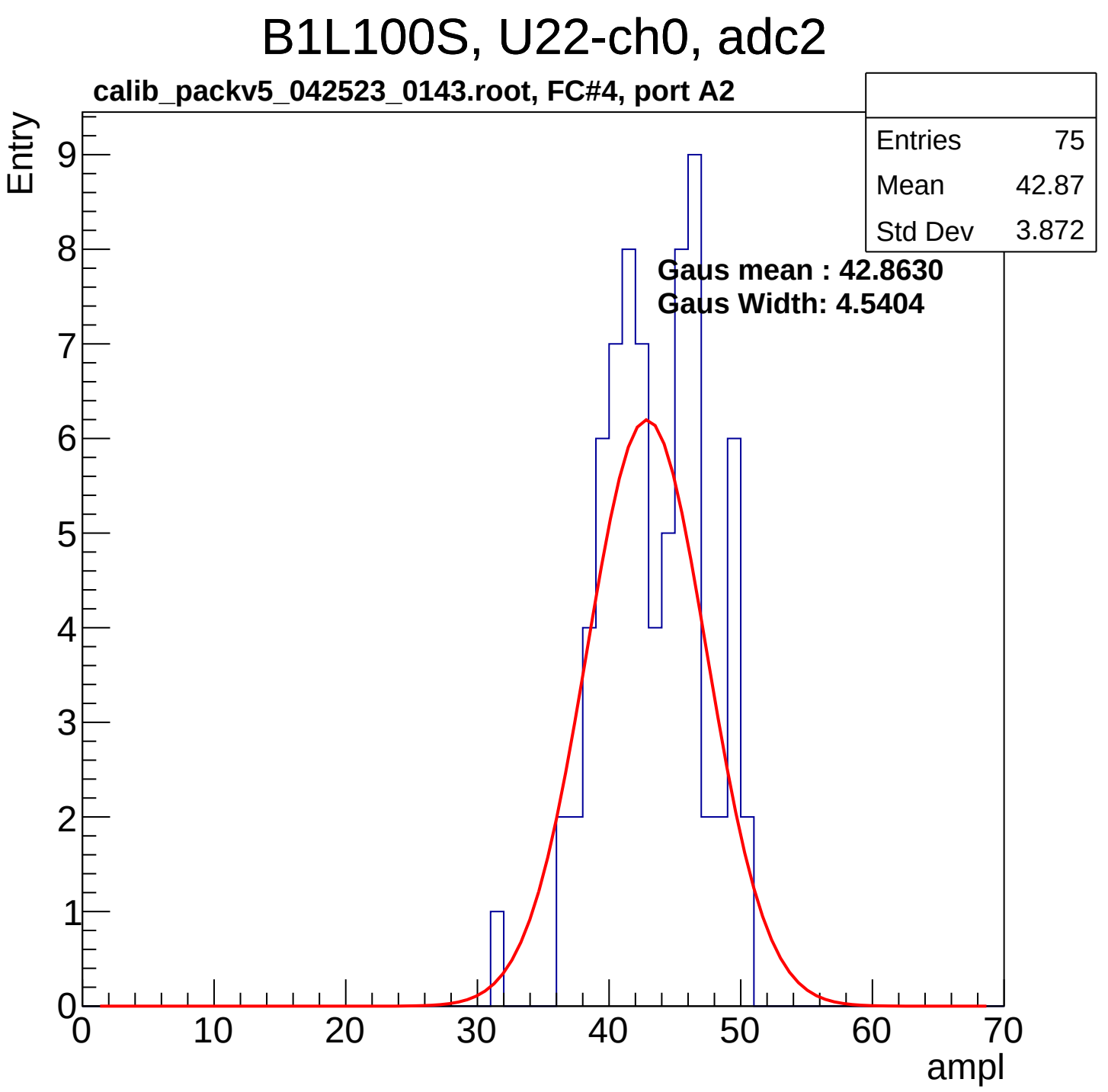
B1L100S, U22-ch0, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 42.87
Std Dev 3.872
Gaus mean : 42.8630
Gaus Width: 4.5404
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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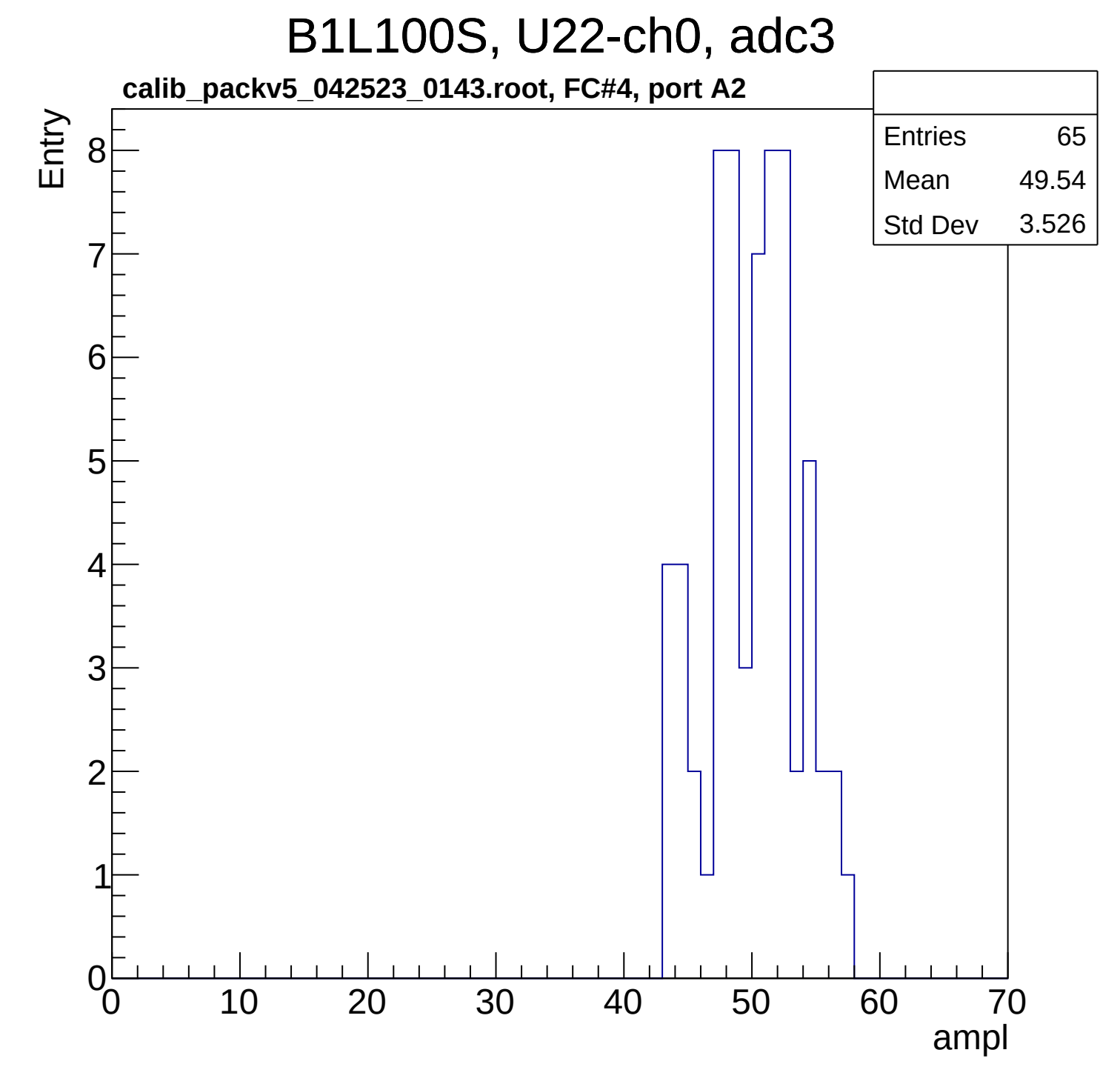
B1L100S, U22-ch0, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 49.54
Std Dev 3.526
Entry
ampl
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2
3
4
5
6
7
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0
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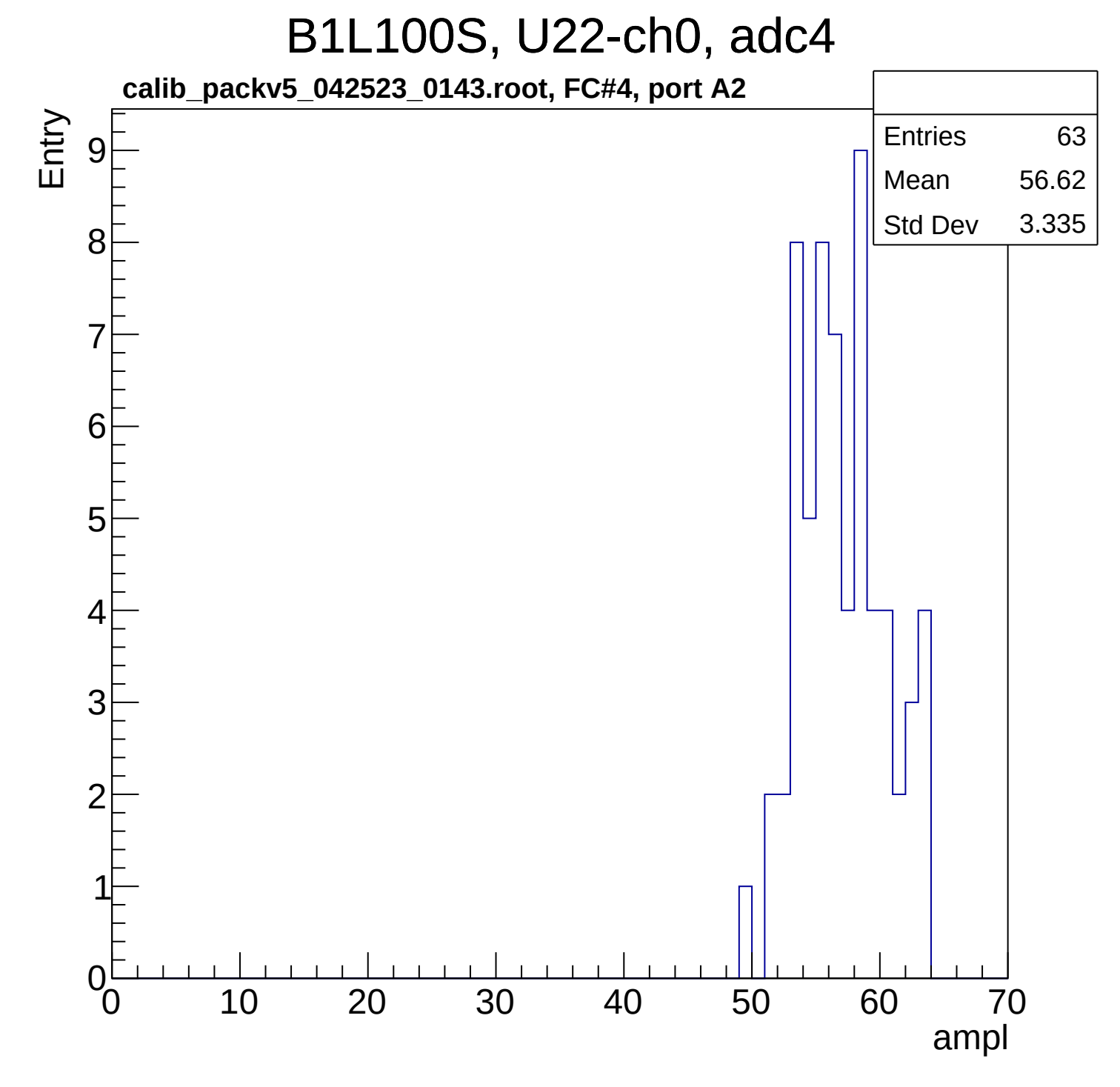
B1L100S, U22-ch0, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.62
Std Dev 3.335
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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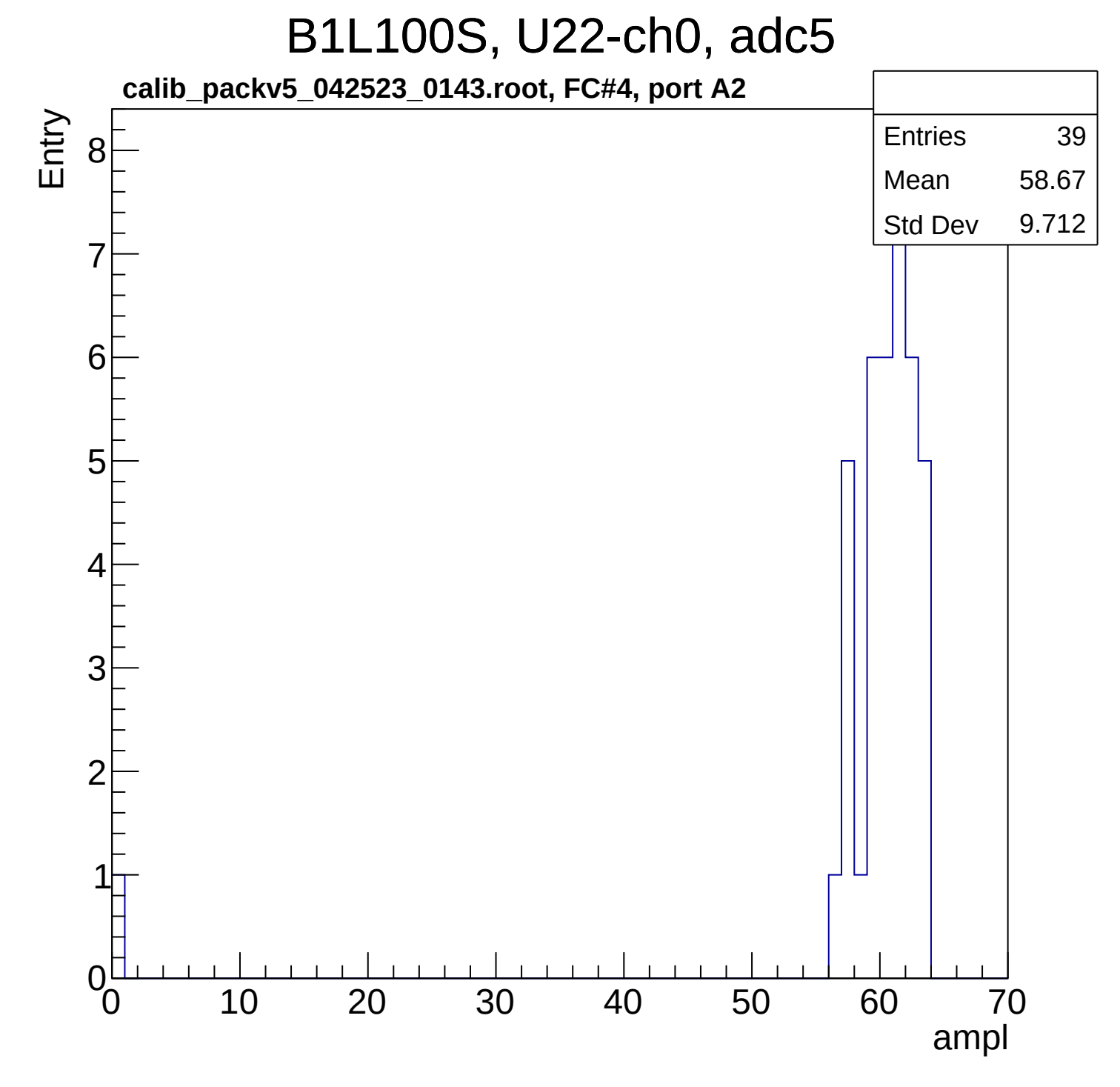
B1L100S, U22-ch0, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.67
Std Dev 9.712
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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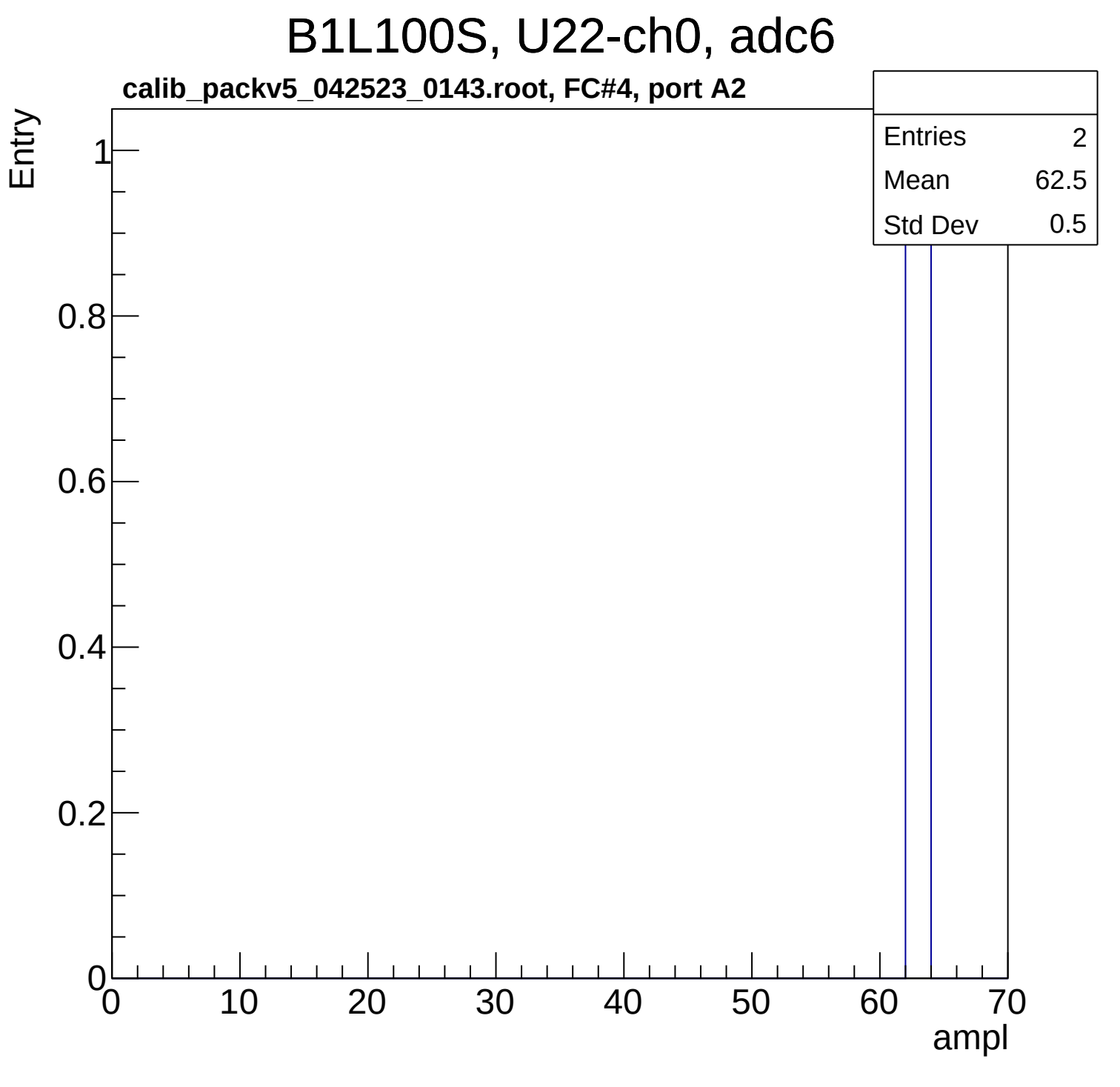
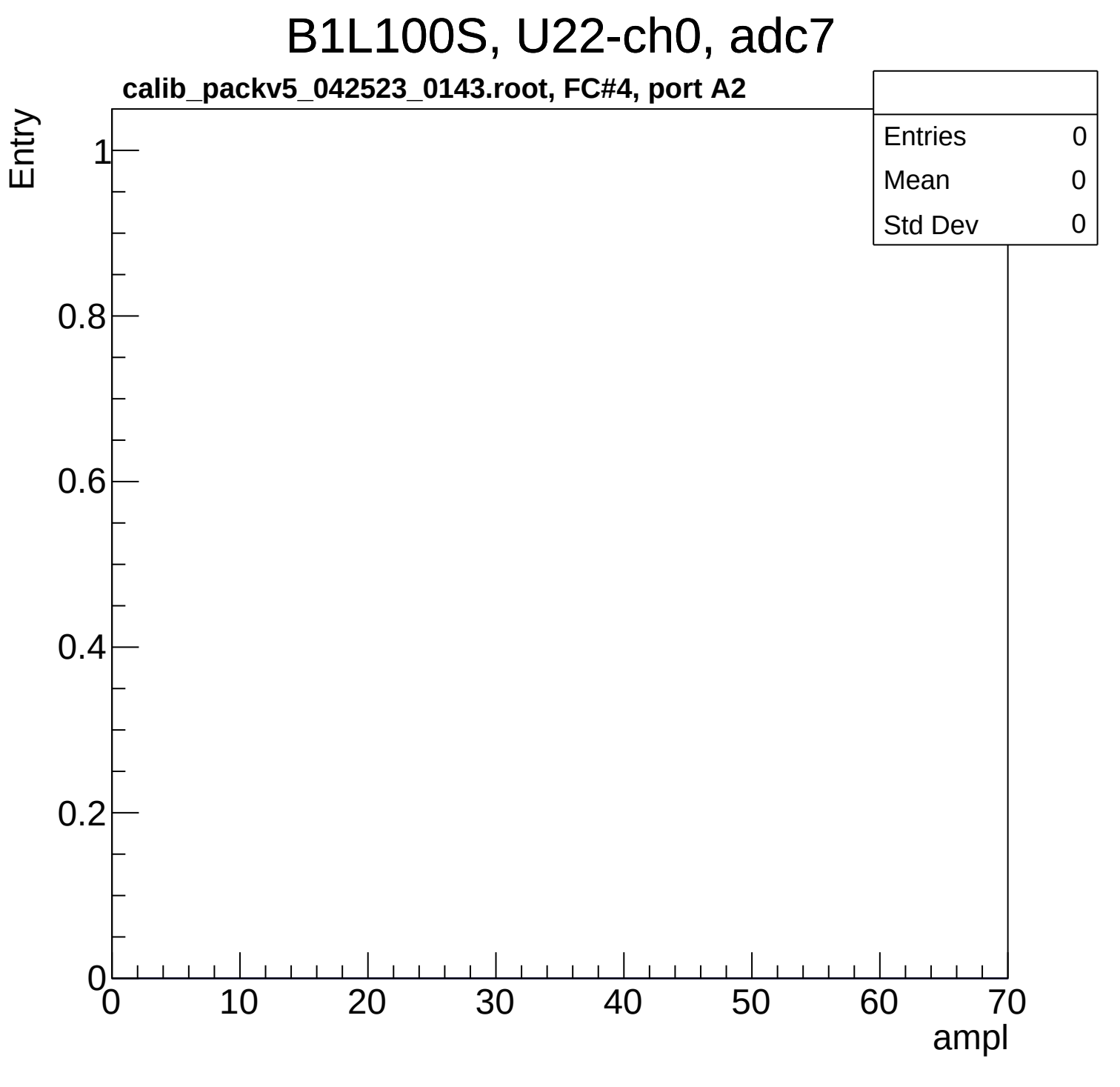

B1L100S, U22-ch0, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch0, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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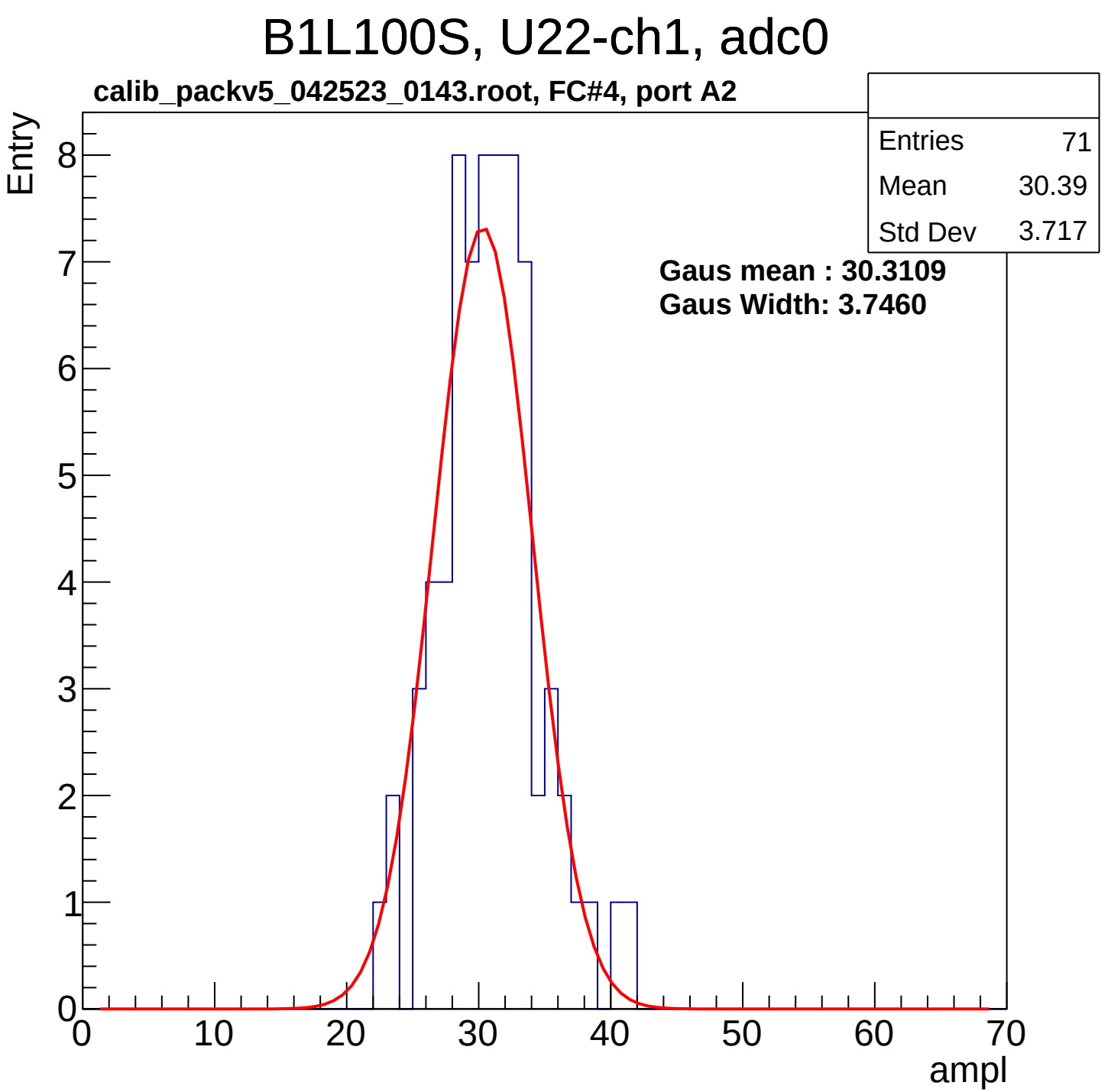
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch1, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 30.39
Std Dev 3.717
Gaus mean : 30.3109
Gaus Width: 3.7460
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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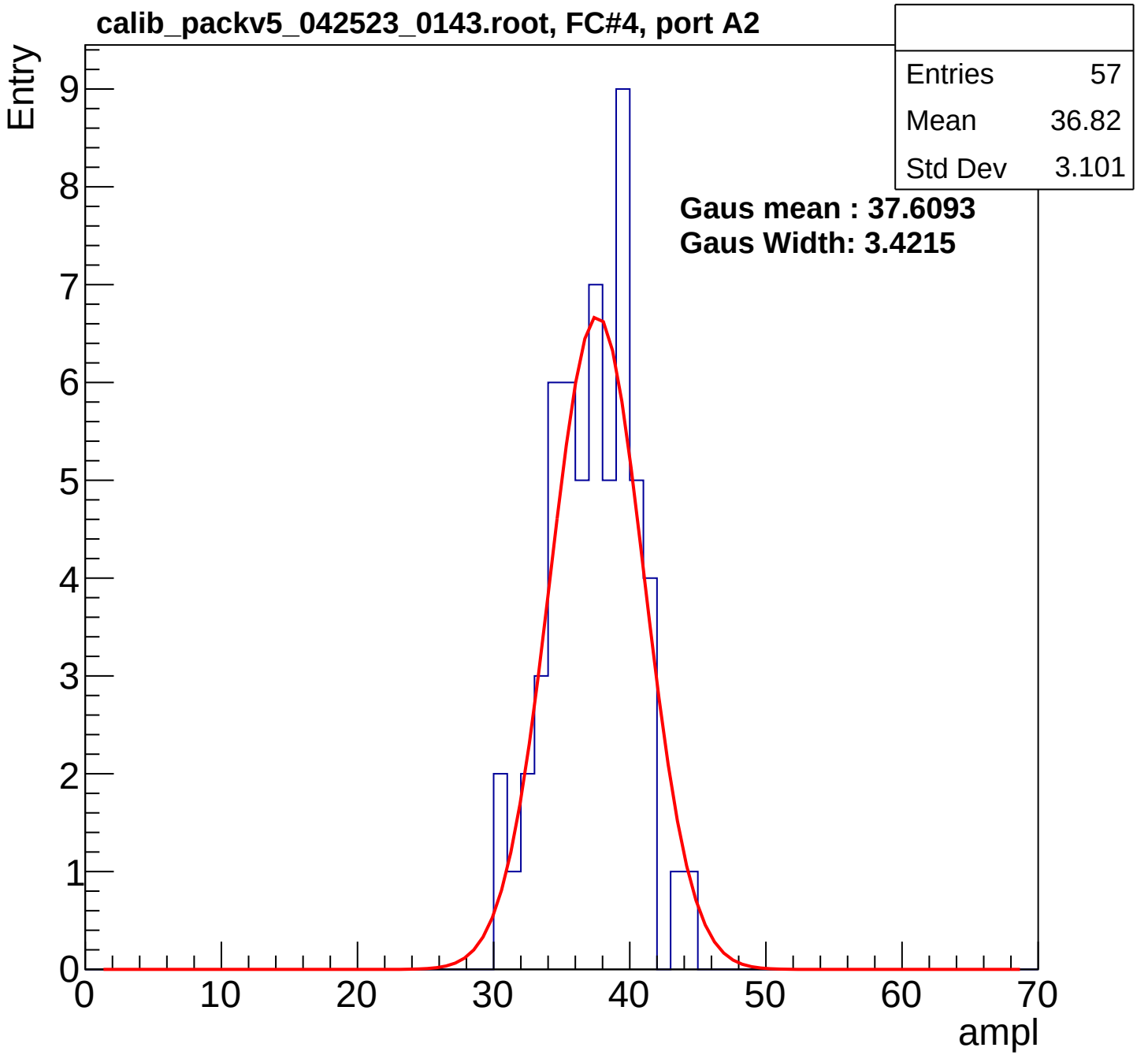
B1L100S, U22-ch1, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 36.82
Std Dev 3.101
Gaus mean : 37.6093
Gaus Width: 3.4215
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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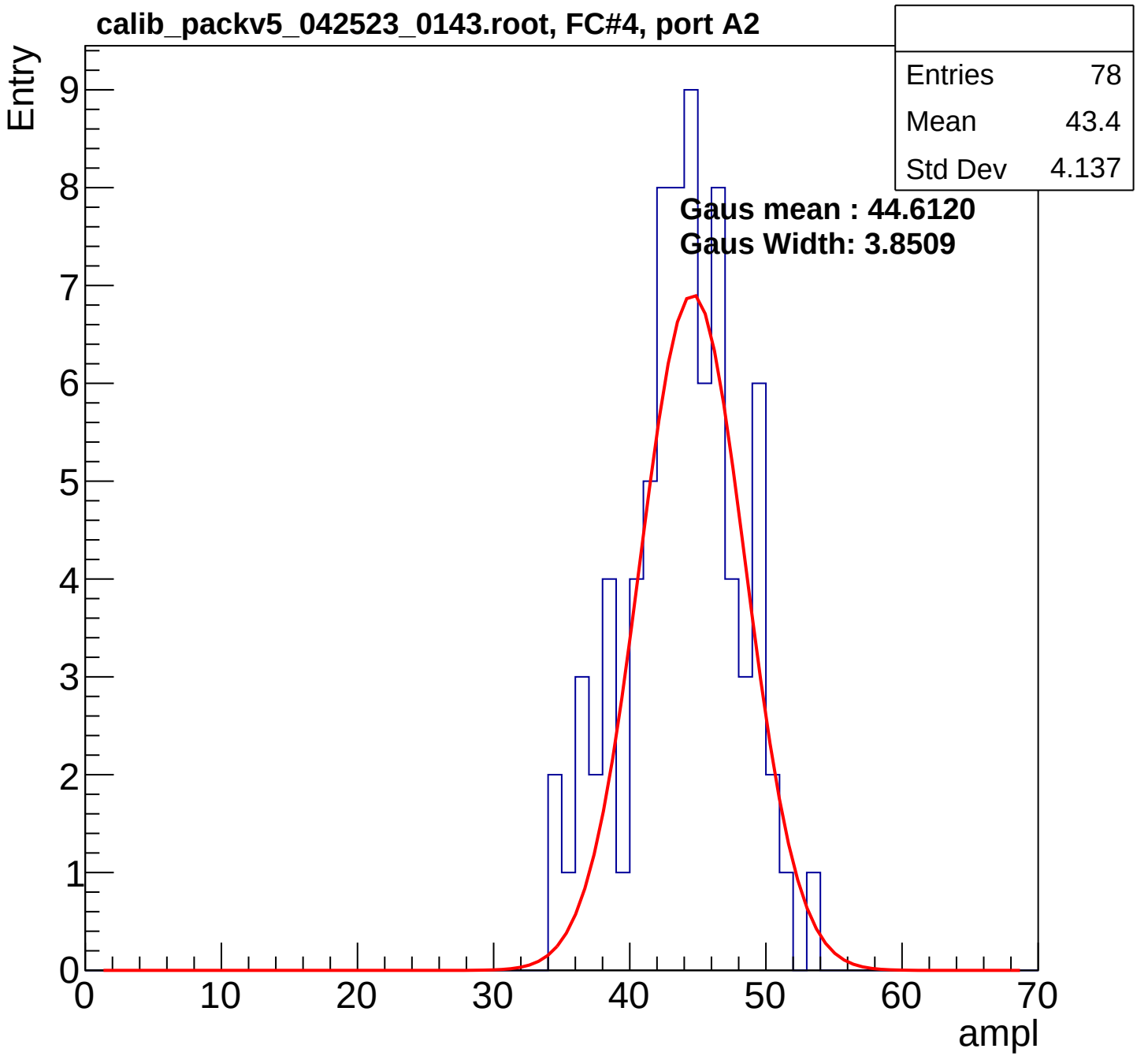
B1L100S, U22-ch1, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.4
Std Dev 4.137
Gaus mean : 44.6120
Gaus Width: 3.8509
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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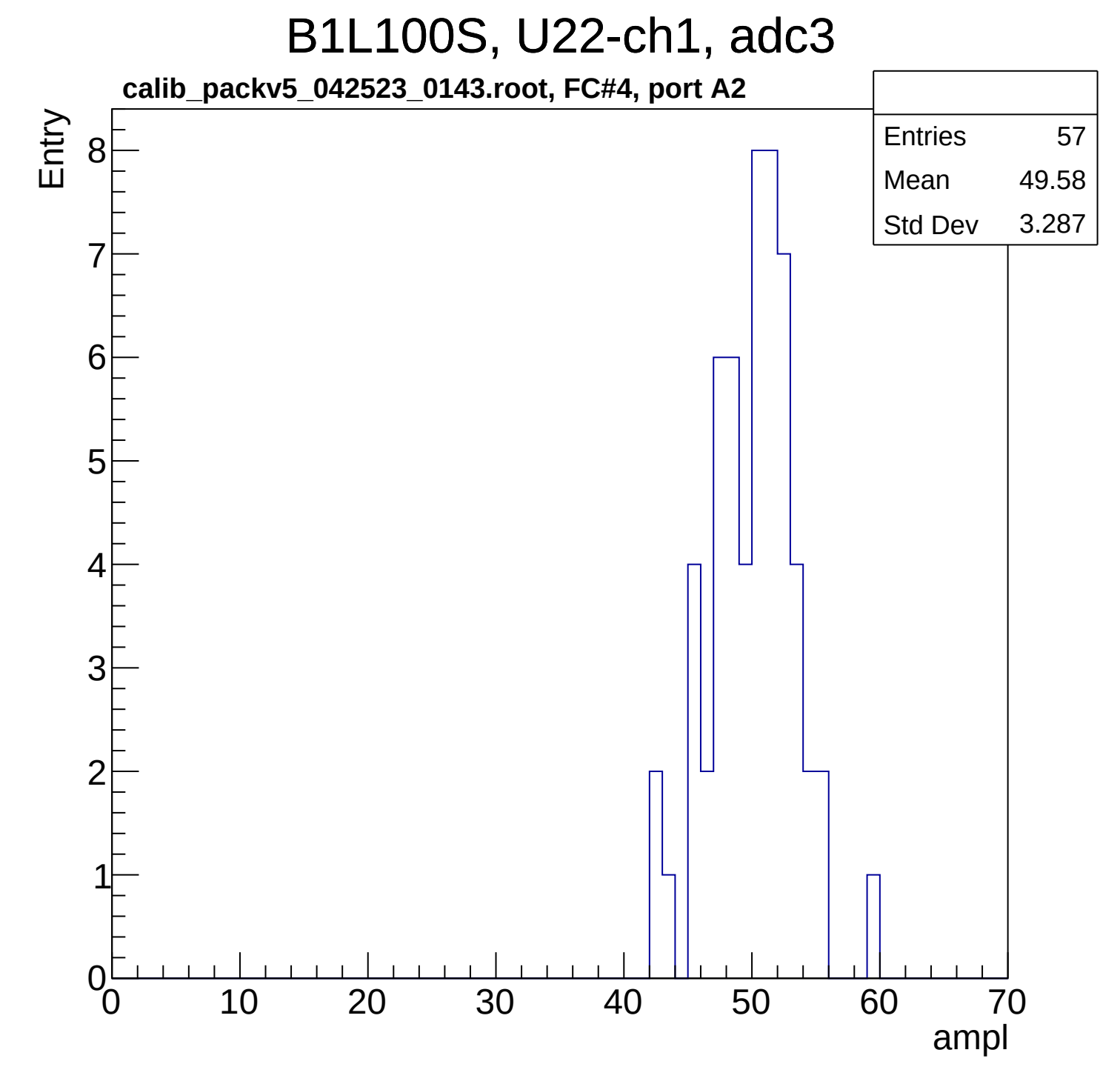
B1L100S, U22-ch1, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.58
Std Dev 3.287
Entry
ampl
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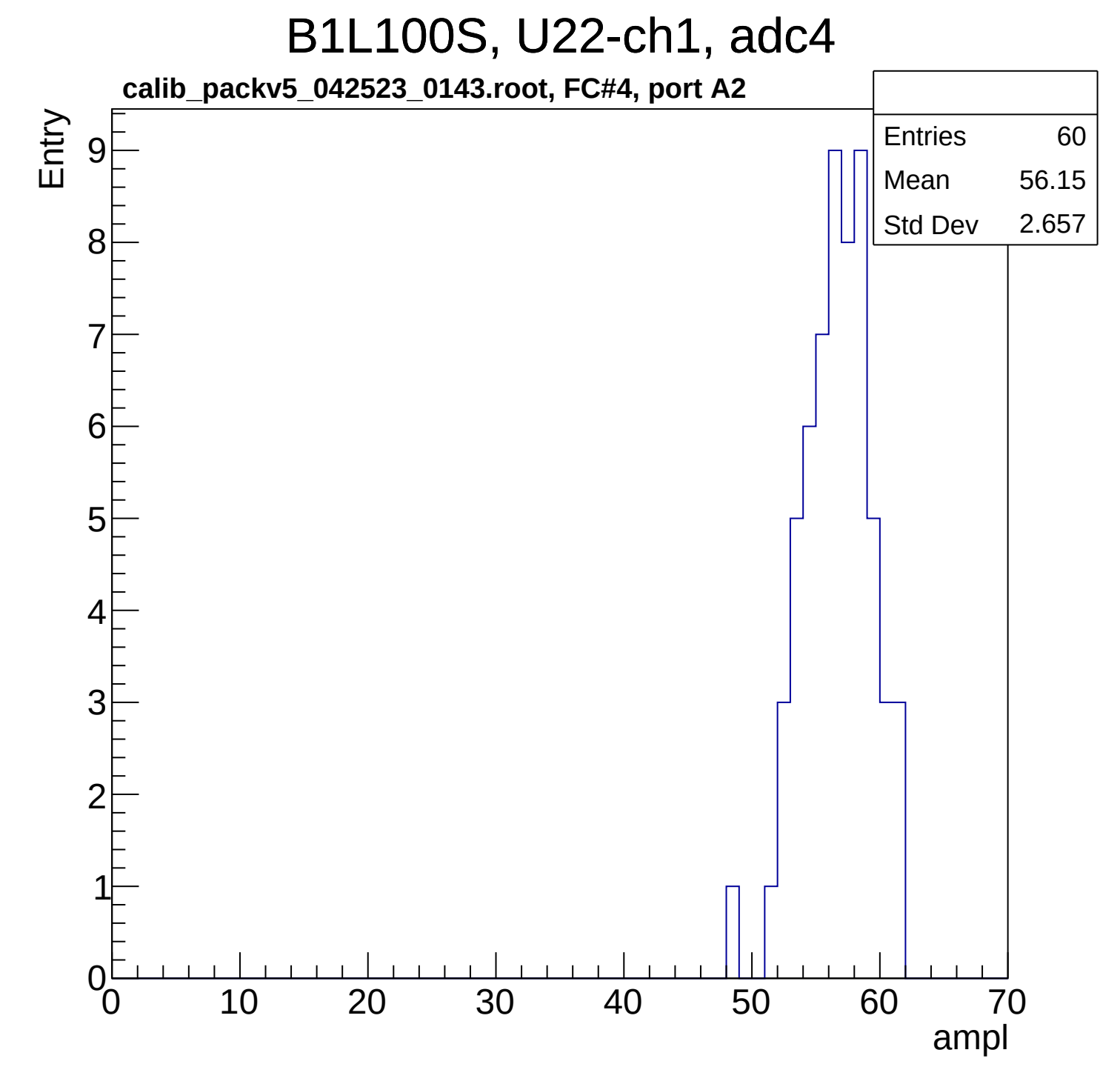
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch1, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.15
Std Dev 2.657
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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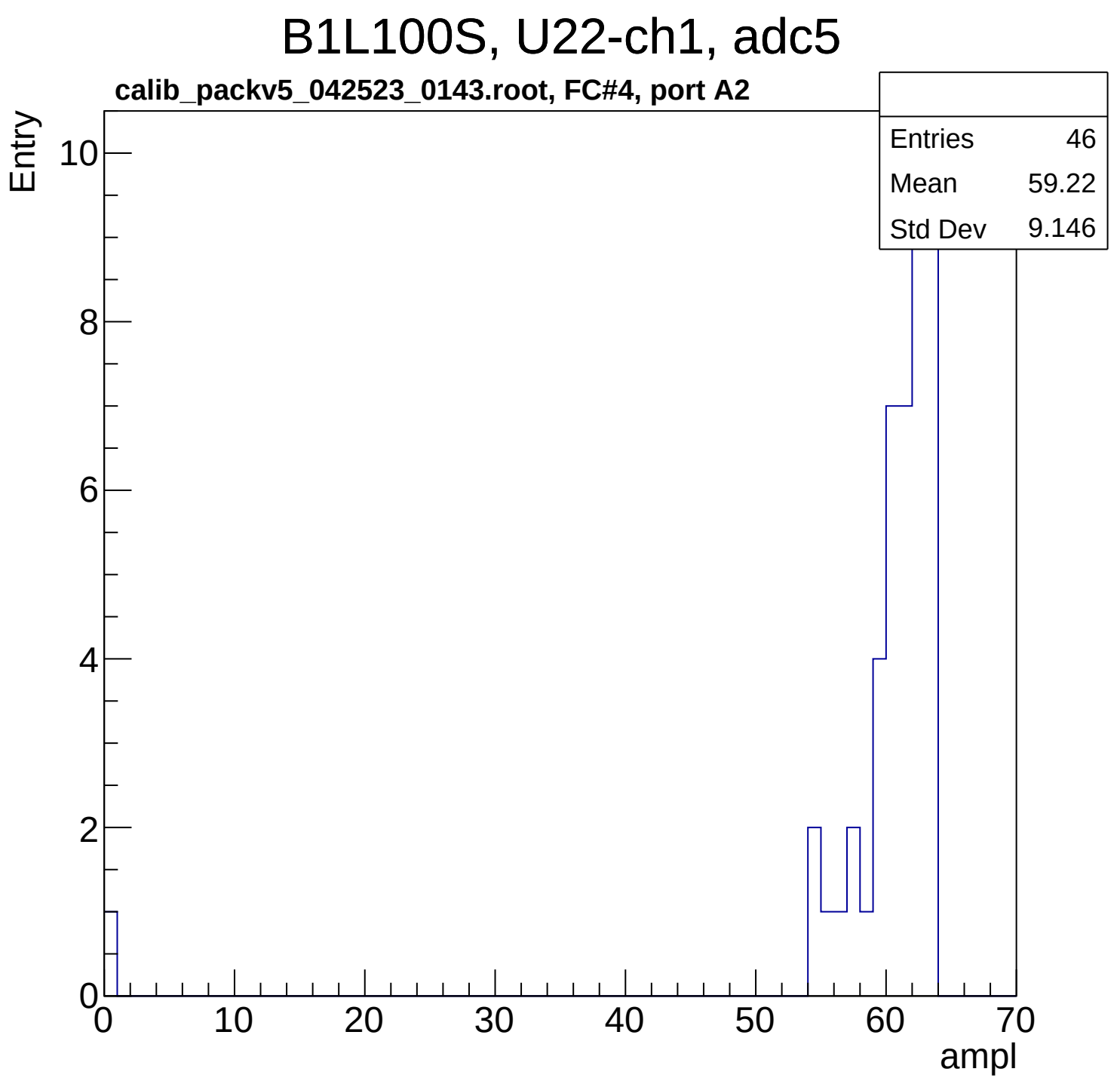
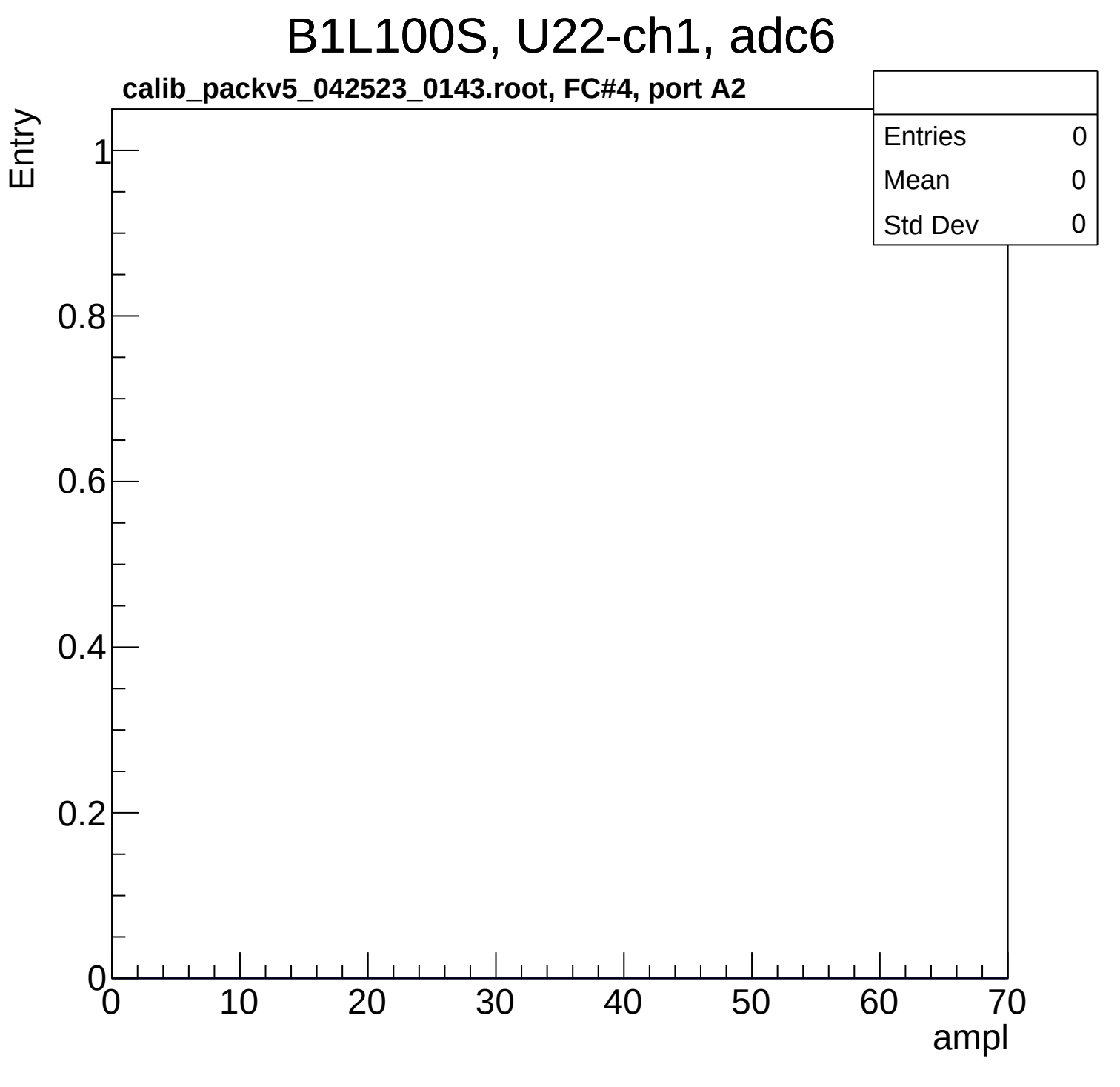

B1L100S, U22-ch1, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 59.22
Std Dev 9.146
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch1, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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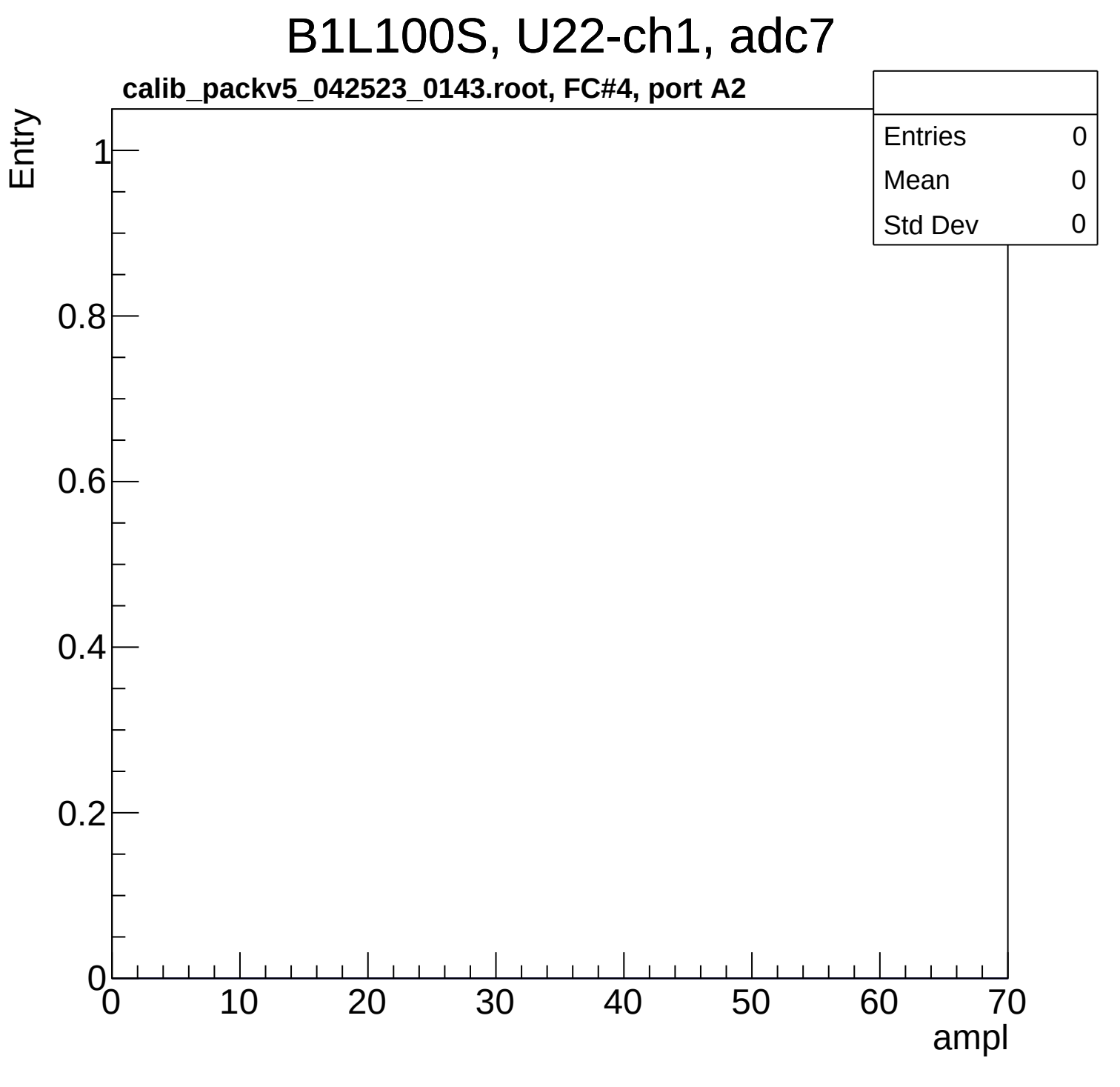
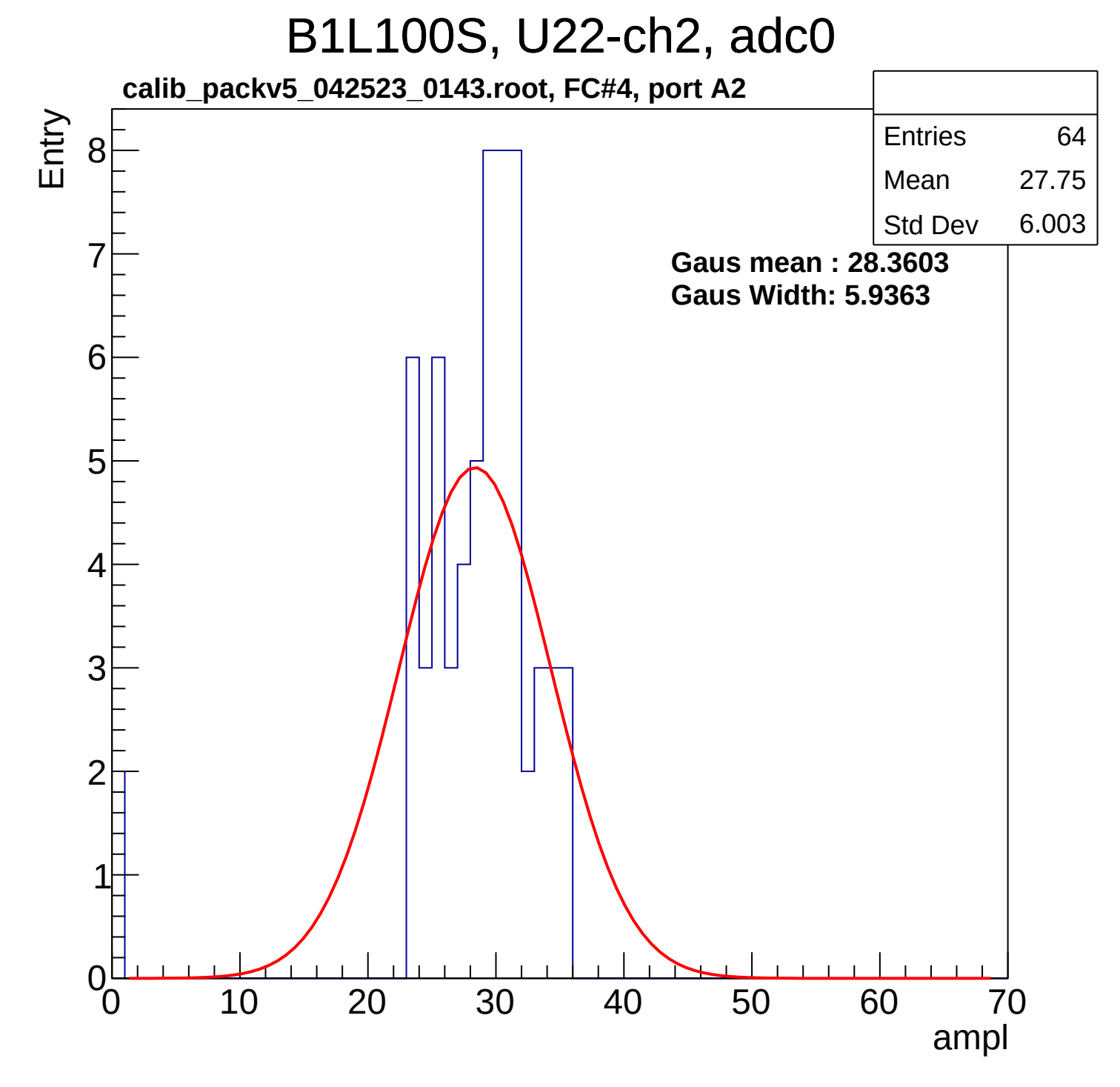
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch1, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch2, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 27.75
Std Dev 6.003
Gaus mean : 28.3603
Gaus Width: 5.9363
Entry
ampl
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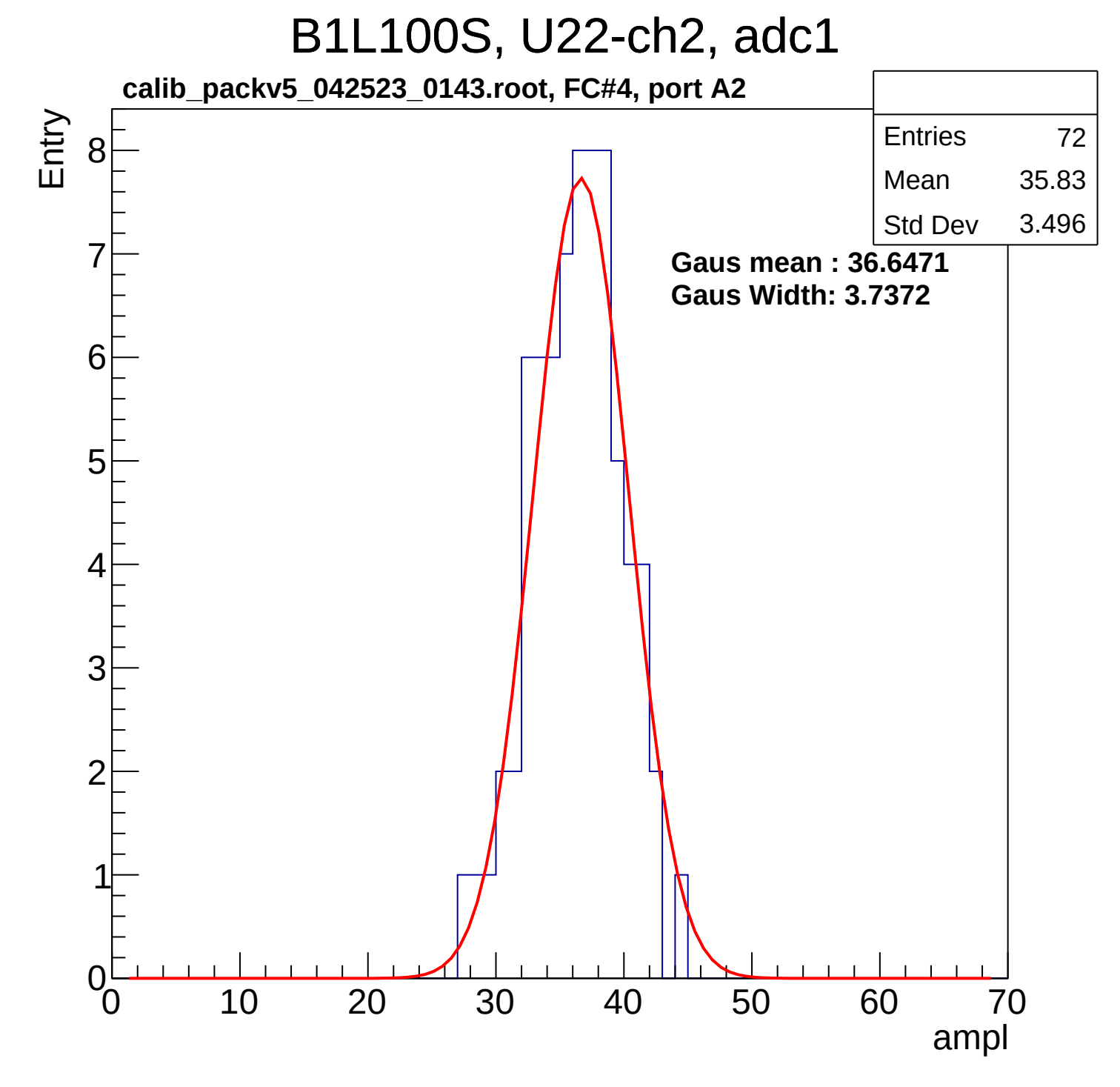
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch2, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 35.83
Std Dev 3.496
Gaus mean : 36.6471
Gaus Width: 3.7372
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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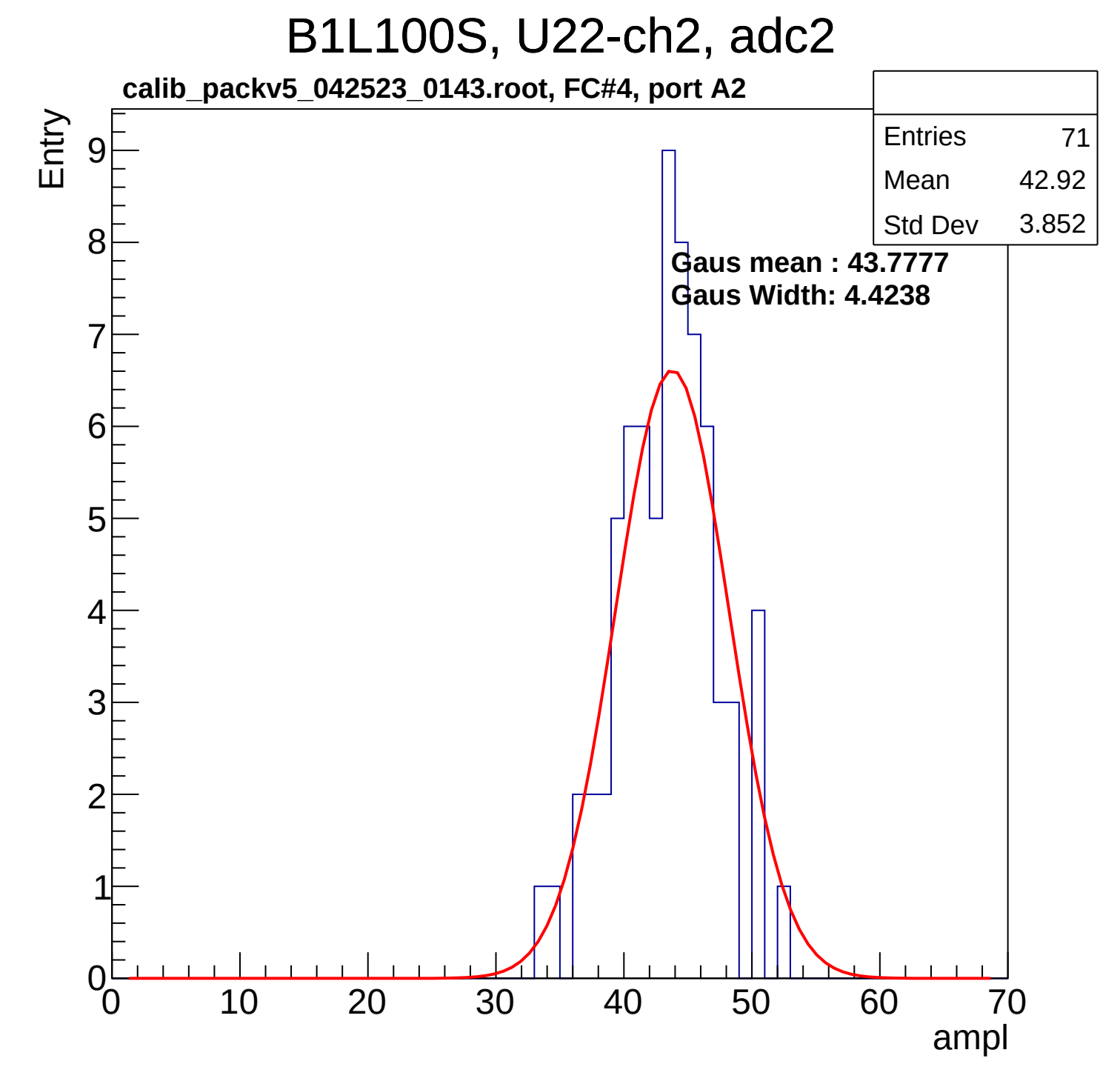
B1L100S, U22-ch2, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.92
Std Dev 3.852
Gaus mean : 43.7777
Gaus Width: 4.4238
Entry
ampl
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2
3
4
5
6
7
8
9
0
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40
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60
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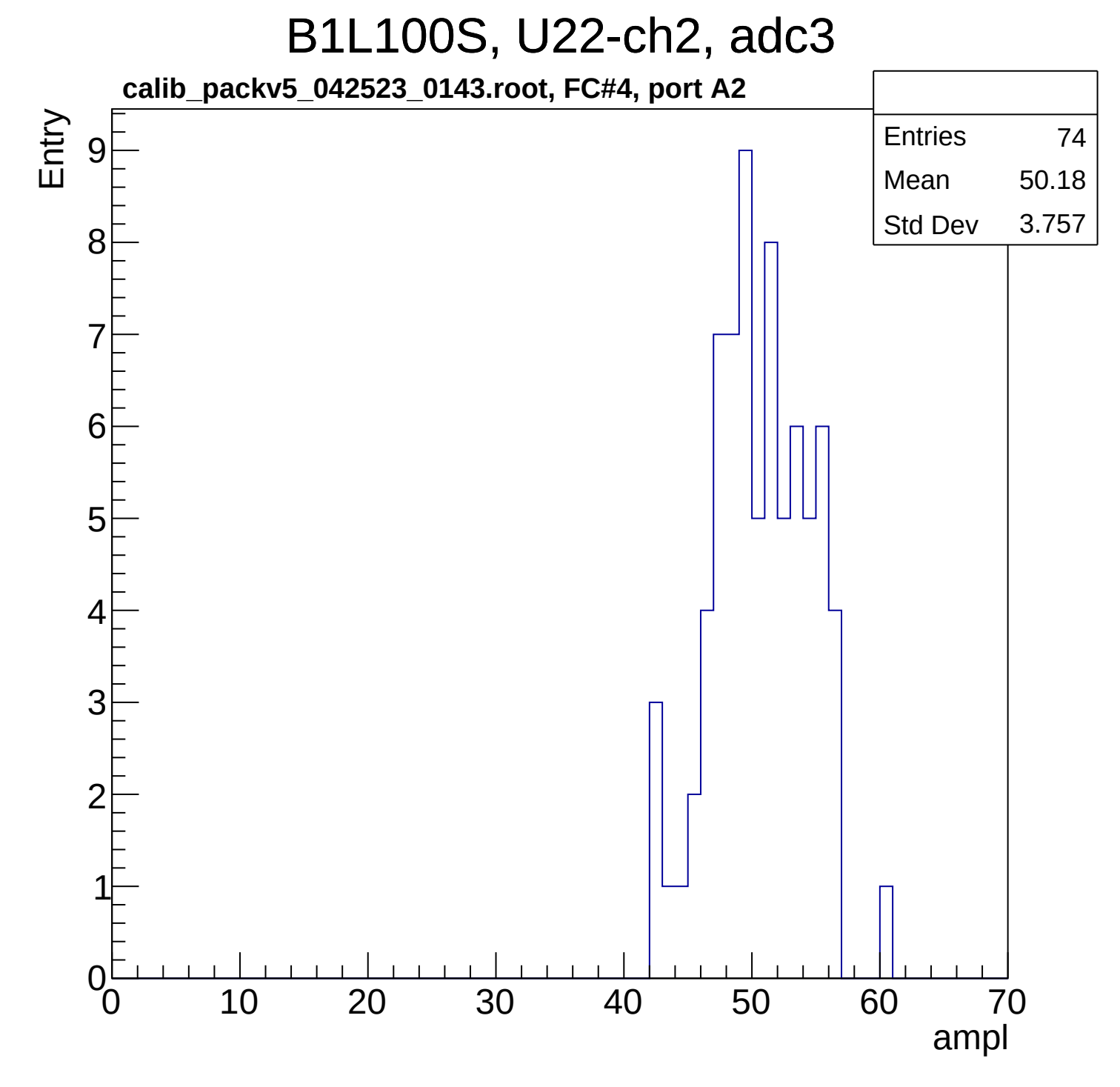
B1L100S, U22-ch2, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.18
Std Dev 3.757
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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60
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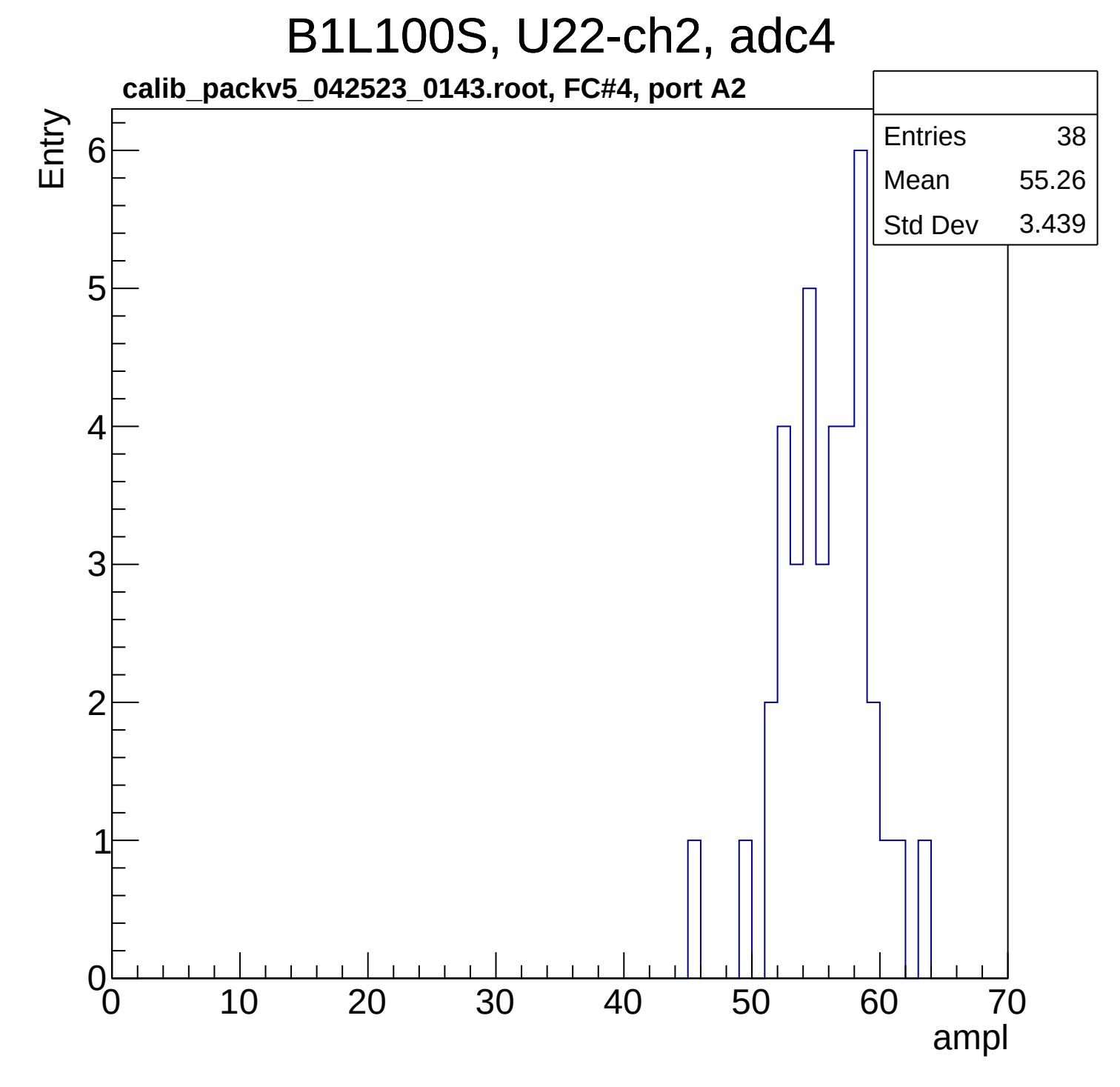
B1L100S, U22-ch2, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 55.26
Std Dev 3.439
Entry
ampl
0
1
2
3
4
5
6
0
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60
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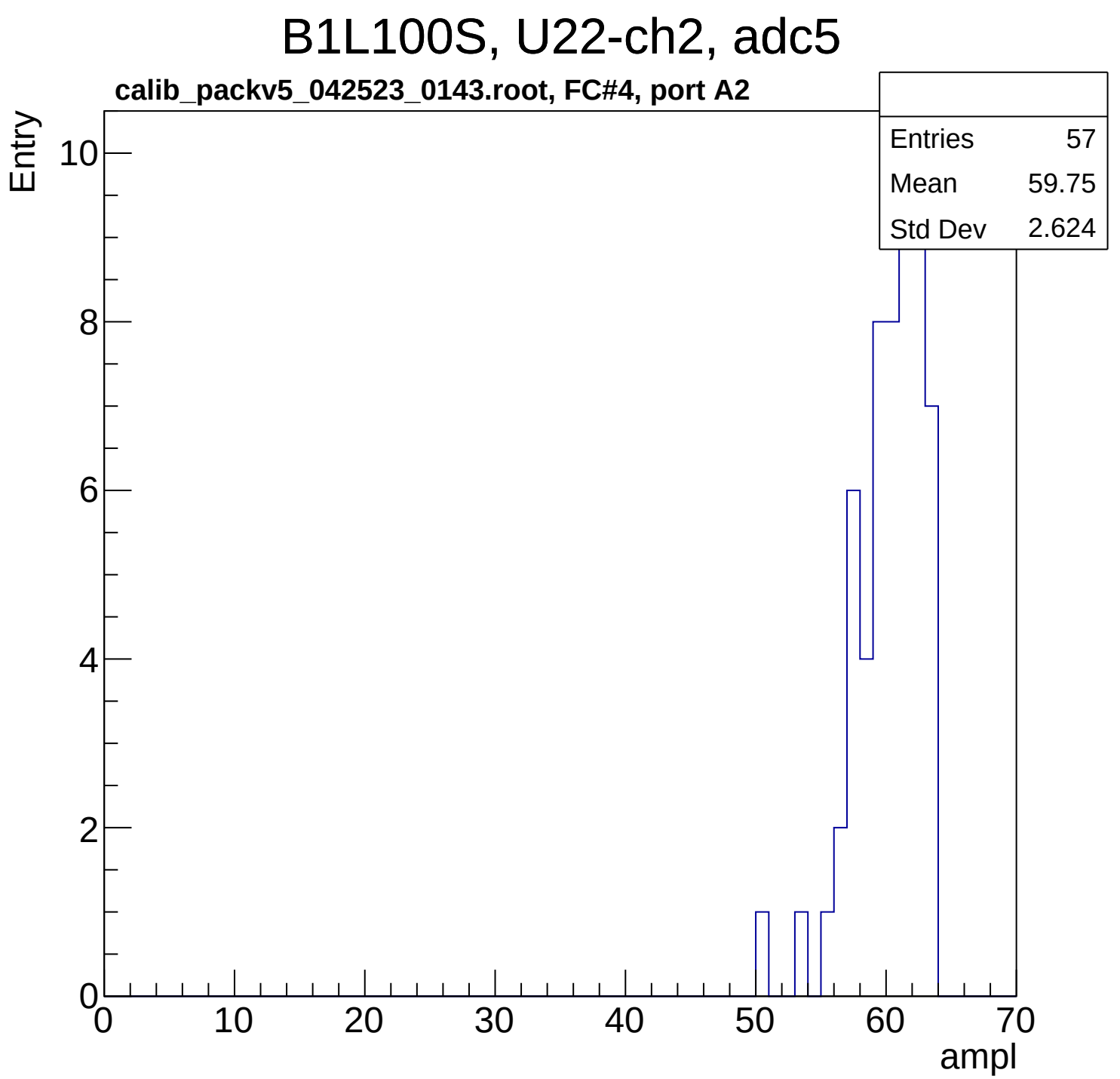
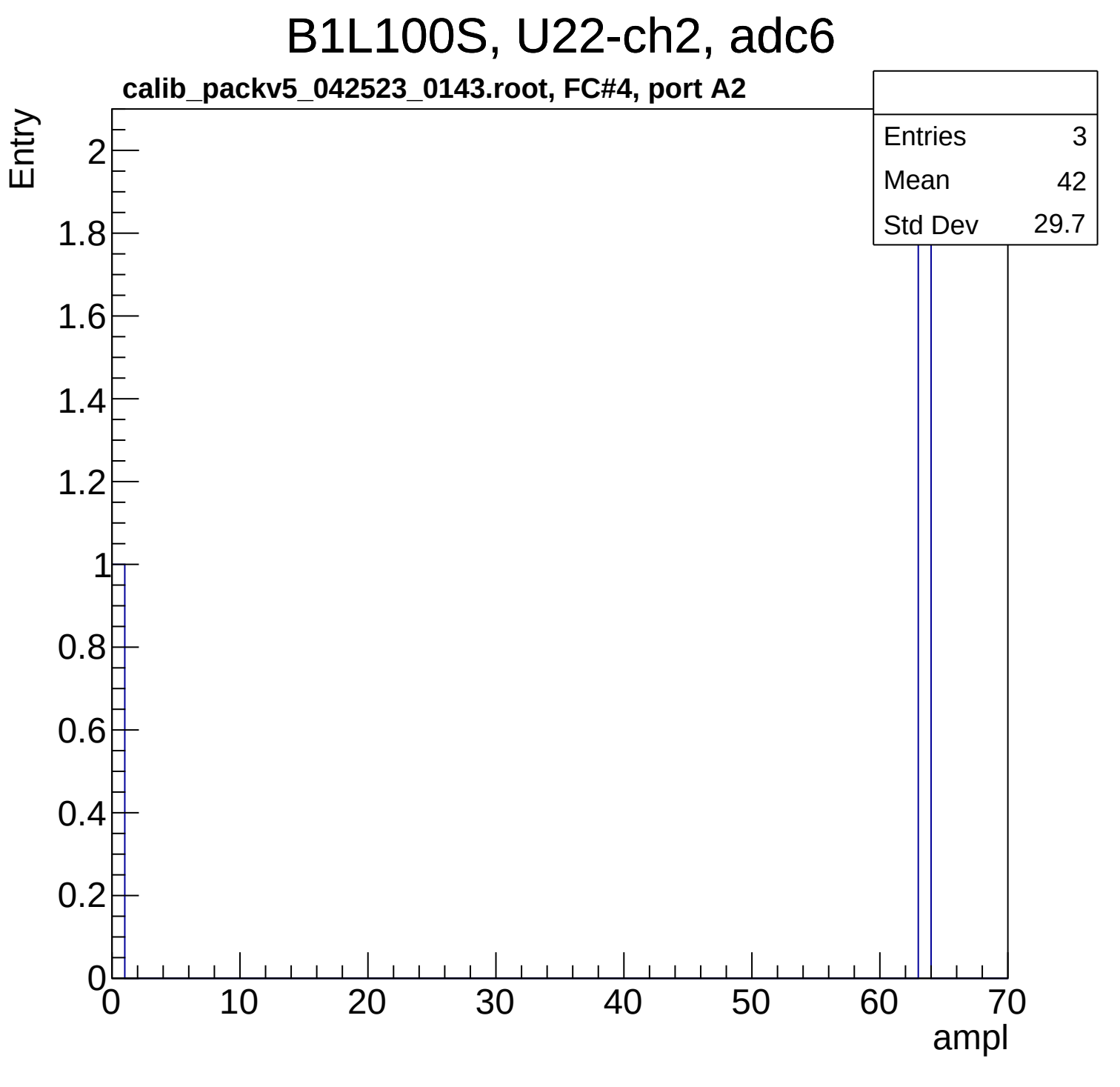
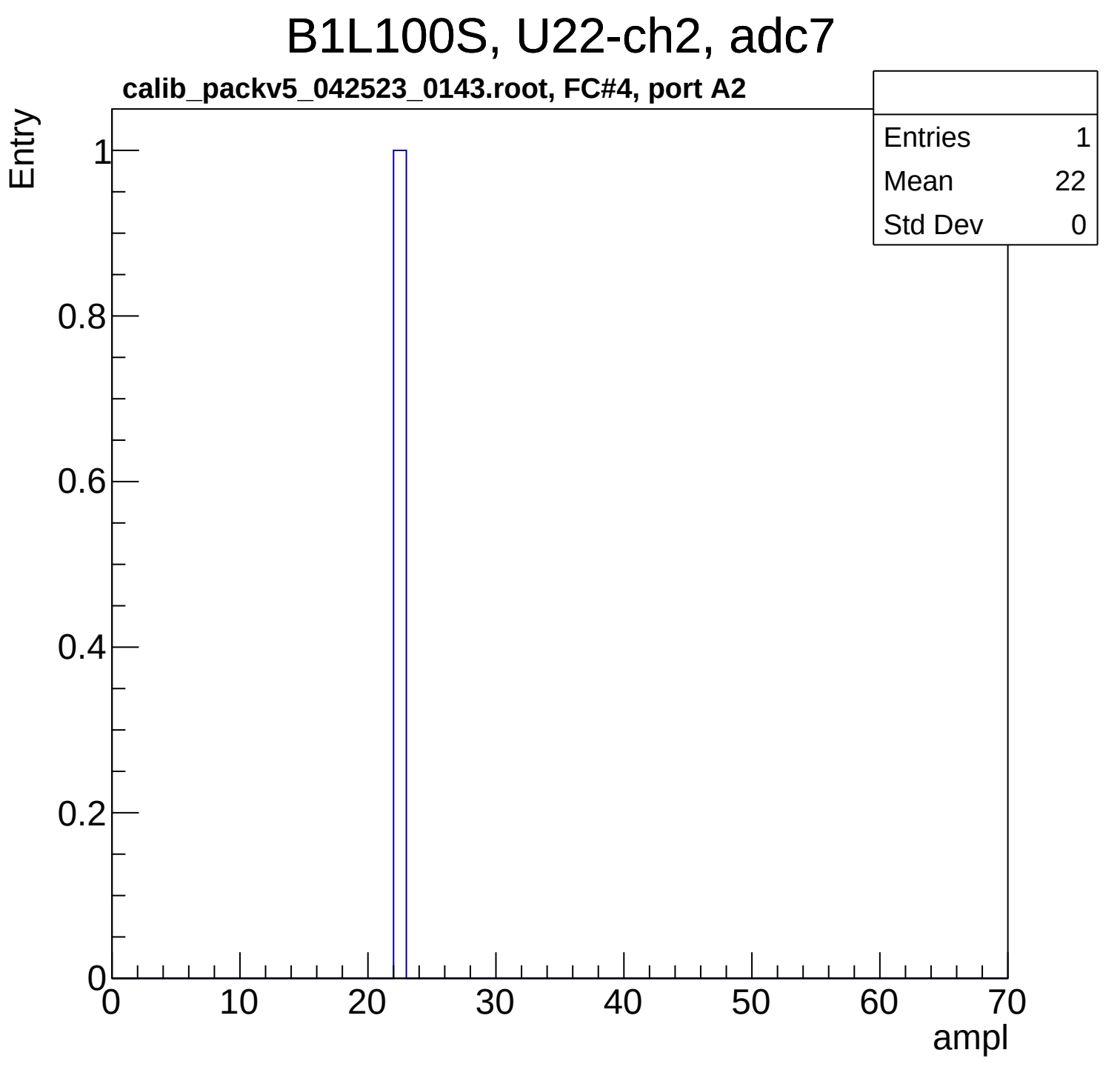

B1L100S, U22-ch2, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 59.75
Std Dev 2.624
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch2, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 42
Std Dev 29.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch2, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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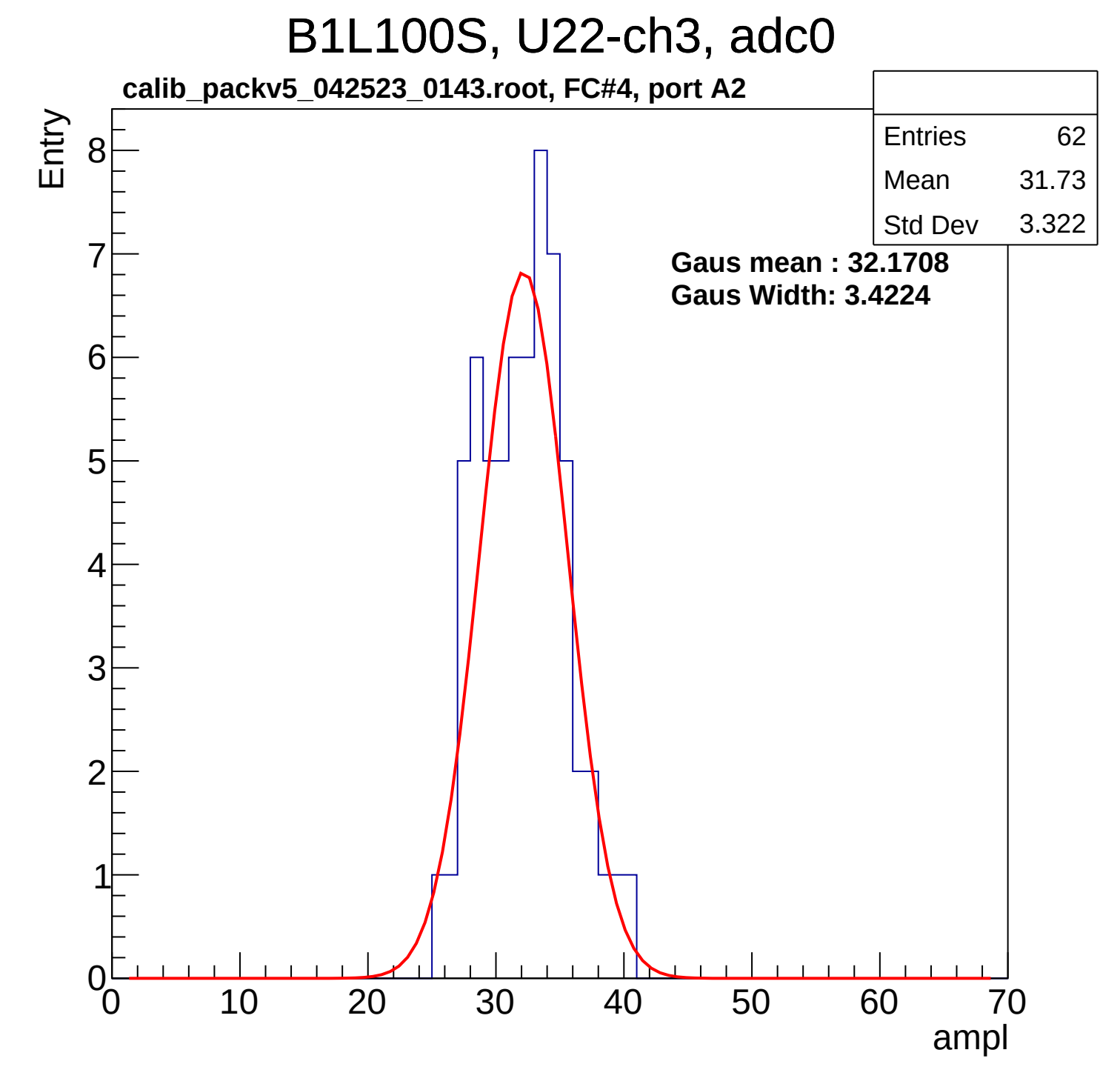
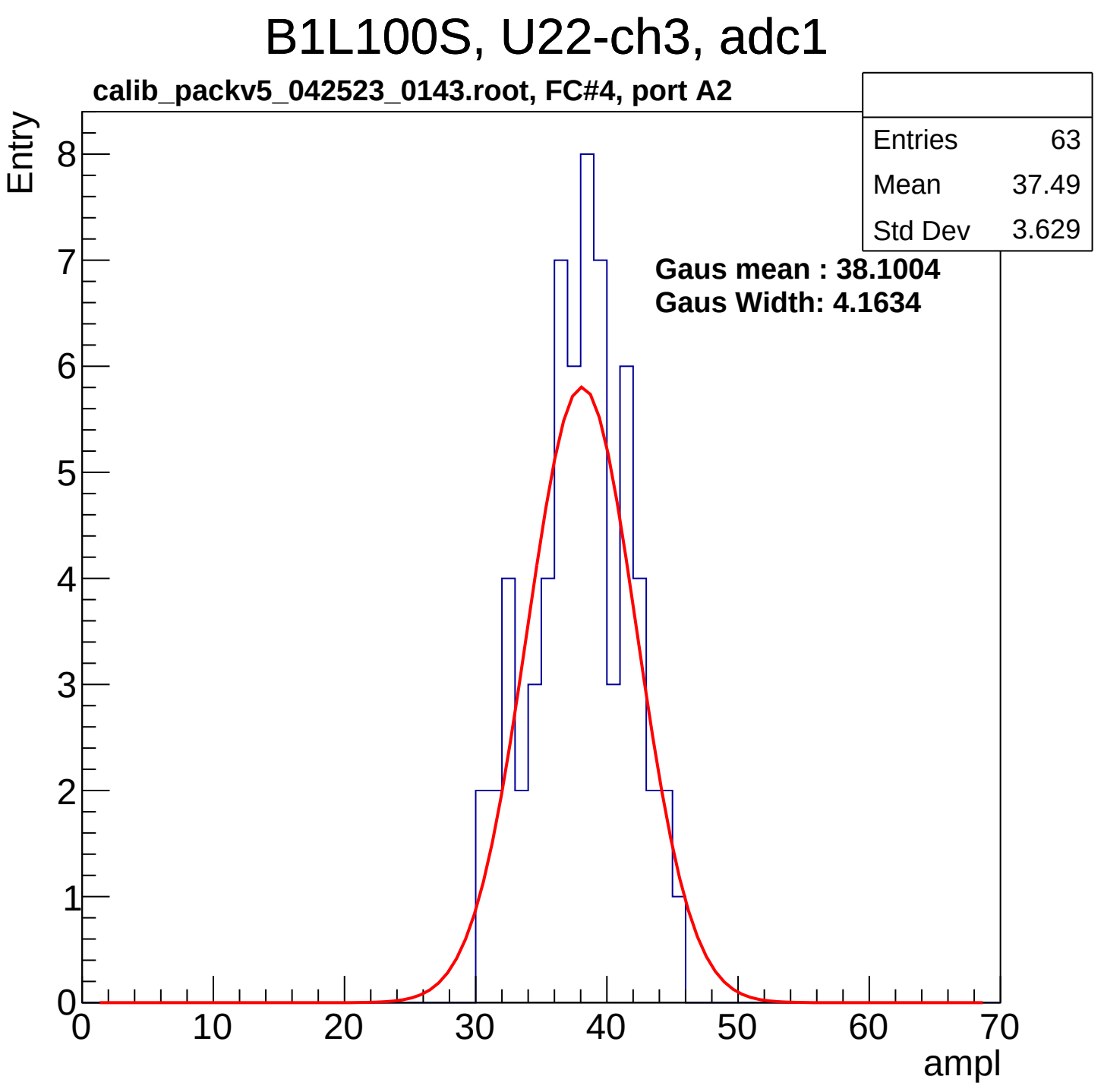
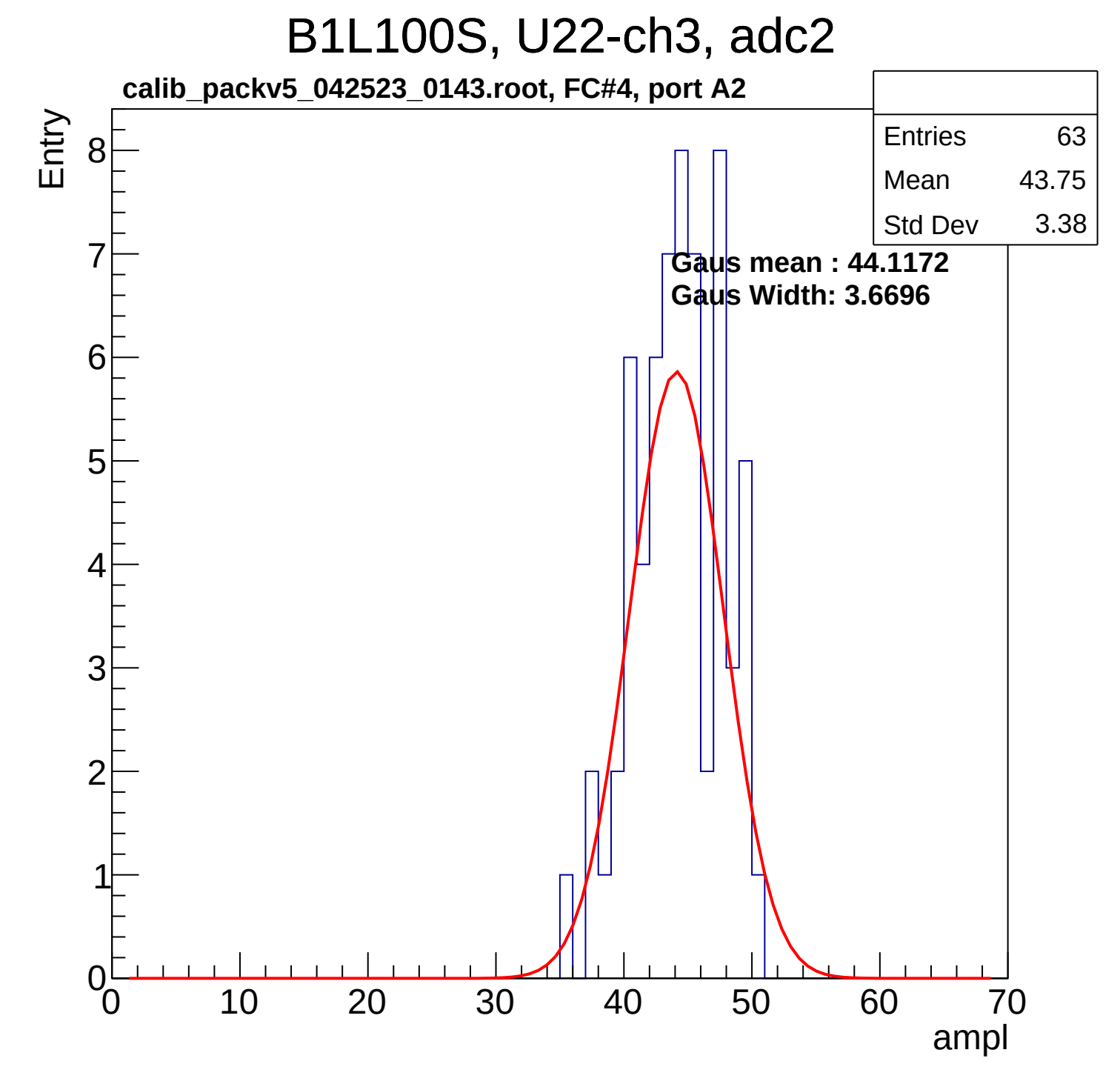
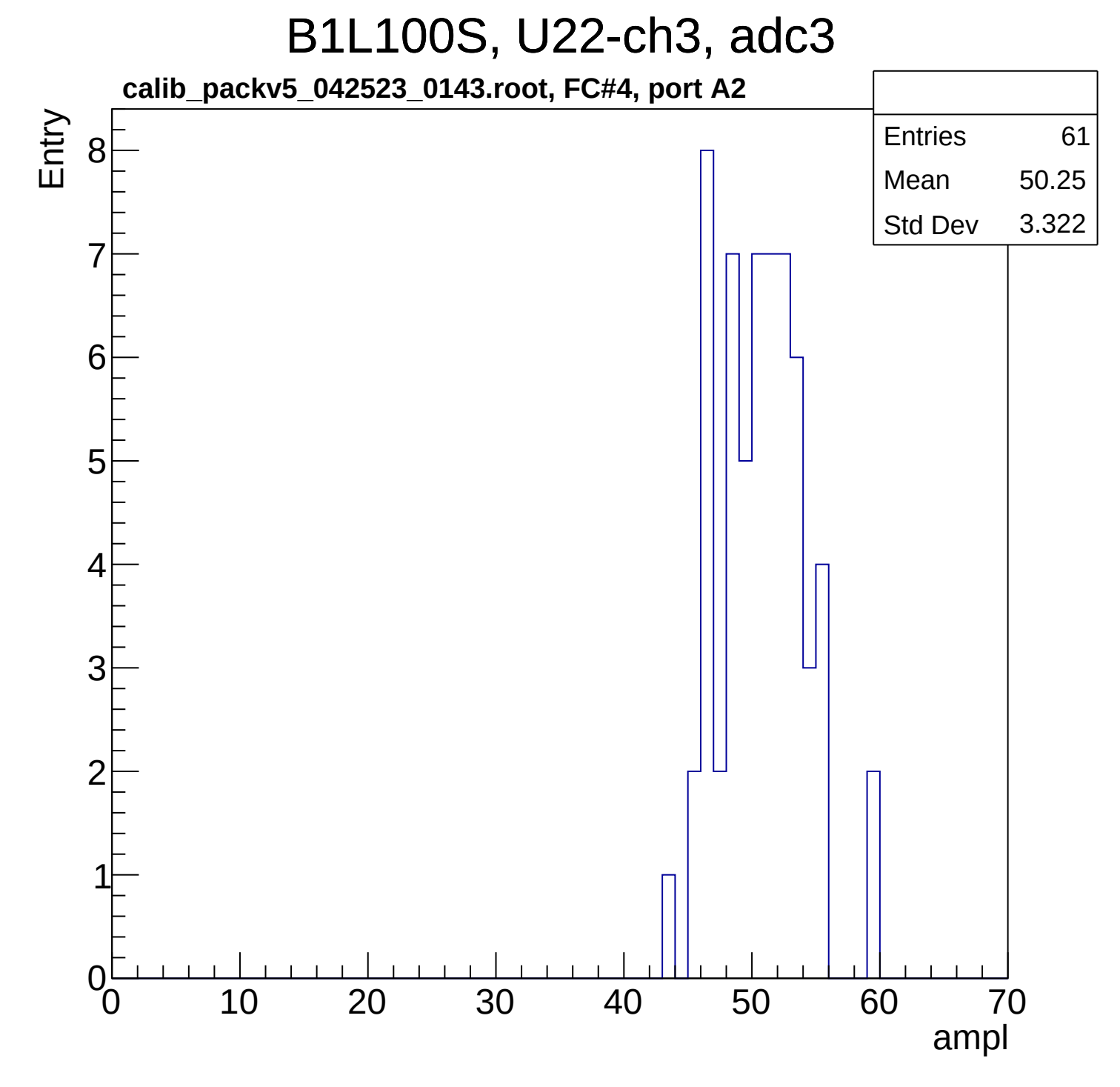
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch3, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 31.73
Std Dev 3.322
Gaus mean : 32.1708
Gaus Width: 3.4224
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch3, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 37.49
Std Dev 3.629
Gaus mean : 38.1004
Gaus Width: 4.1634
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch3, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.75
Std Dev 3.38
Gaus mean : 44.1172
Gaus Width: 3.6696
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch3, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.25
Std Dev 3.322
Entry
ampl
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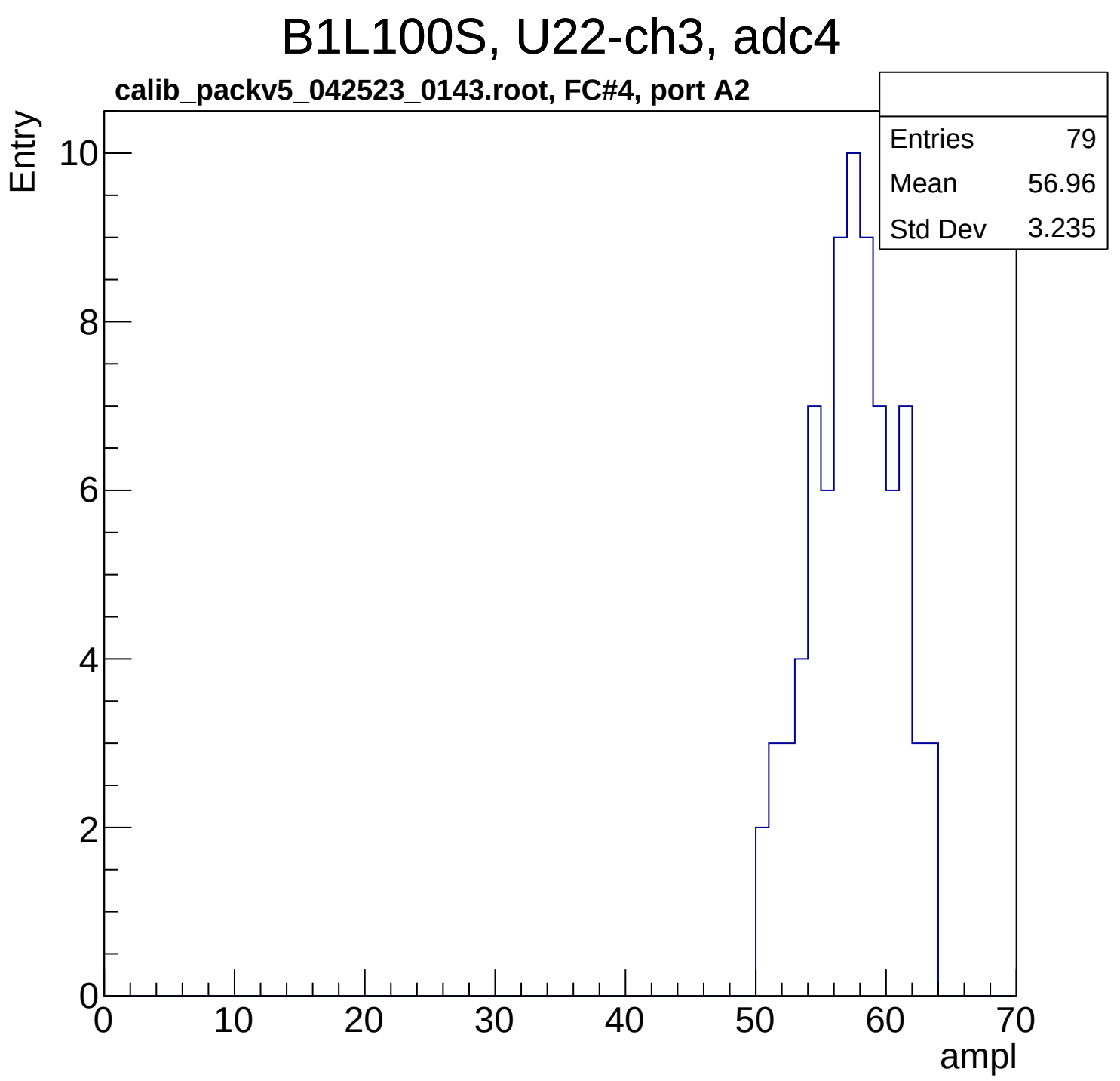
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch3, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 56.96
Std Dev 3.235
Entry
ampl
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2
4
6
8
10
0
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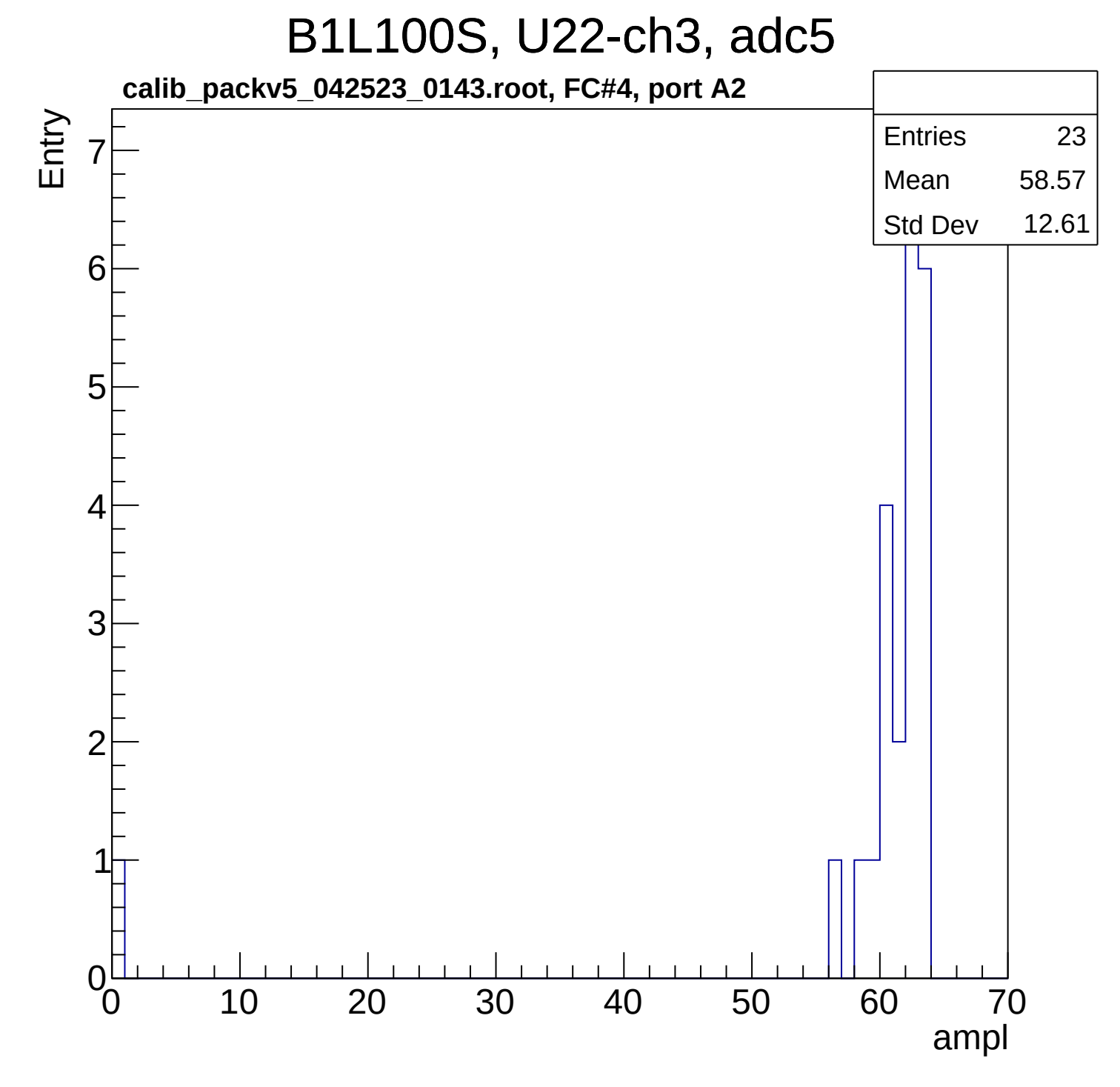
40
50
60
70


B1L100S, U22-ch3, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 58.57
Std Dev 12.61
Entry
ampl
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3
4
5
6
7
0
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40
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60
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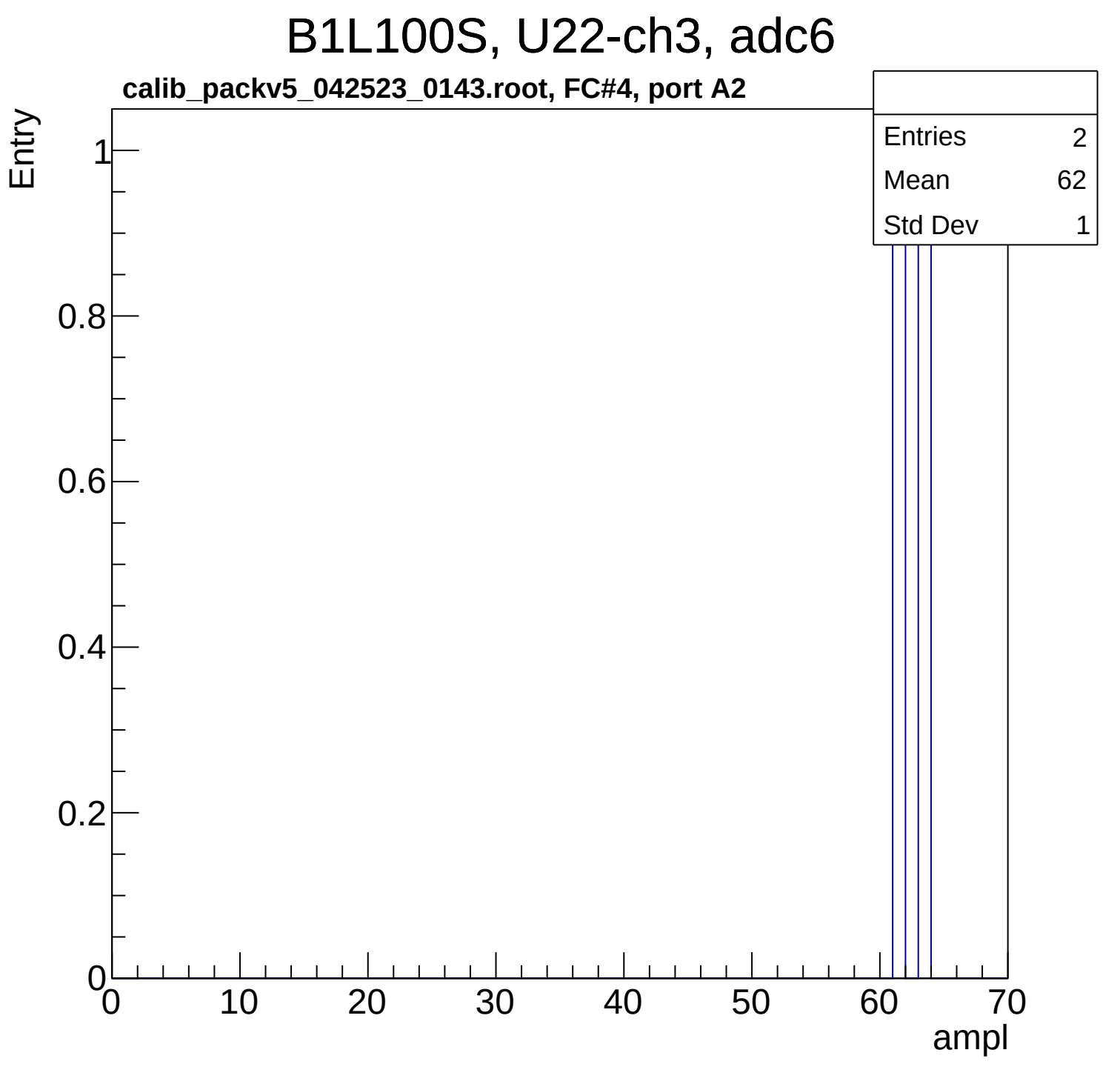
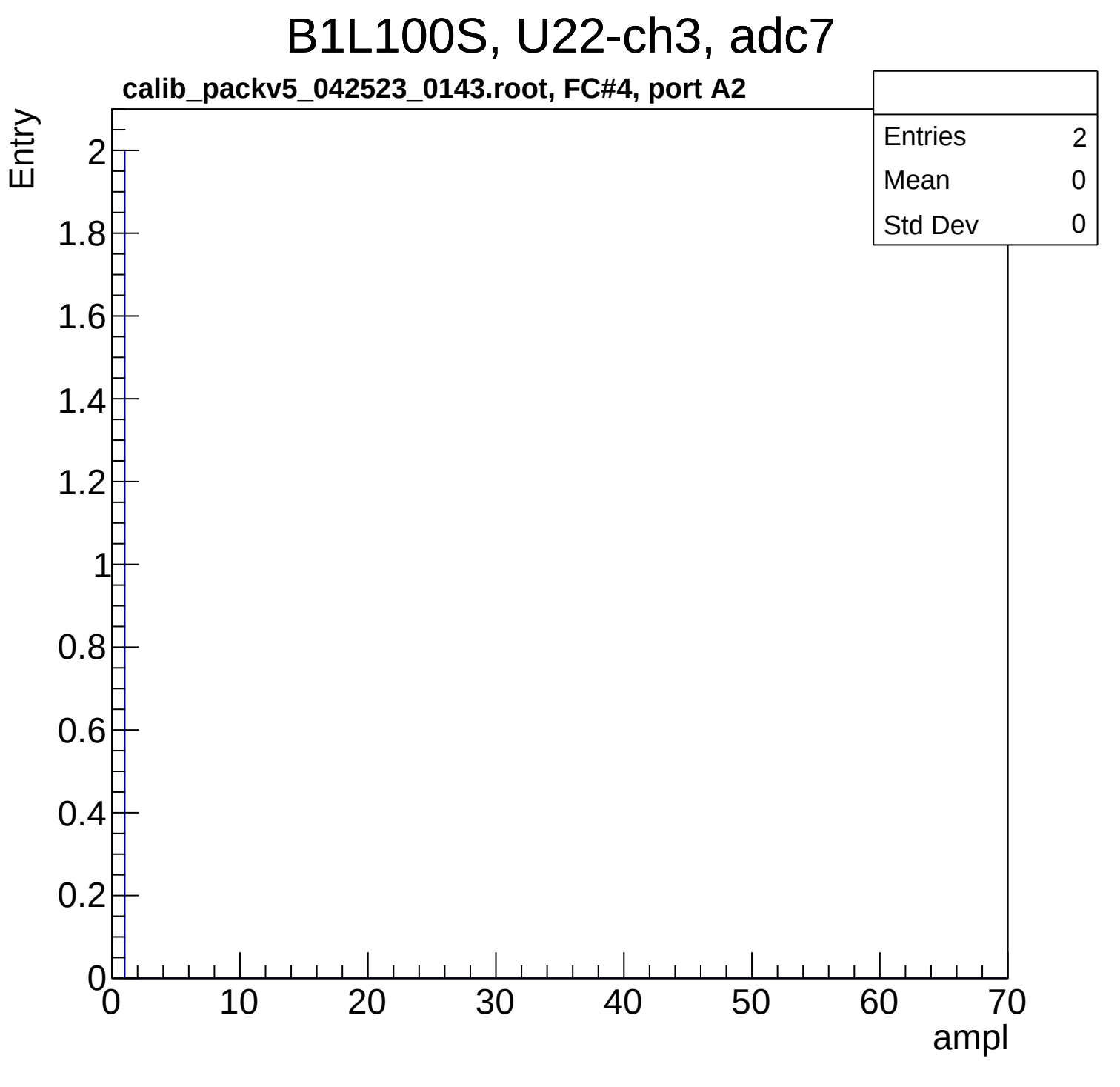

B1L100S, U22-ch3, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch3, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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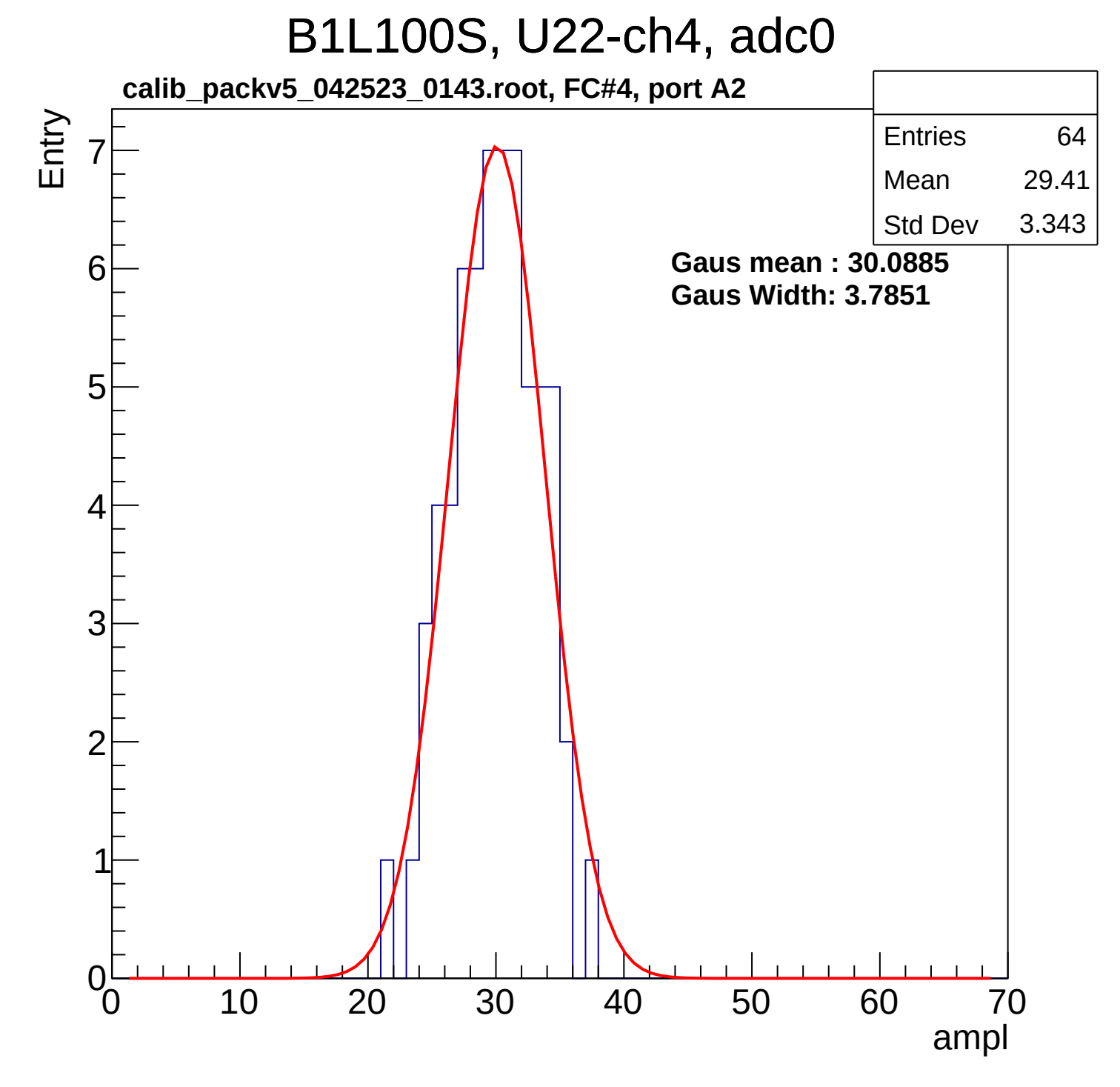
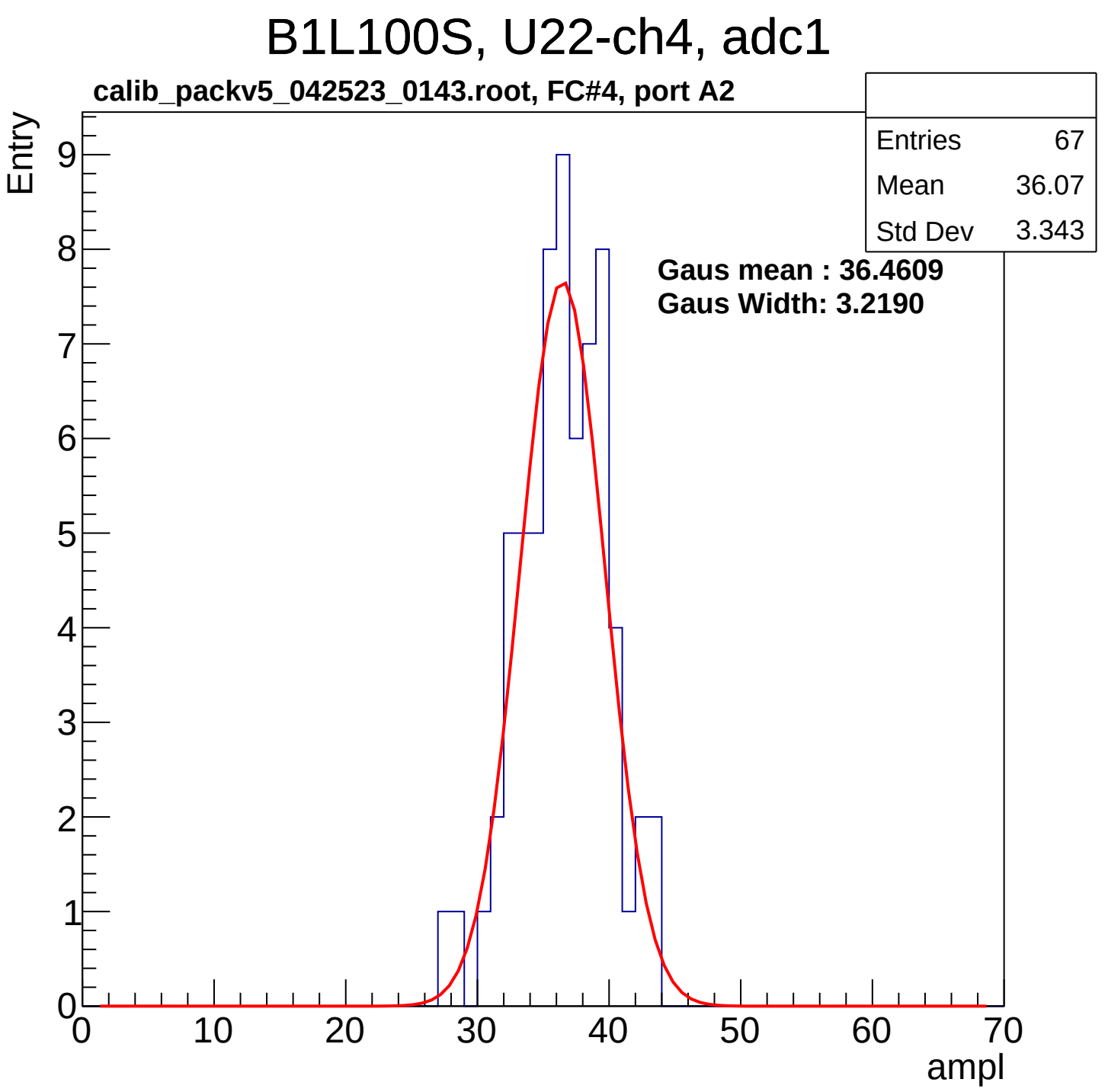
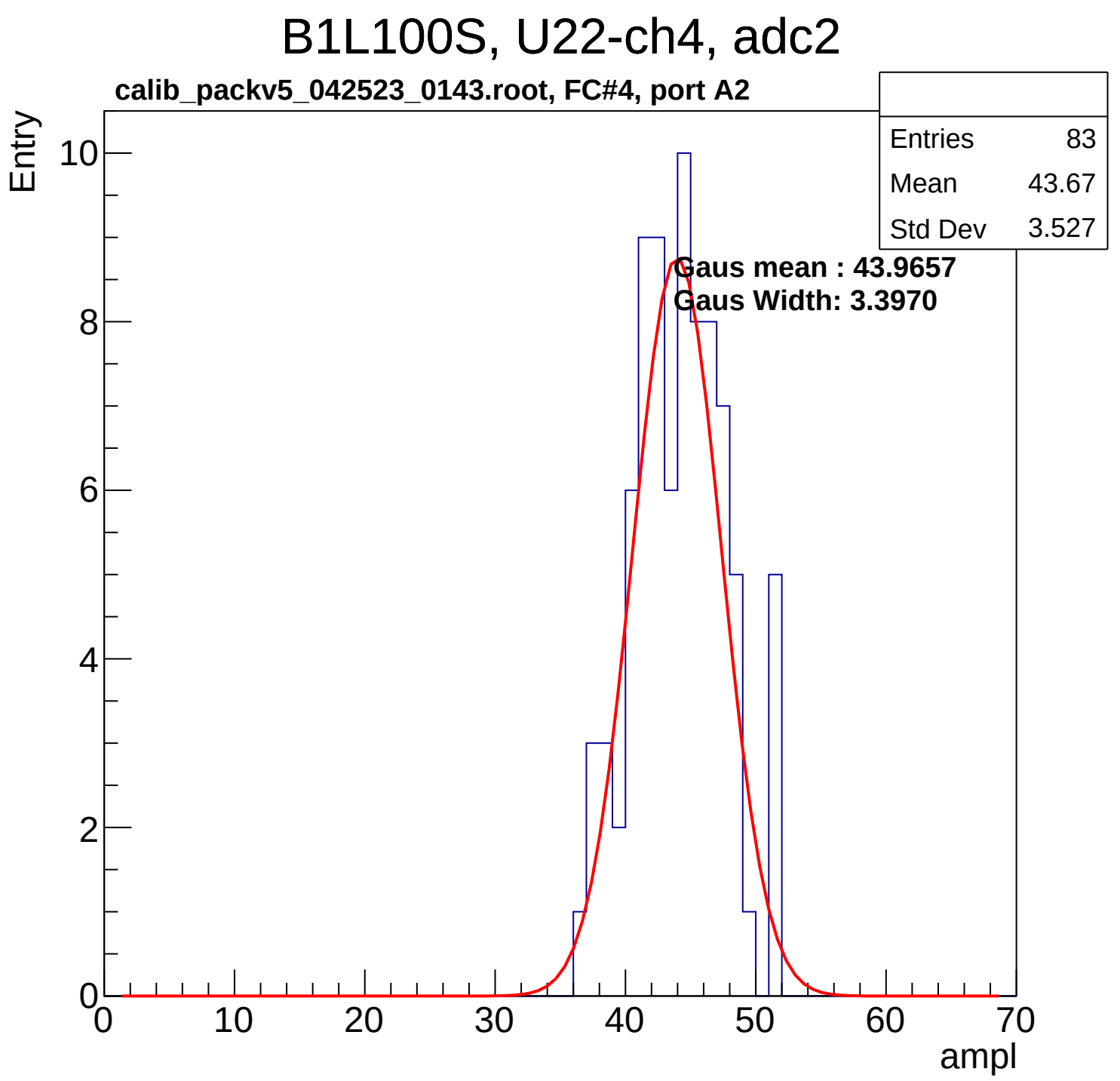
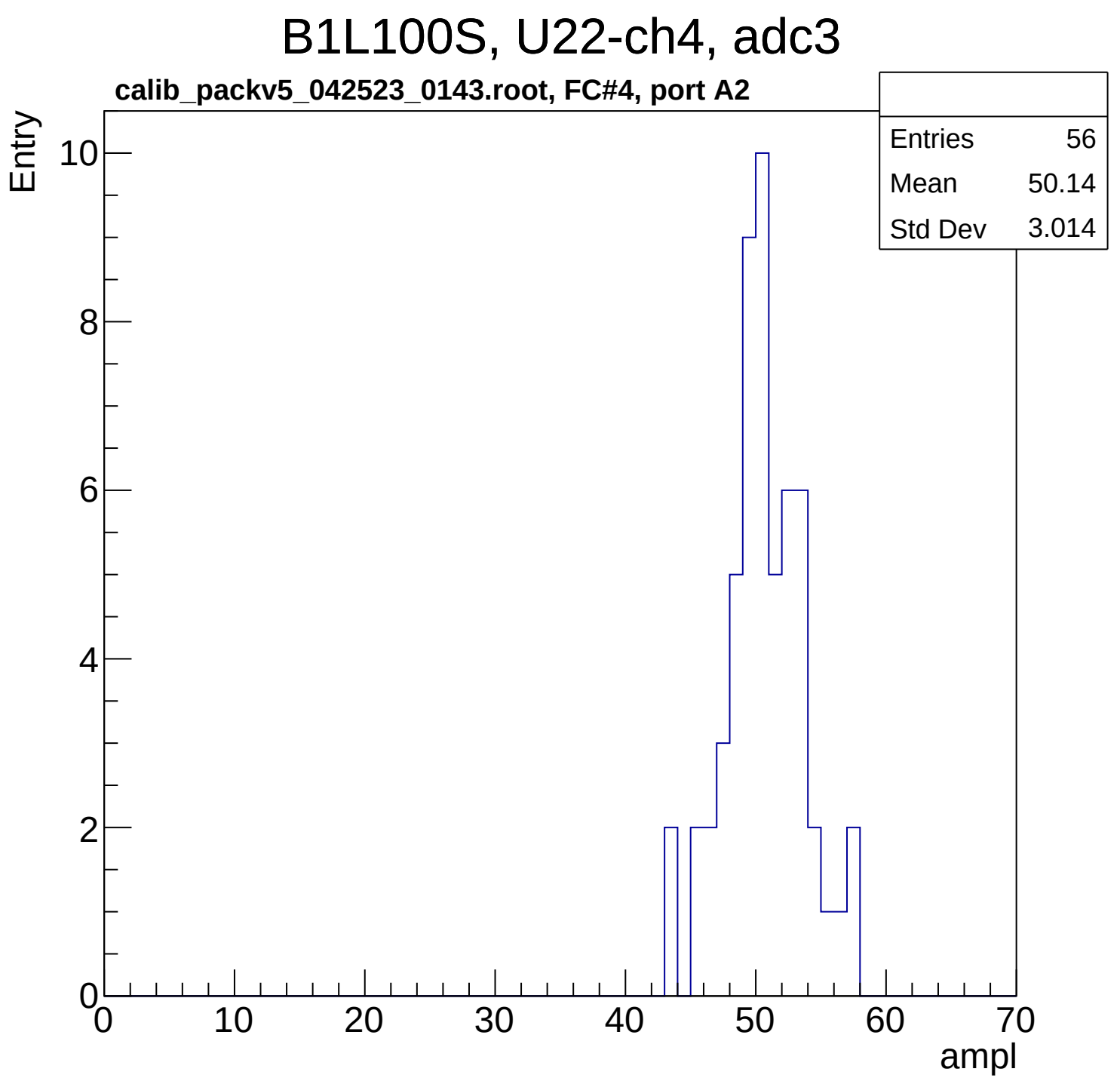
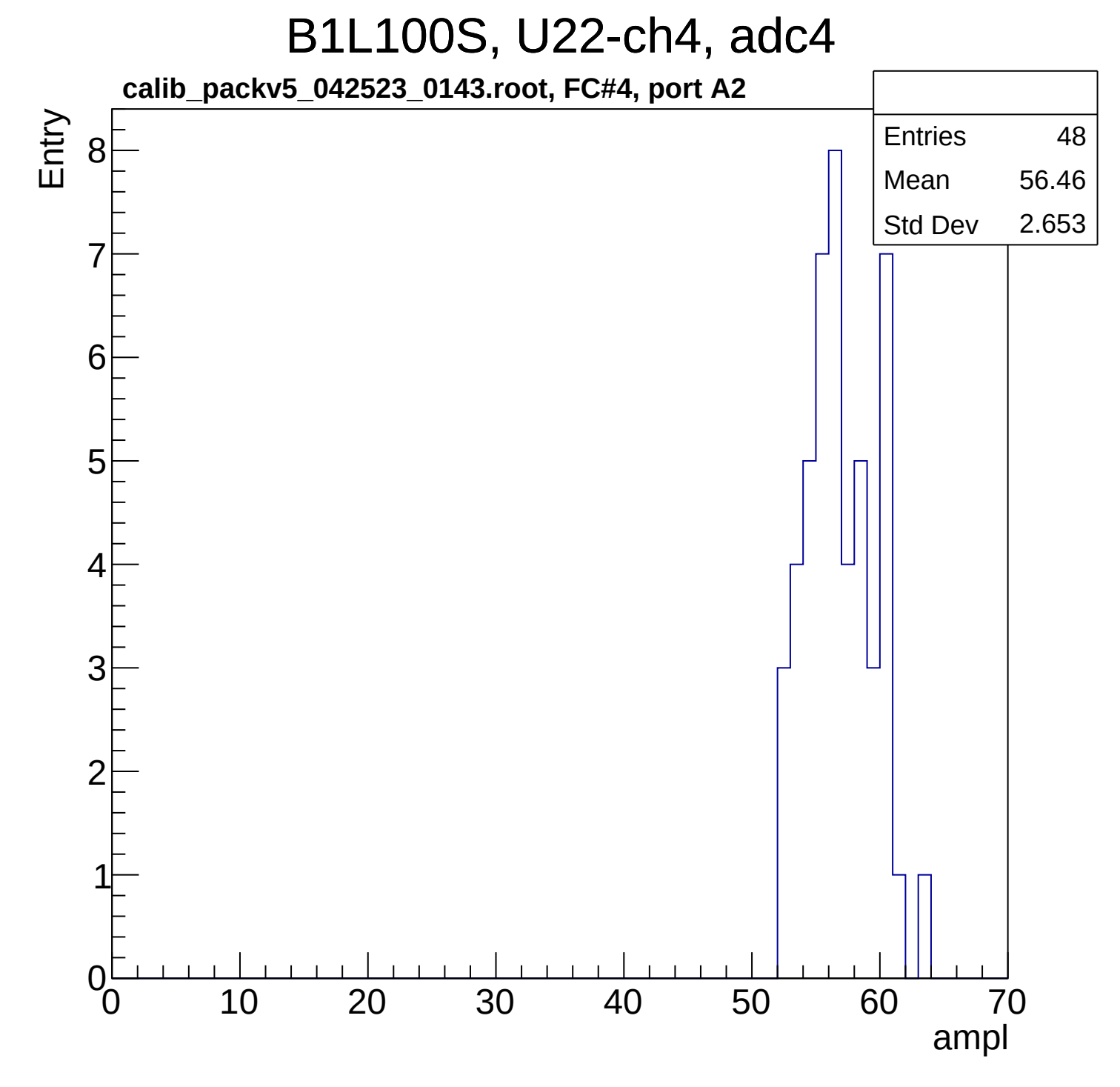
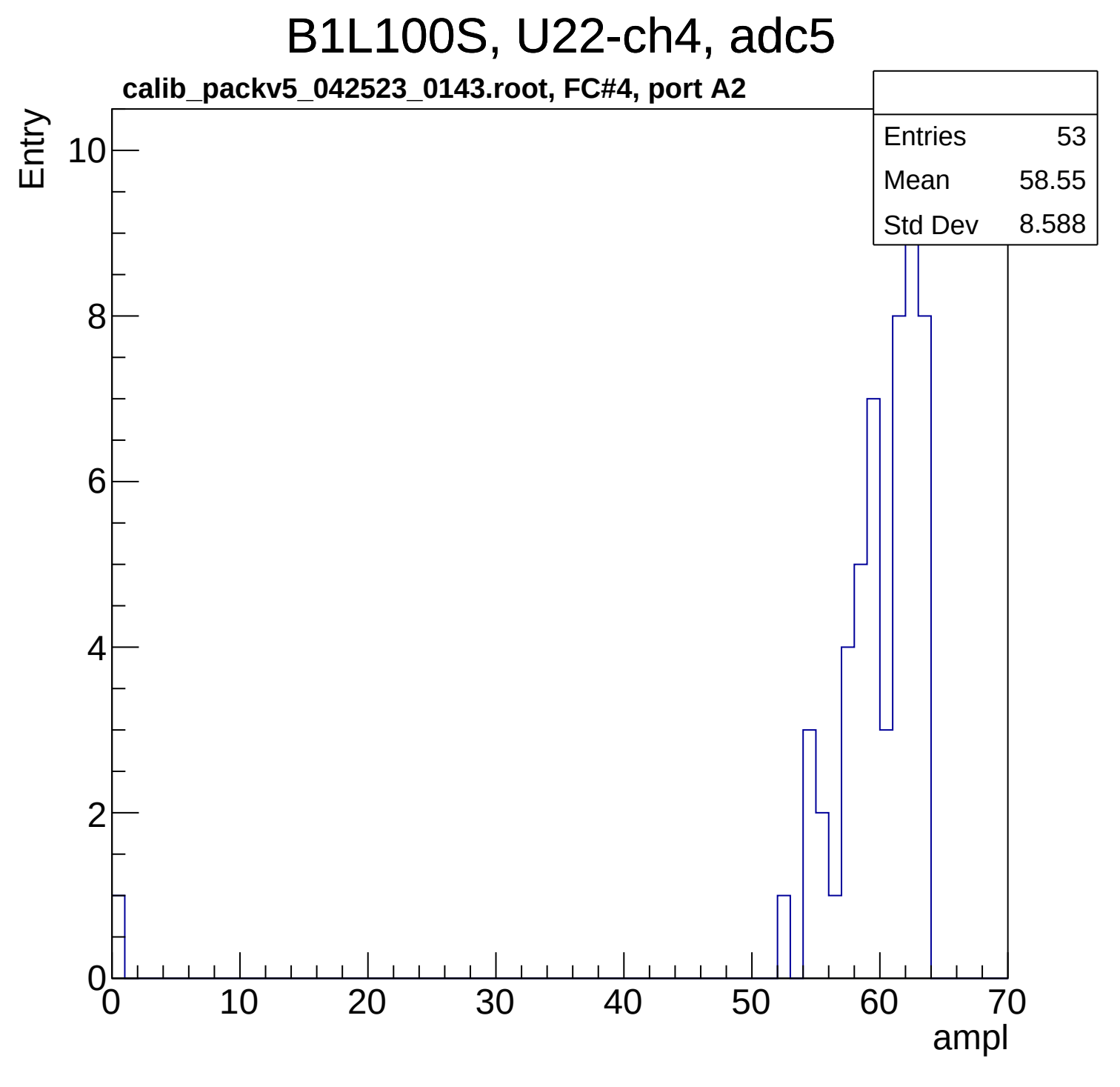

B1L100S, U22-ch4, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.41
Std Dev 3.343
Gaus mean : 30.0885
Gaus Width: 3.7851
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

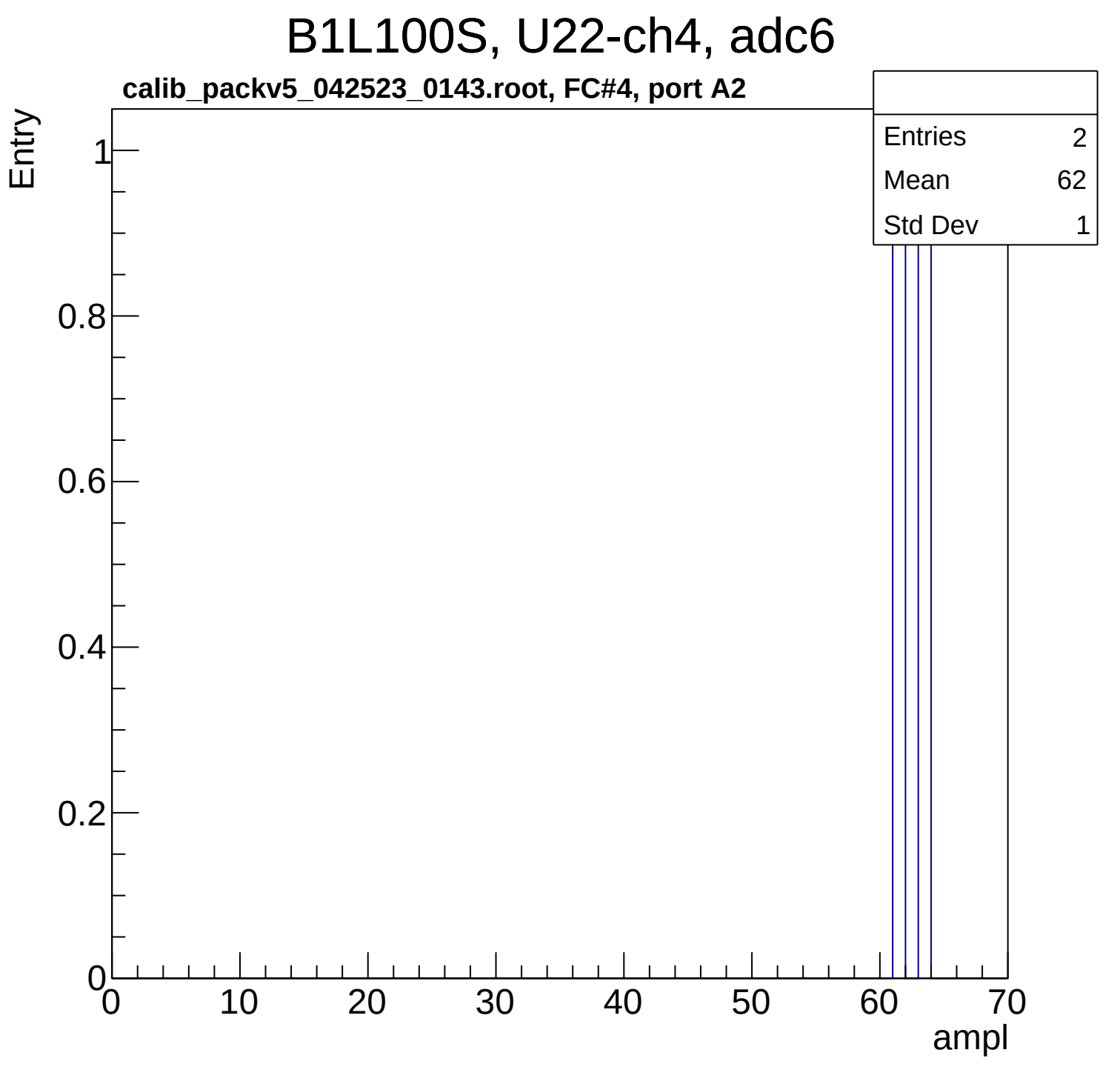

B1L100S, U22-ch4, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.07
Std Dev 3.343
Gaus mean : 36.4609
Gaus Width: 3.2190
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

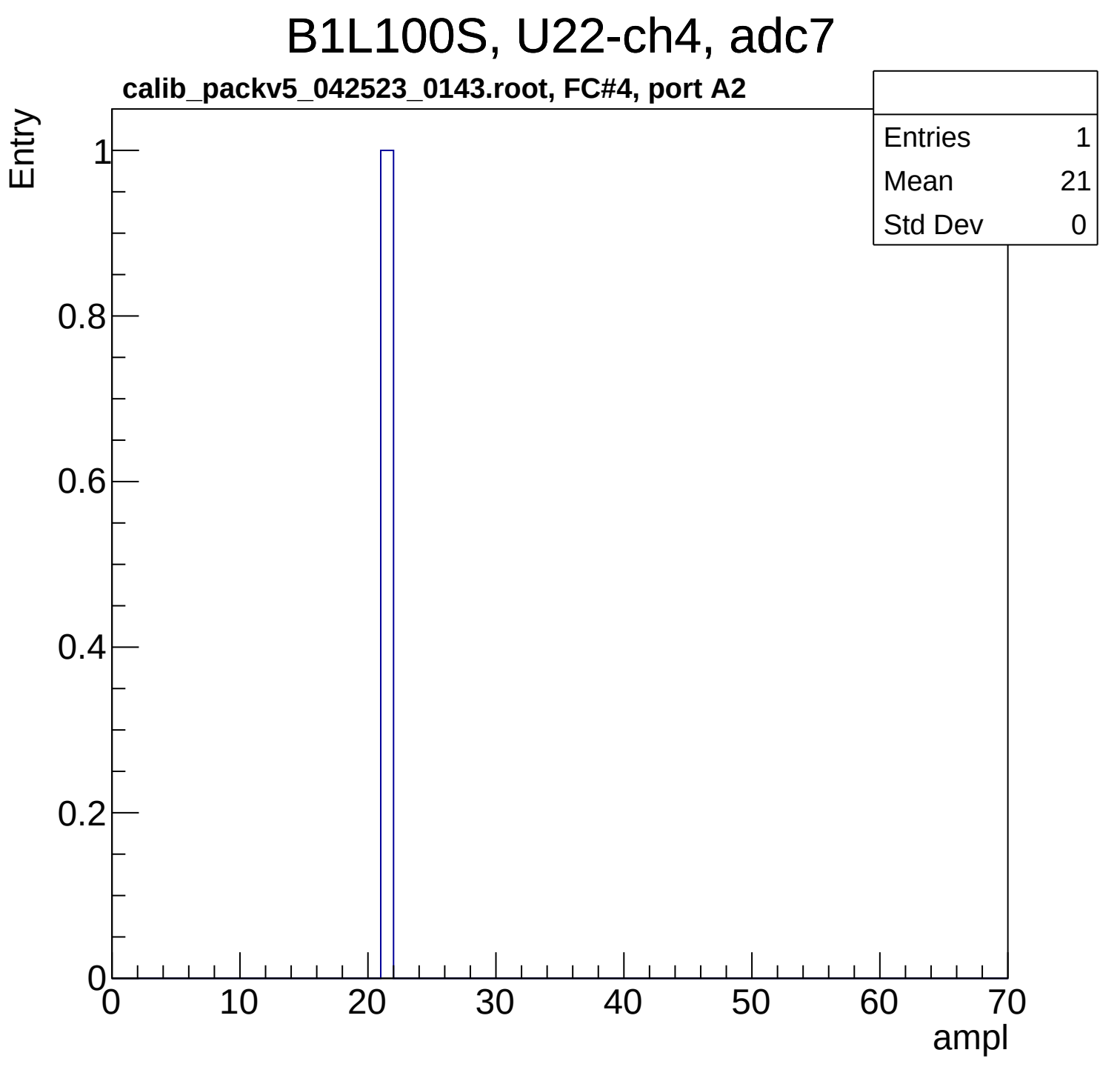

B1L100S, U22-ch4, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 43.67
Std Dev 3.527
Gaus mean : 43.9657
Gaus Width: 3.3970
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

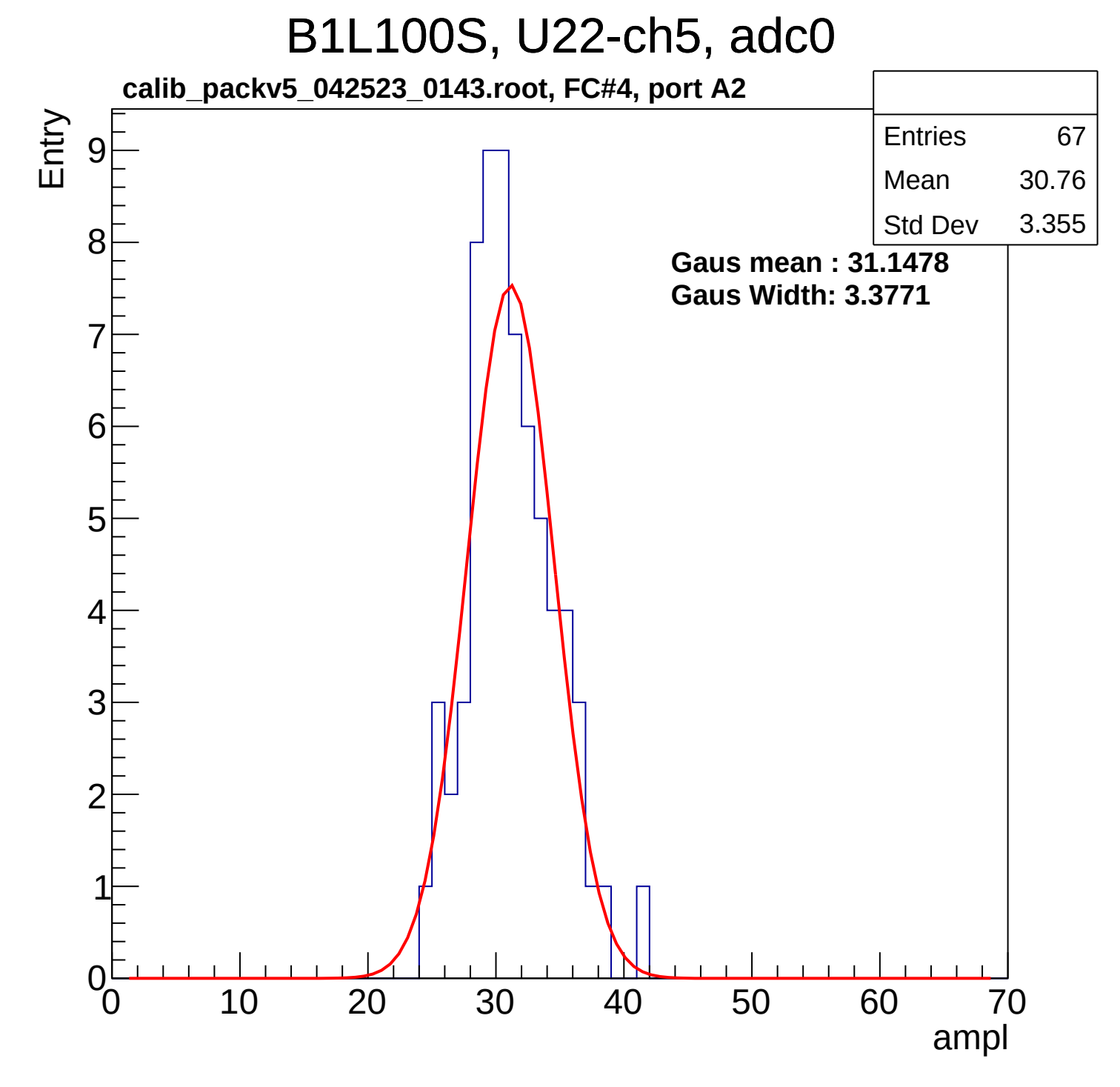

B1L100S, U22-ch4, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.14
Std Dev 3.014
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

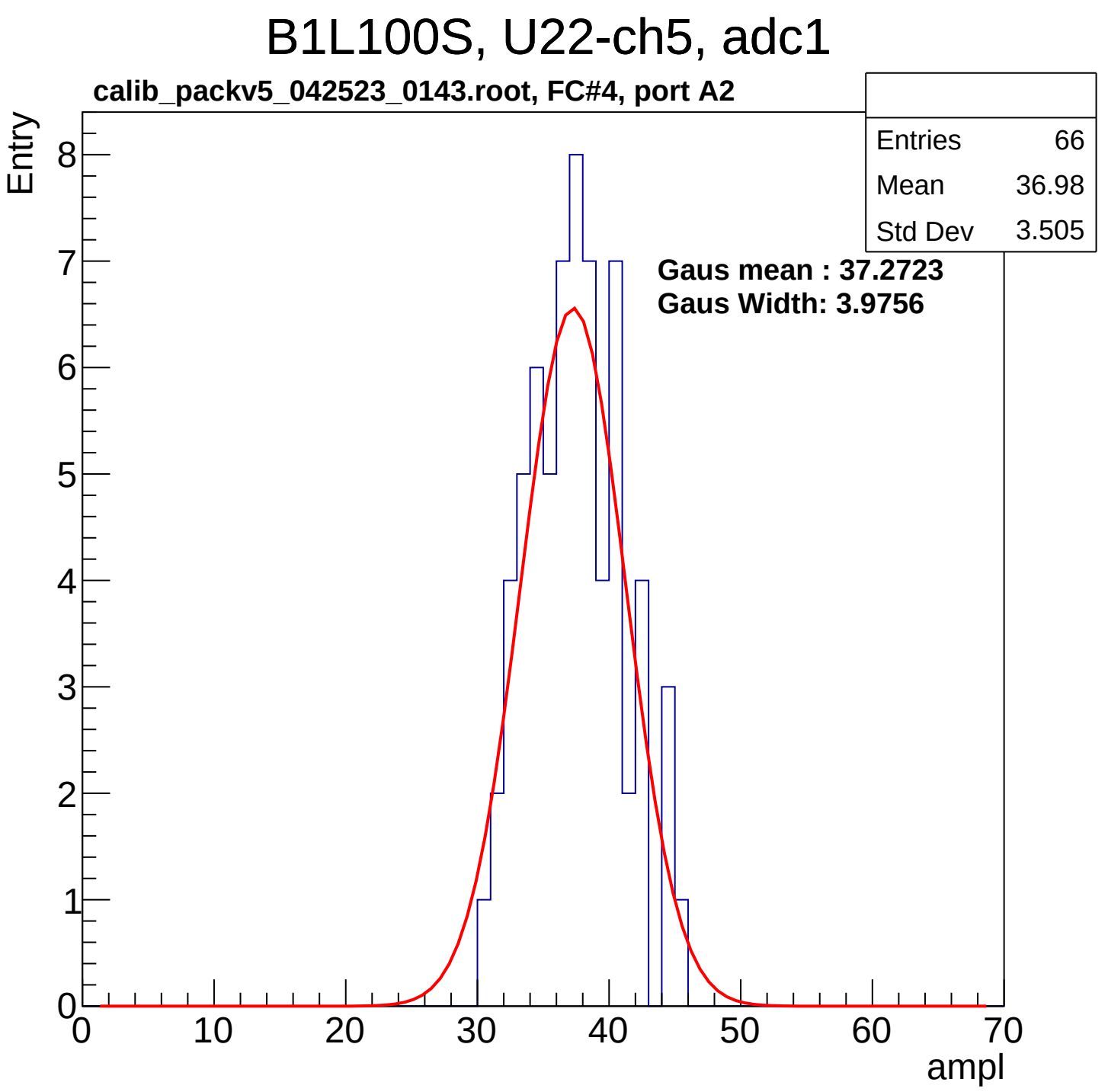

B1L100S, U22-ch4, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.46
Std Dev 2.653
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

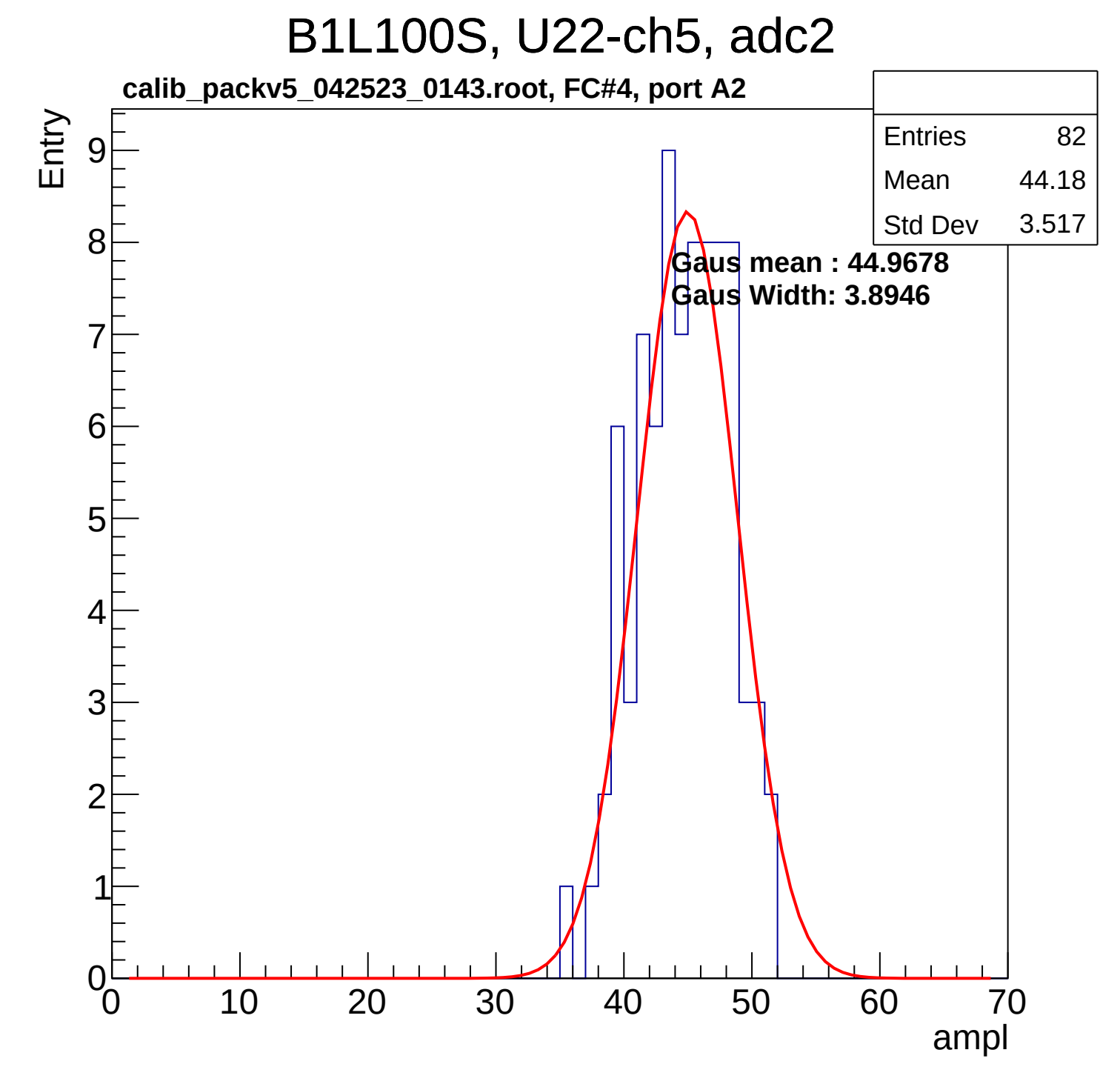

B1L100S, U22-ch4, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.55
Std Dev 8.588
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

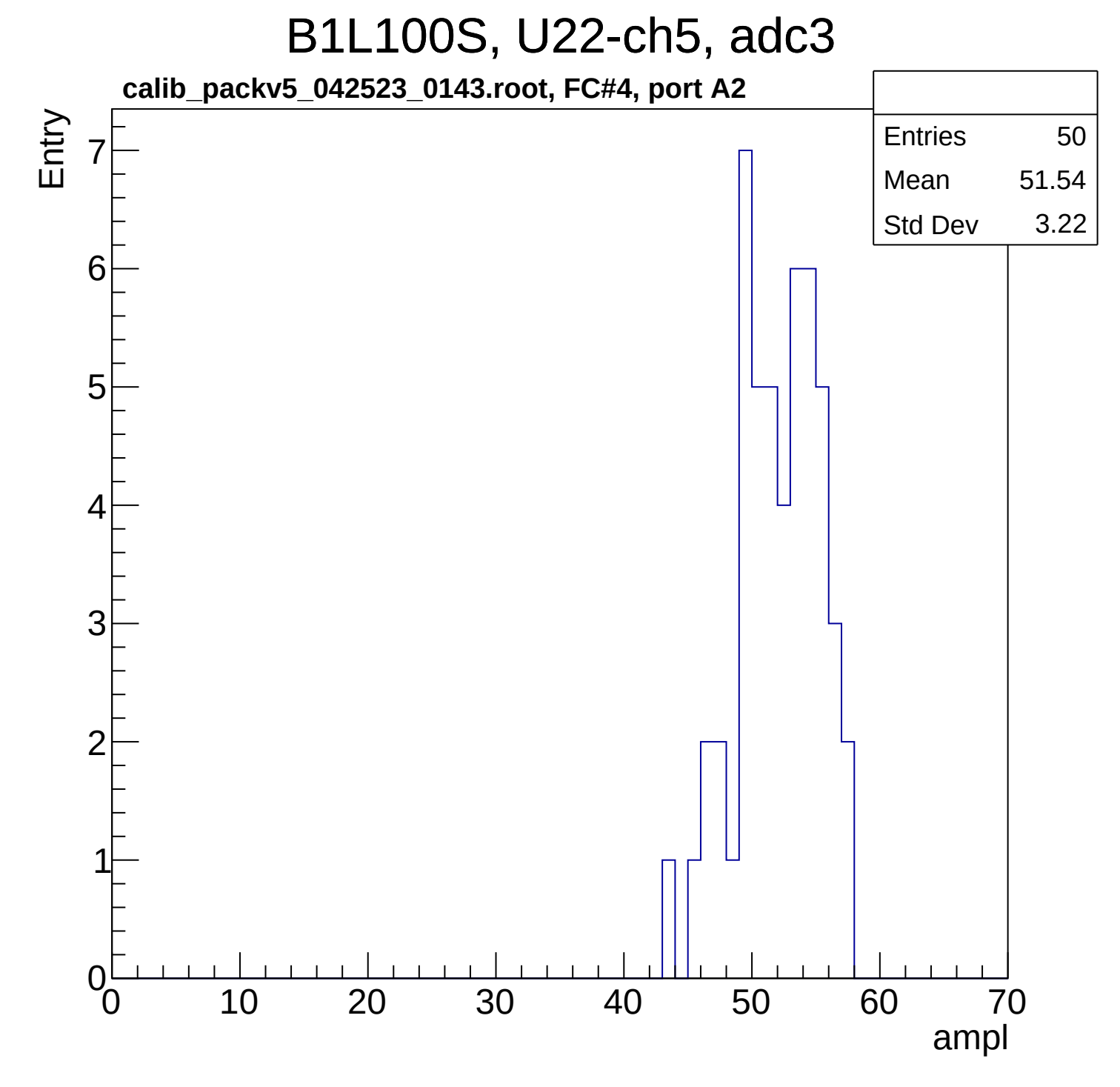

B1L100S, U22-ch4, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

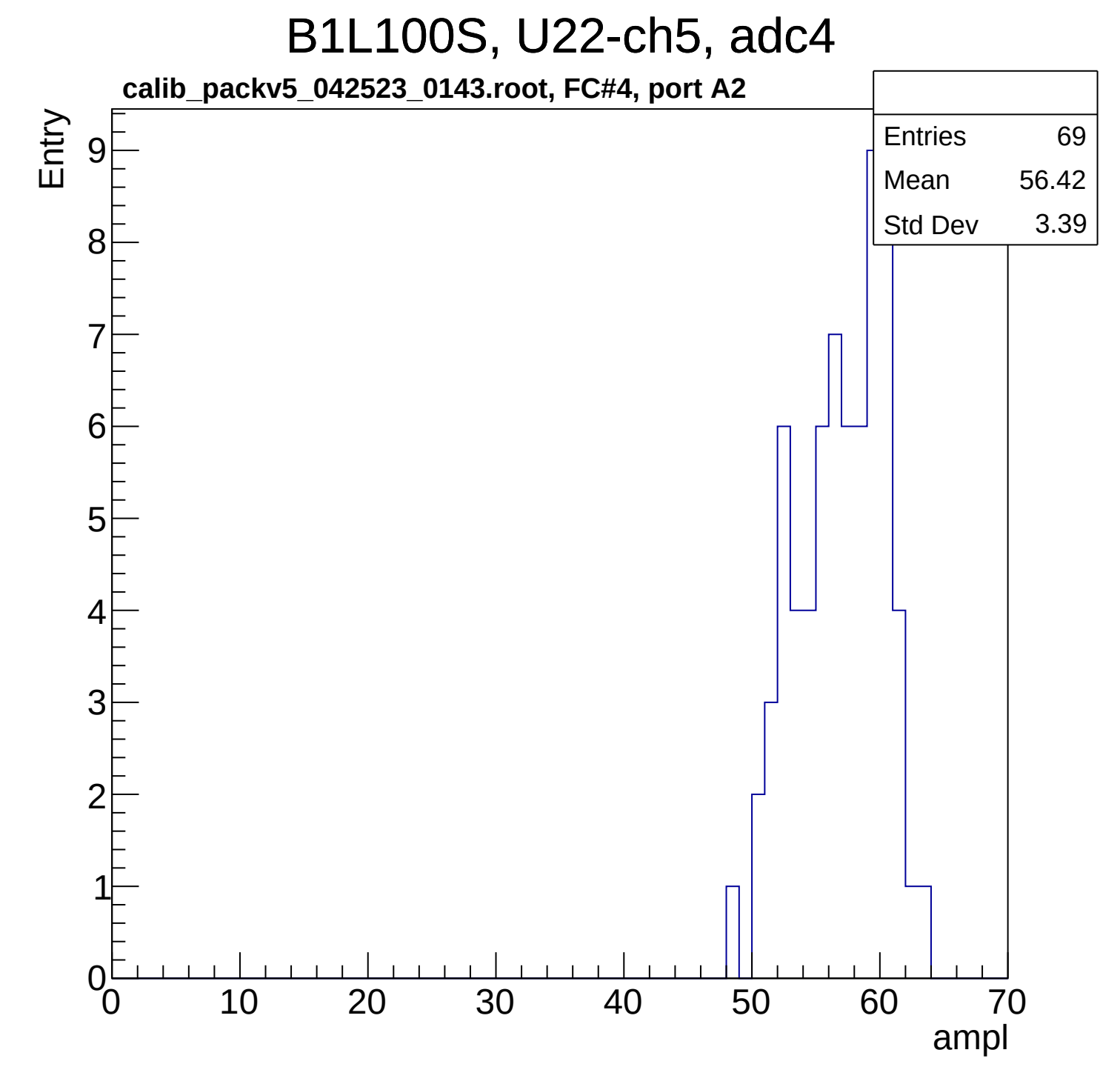

B1L100S, U22-ch4, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

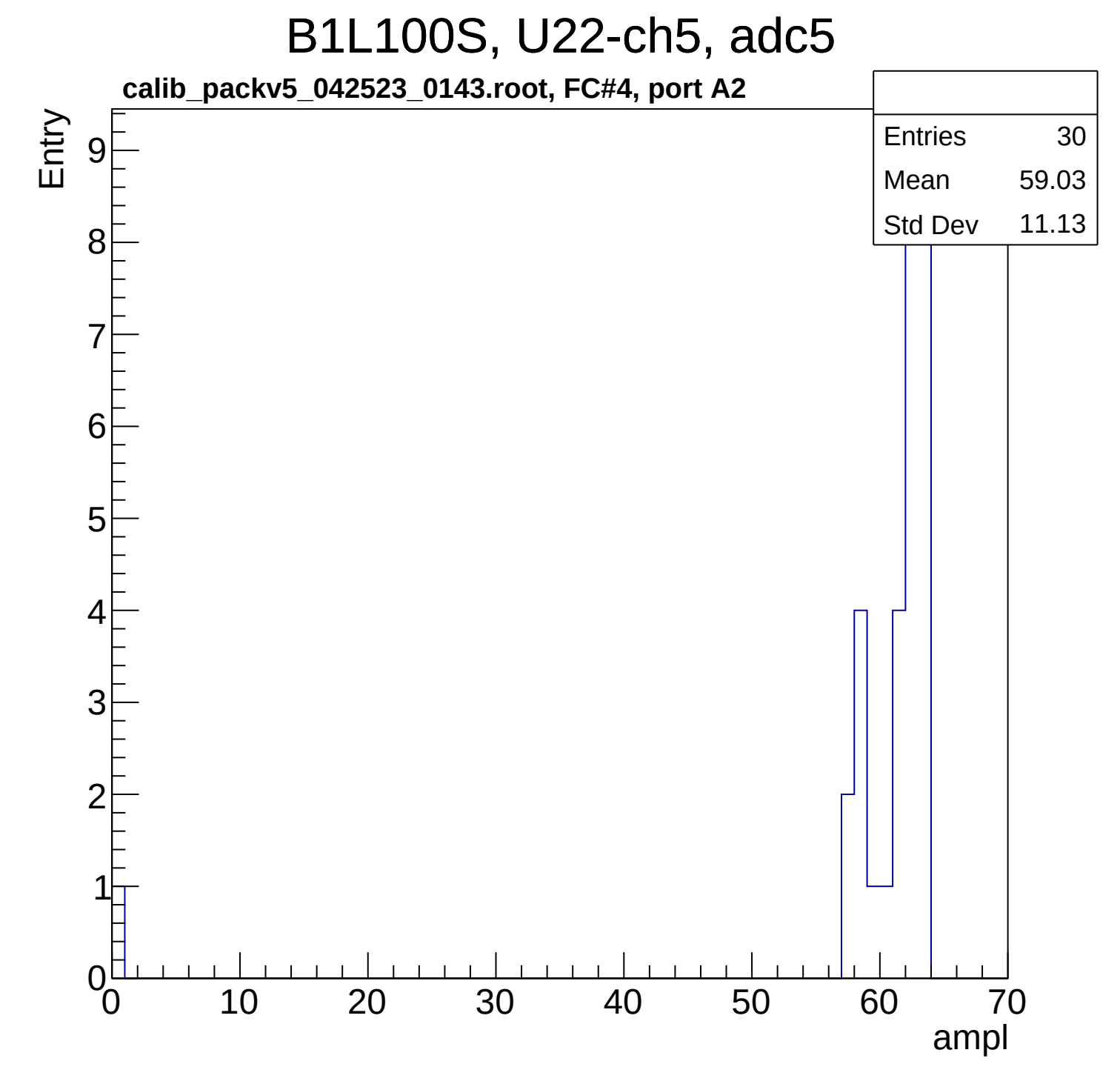

B1L100S, U22-ch5, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.76
Std Dev 3.355
Gaus mean : 31.1478
Gaus Width: 3.3771
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

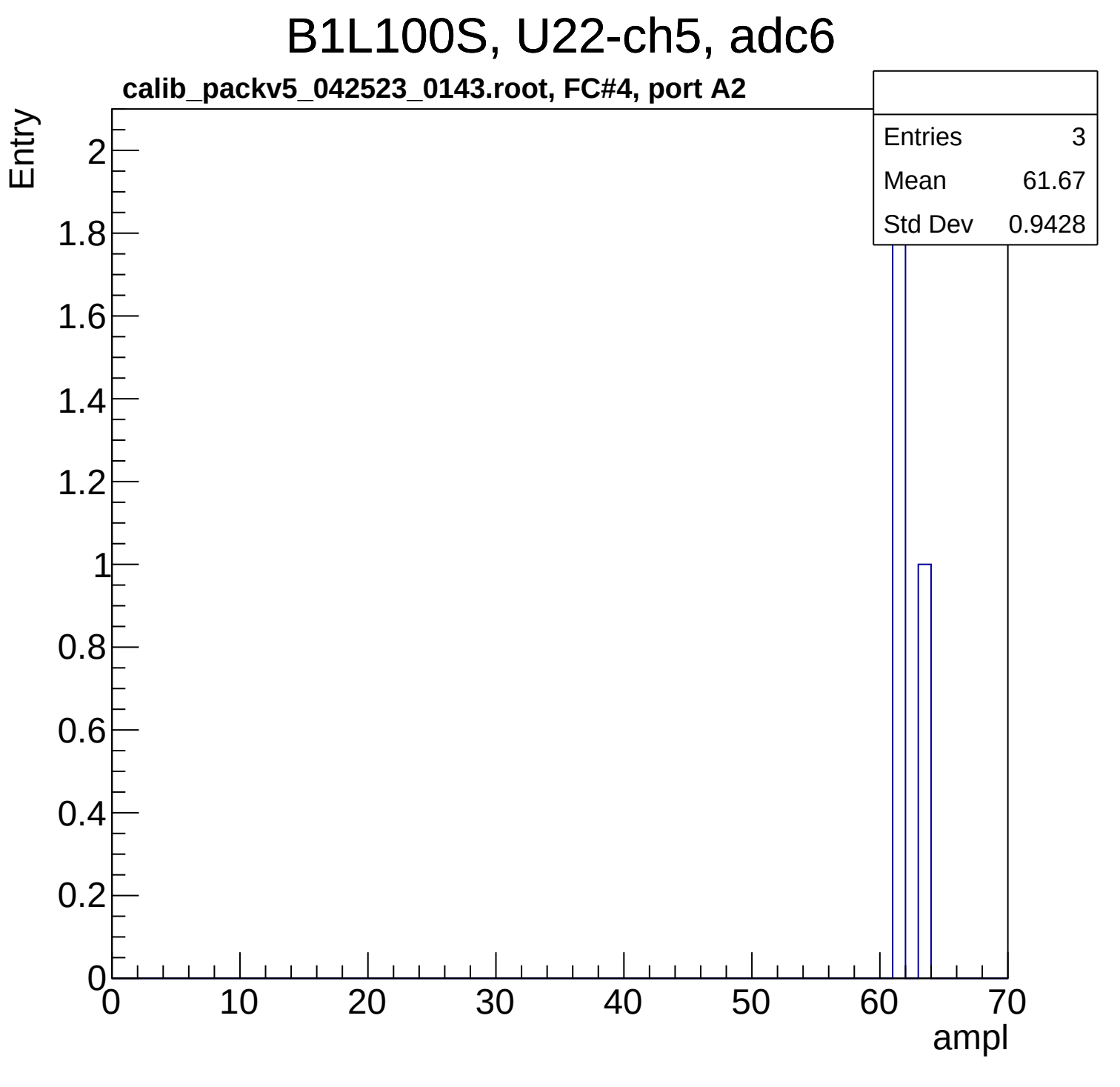

B1L100S, U22-ch5, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.98
Std Dev 3.505
Gaus mean : 37.2723
Gaus Width: 3.9756
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

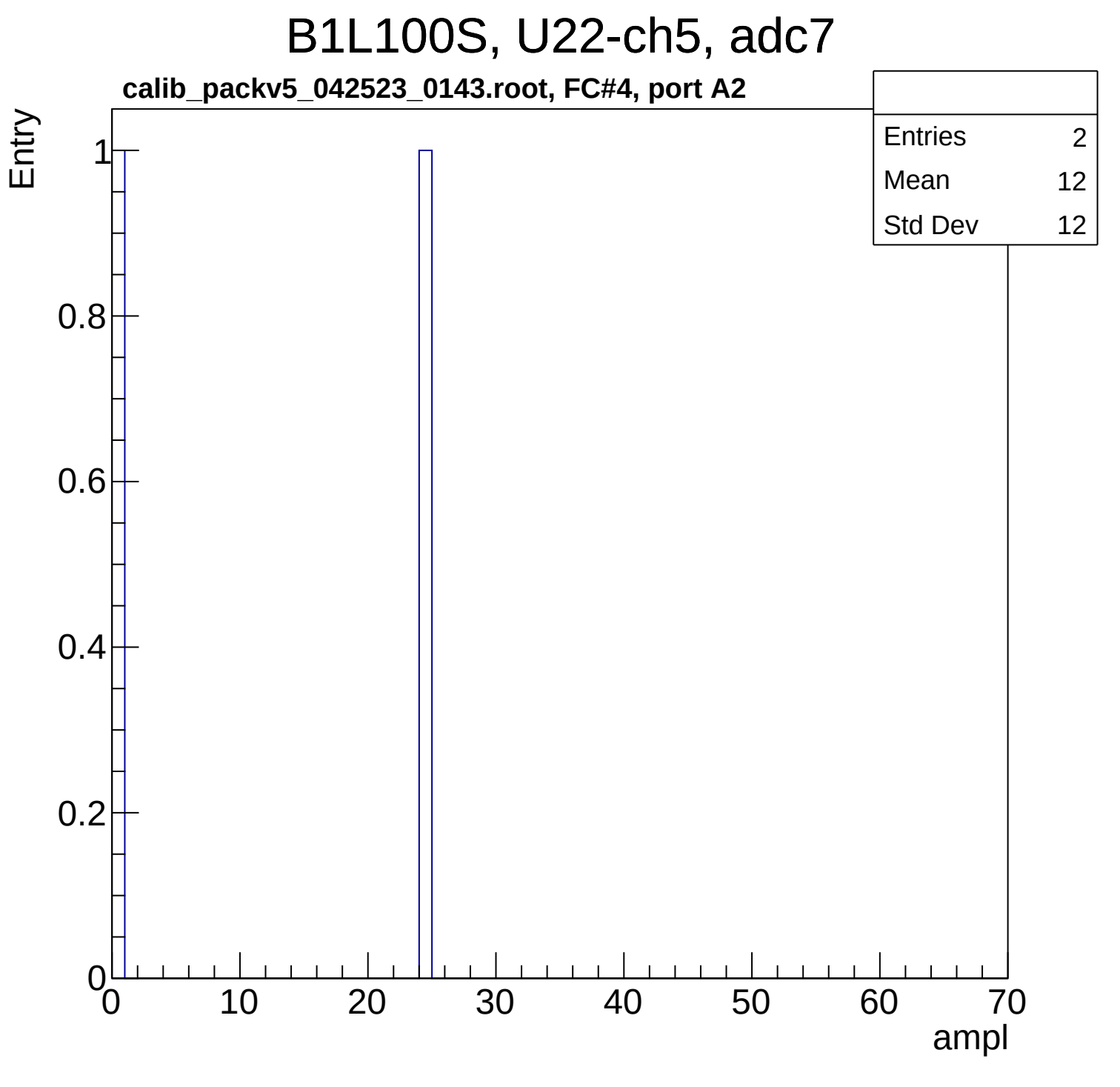

B1L100S, U22-ch5, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 44.18
Std Dev 3.517
Gaus mean : 44.9678
Gaus Width: 3.8946
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9

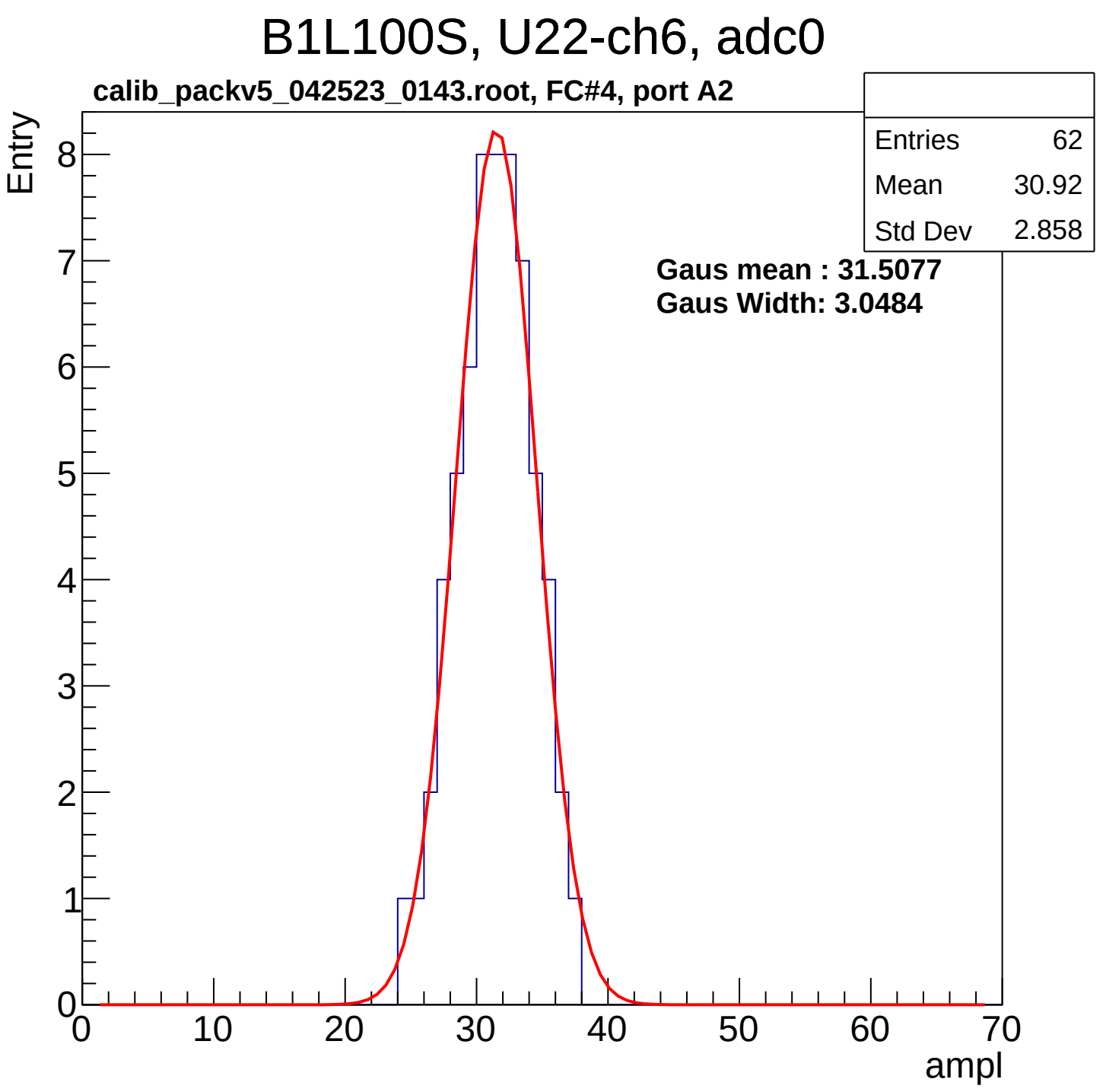

B1L100S, U22-ch5, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 51.54
Std Dev 3.22
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

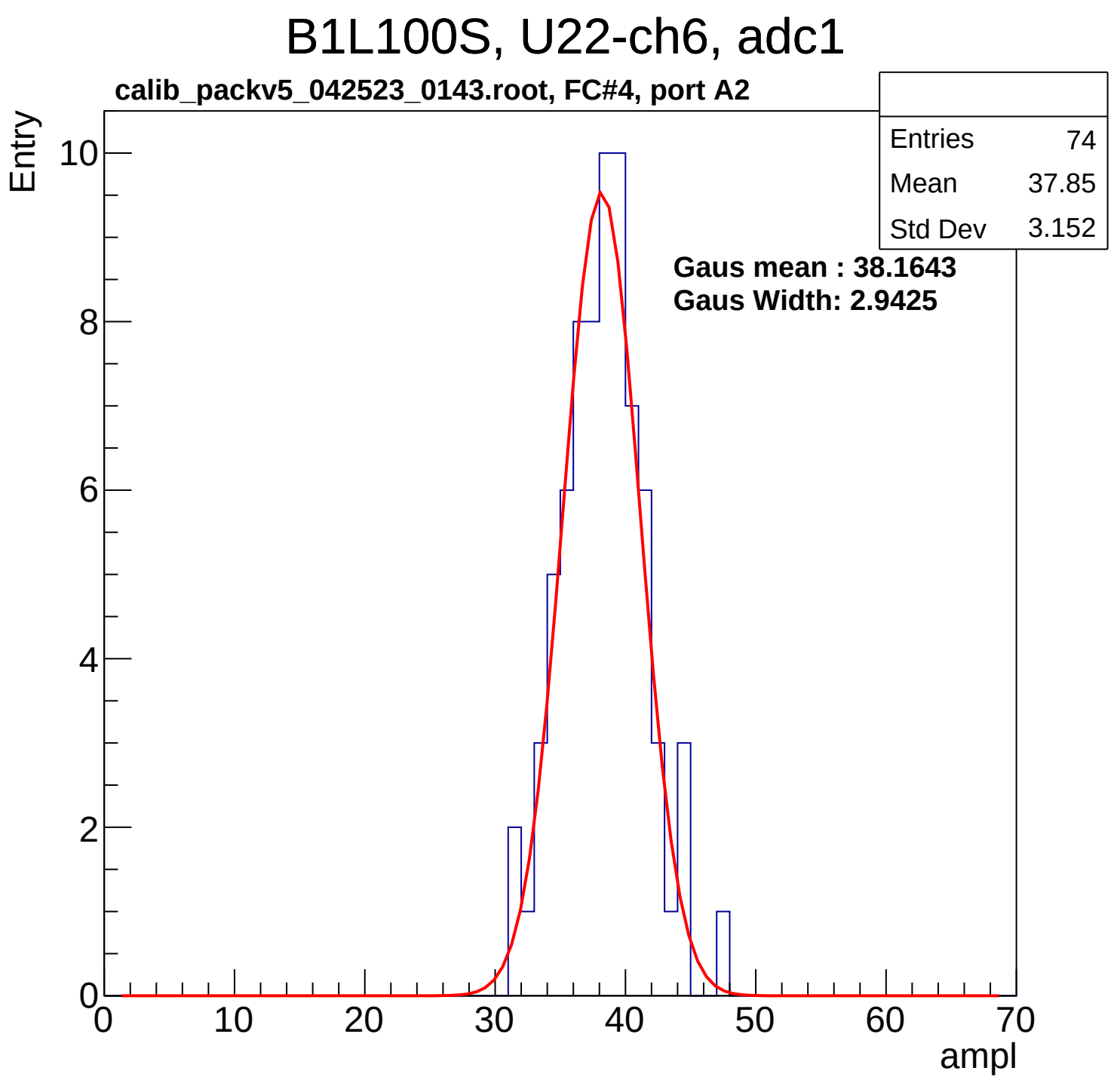

B1L100S, U22-ch5, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 56.42
Std Dev 3.39
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

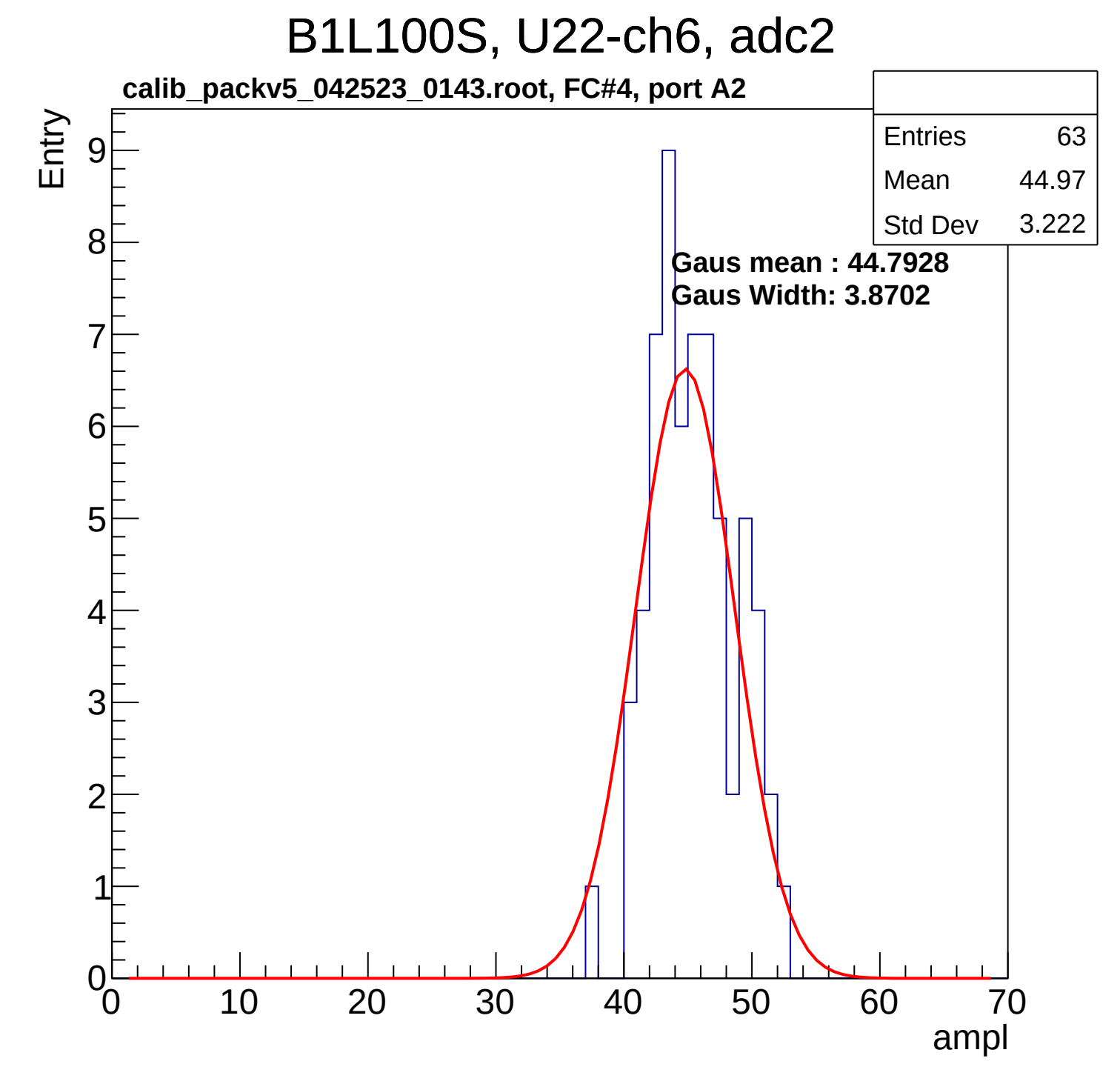

B1L100S, U22-ch5, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 59.03
Std Dev 11.13
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

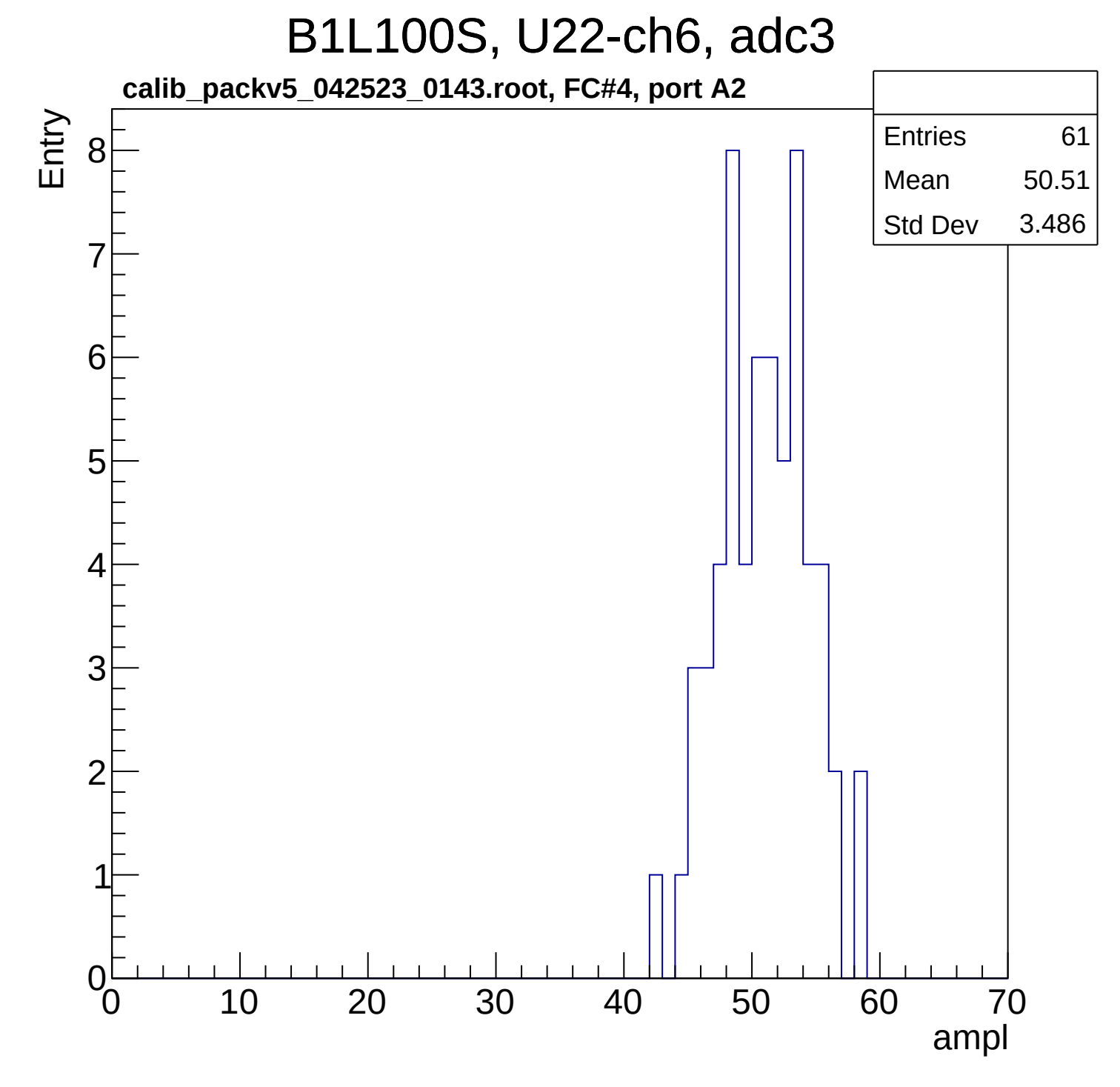

B1L100S, U22-ch5, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch5, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12
Std Dev 12
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch6, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30.92
Std Dev 2.858
Gaus mean : 31.5077
Gaus Width: 3.0484
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch6, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 37.85
Std Dev 3.152
Gaus mean : 38.1643
Gaus Width: 2.9425
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch6, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.97
Std Dev 3.222
Gaus mean : 44.7928
Gaus Width: 3.8702
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch6, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.51
Std Dev 3.486
Entry
ampl
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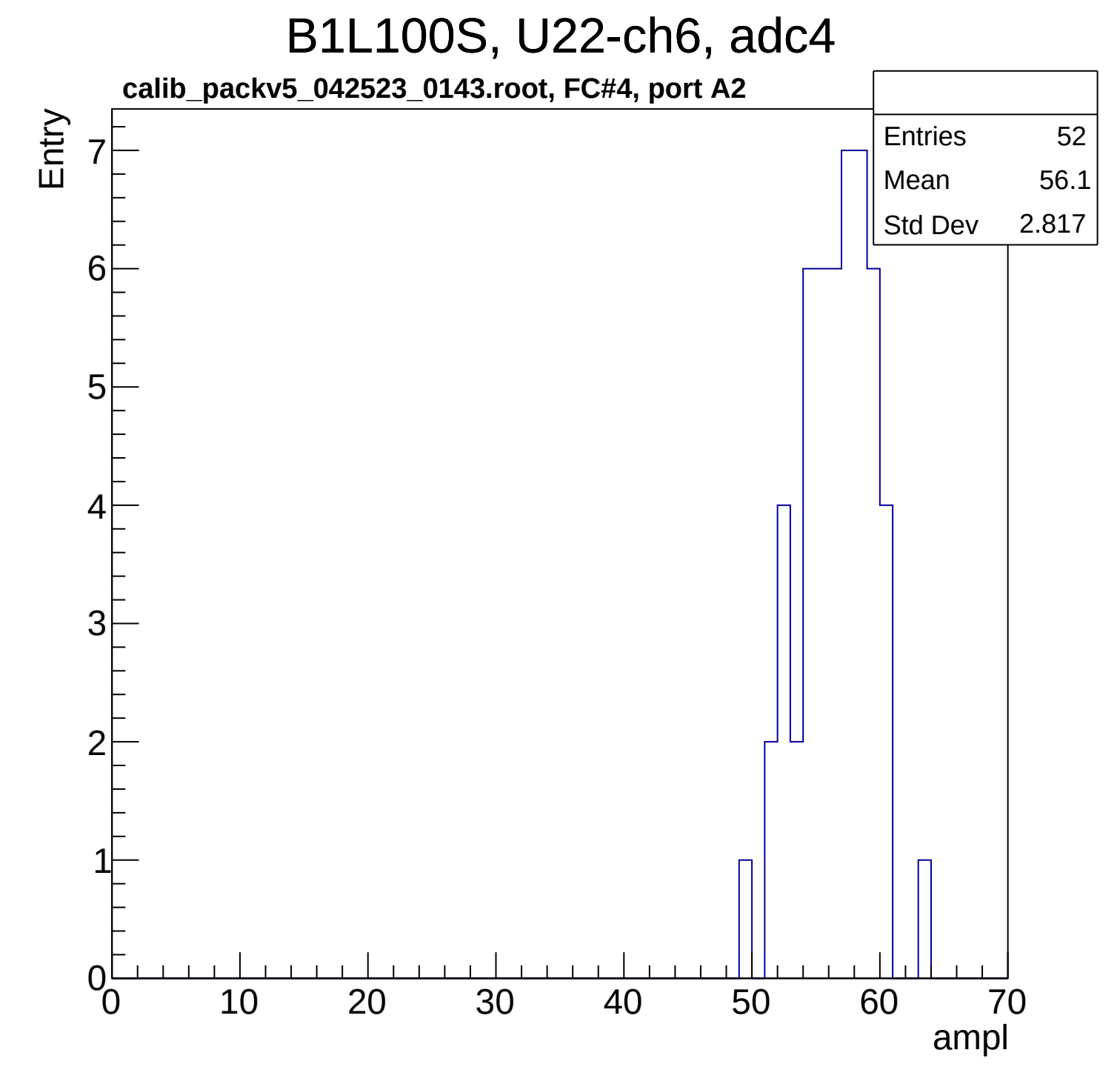
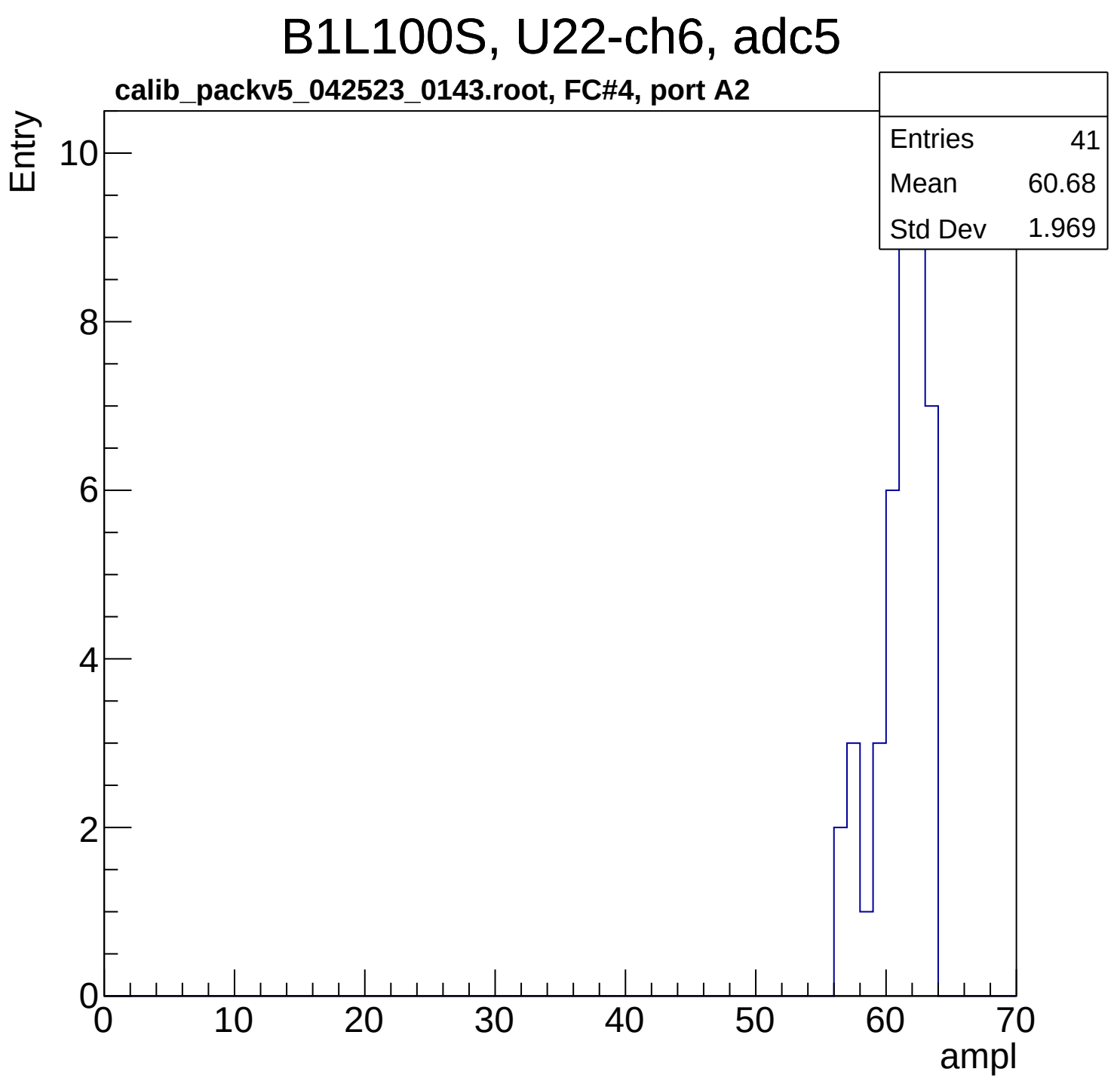
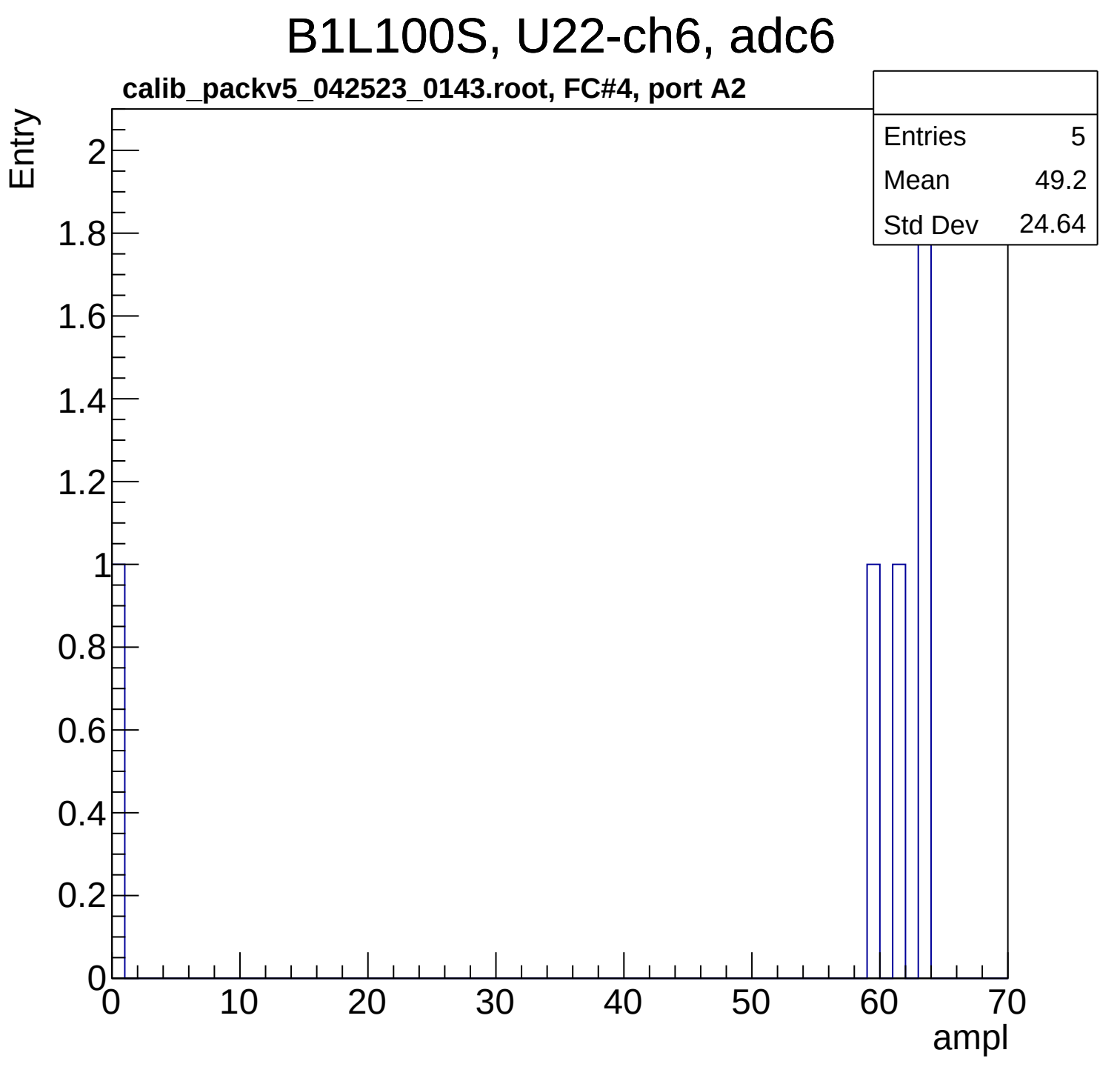
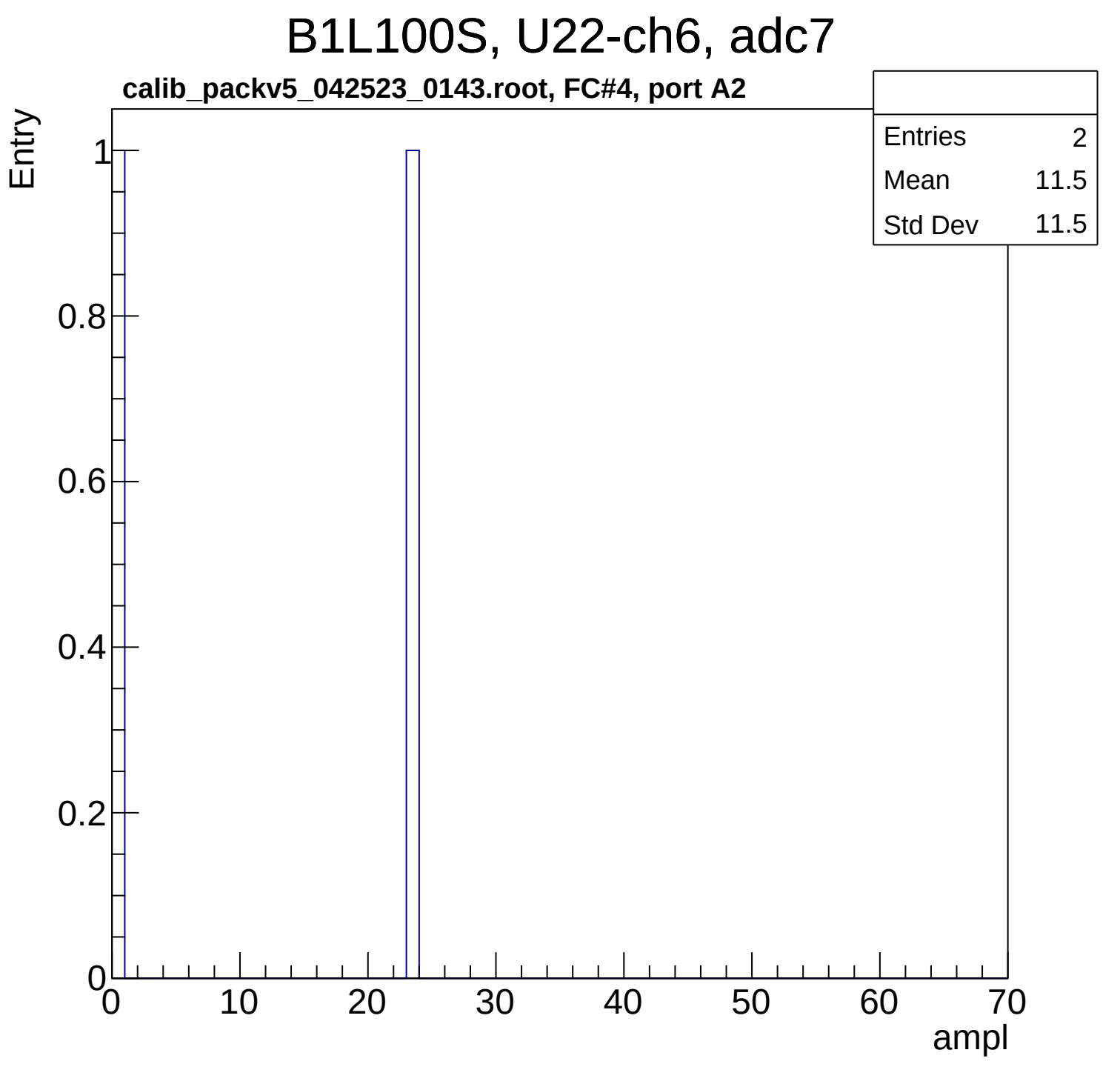
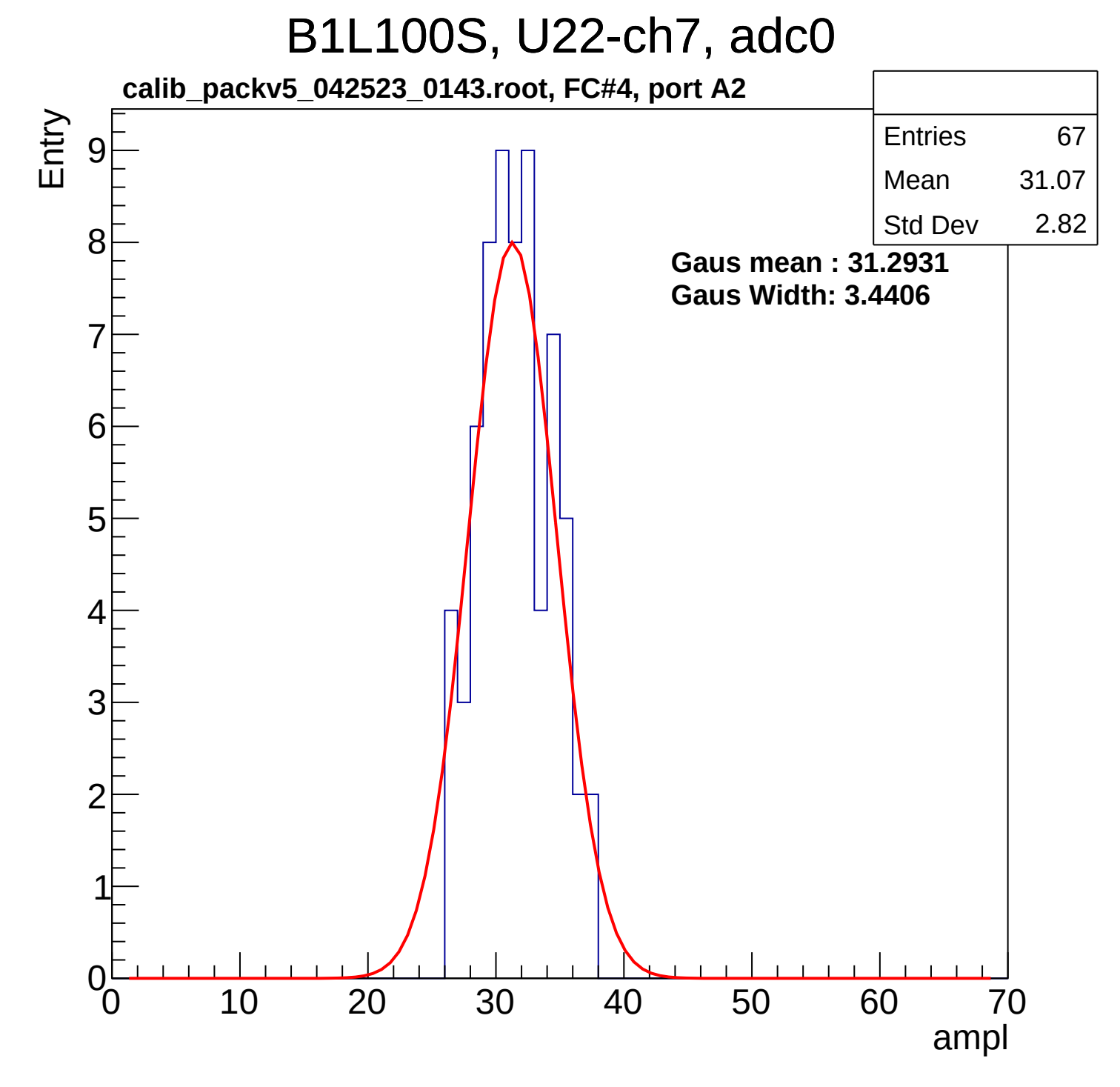
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

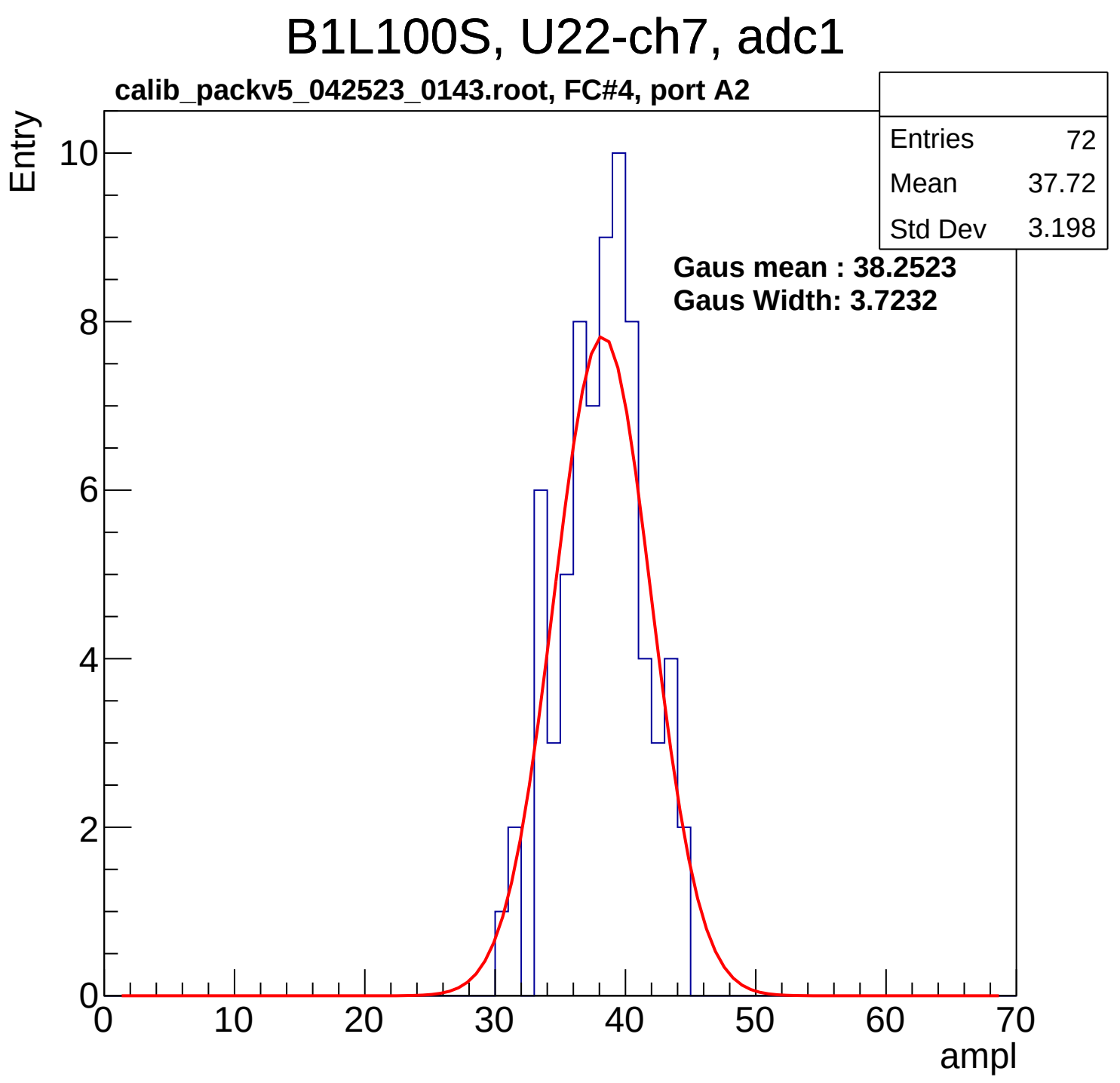

B1L100S, U22-ch6, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 56.1
Std Dev 2.817
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

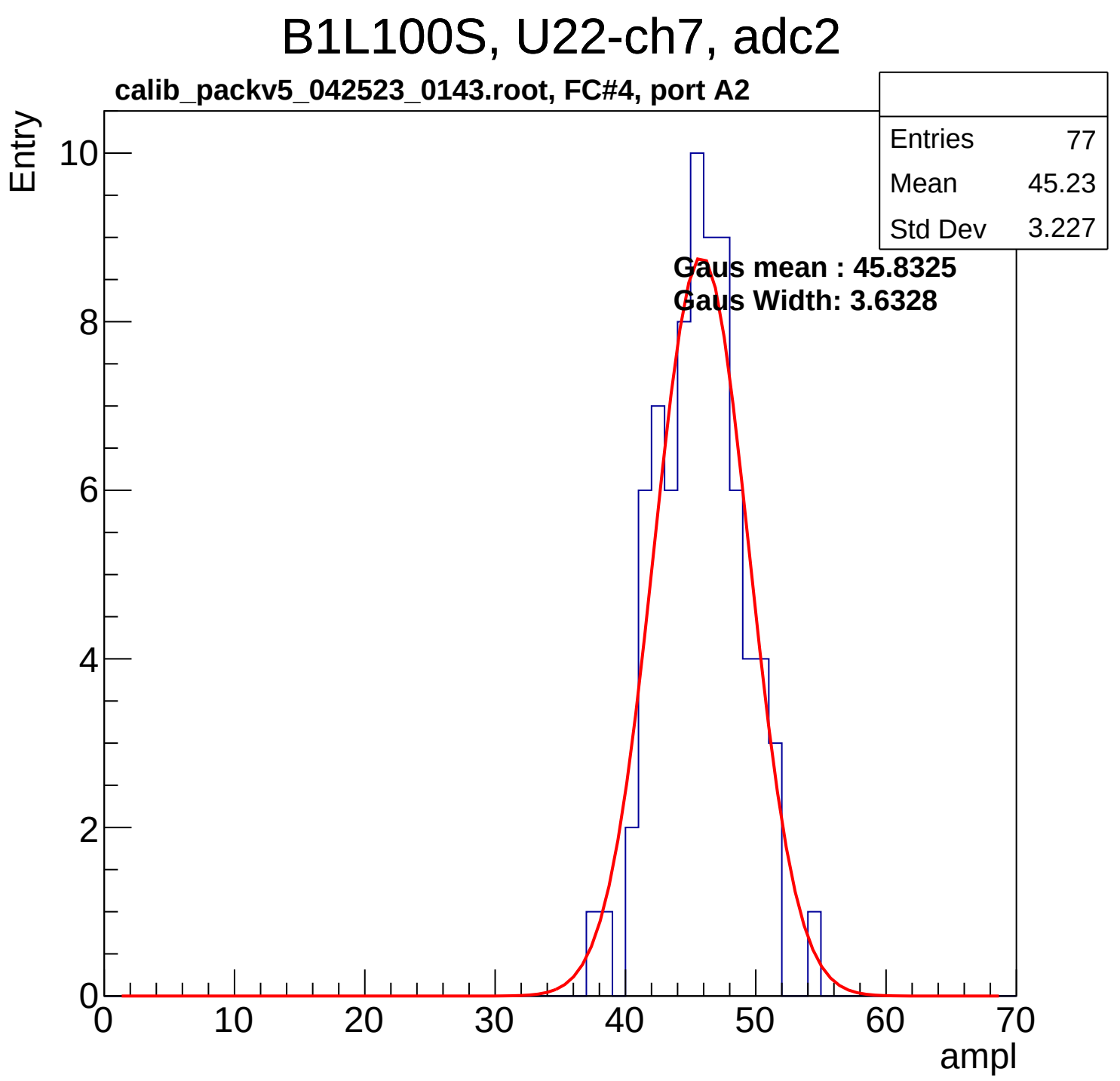

B1L100S, U22-ch6, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 60.68
Std Dev 1.969
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

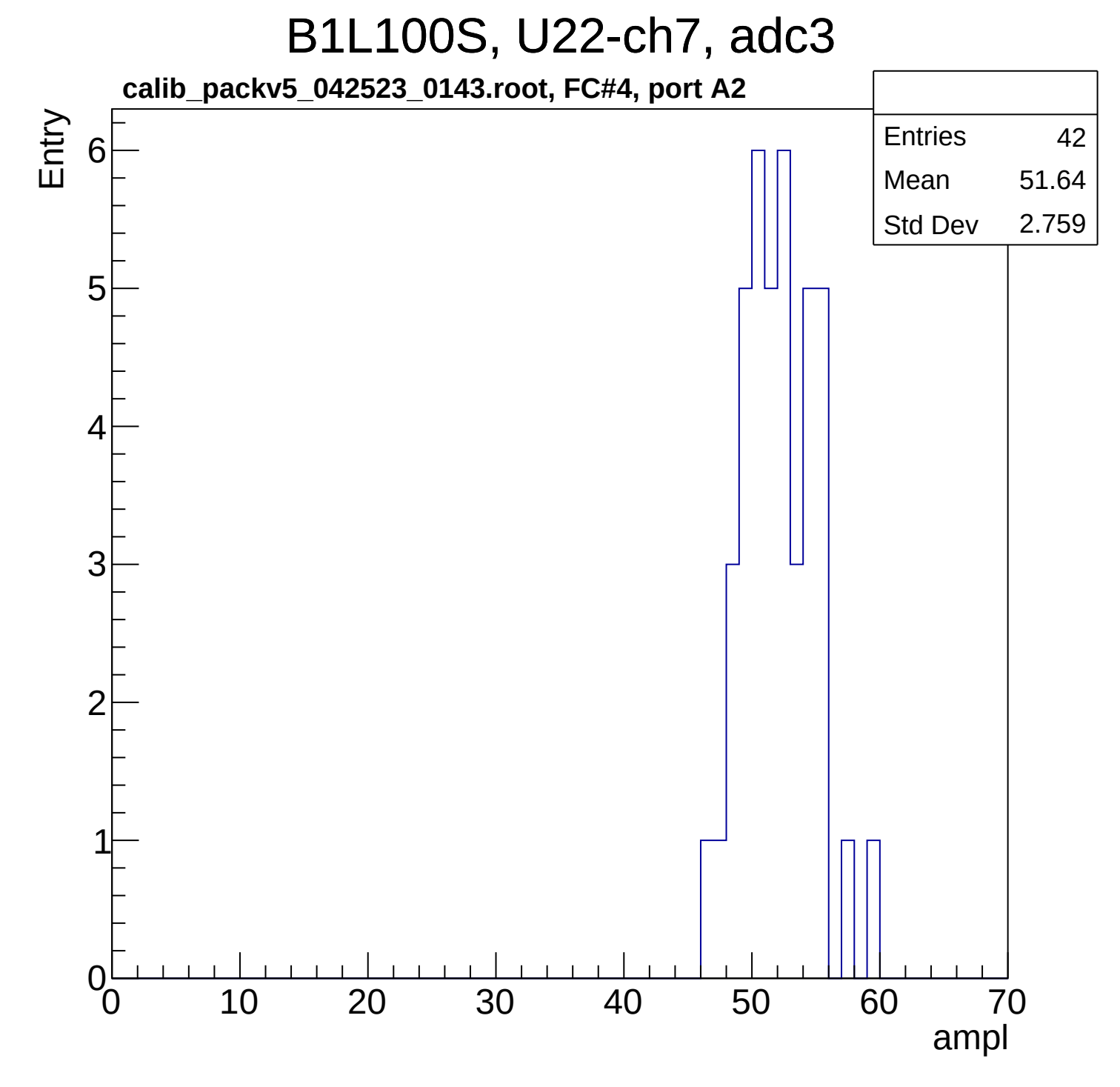

B1L100S, U22-ch6, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.2
Std Dev 24.64
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

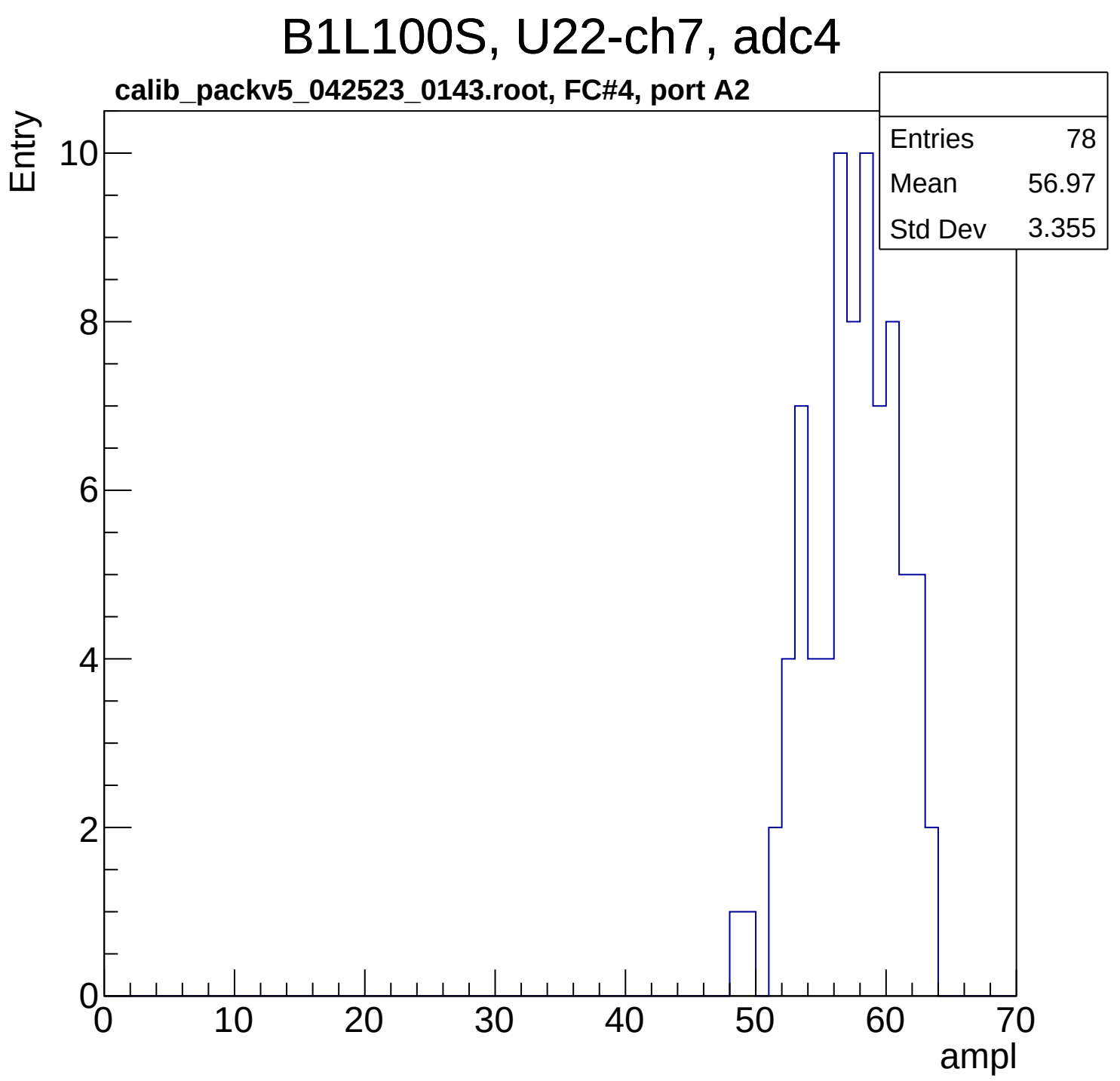

B1L100S, U22-ch6, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11.5
Std Dev 11.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70

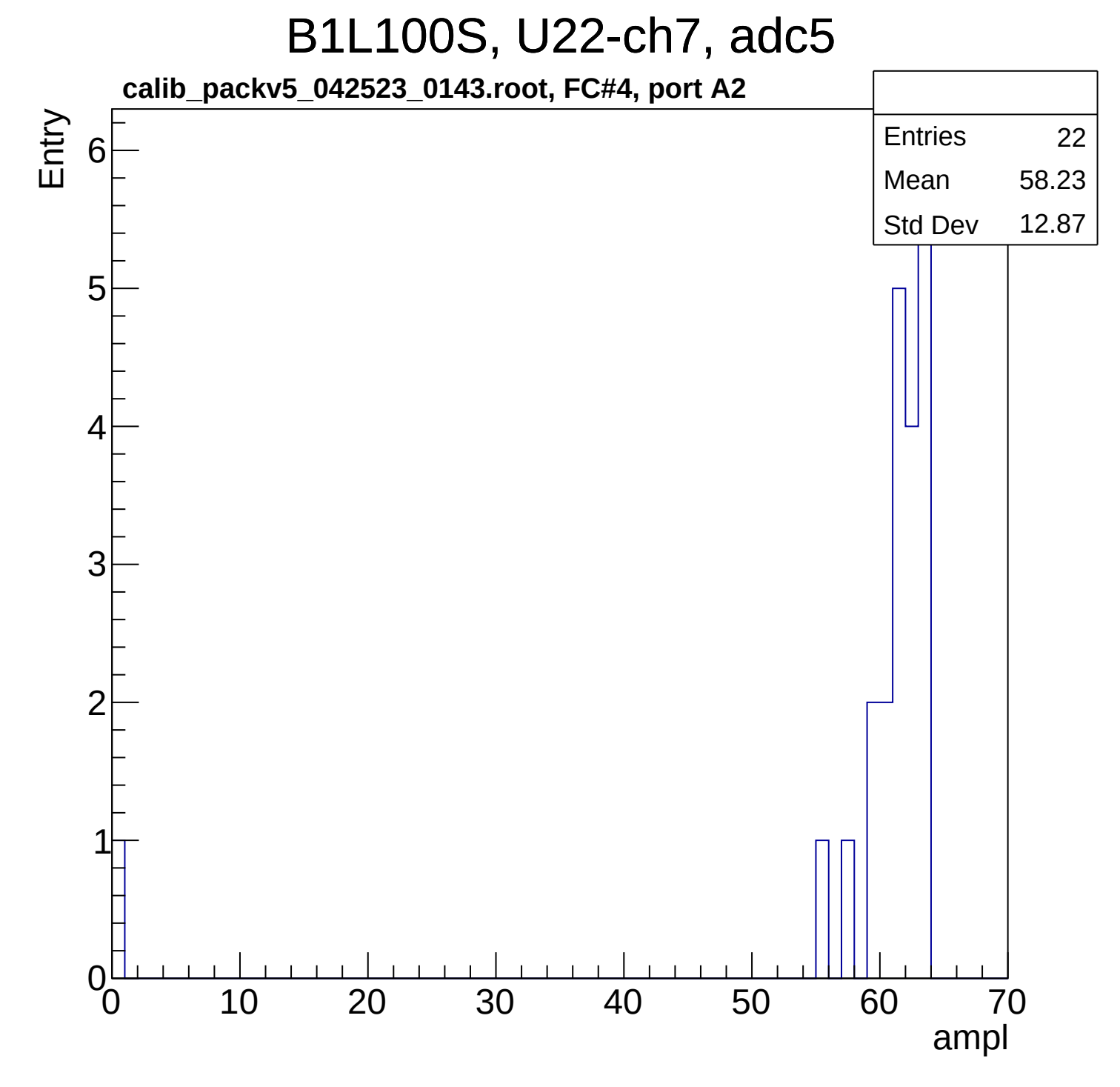

B1L100S, U22-ch7, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 31.07
Std Dev 2.82
Gaus mean : 31.2931
Gaus Width: 3.4406
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

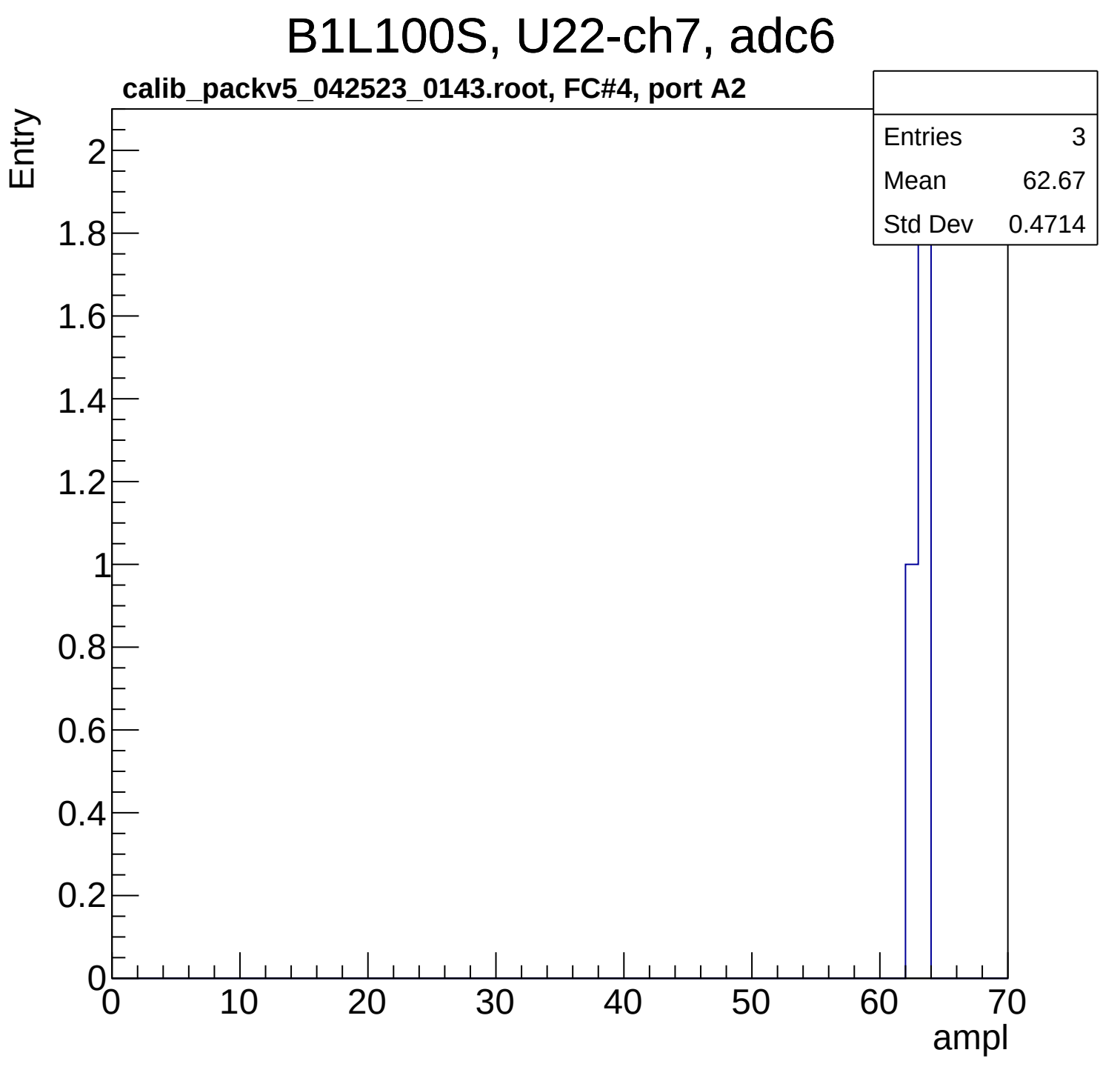

B1L100S, U22-ch7, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.72
Std Dev 3.198
Gaus mean : 38.2523
Gaus Width: 3.7232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch7, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 45.23
Std Dev 3.227
Gaus mean : 45.8325
Gaus Width: 3.6328
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch7, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 51.64
Std Dev 2.759
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch7, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 56.97
Std Dev 3.355
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch7, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 22
Mean 58.23
Std Dev 12.87
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch7, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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60
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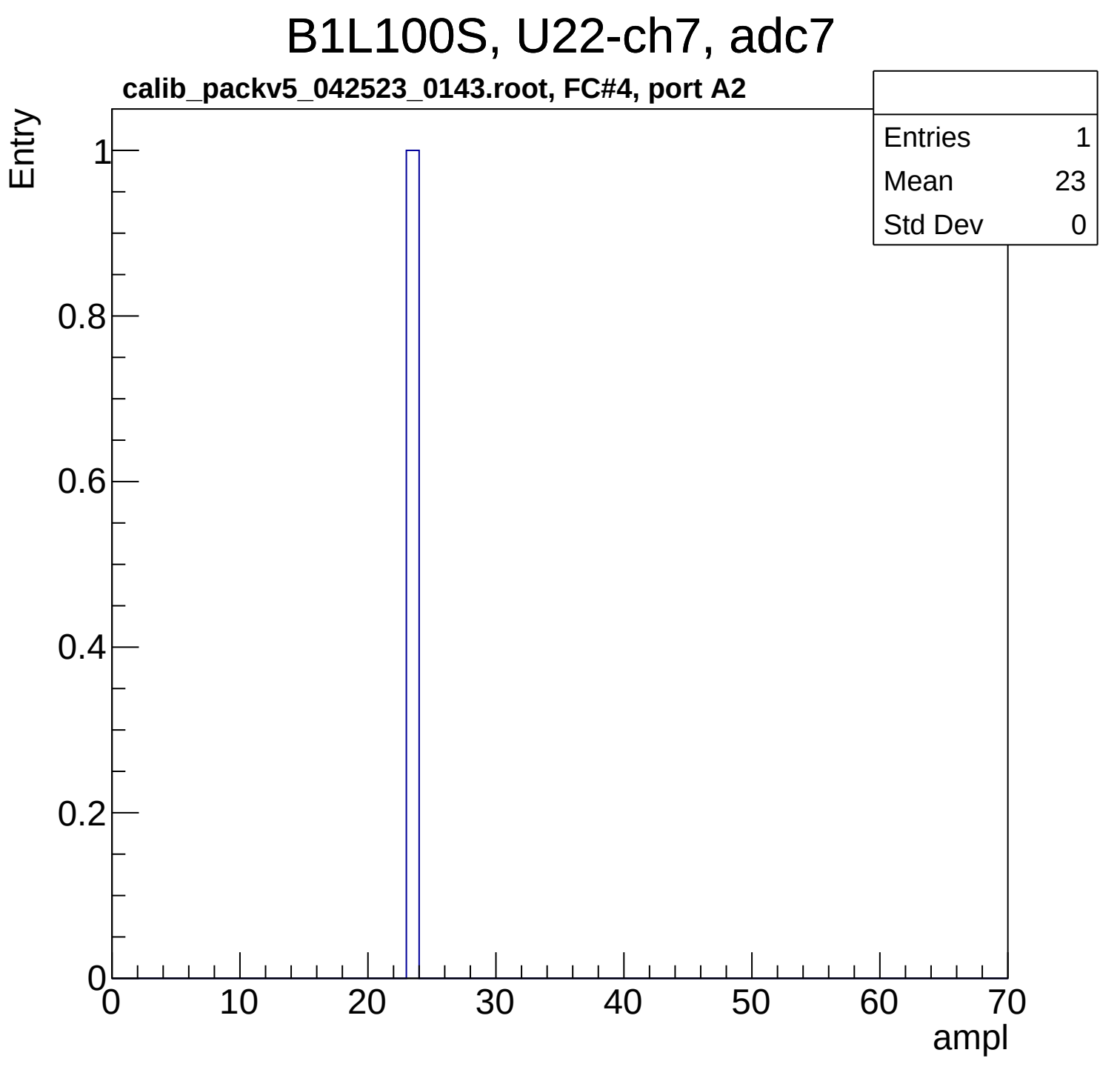
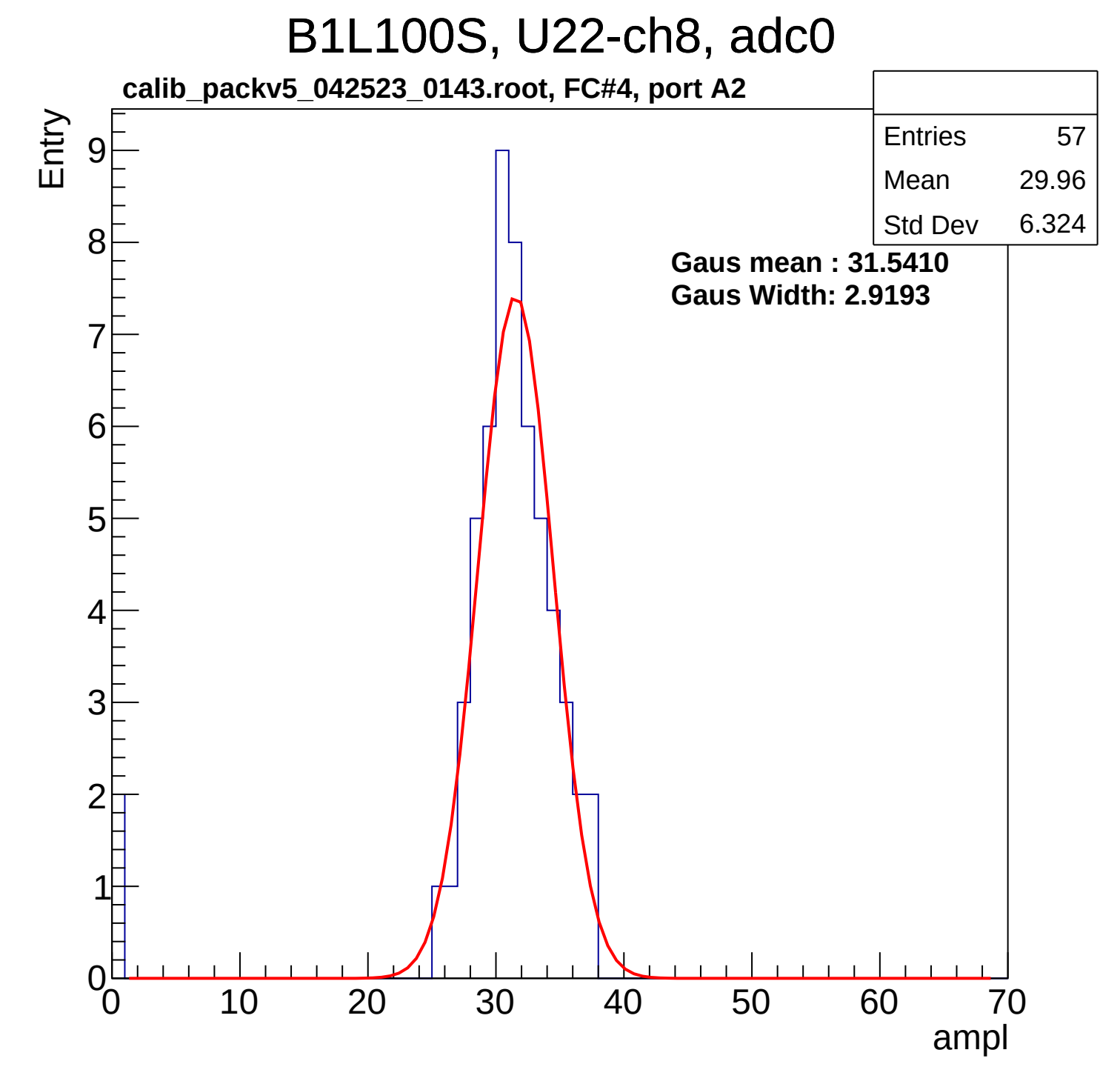

B1L100S, U22-ch7, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch8, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.96
Std Dev 6.324
Gaus mean : 31.5410
Gaus Width: 2.9193
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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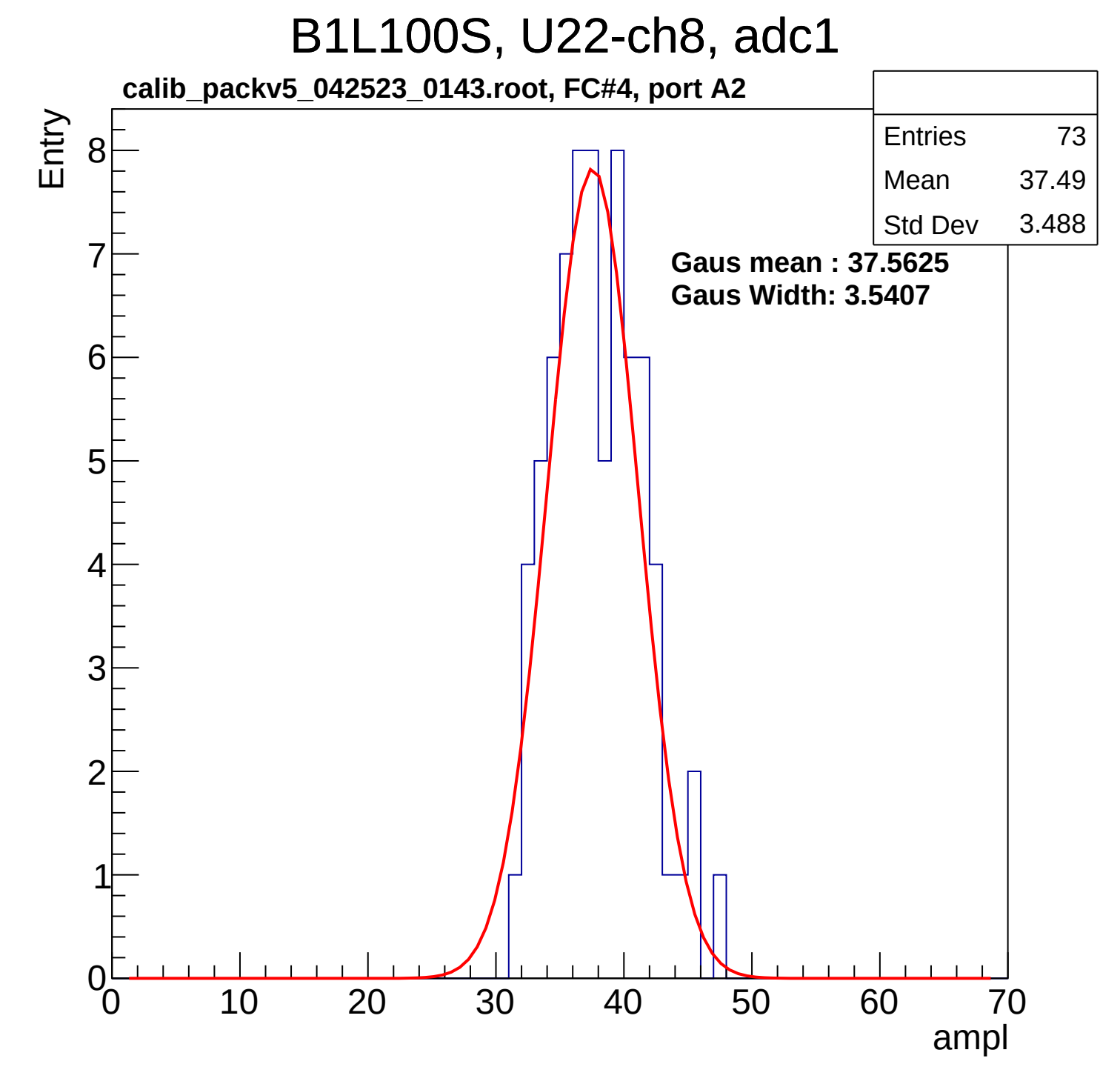
B1L100S, U22-ch8, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.49
Std Dev 3.488
Gaus mean : 37.5625
Gaus Width: 3.5407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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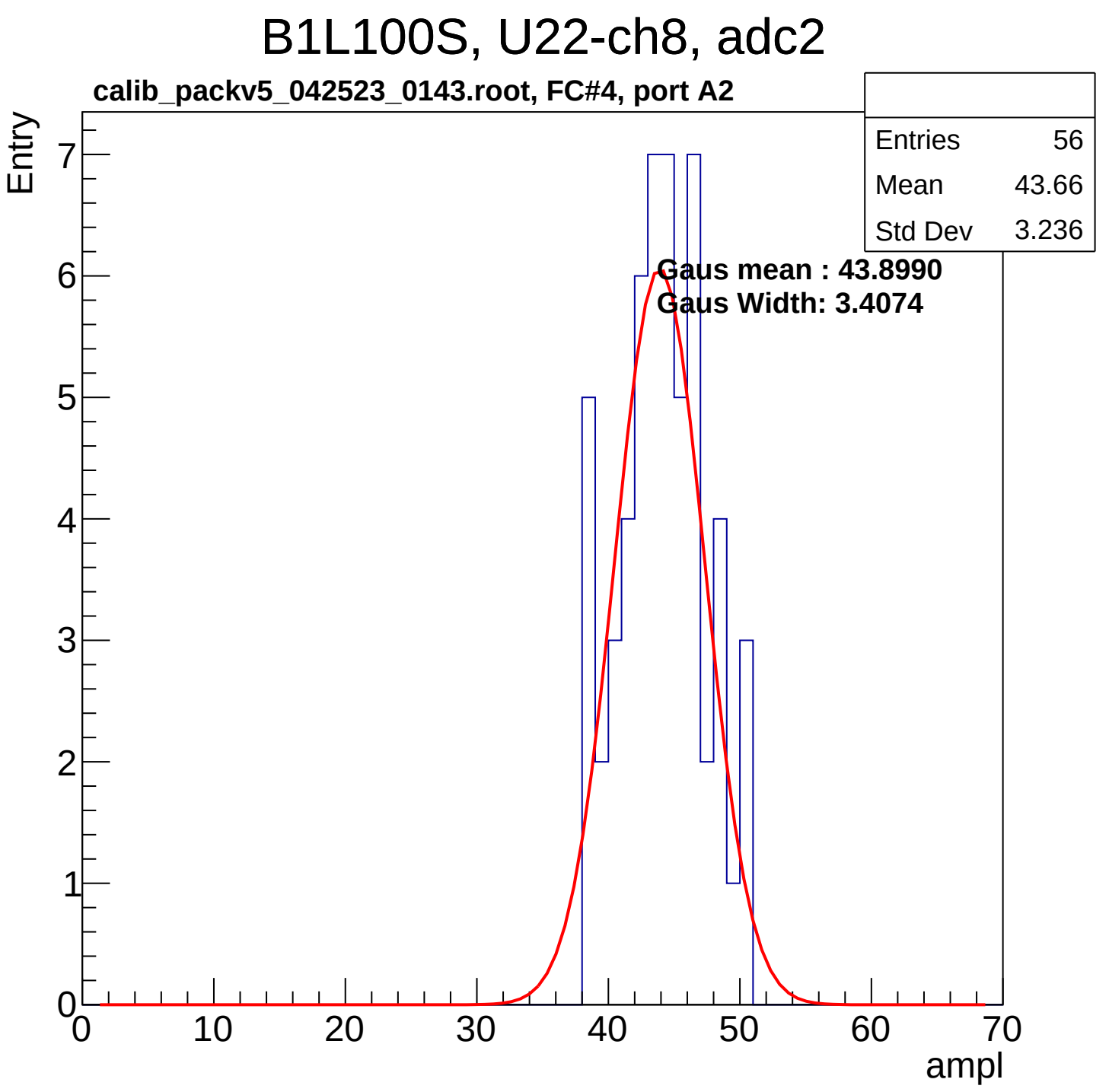
B1L100S, U22-ch8, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 43.66
Std Dev 3.236
Gaus mean : 43.8990
Gaus Width: 3.4074
Entry
ampl
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1
2
3
4
5
6
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0
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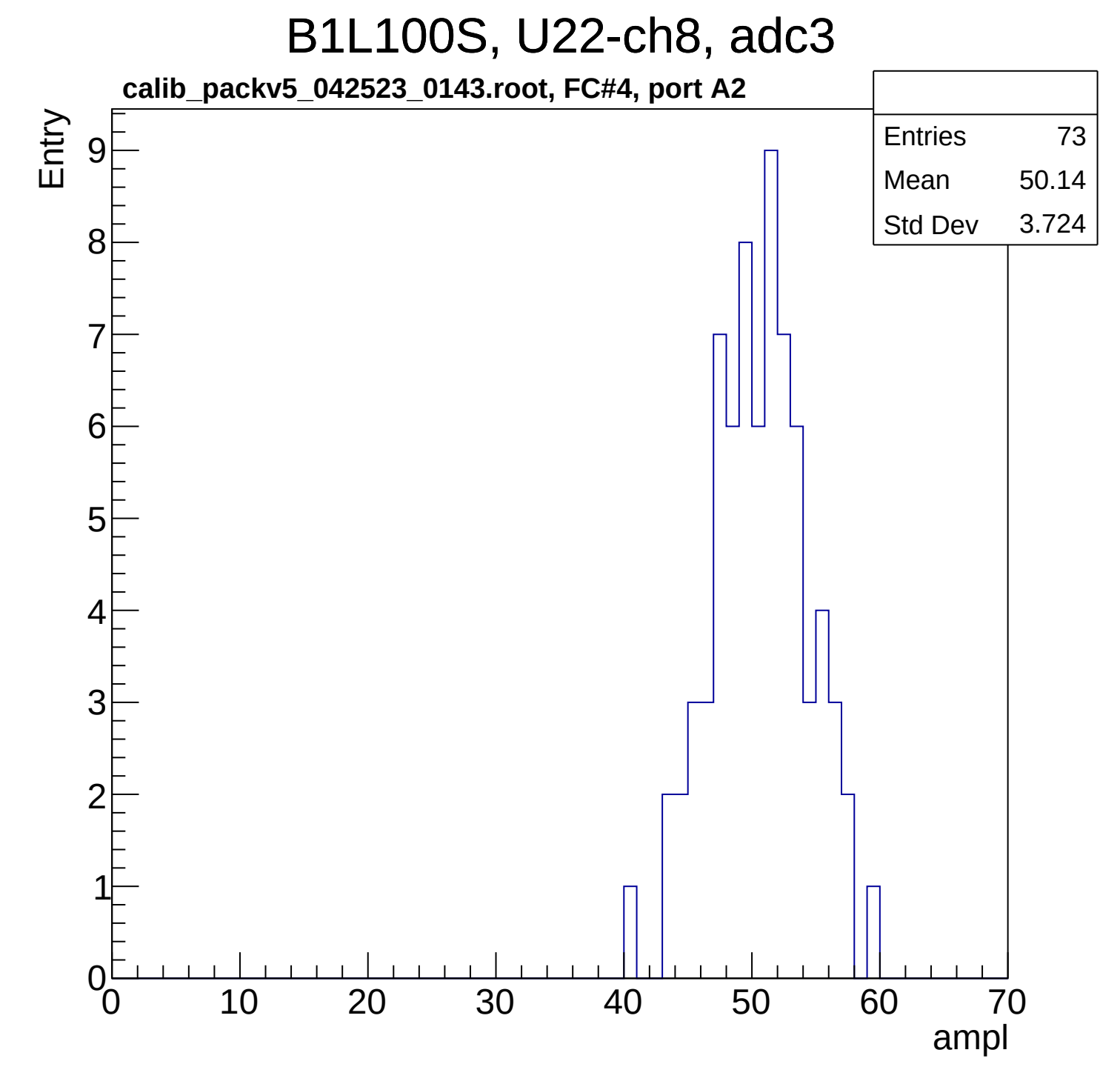
B1L100S, U22-ch8, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 50.14
Std Dev 3.724
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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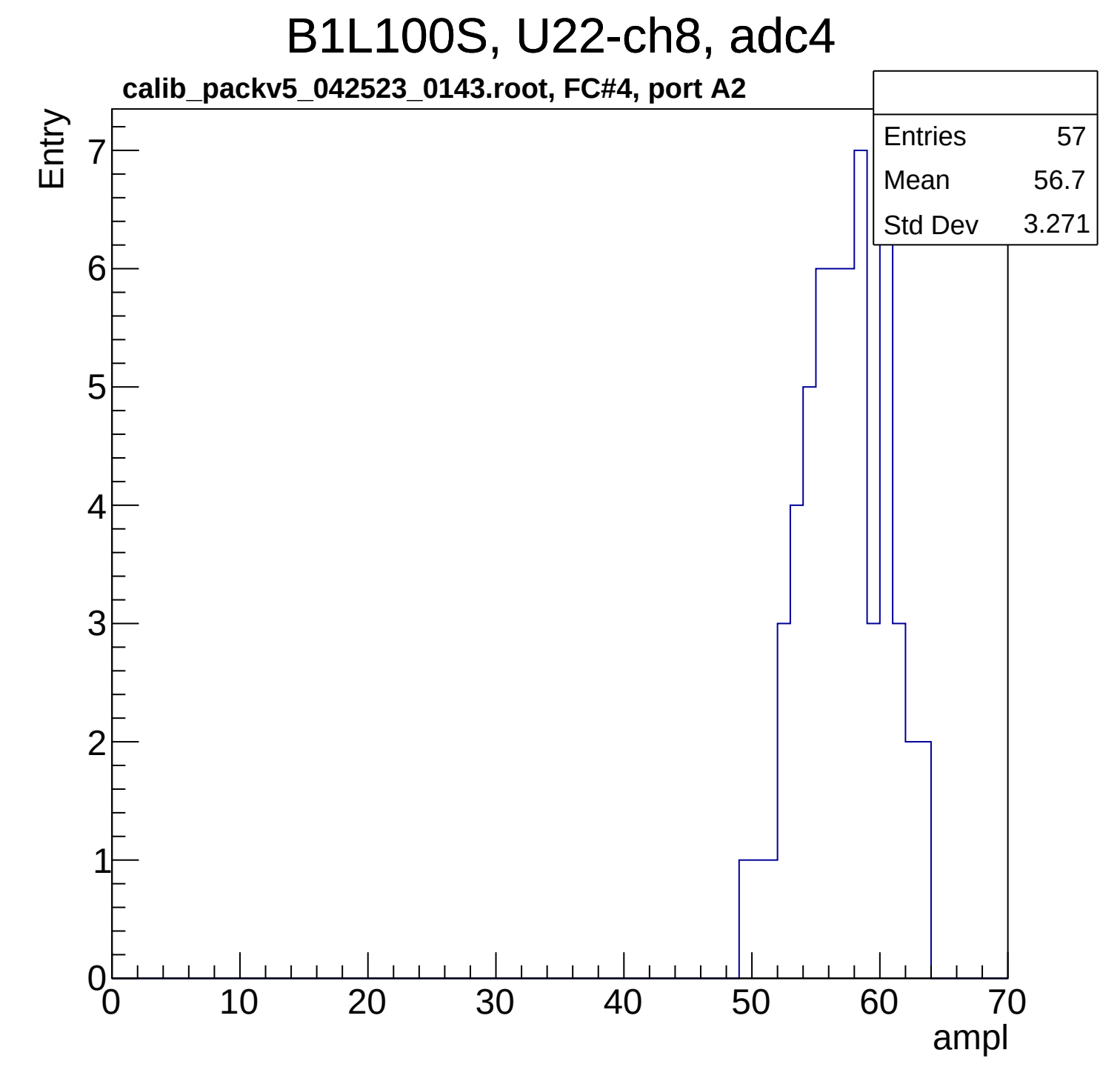
B1L100S, U22-ch8, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.7
Std Dev 3.271
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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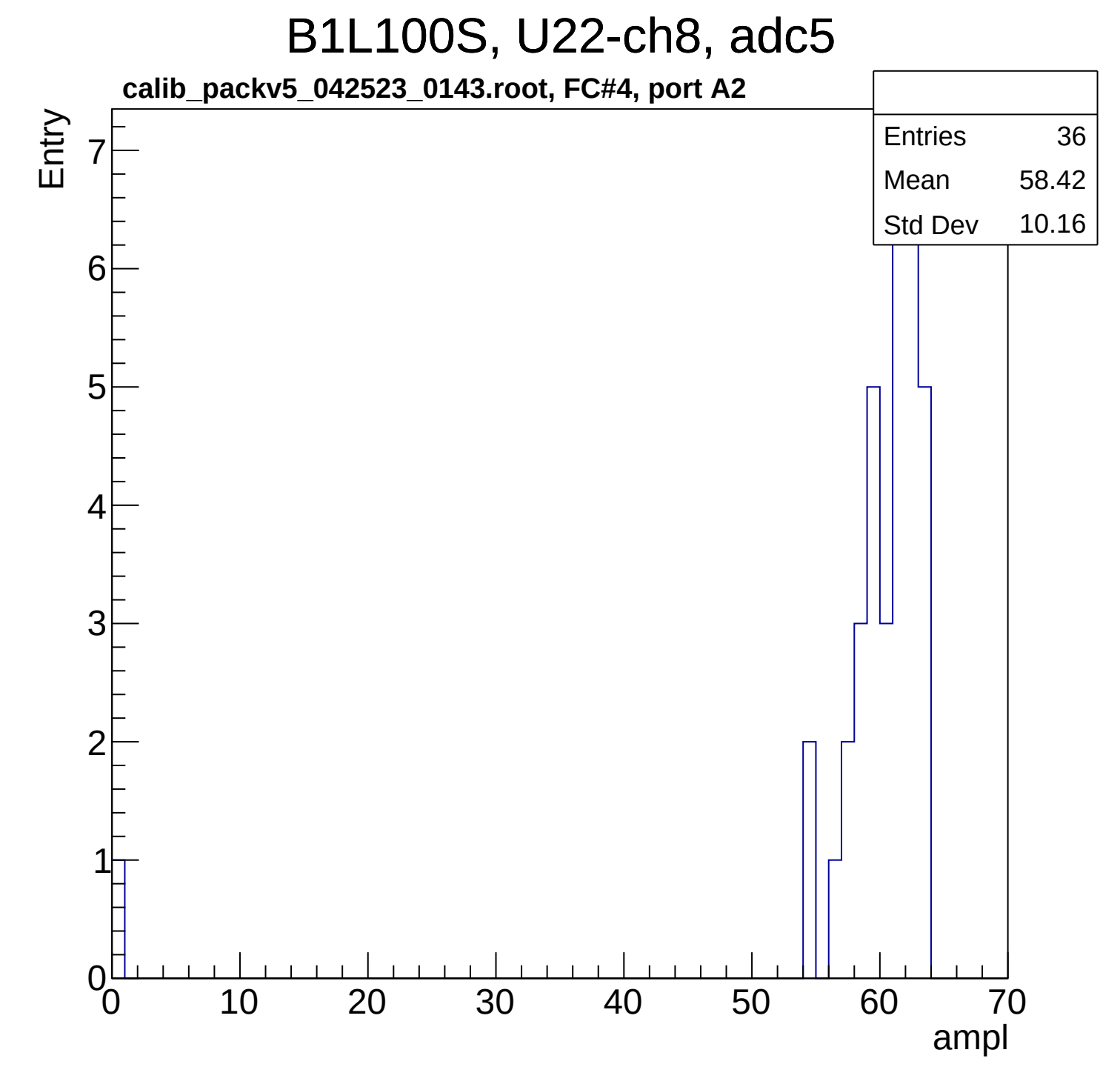
B1L100S, U22-ch8, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.42
Std Dev 10.16
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
50
60
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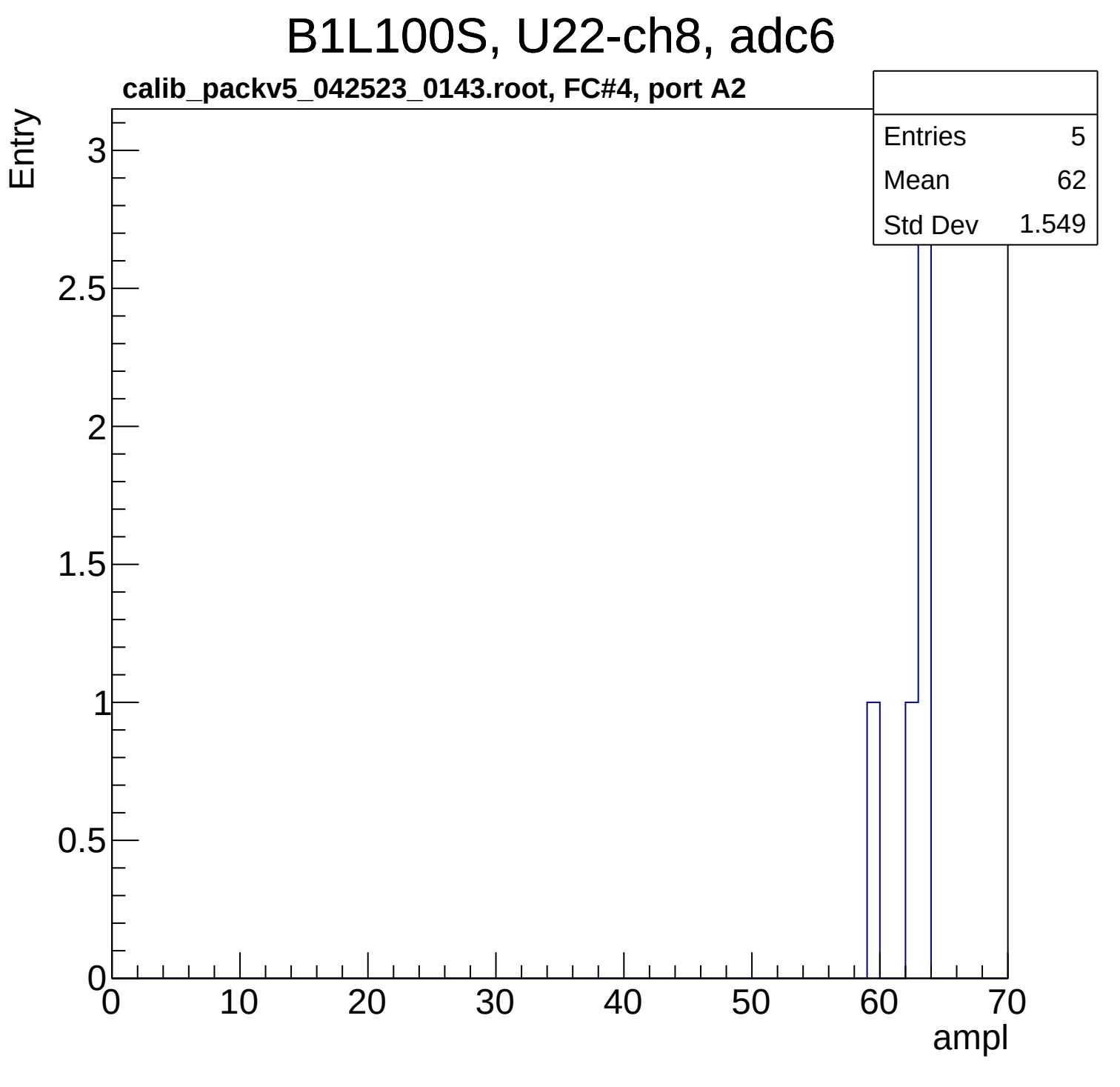
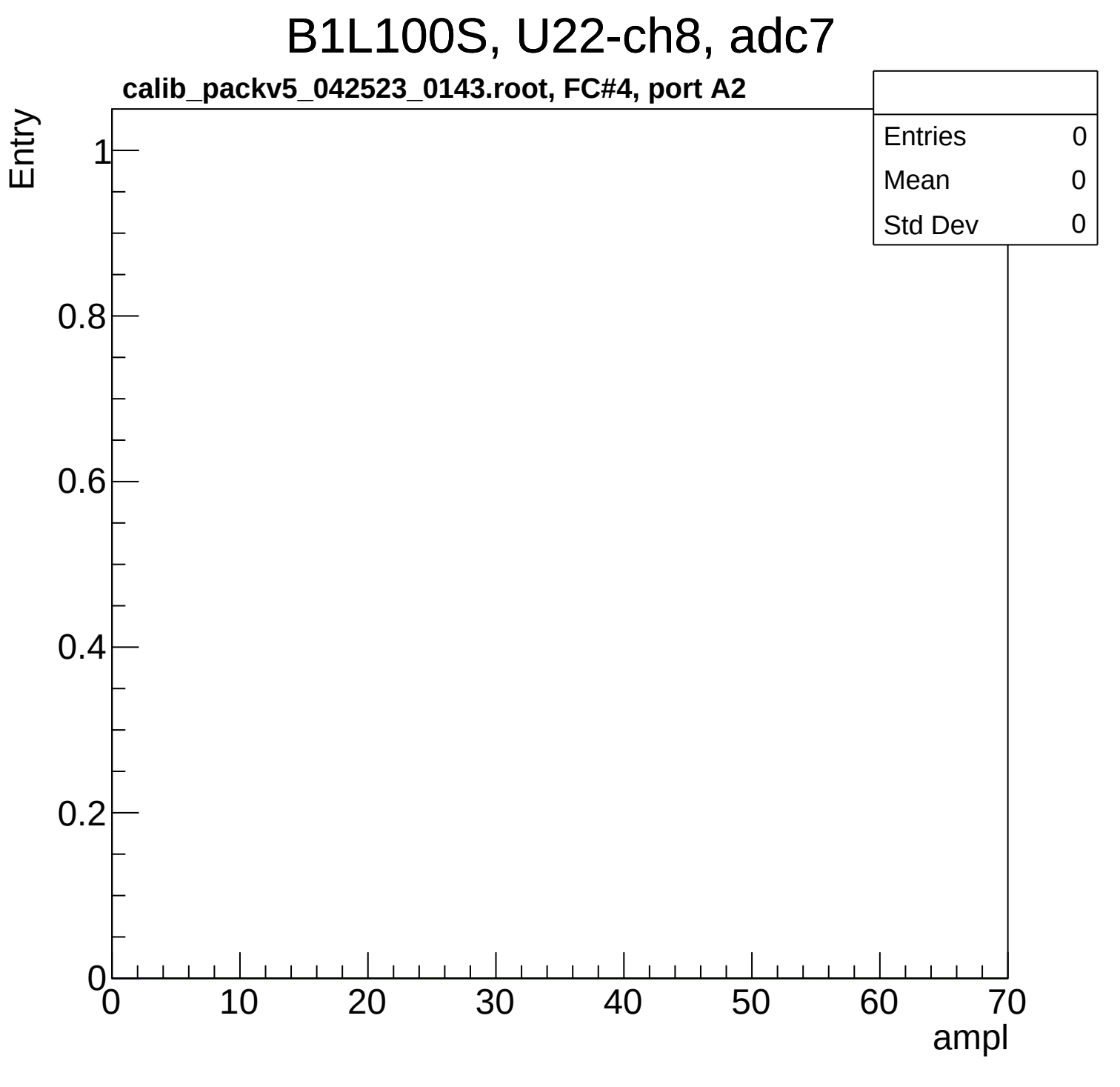
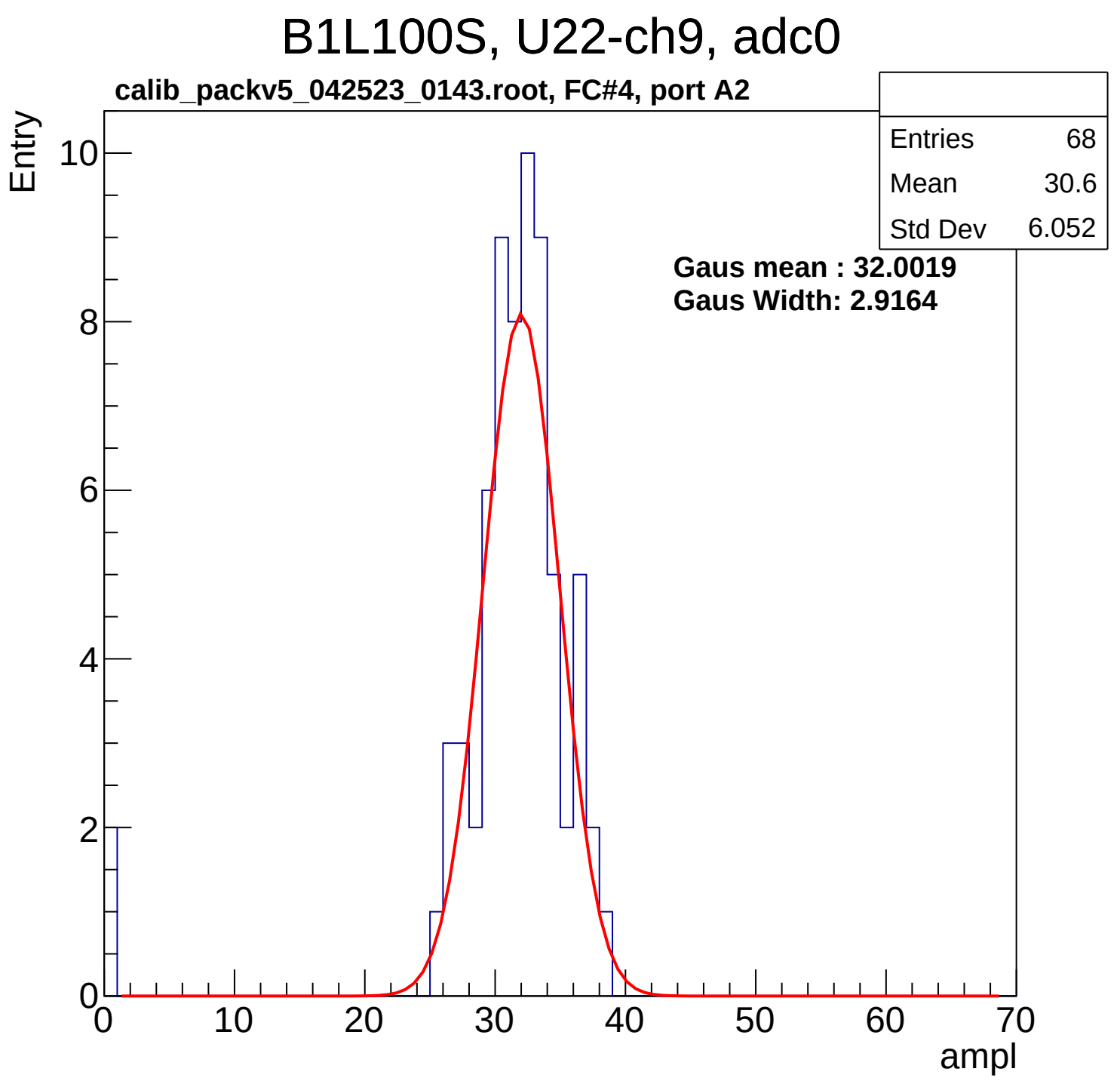
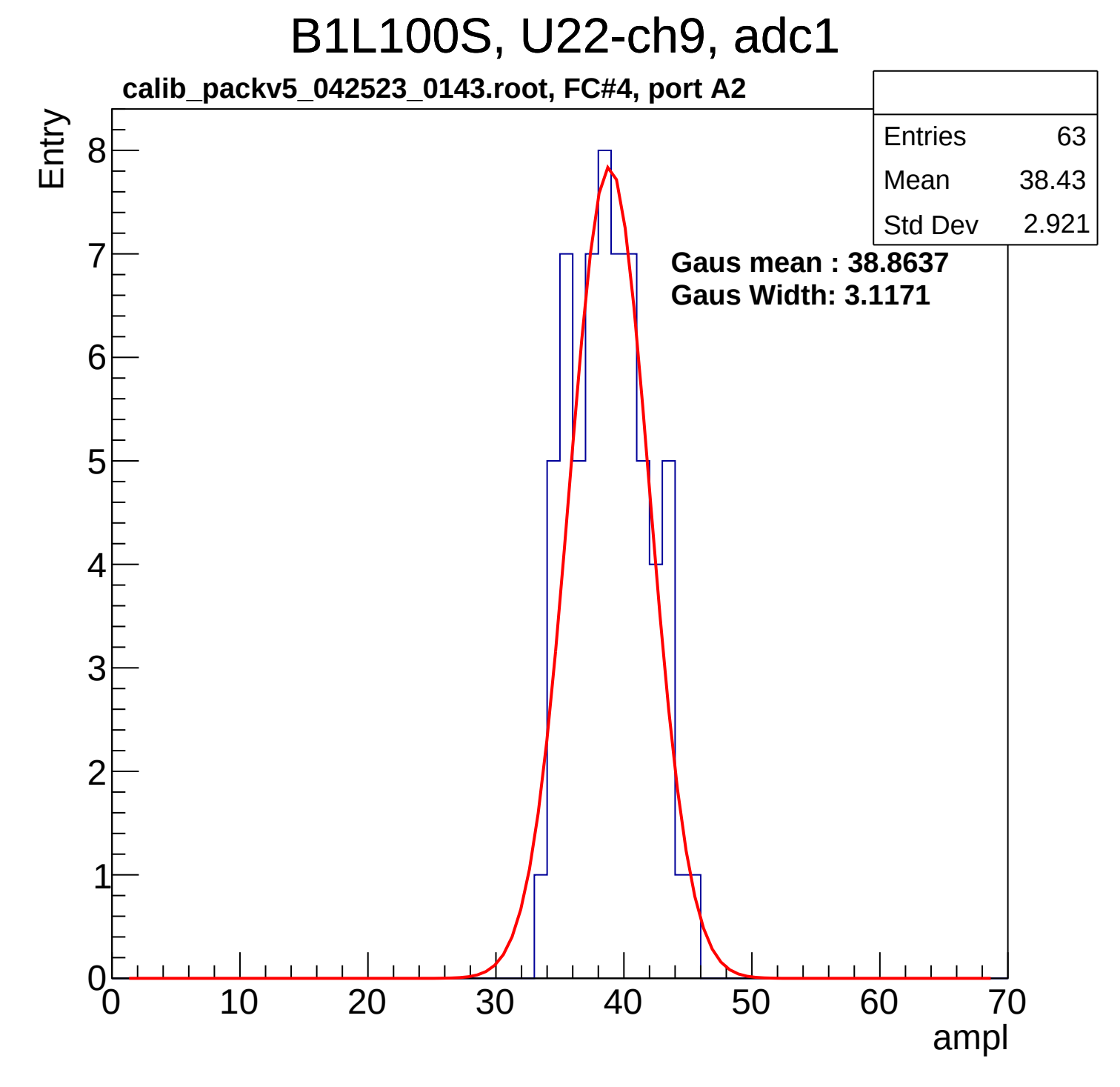

B1L100S, U22-ch8, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 1.549
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch8, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch9, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 30.6
Std Dev 6.052
Gaus mean : 32.0019
Gaus Width: 2.9164
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch9, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 38.43
Std Dev 2.921
Gaus mean : 38.8637
Gaus Width: 3.1171
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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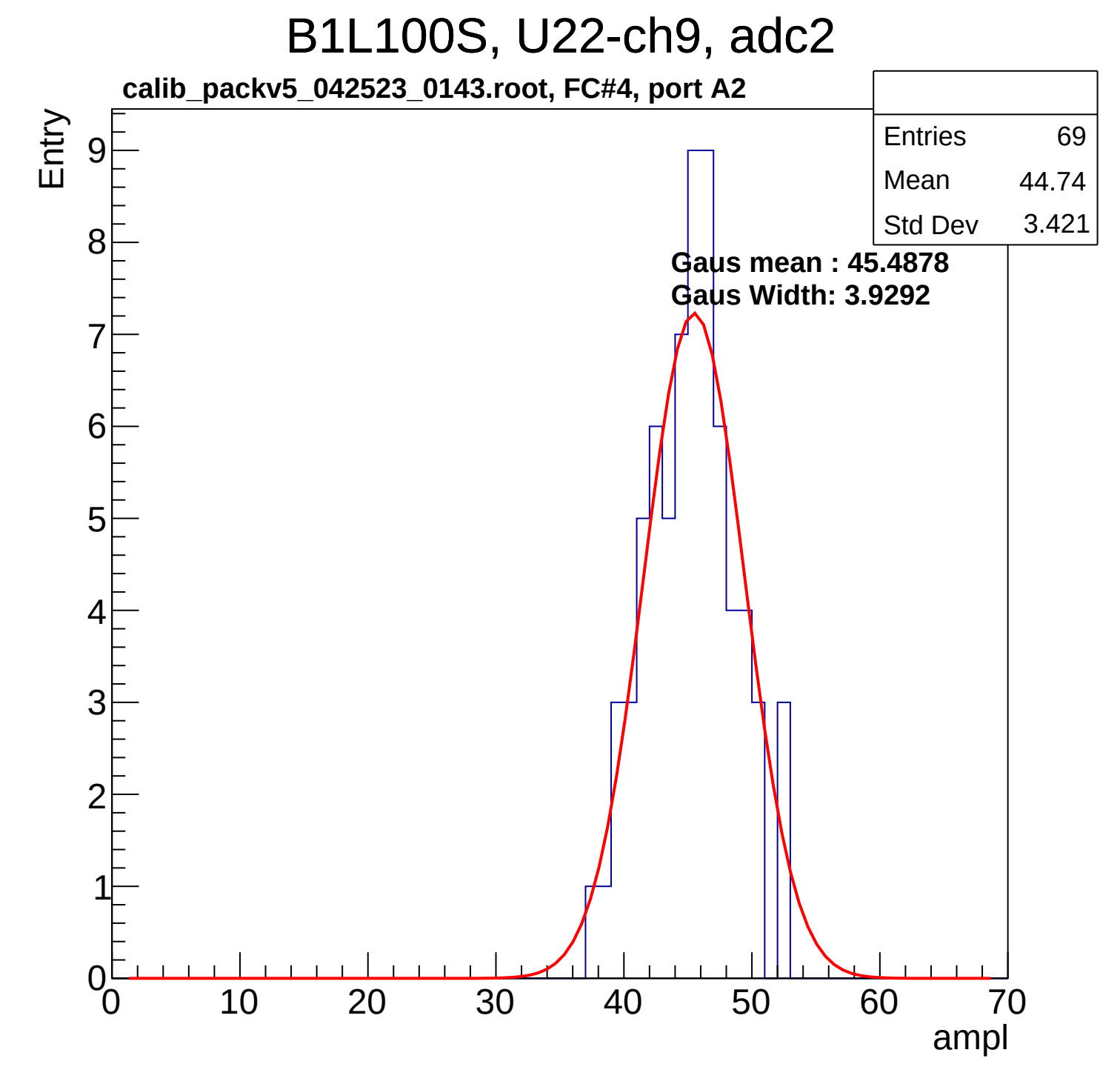
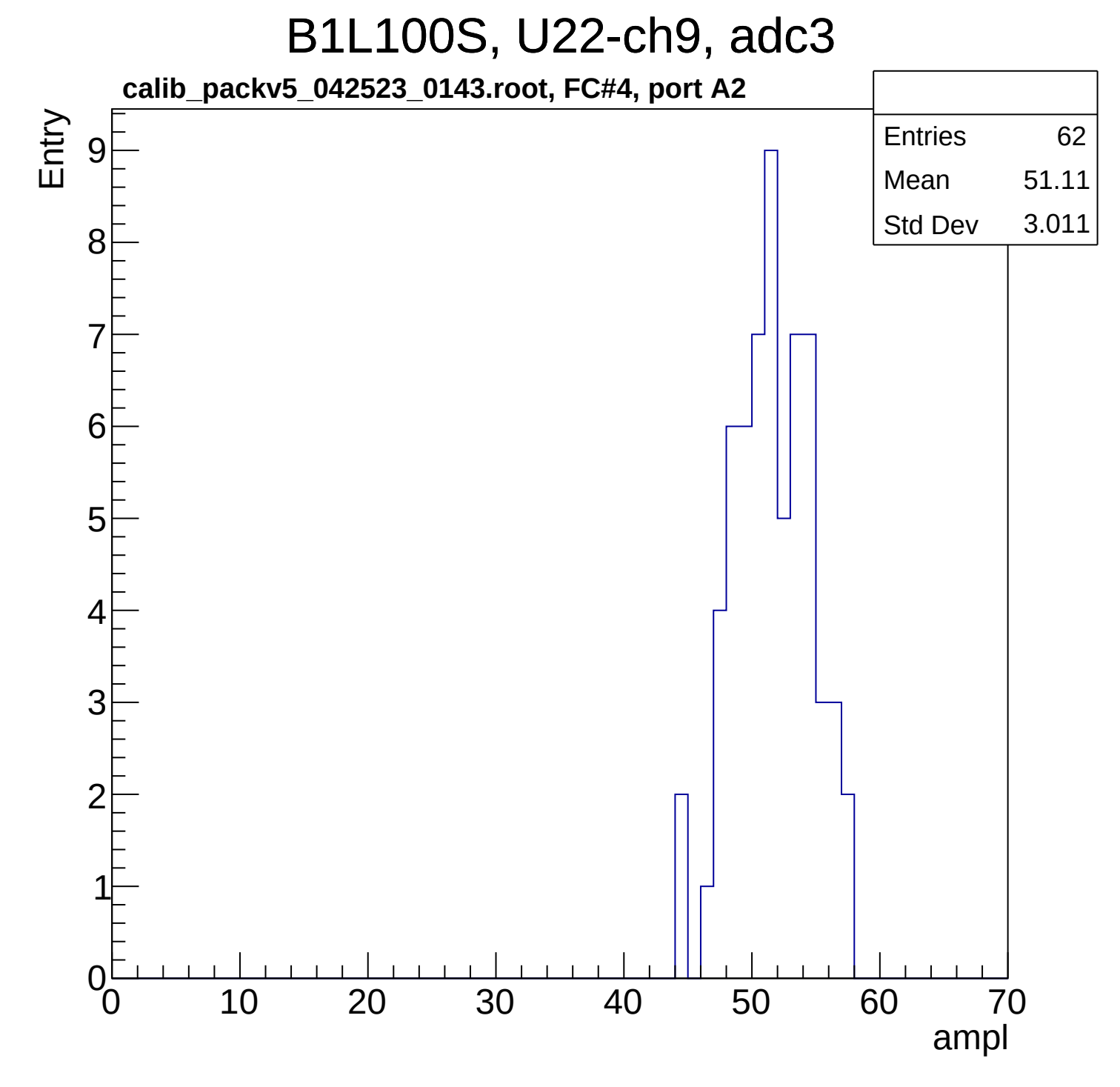

B1L100S, U22-ch9, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.74
Std Dev 3.421
Gaus mean : 45.4878
Gaus Width: 3.9292
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch9, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.11
Std Dev 3.011
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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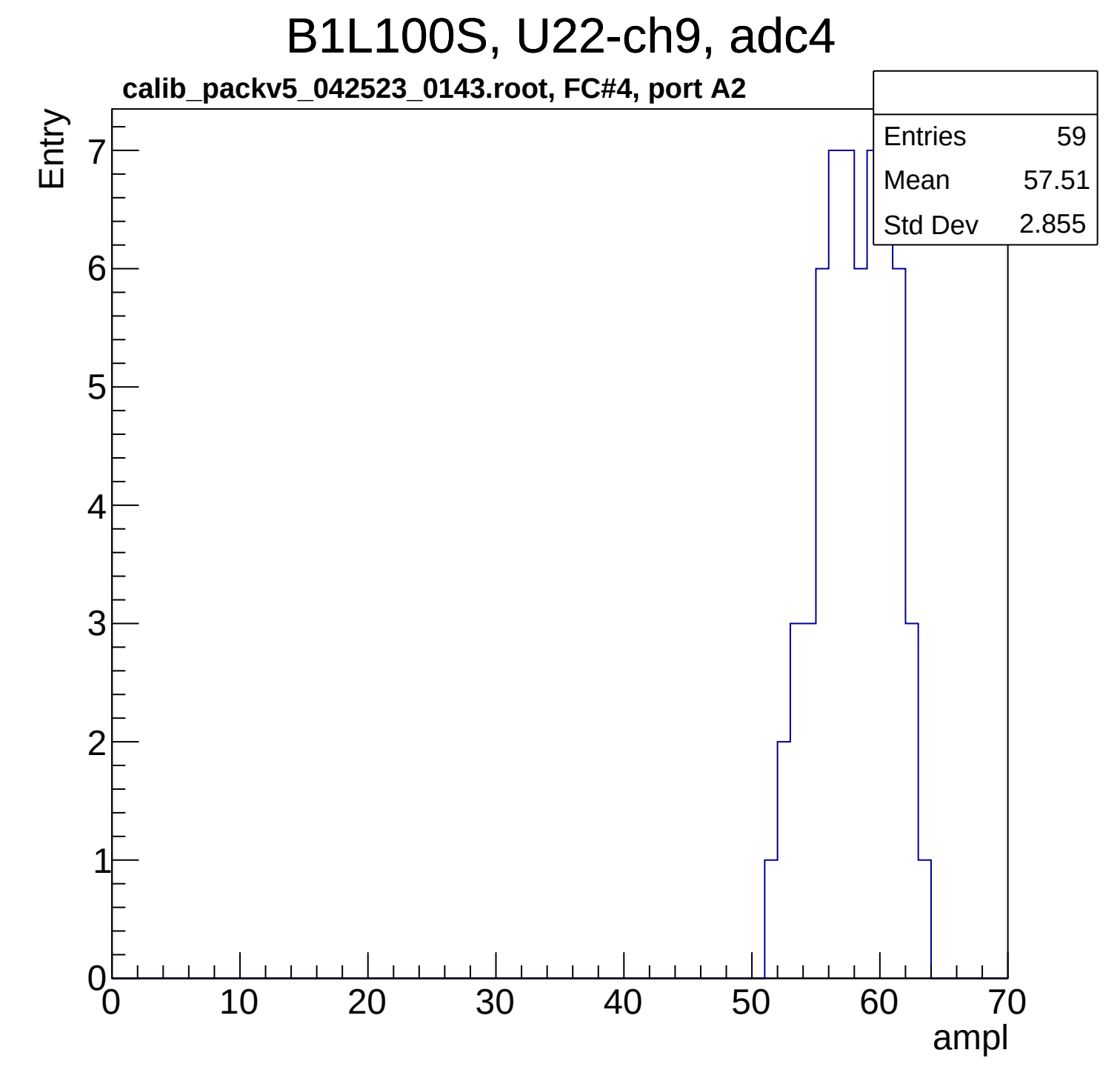
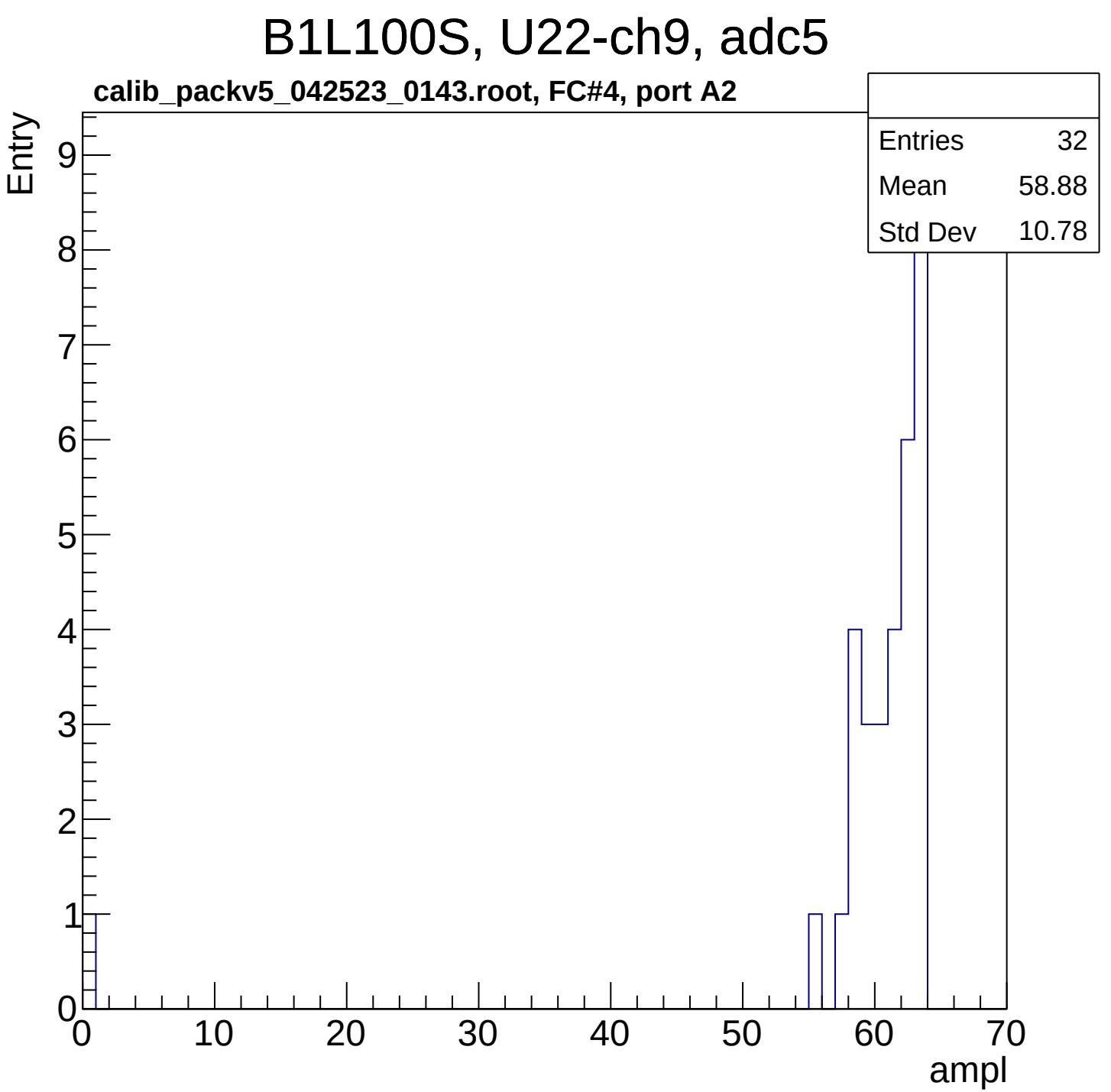

B1L100S, U22-ch9, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 57.51
Std Dev 2.855
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch9, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.88
Std Dev 10.78
Entry
ampl
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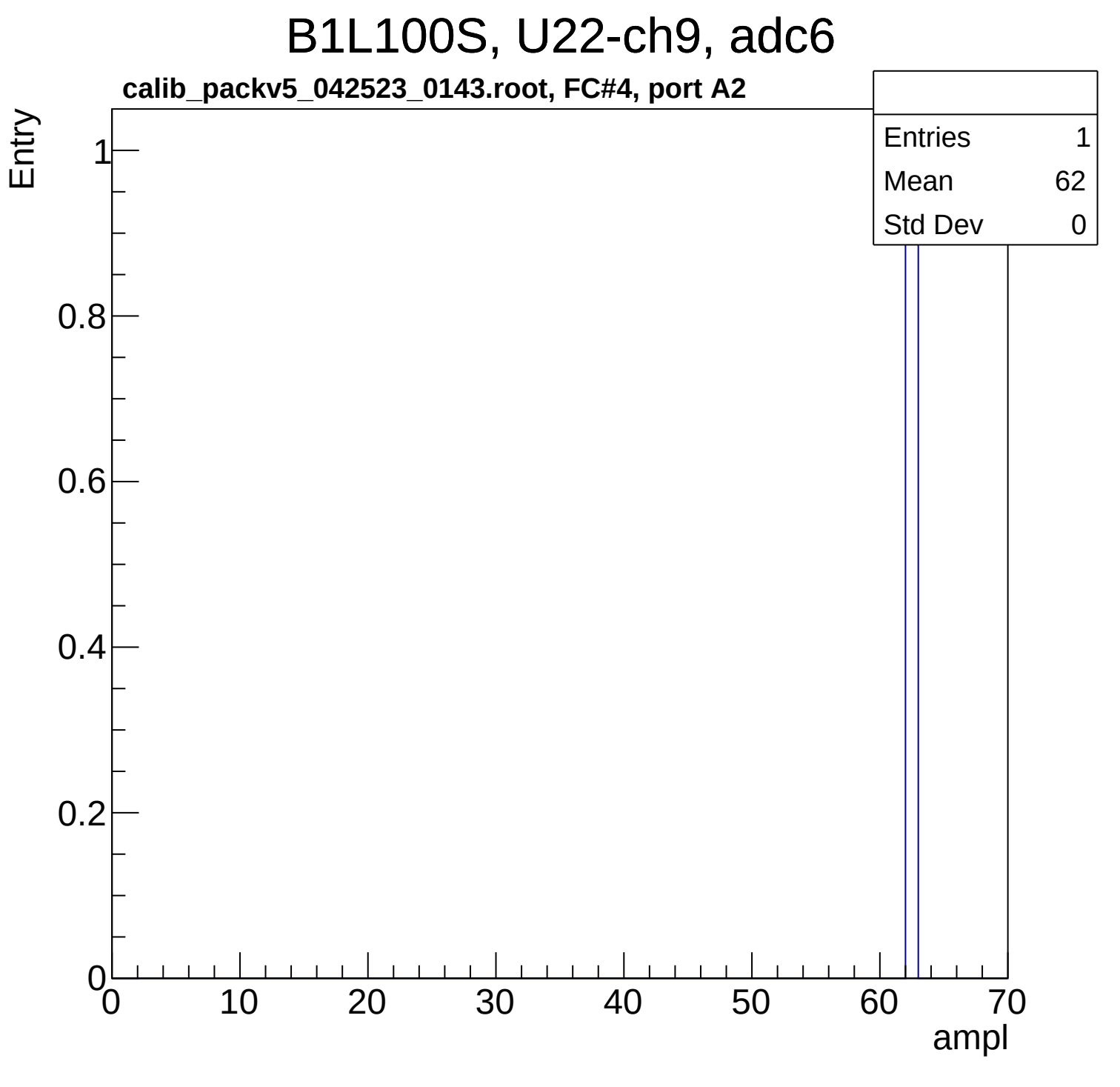
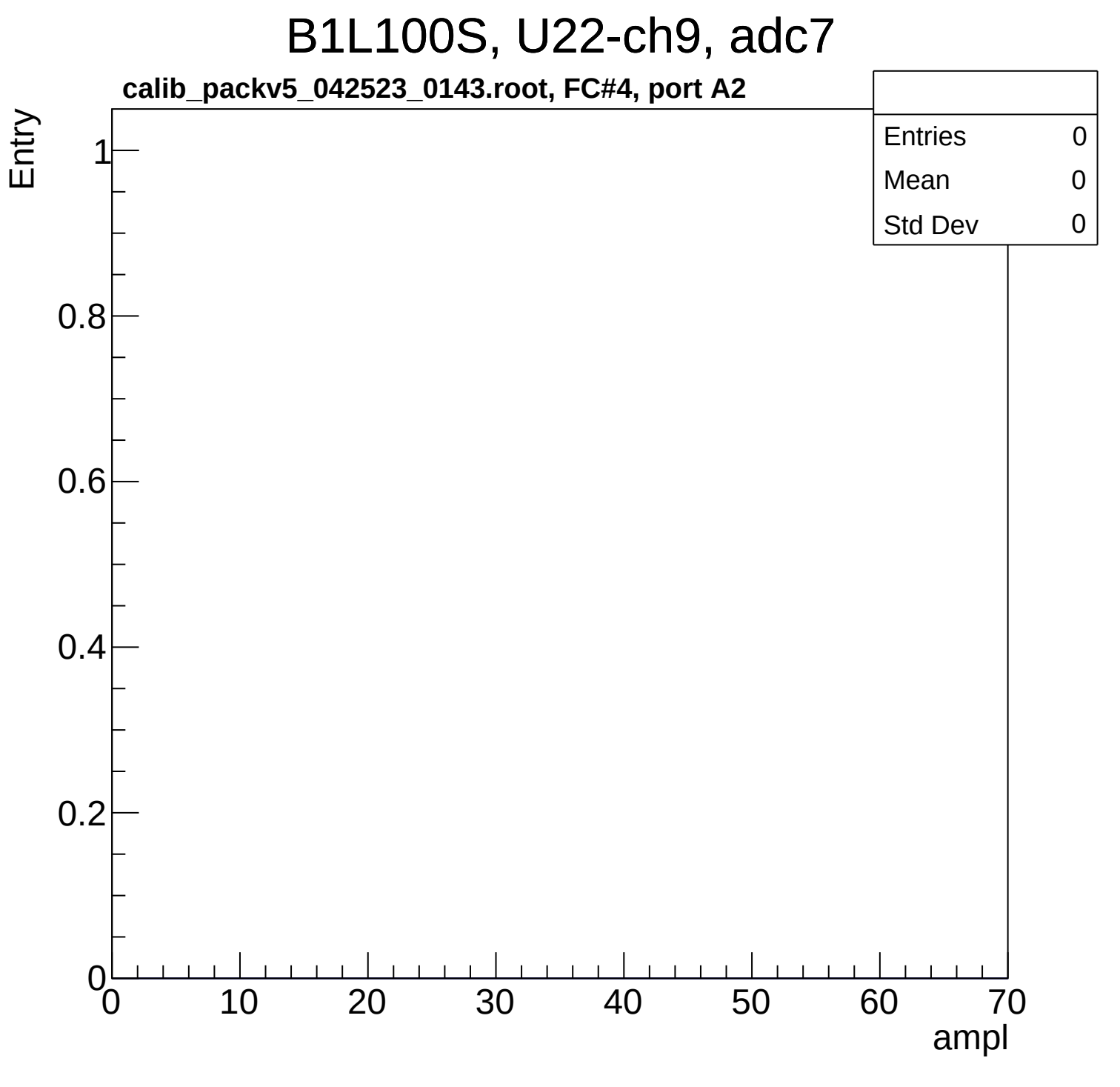
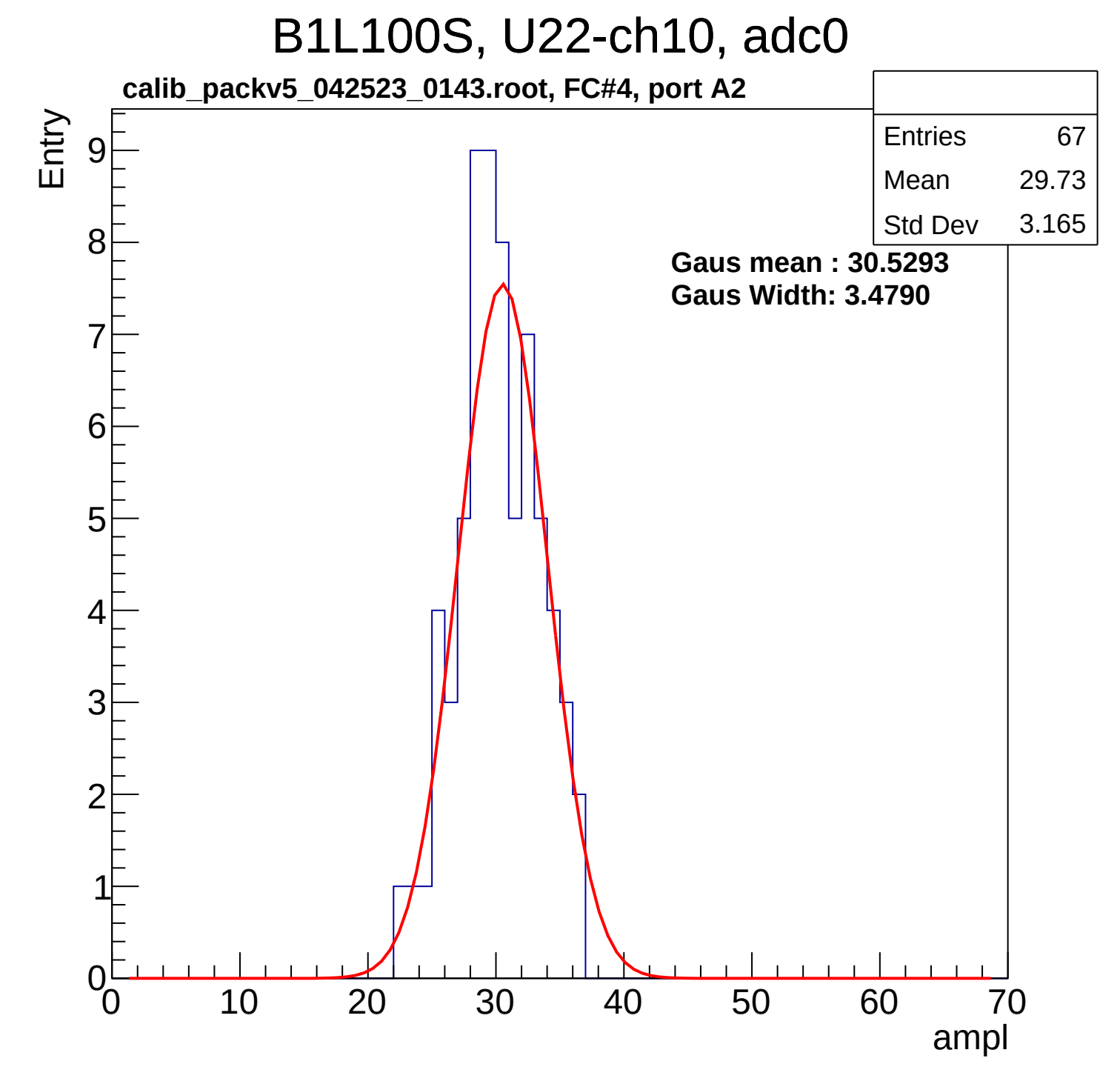
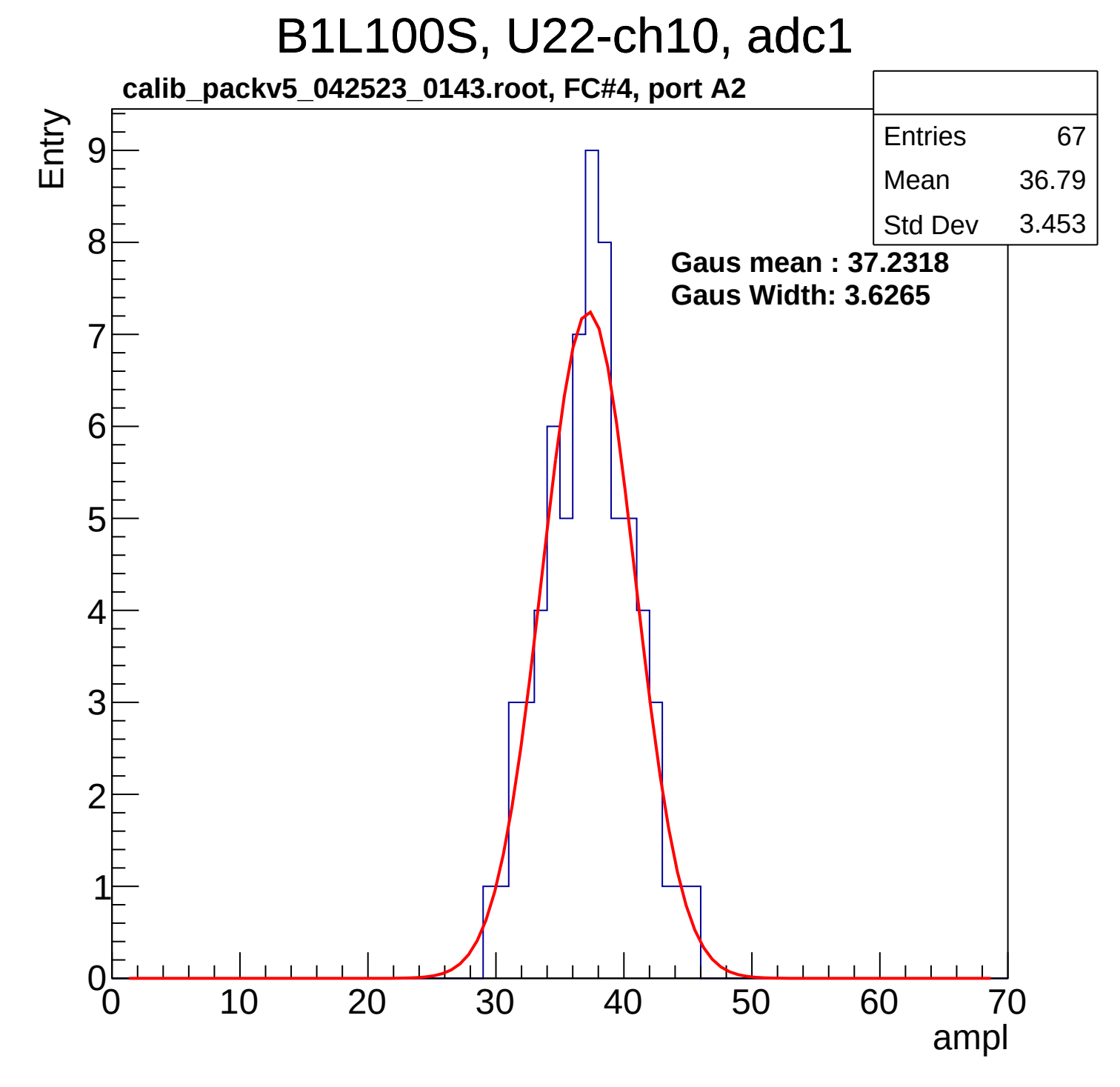
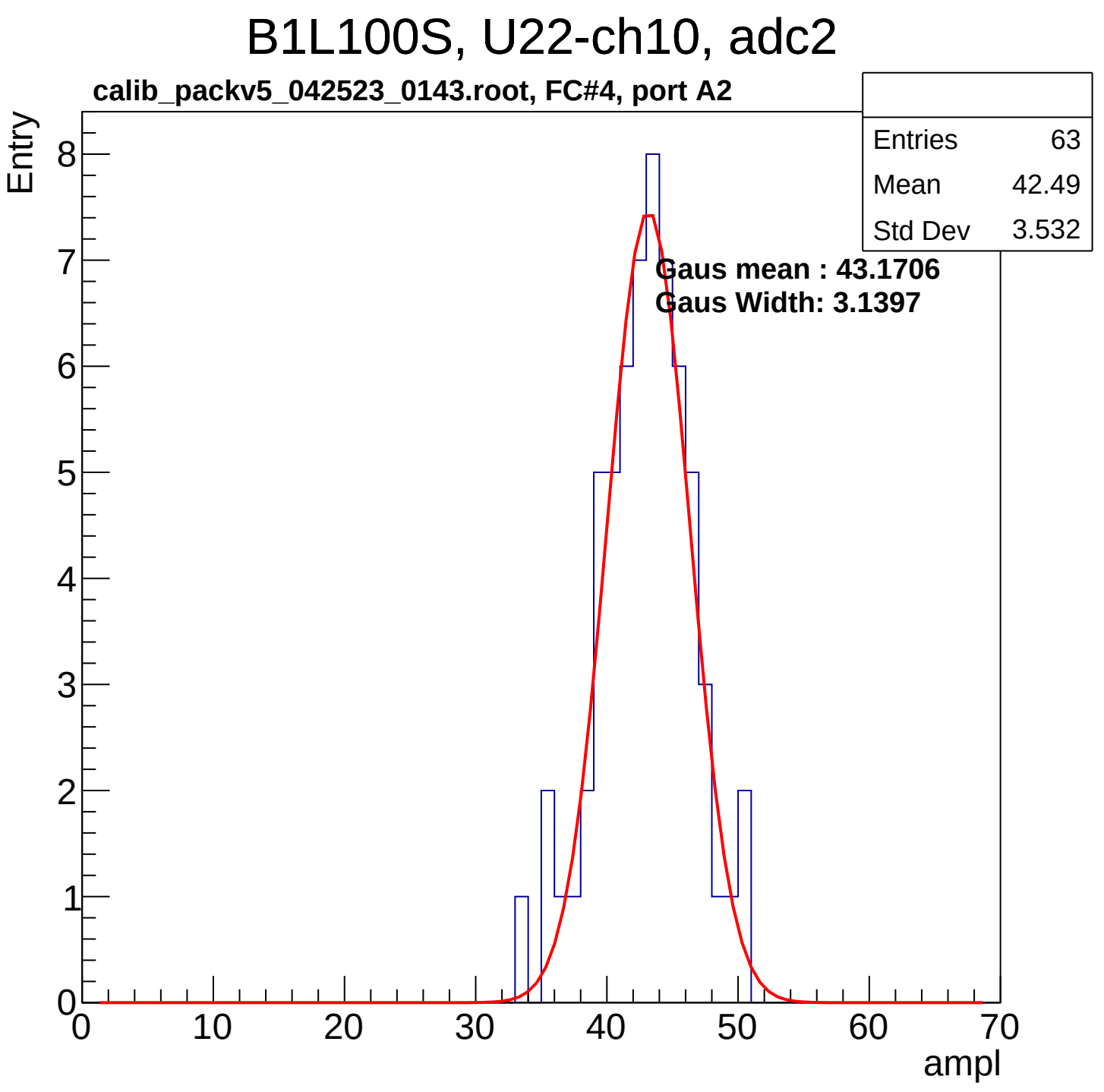
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

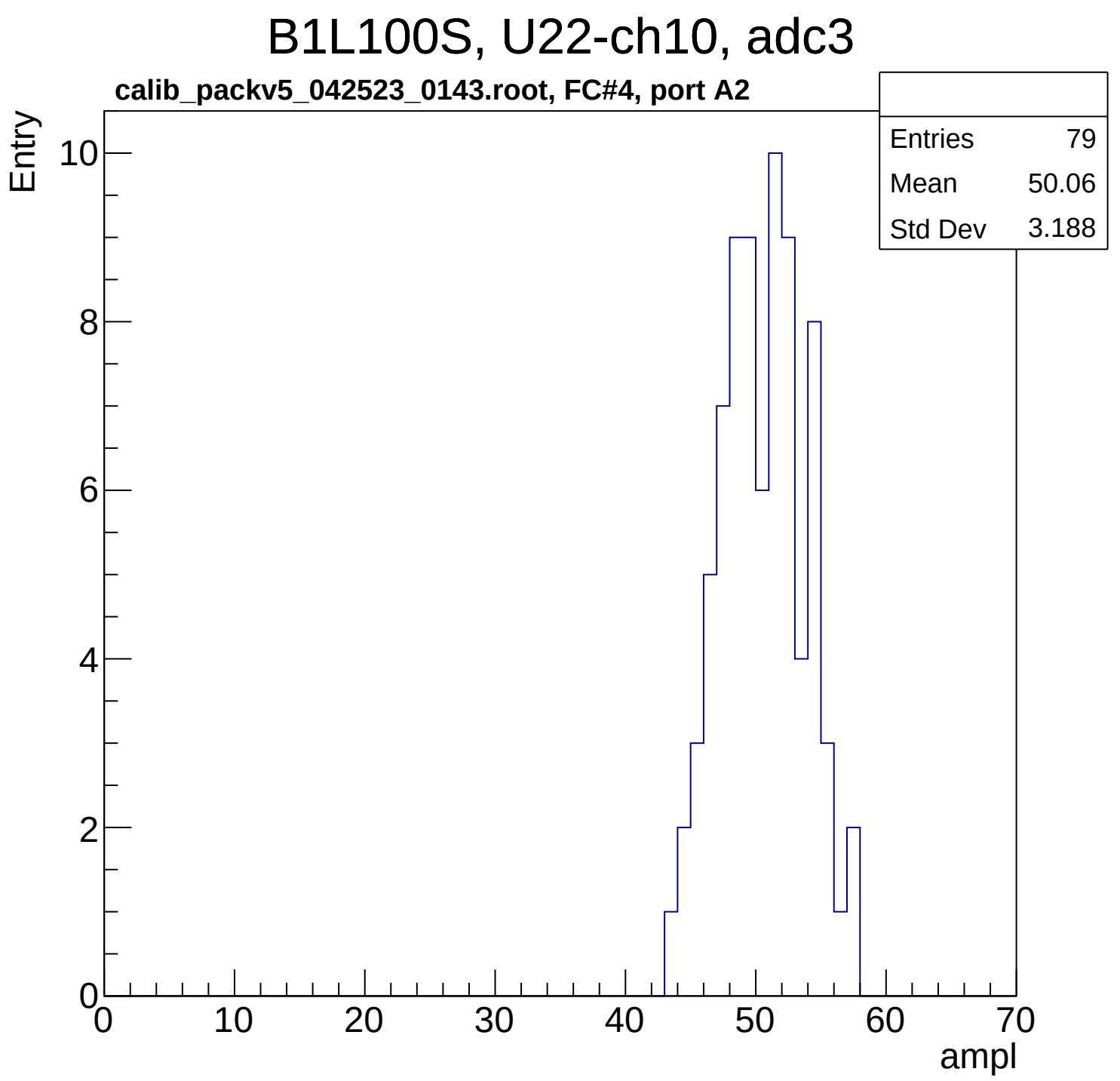

B1L100S, U22-ch9, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

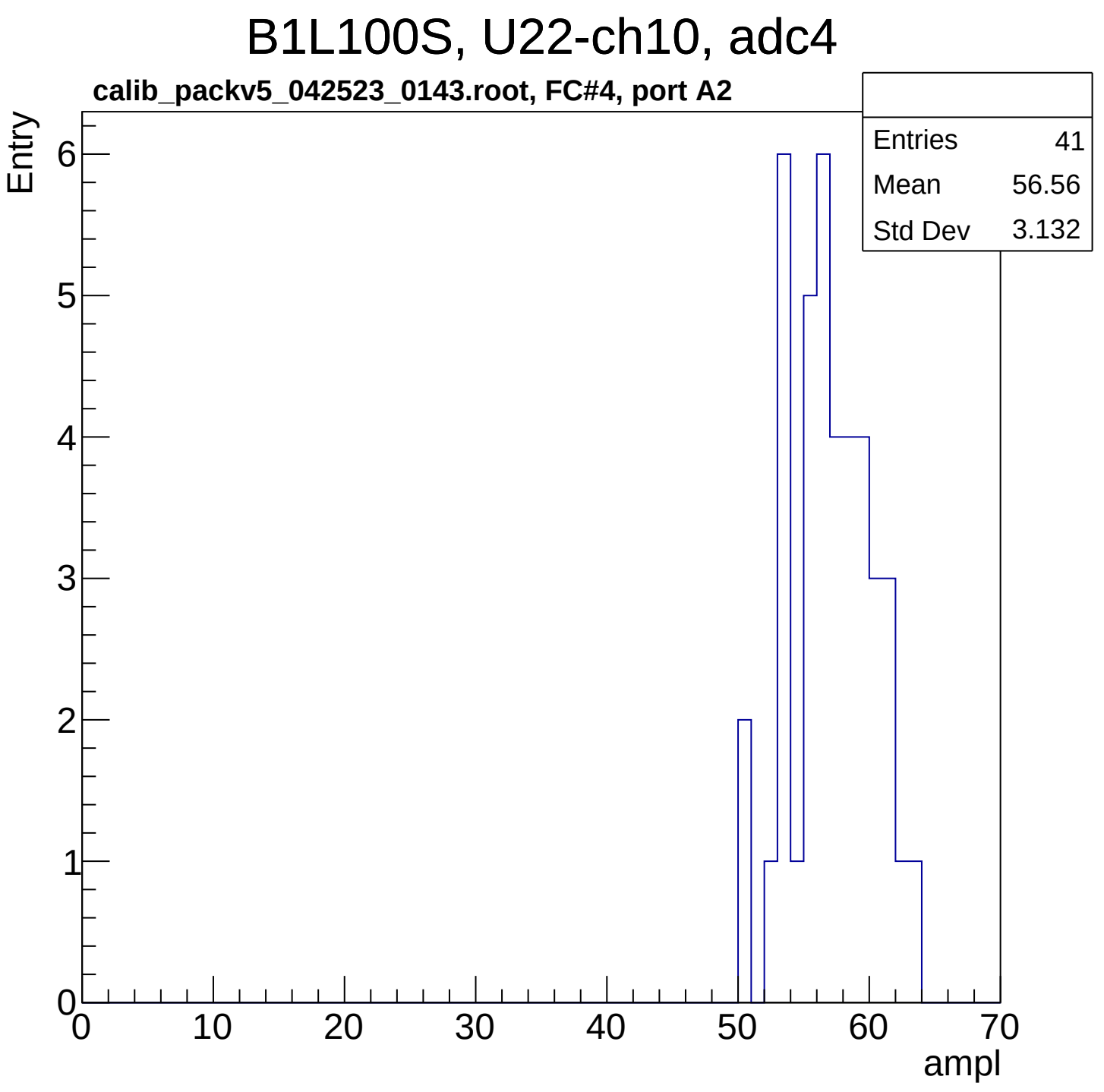

B1L100S, U22-ch9, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

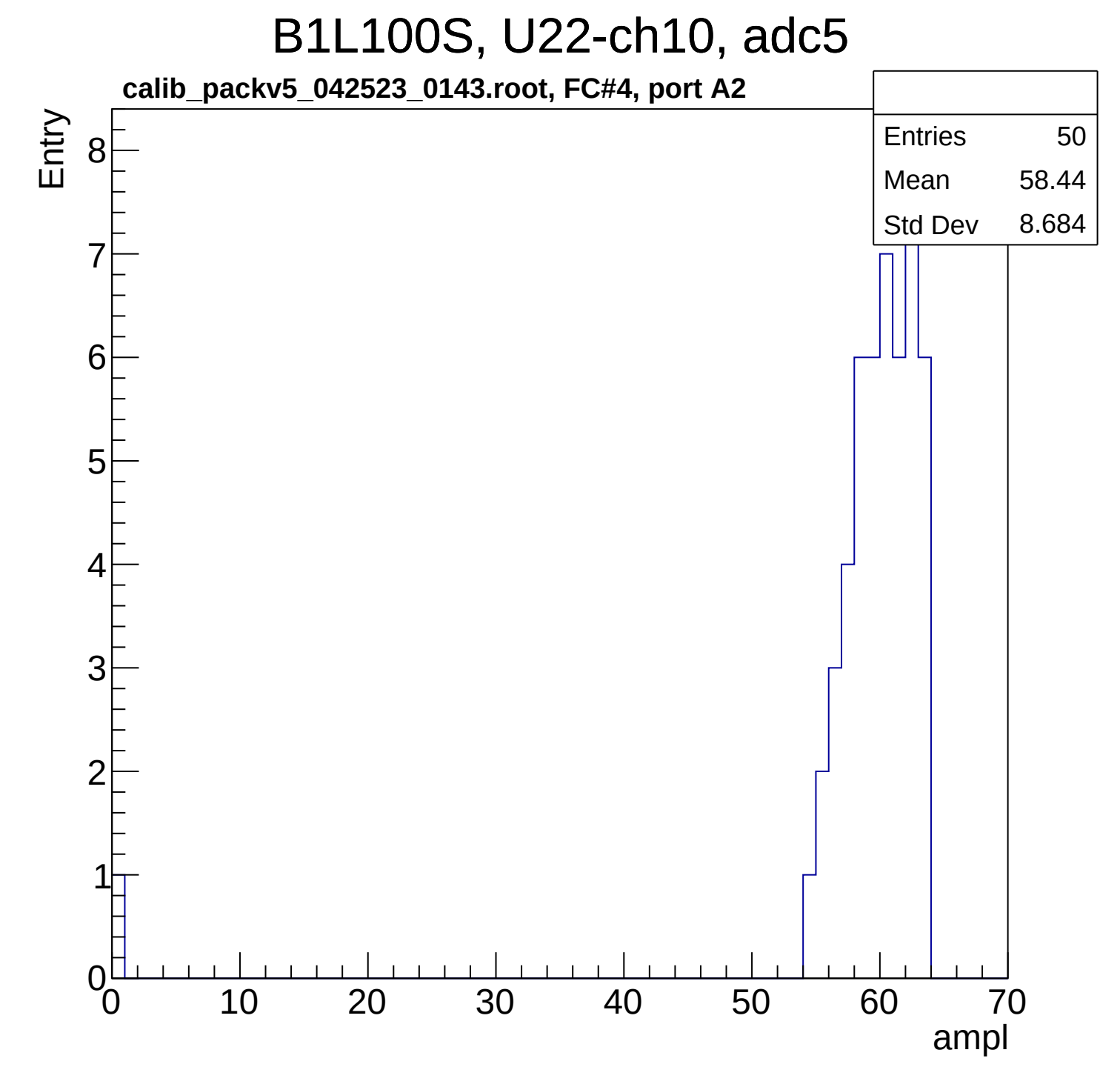

B1L100S, U22-ch10, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.73
Std Dev 3.165
Gaus mean : 30.5293
Gaus Width: 3.4790
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch10, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.79
Std Dev 3.453
Gaus mean : 37.2318
Gaus Width: 3.6265
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch10, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.49
Std Dev 3.532
Gaus mean : 43.1706
Gaus Width: 3.1397
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch10, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 50.06
Std Dev 3.188
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch10, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 56.56
Std Dev 3.132
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch10, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.44
Std Dev 8.684
Entry
ampl
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2
3
4
5
6
7
8
0
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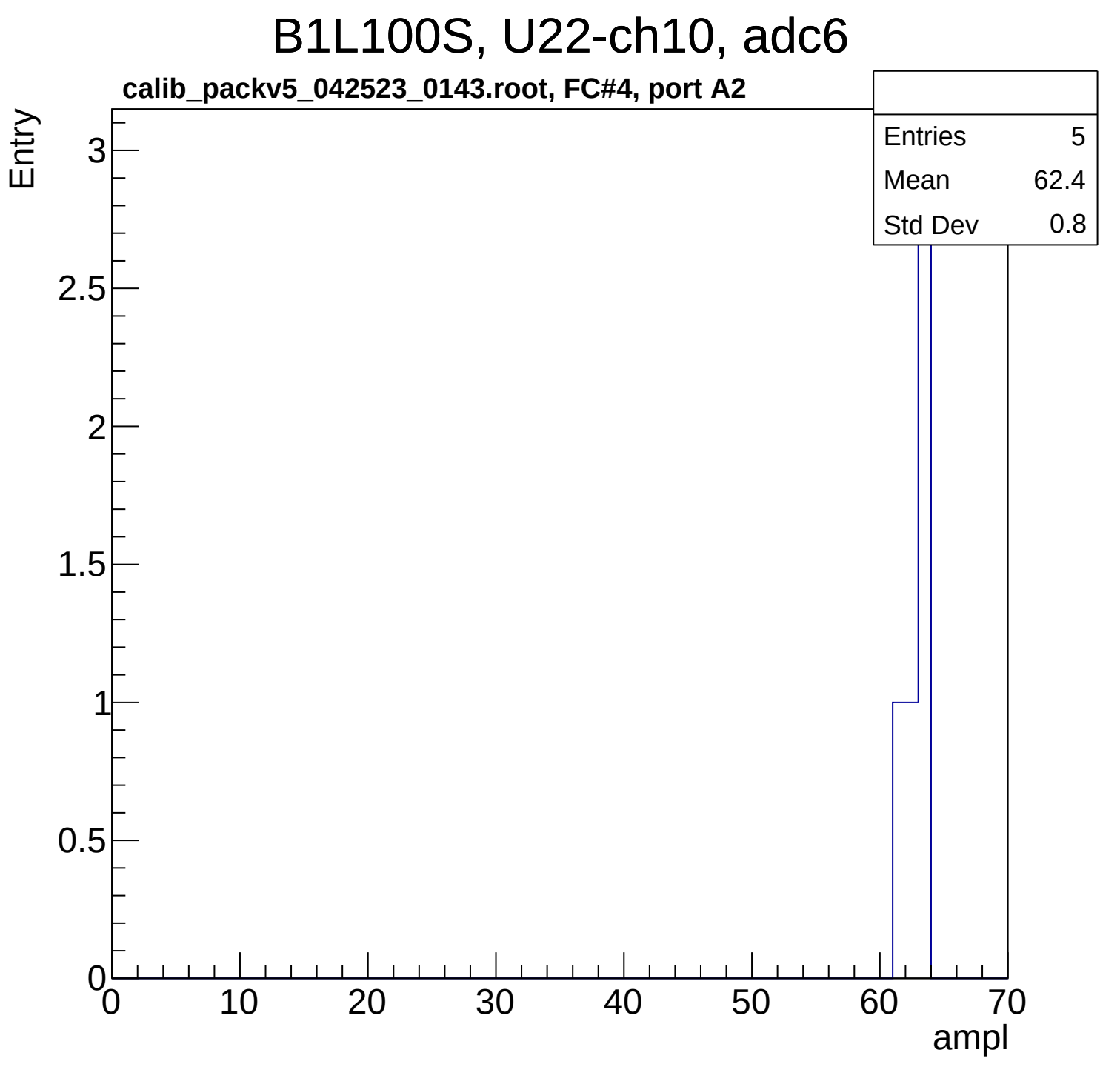
20
30
40
50
60
70


B1L100S, U22-ch10, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 0.8
Entry
ampl
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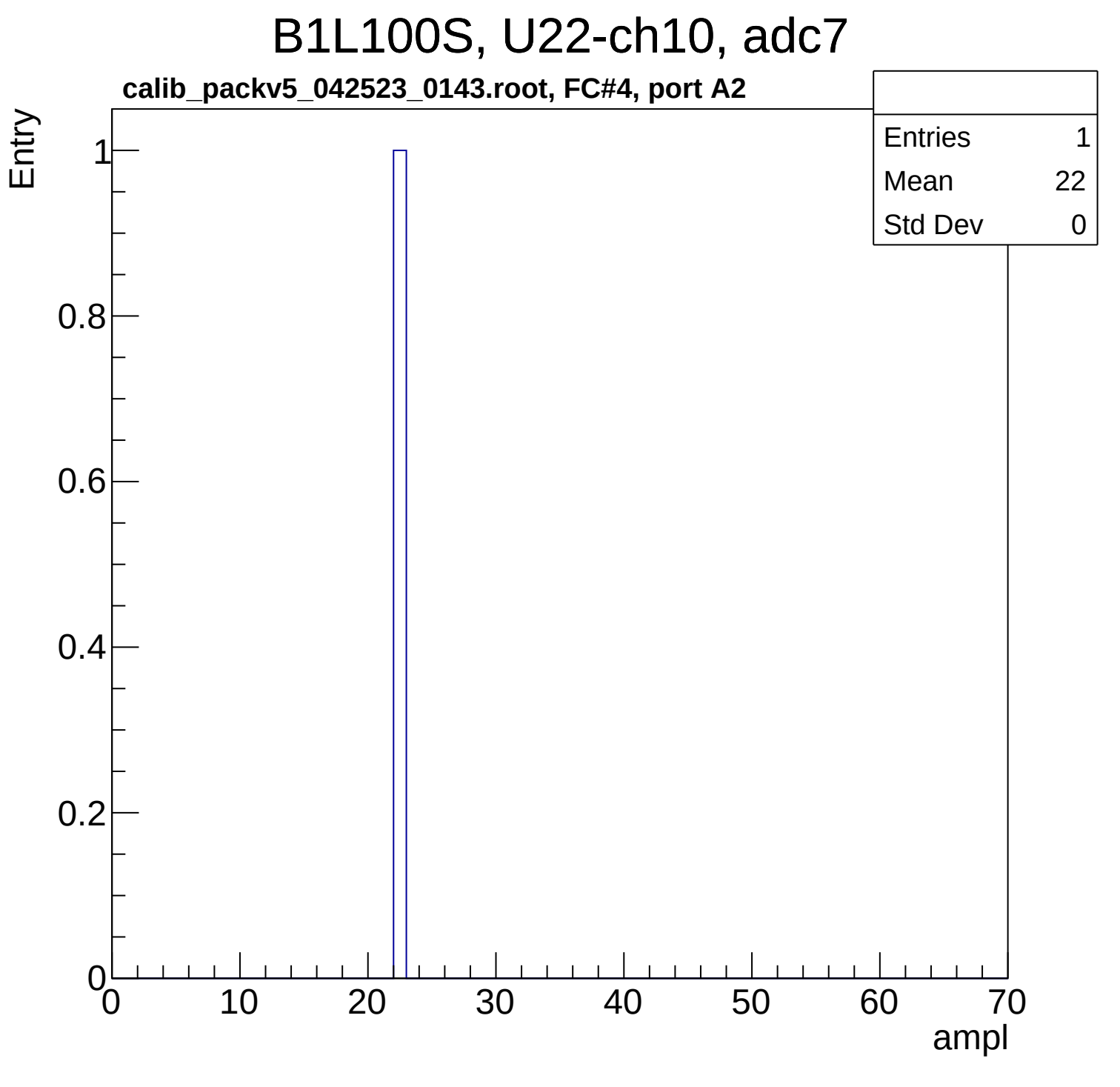
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch10, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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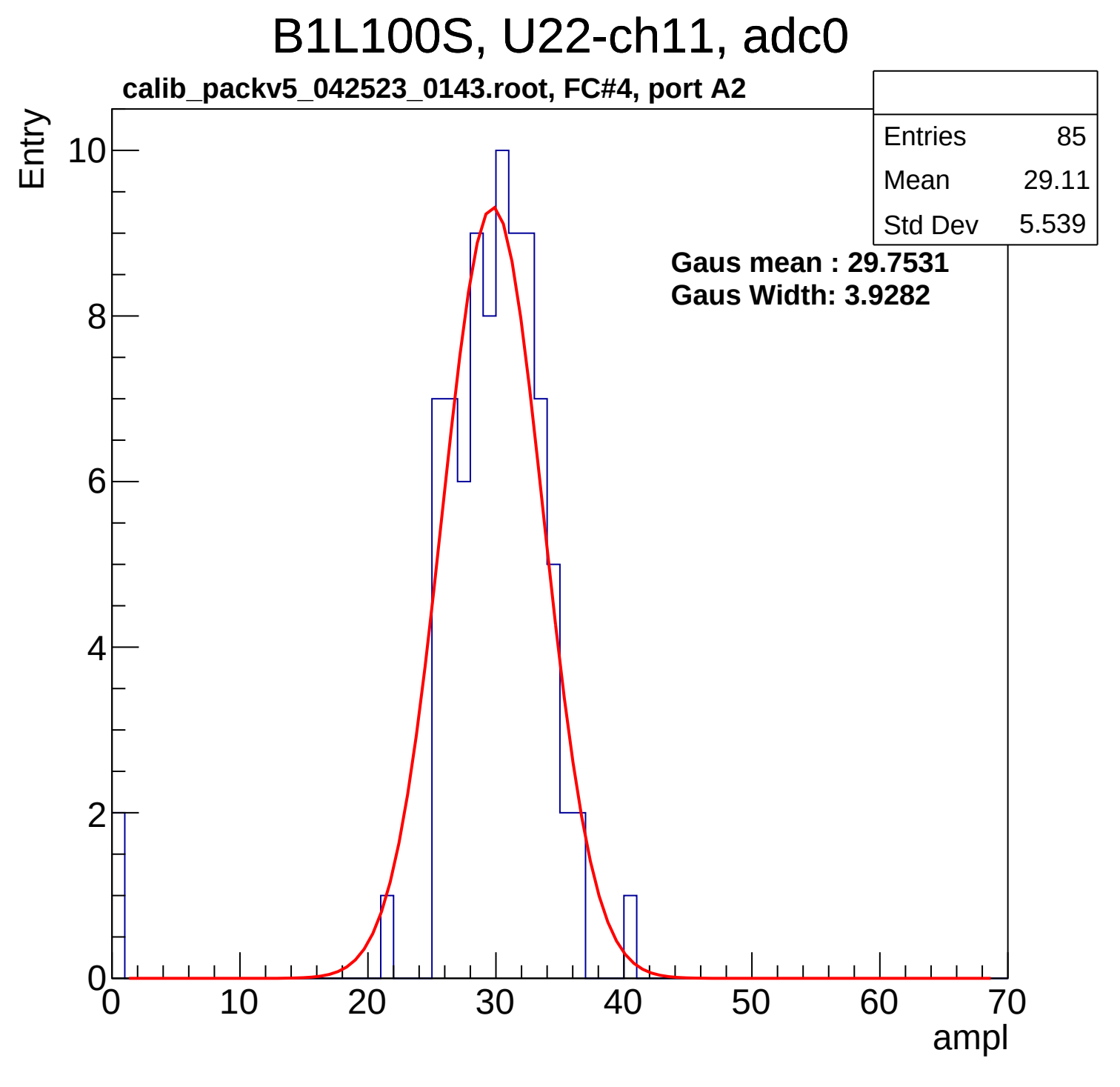
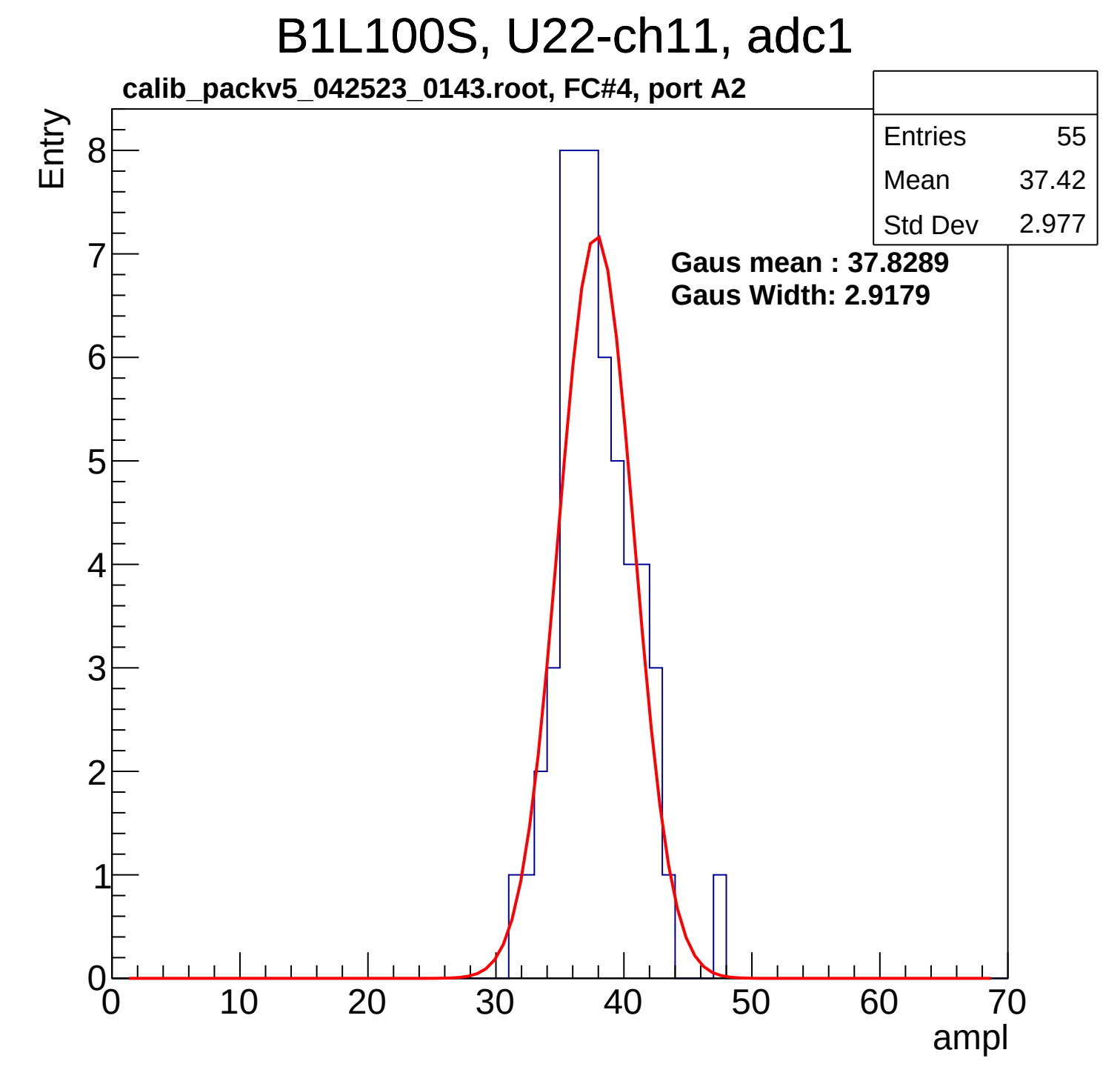
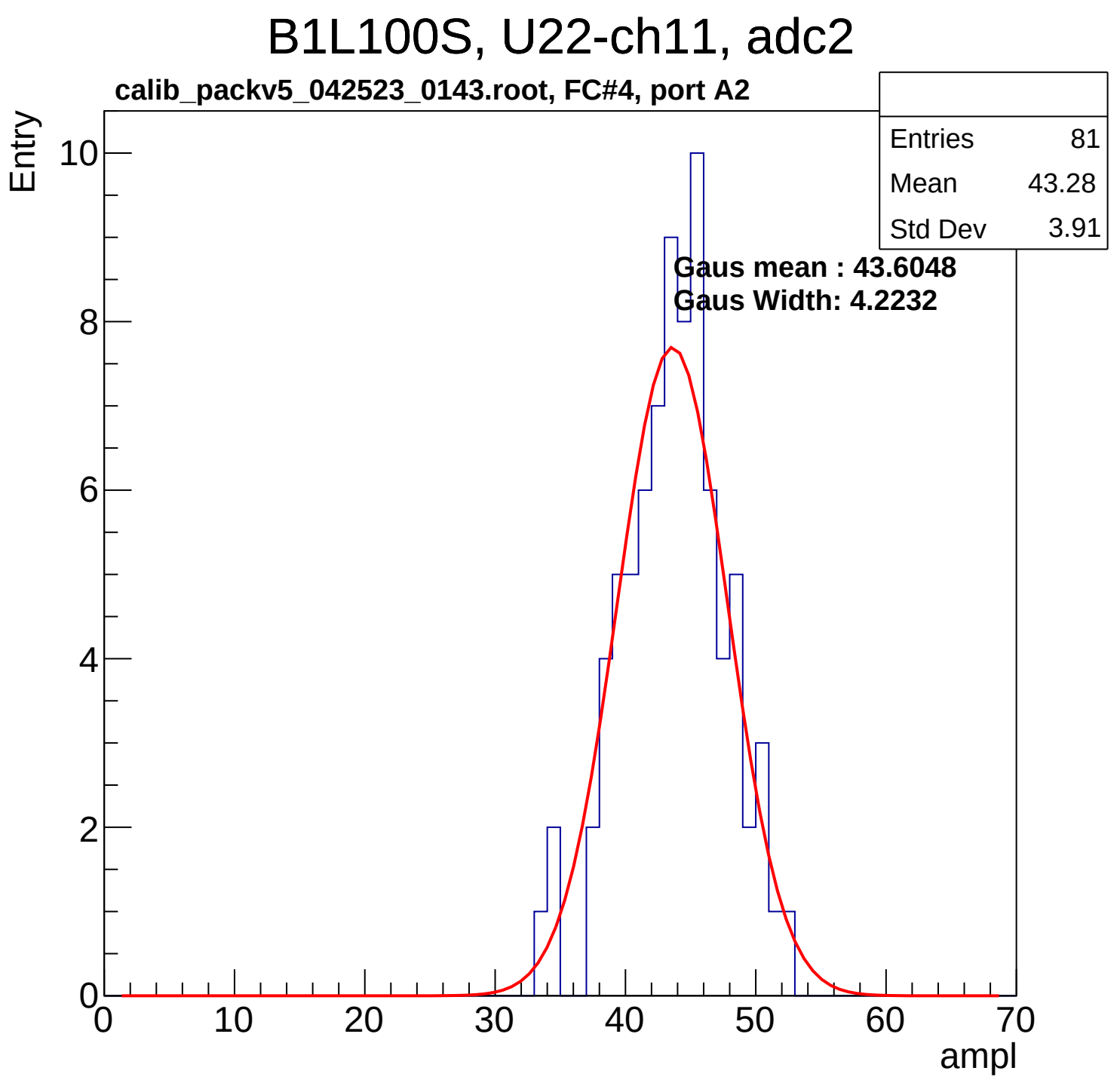
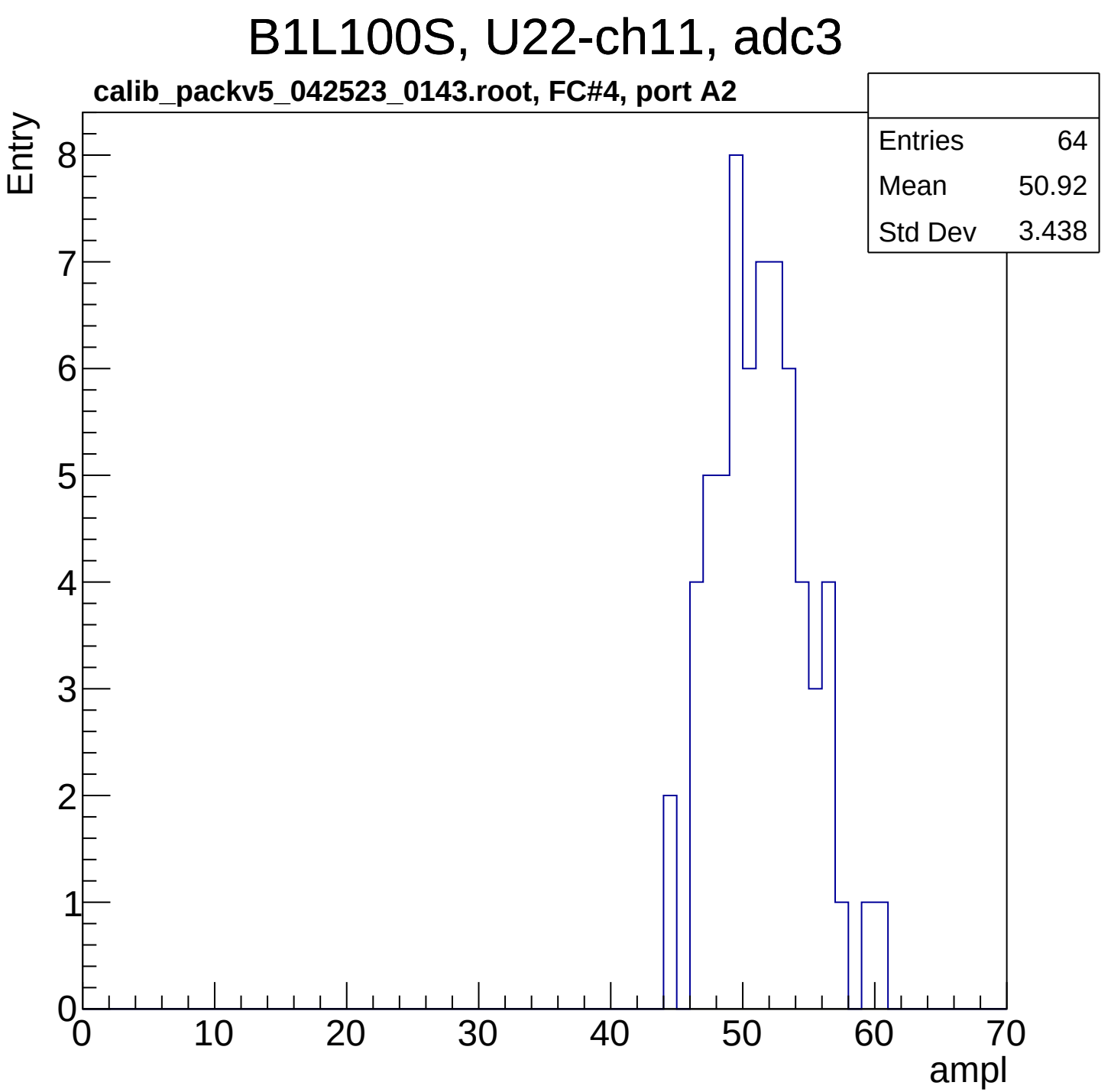
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch11, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 29.11
Std Dev 5.539
Gaus mean : 29.7531
Gaus Width: 3.9282
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch11, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 37.42
Std Dev 2.977
Gaus mean : 37.8289
Gaus Width: 2.9179
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch11, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 43.28
Std Dev 3.91
Gaus mean : 43.6048
Gaus Width: 4.2232
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch11, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.92
Std Dev 3.438
Entry
ampl
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1
2
3
4
5
6
7
8
0
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60
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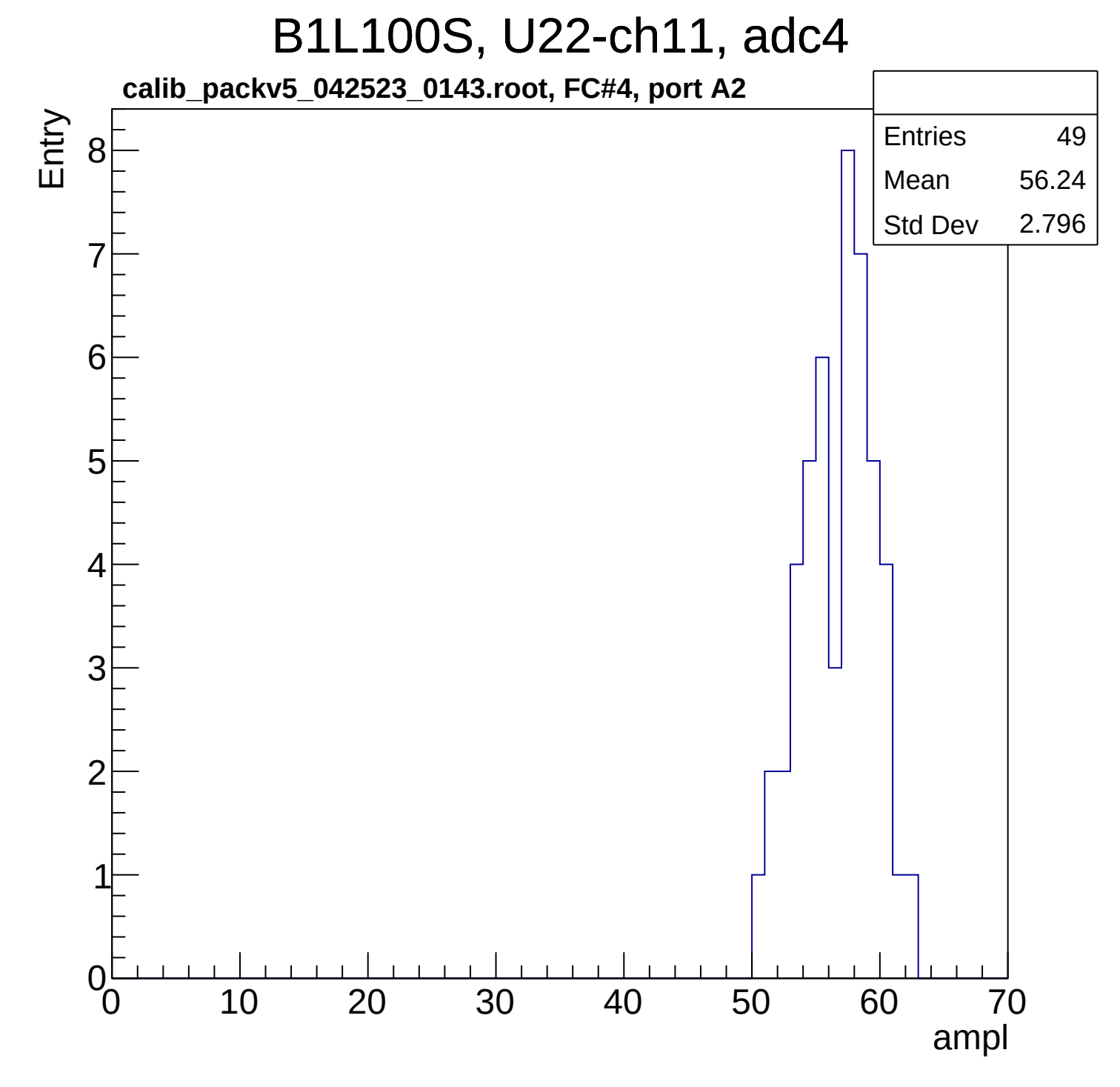
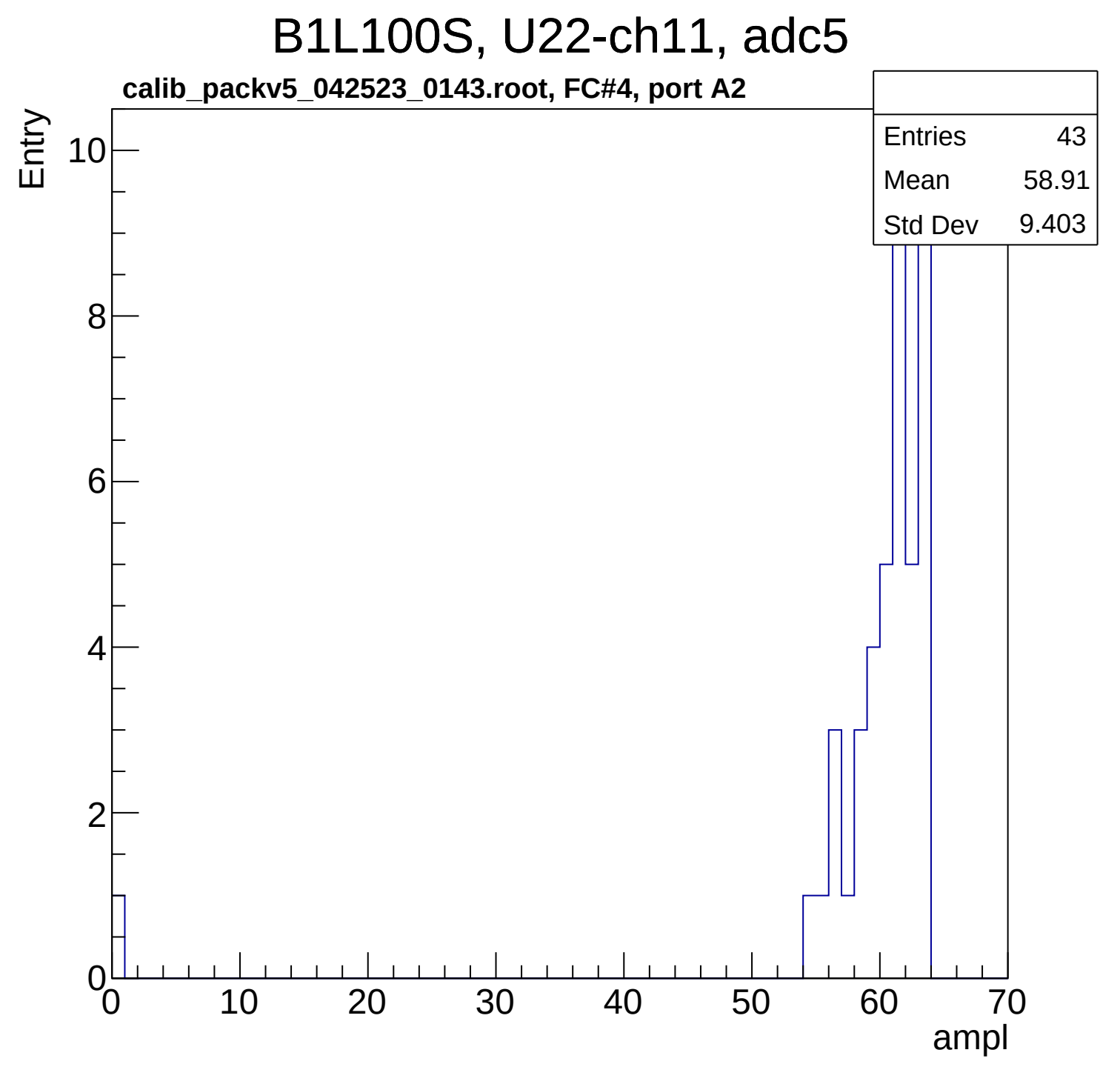

B1L100S, U22-ch11, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.24
Std Dev 2.796
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch11, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.91
Std Dev 9.403
Entry
ampl
0
2
4
6
8
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0
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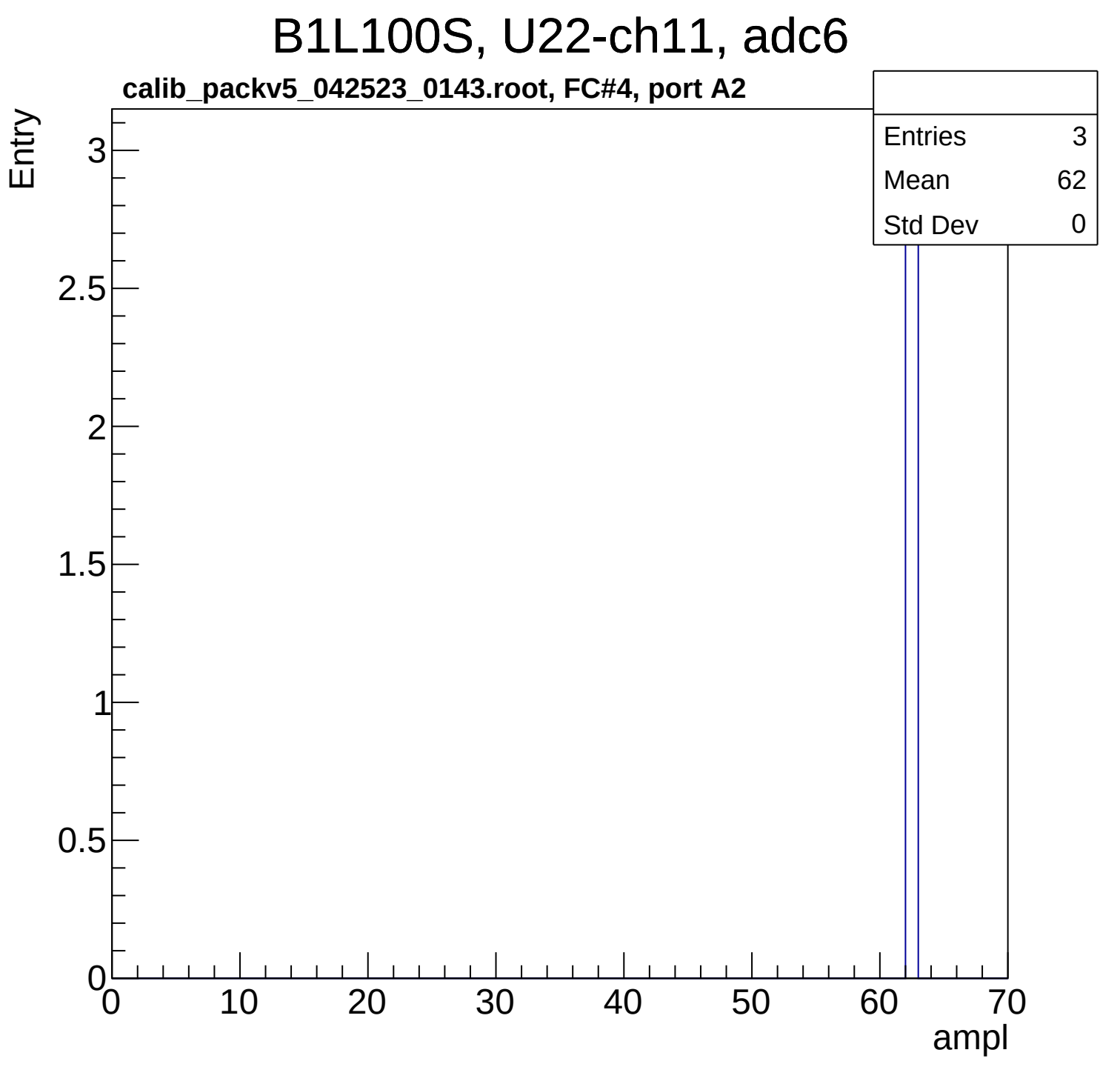
40
50
60
70


B1L100S, U22-ch11, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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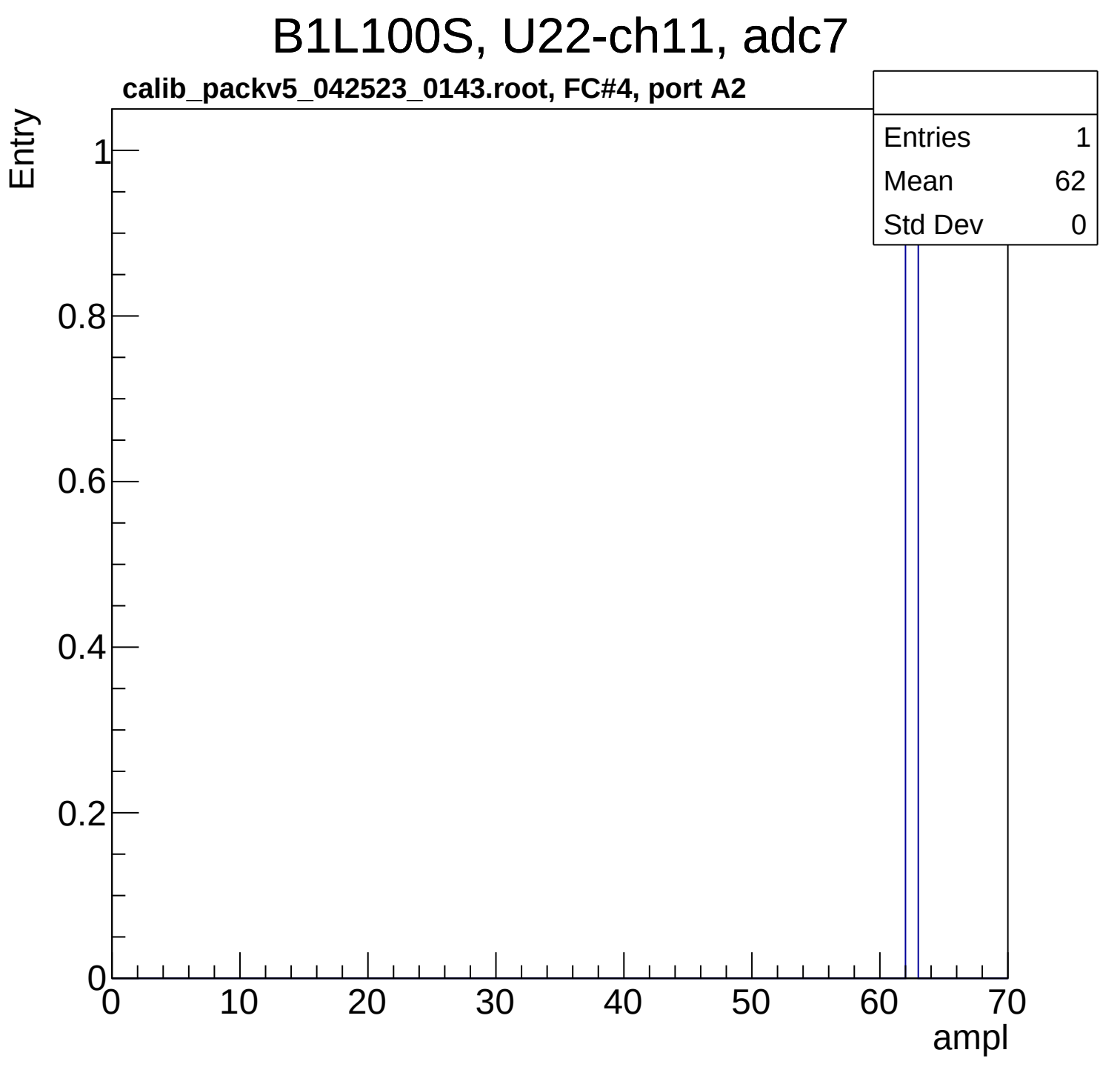
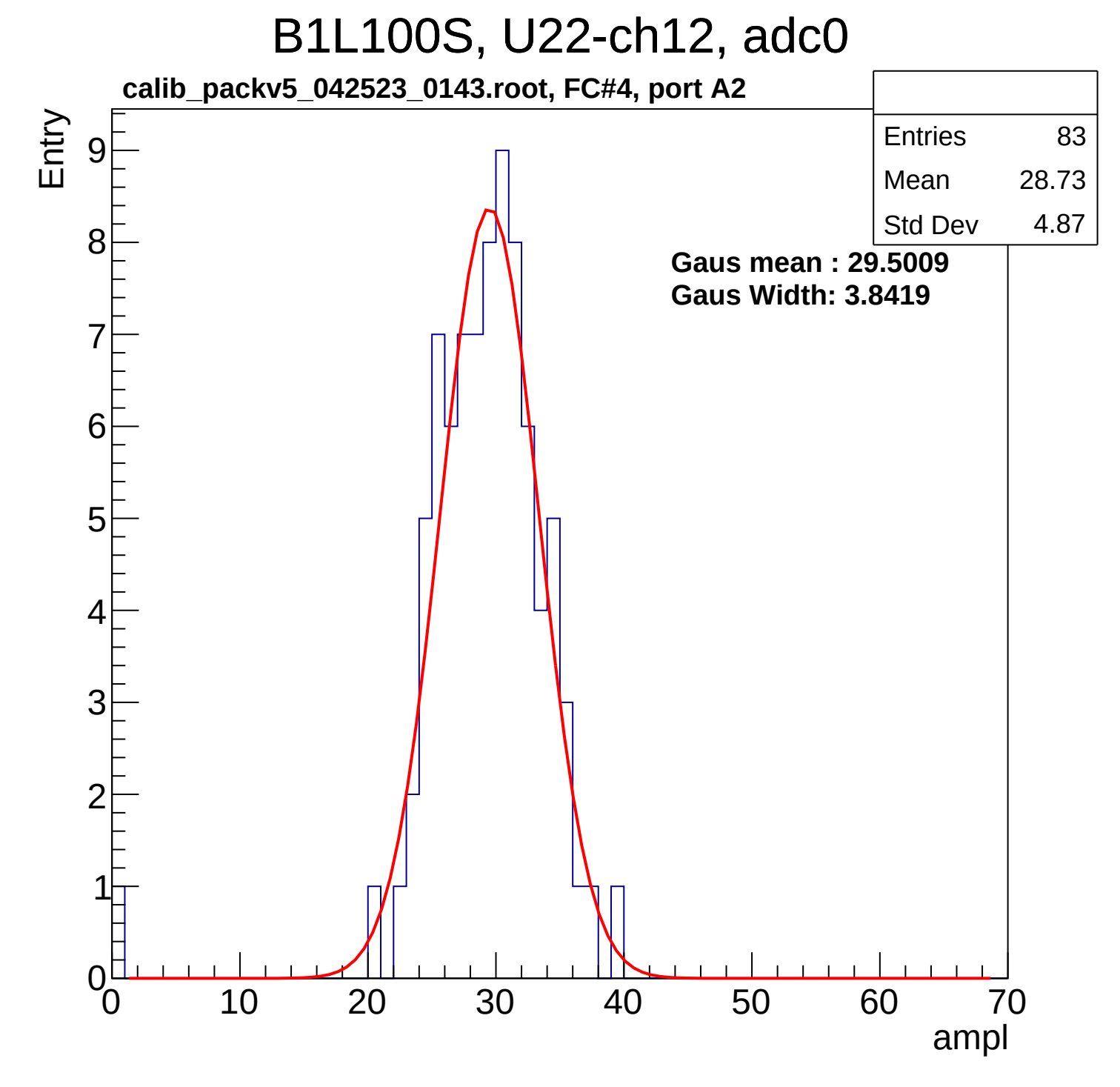

B1L100S, U22-ch11, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch12, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 28.73
Std Dev 4.87
Gaus mean : 29.5009
Gaus Width: 3.8419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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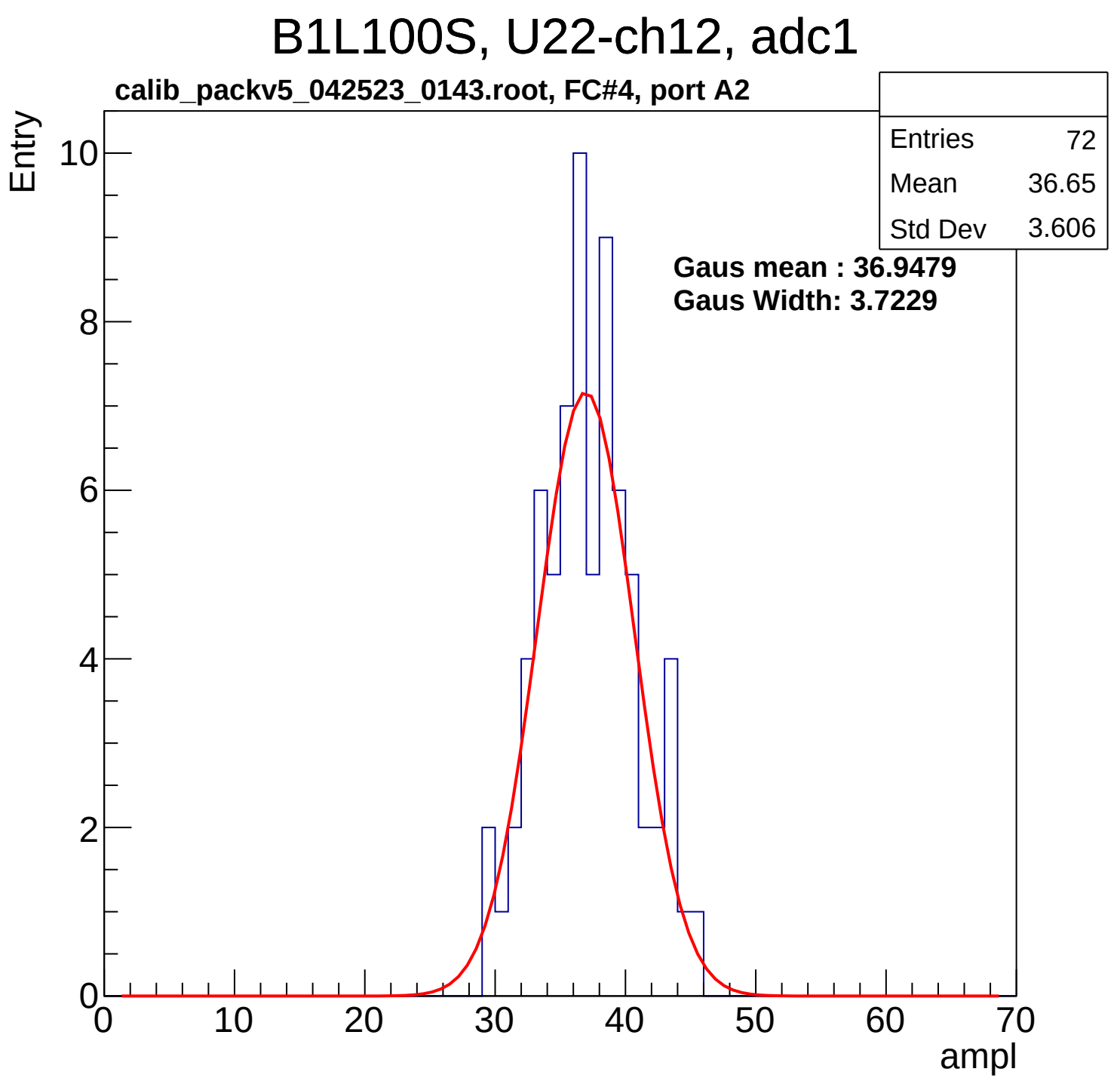
B1L100S, U22-ch12, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.65
Std Dev 3.606
Gaus mean : 36.9479
Gaus Width: 3.7229
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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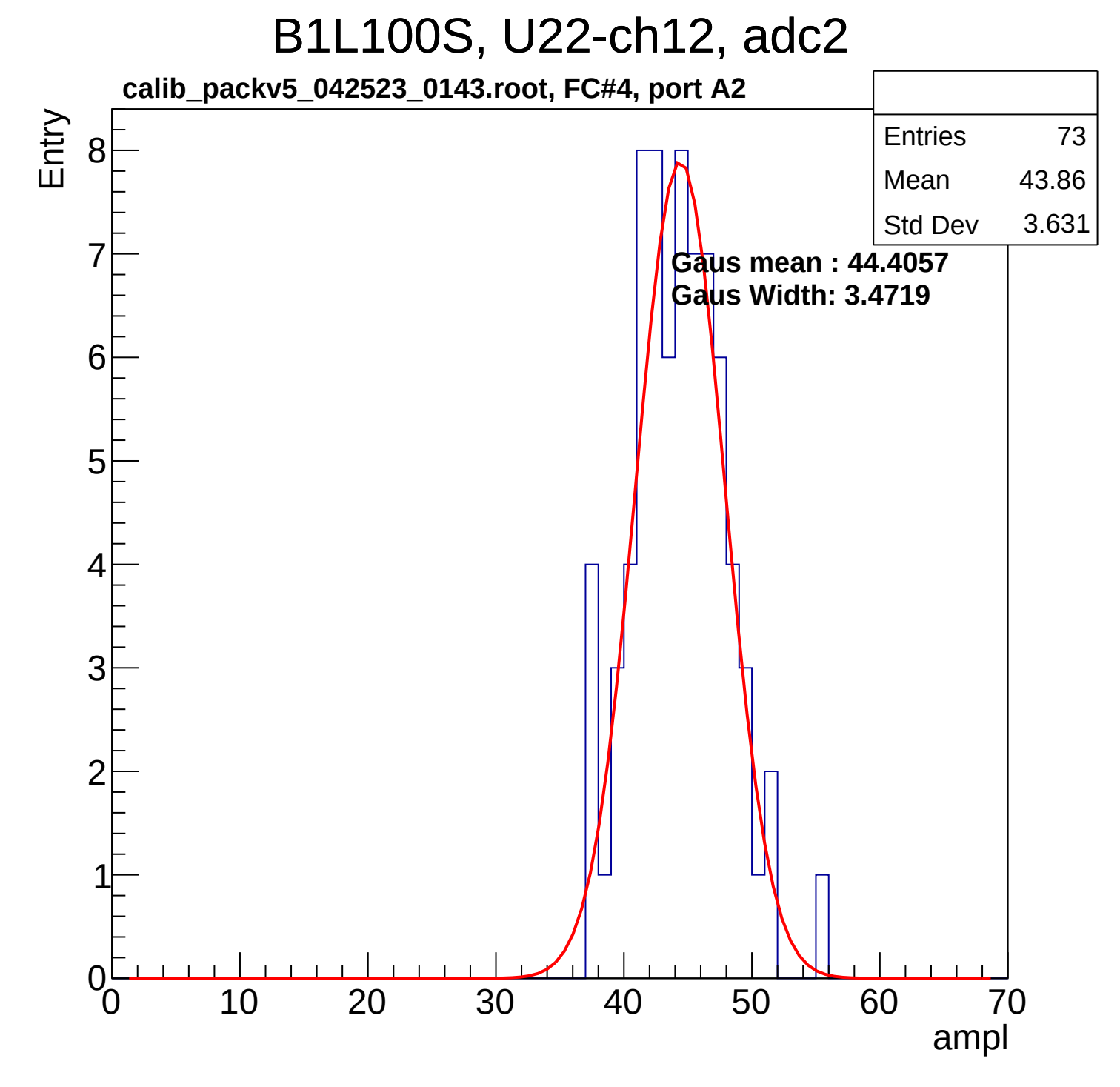
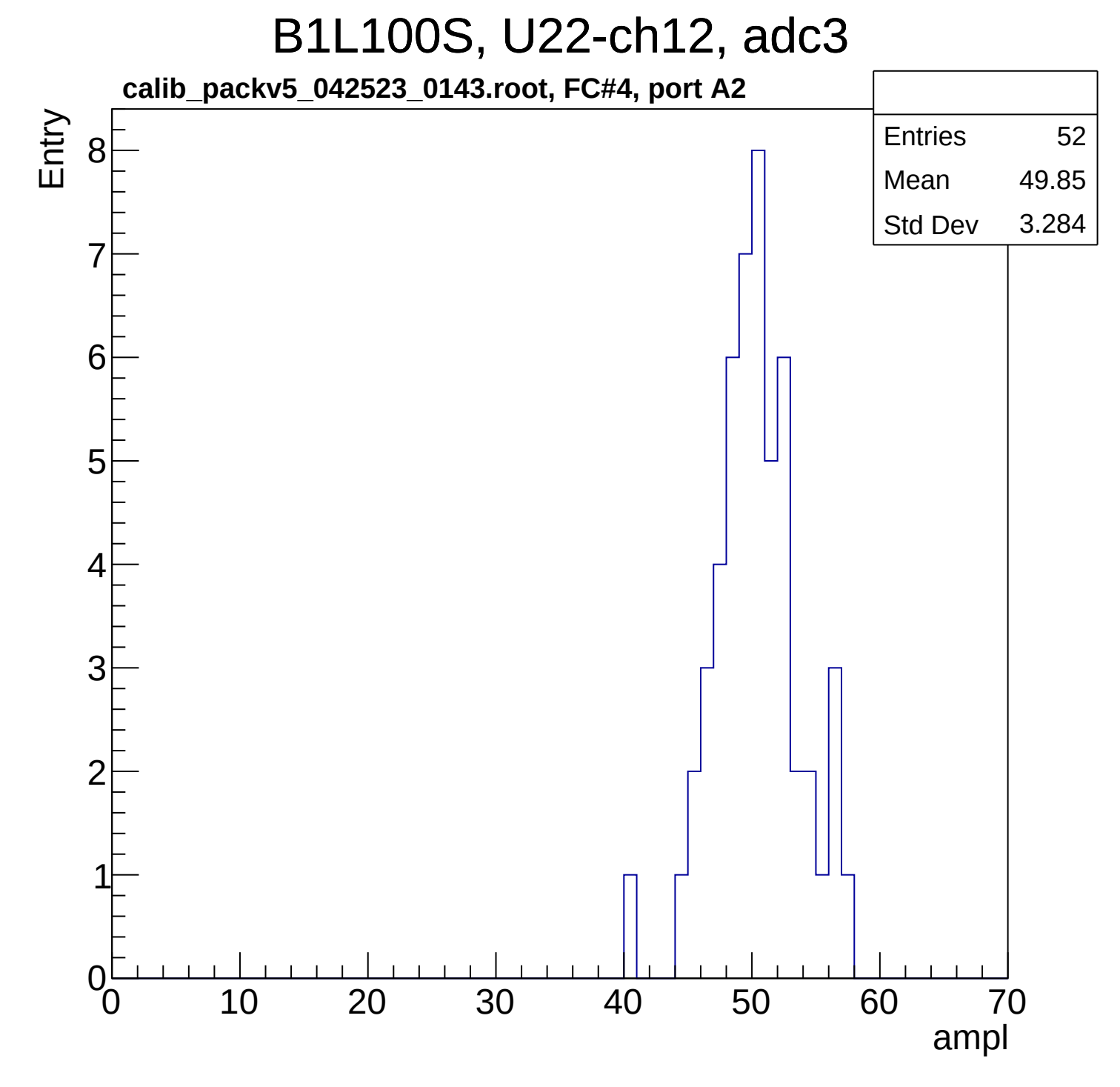
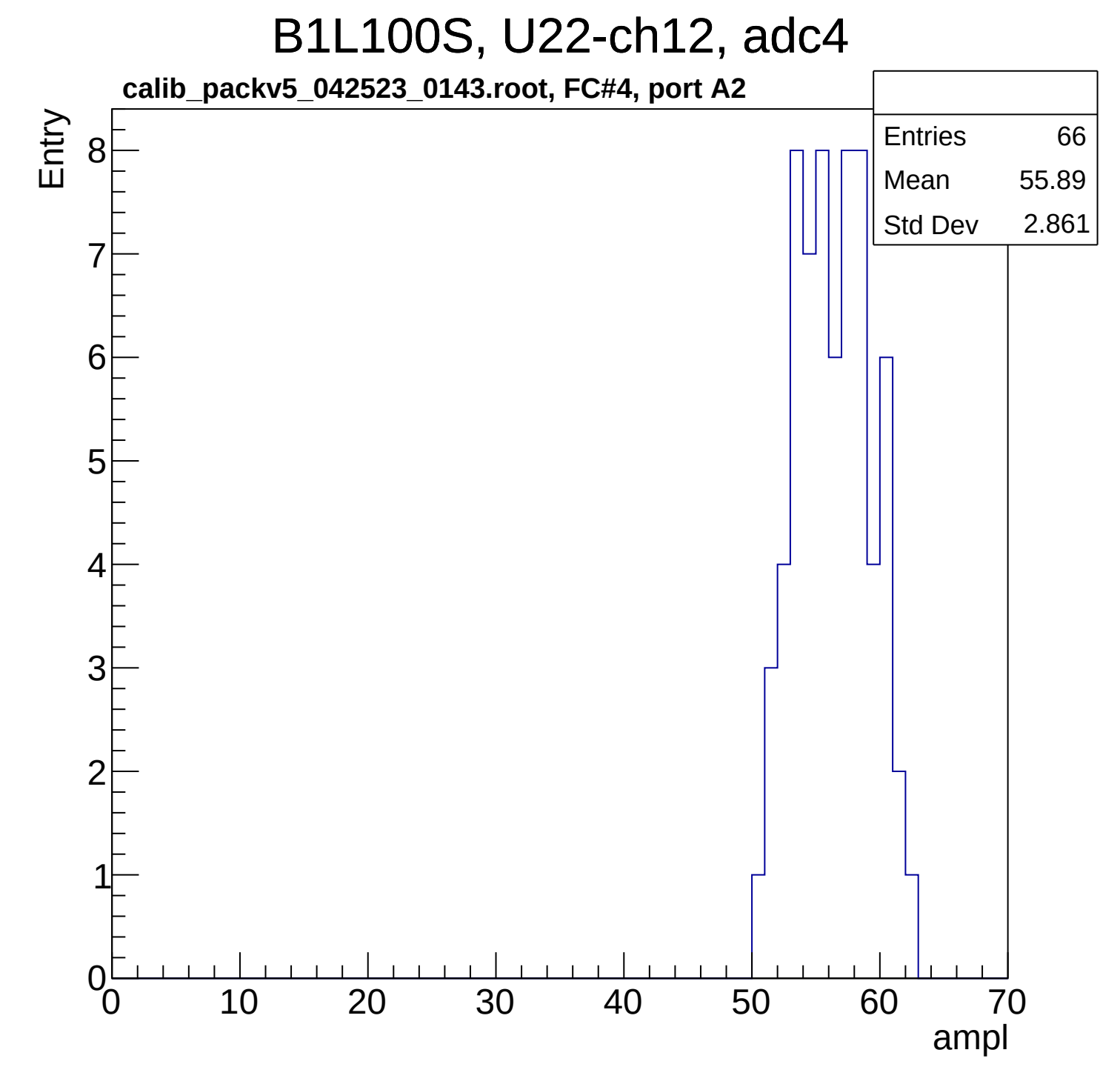
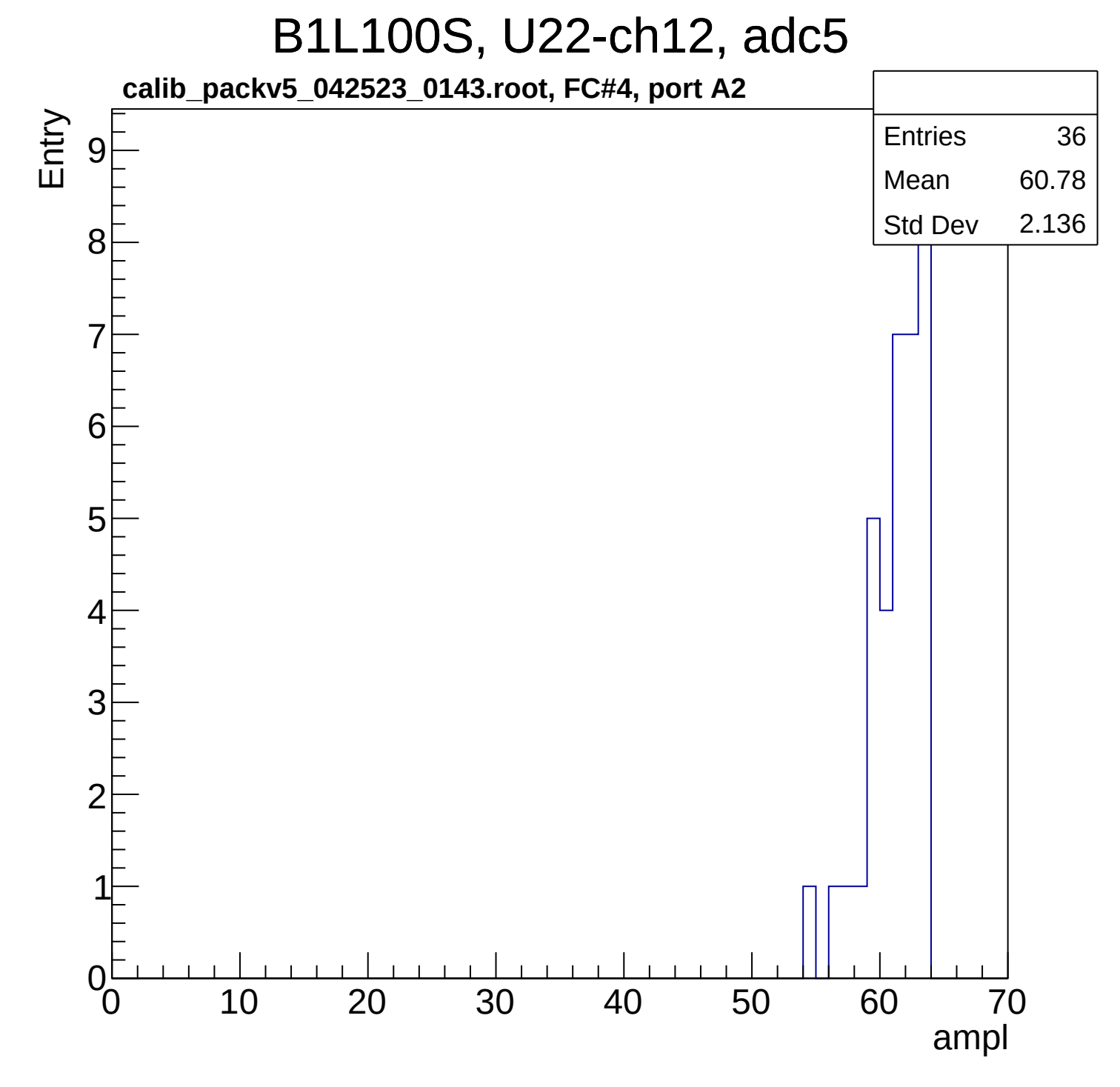
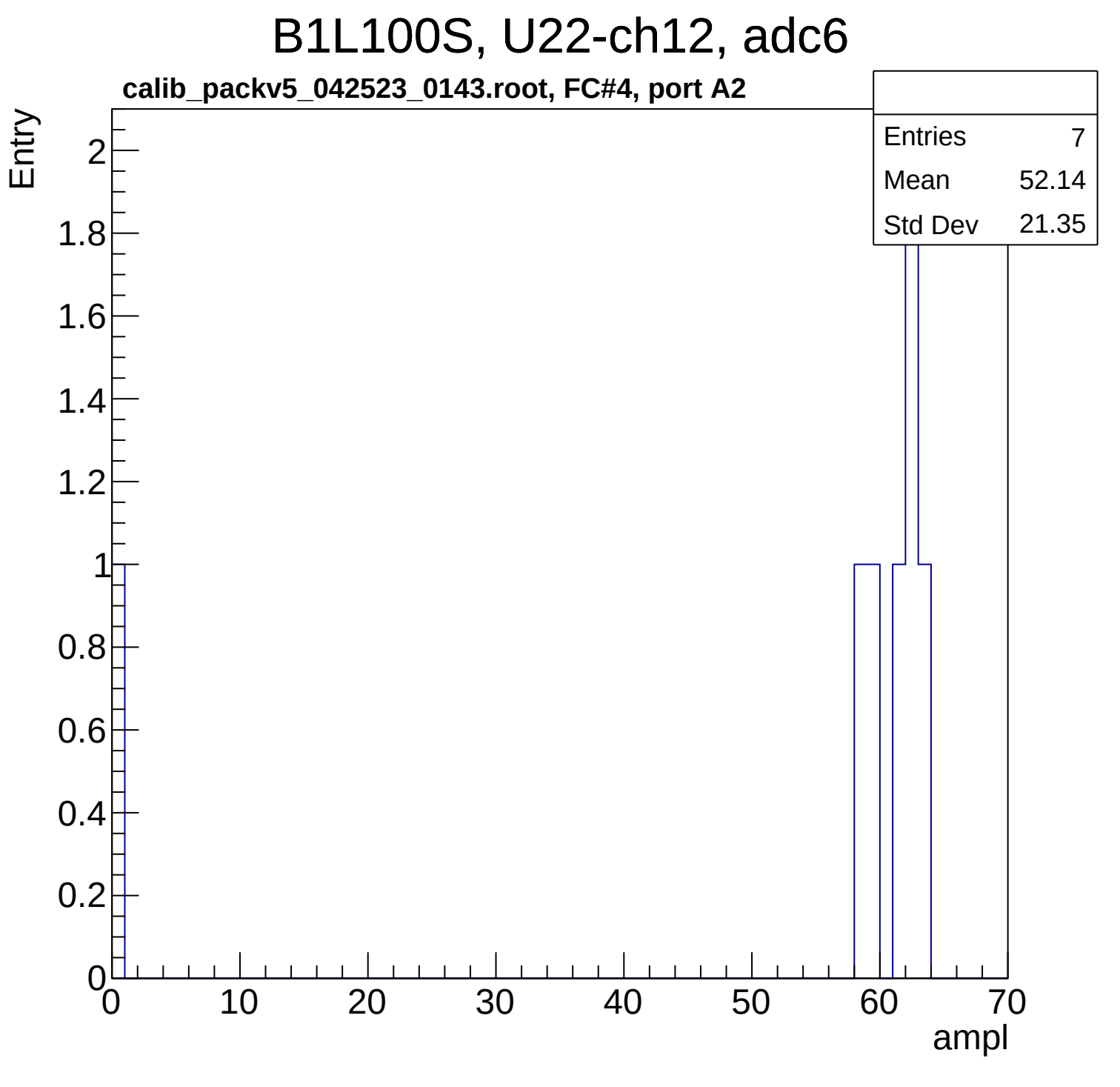
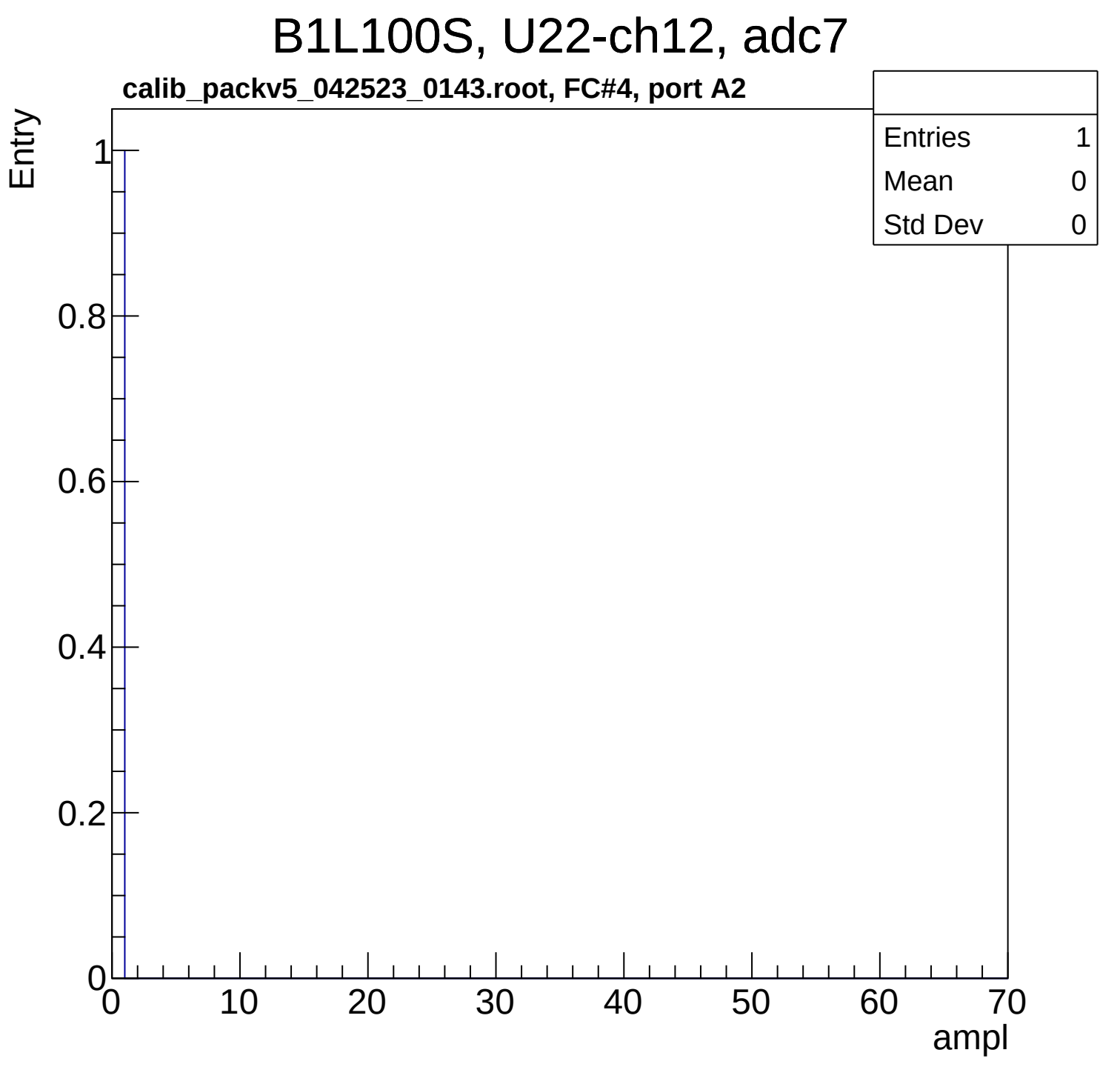

B1L100S, U22-ch12, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.86
Std Dev 3.631
Gaus mean : 44.4057
Gaus Width: 3.4719
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

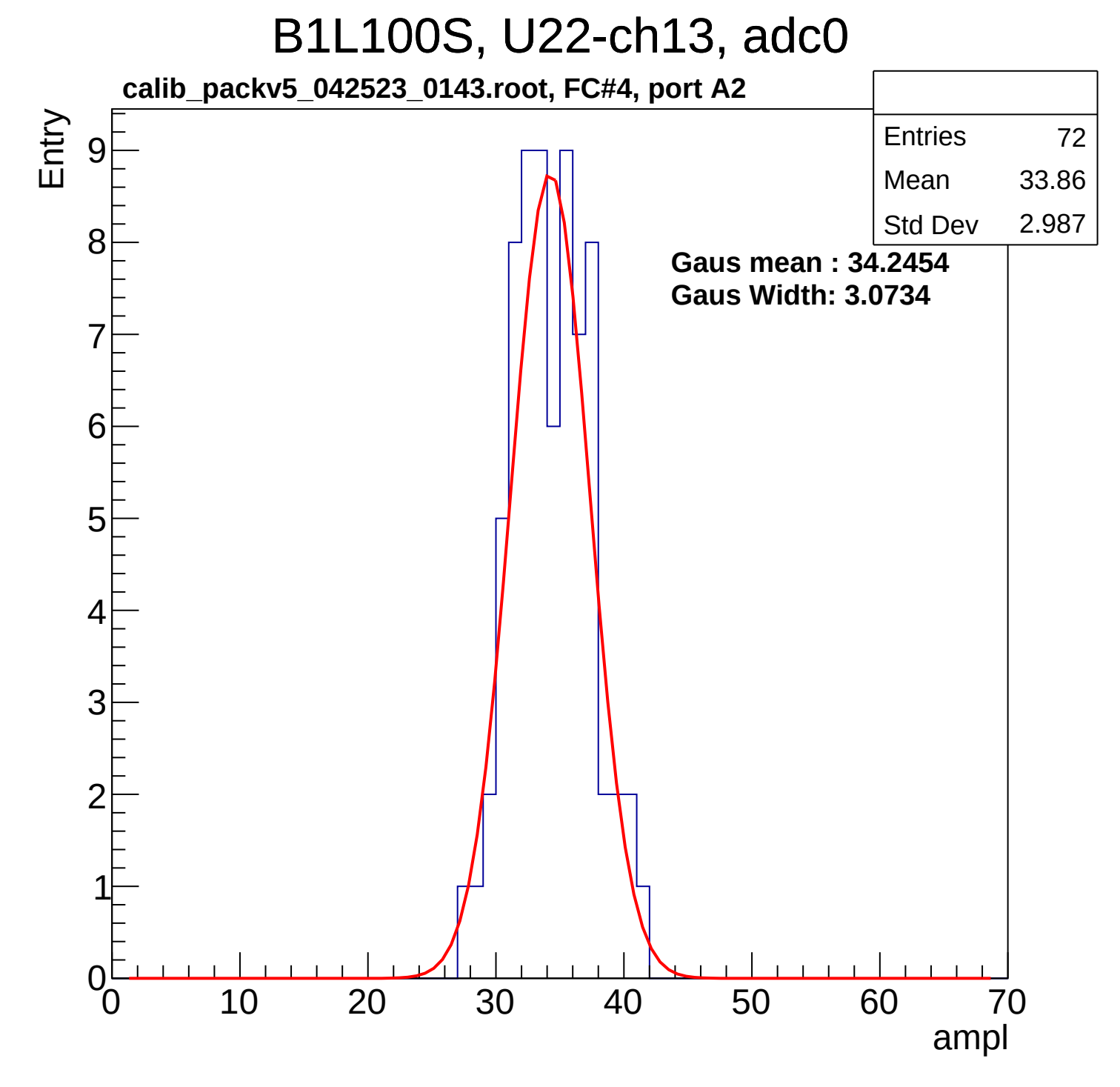

B1L100S, U22-ch12, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 49.85
Std Dev 3.284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

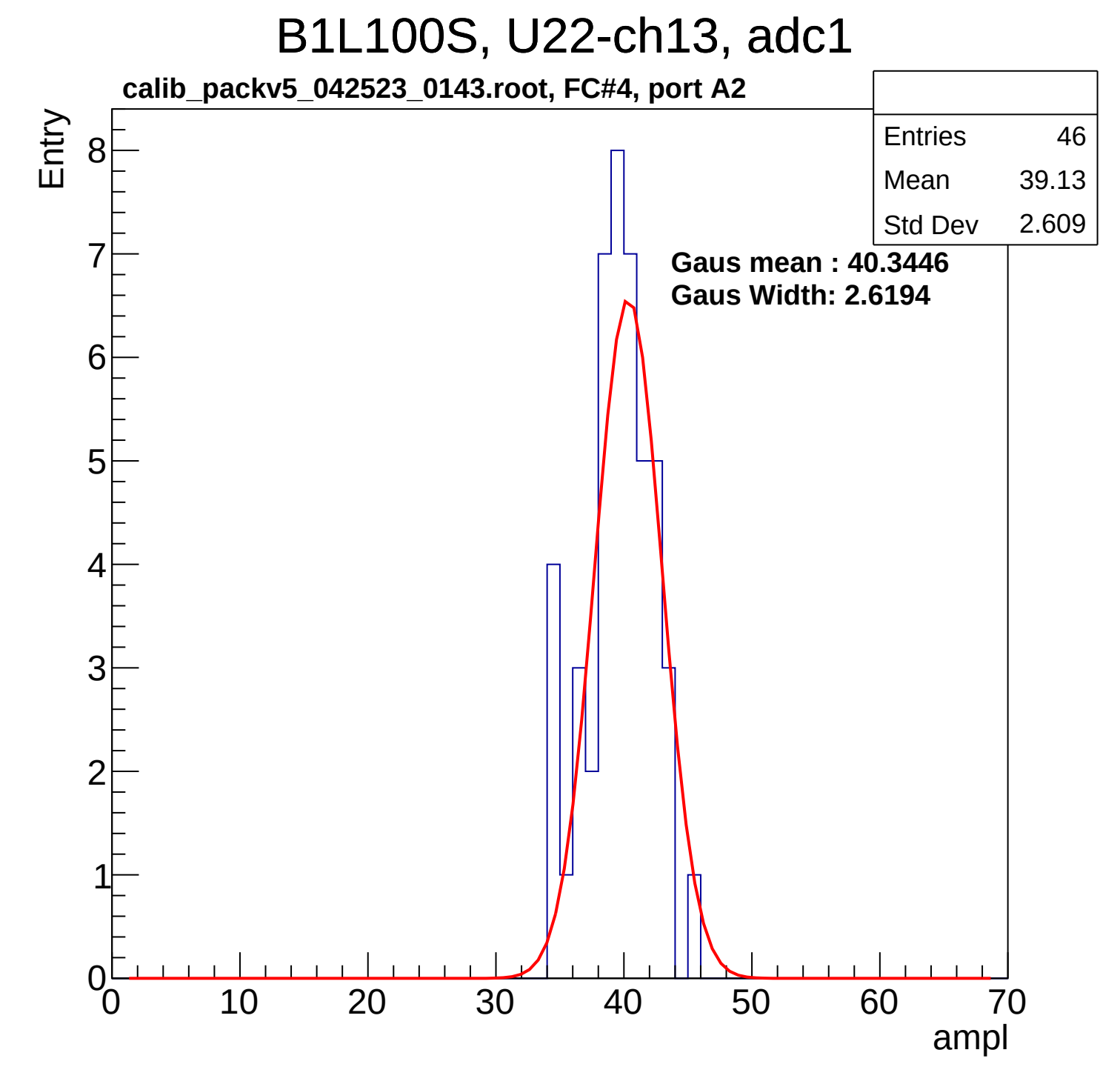

B1L100S, U22-ch12, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 55.89
Std Dev 2.861
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

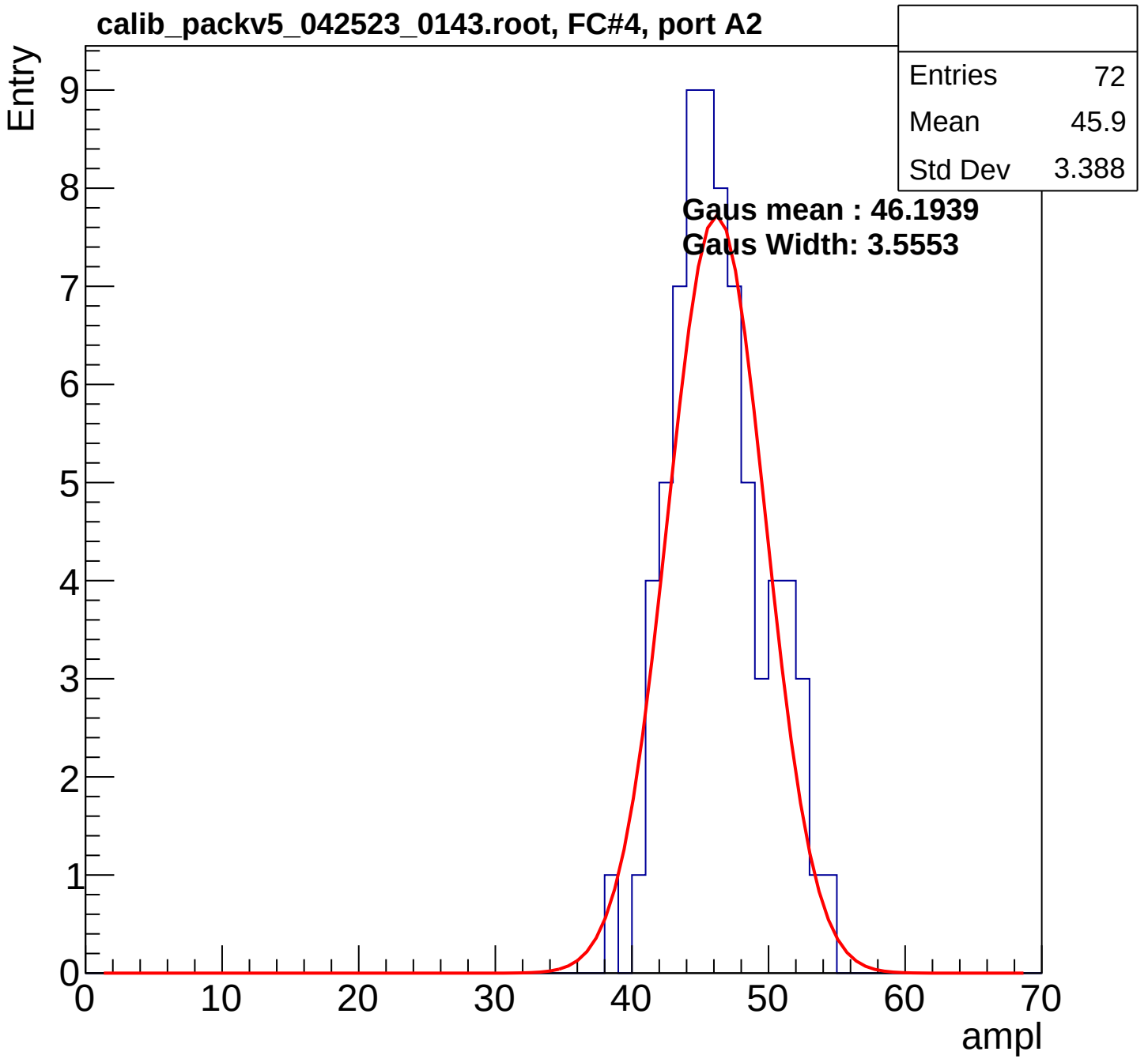

B1L100S, U22-ch12, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 60.78
Std Dev 2.136
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch12, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 52.14
Std Dev 21.35
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch12, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch13, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 33.86
Std Dev 2.987
Gaus mean : 34.2454
Gaus Width: 3.0734
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch13, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 39.13
Std Dev 2.609
Gaus mean : 40.3446
Gaus Width: 2.6194
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch13, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 45.9
Std Dev 3.388
Gaus mean : 46.1939
Gaus Width: 3.5553
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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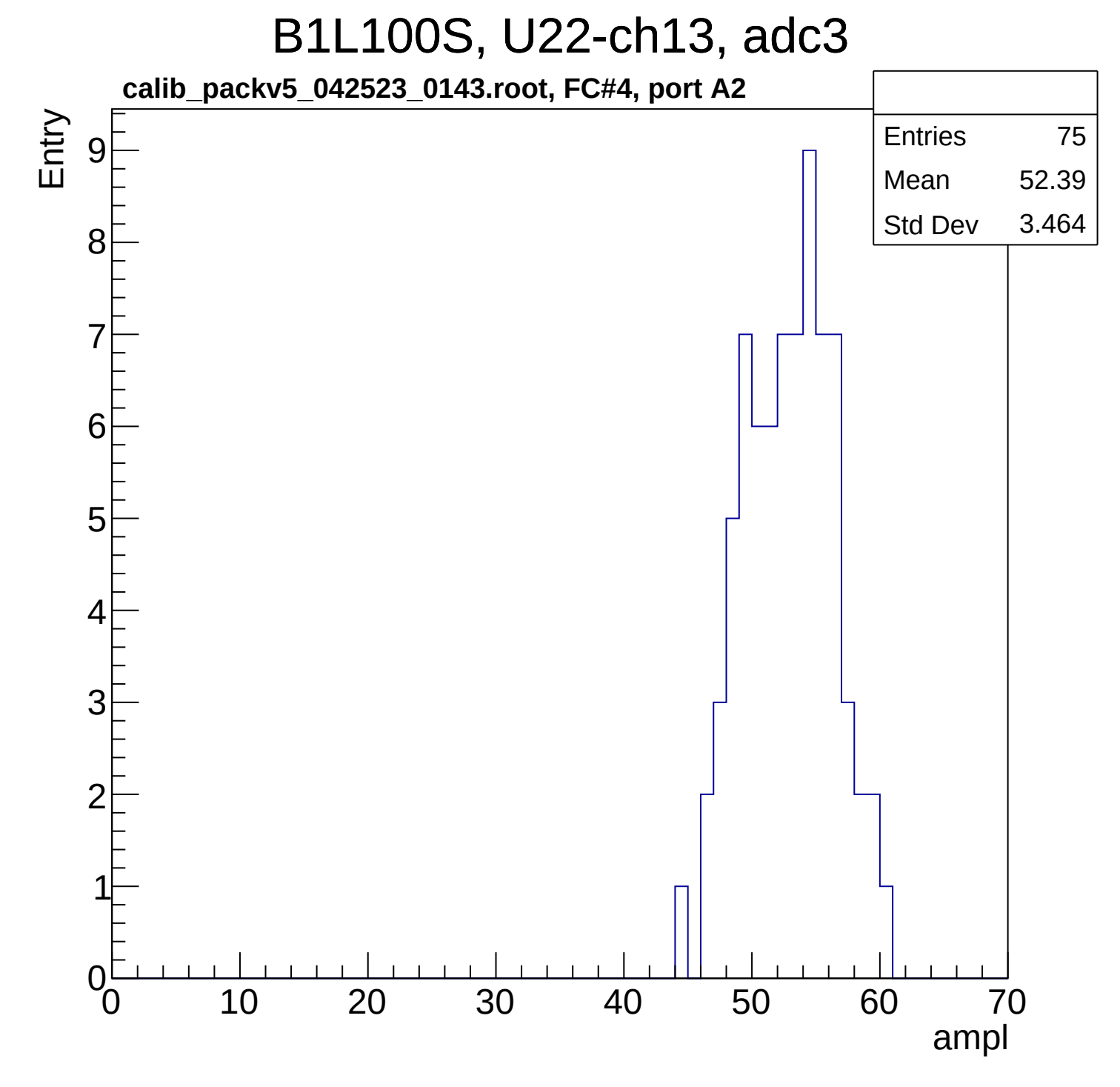
B1L100S, U22-ch13, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 52.39
Std Dev 3.464
Entry
ampl
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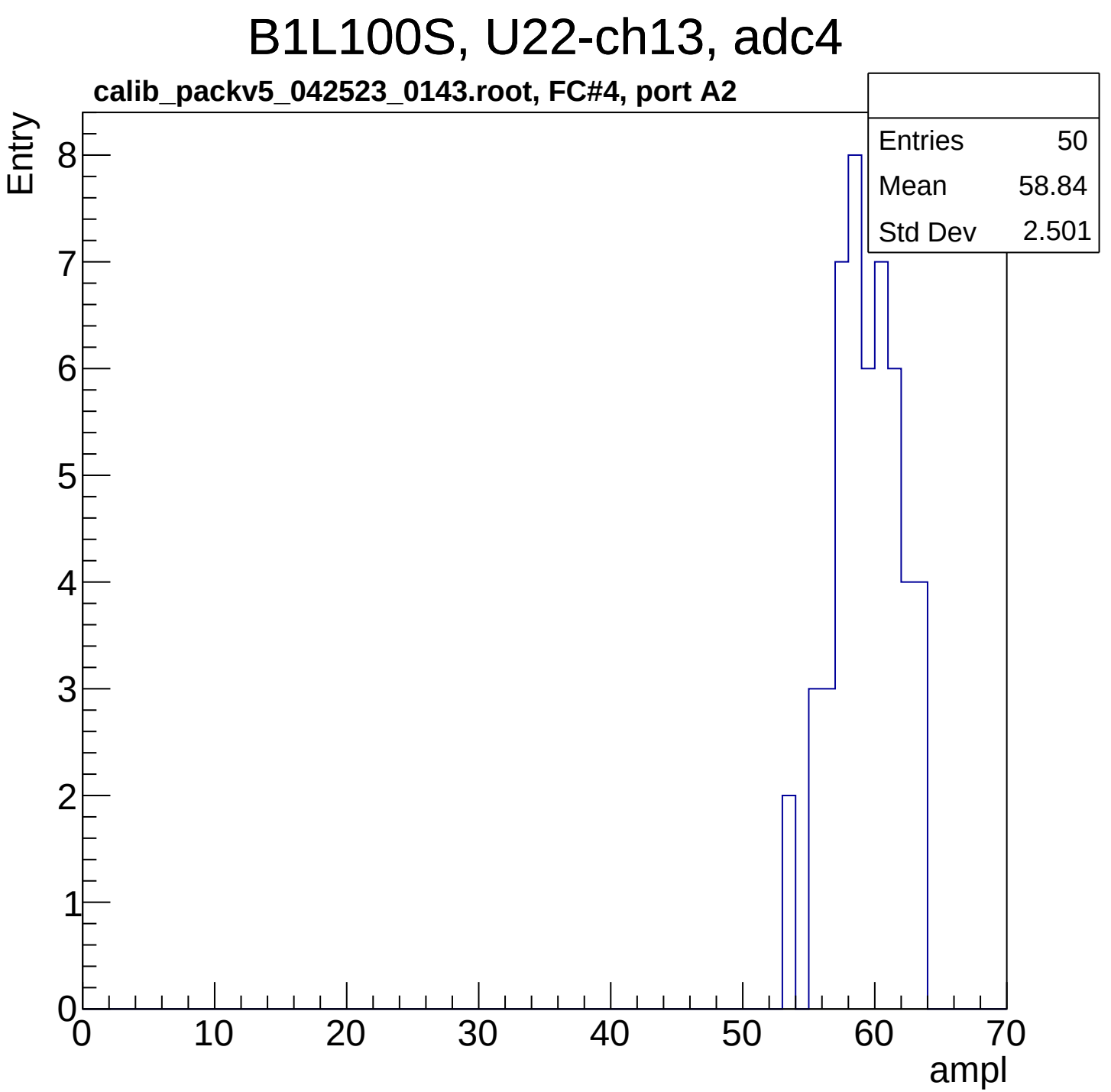
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch13, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.84
Std Dev 2.501
Entry
ampl
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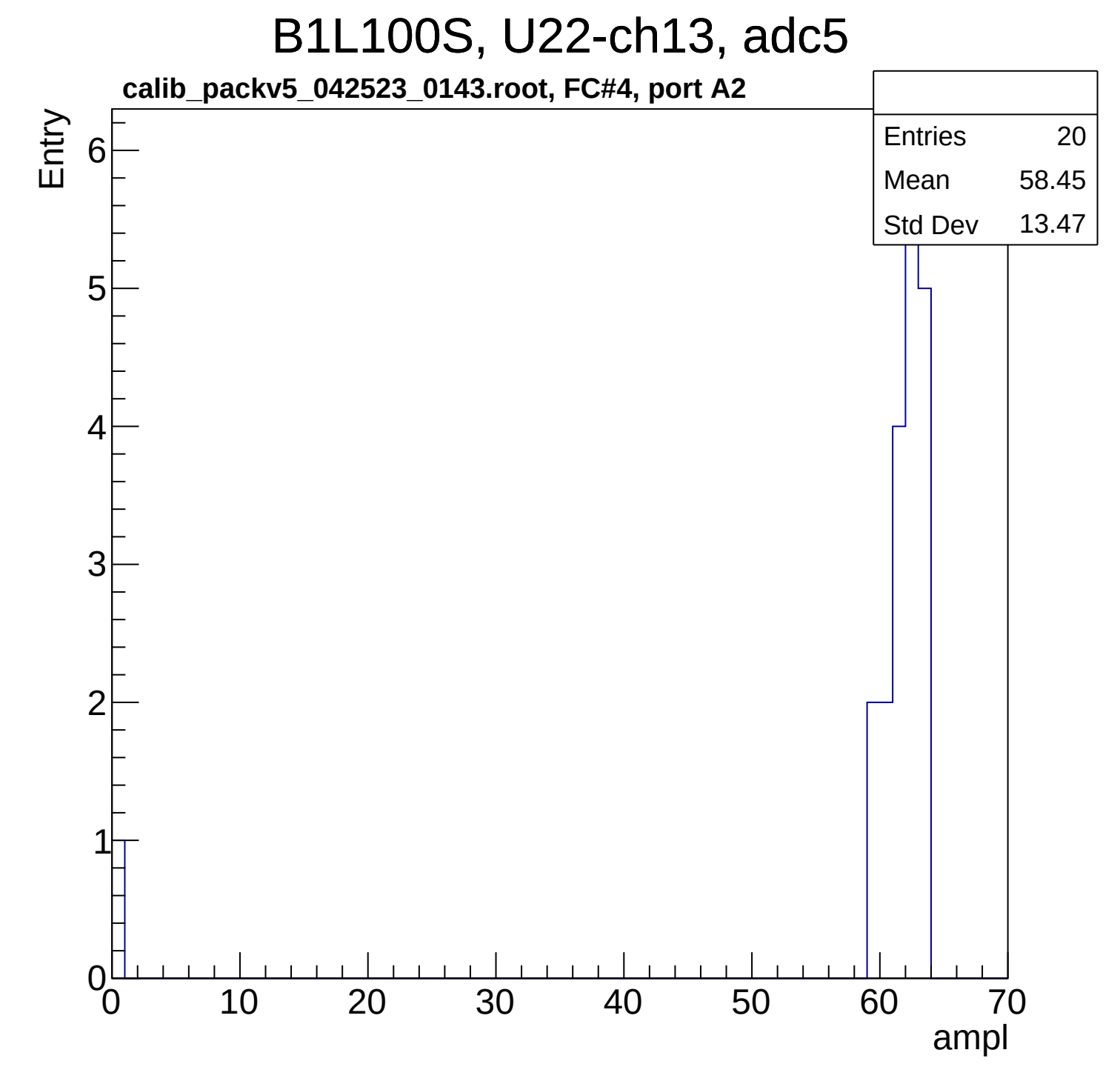
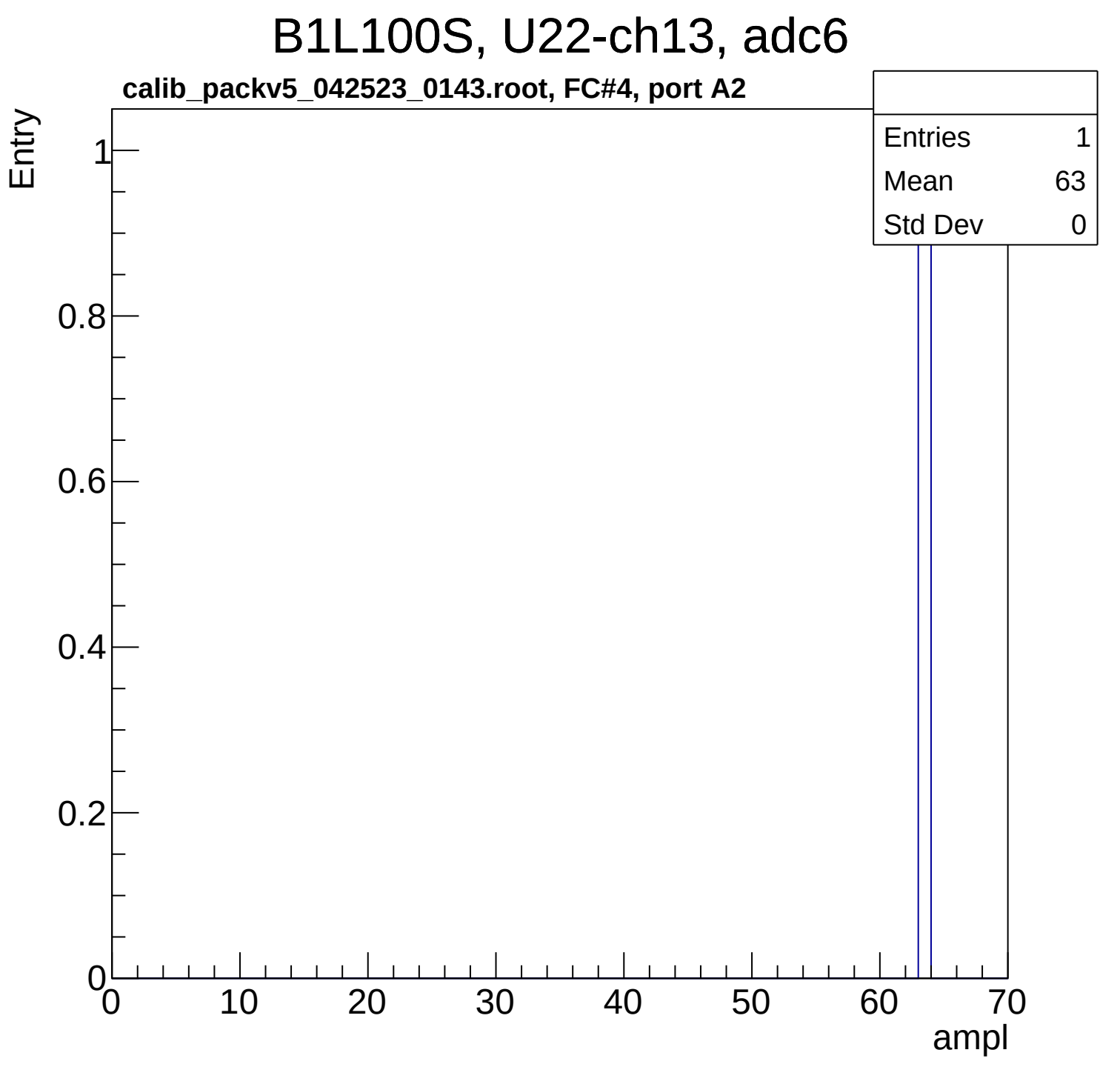
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch13, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 20
Mean 58.45
Std Dev 13.47
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch13, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
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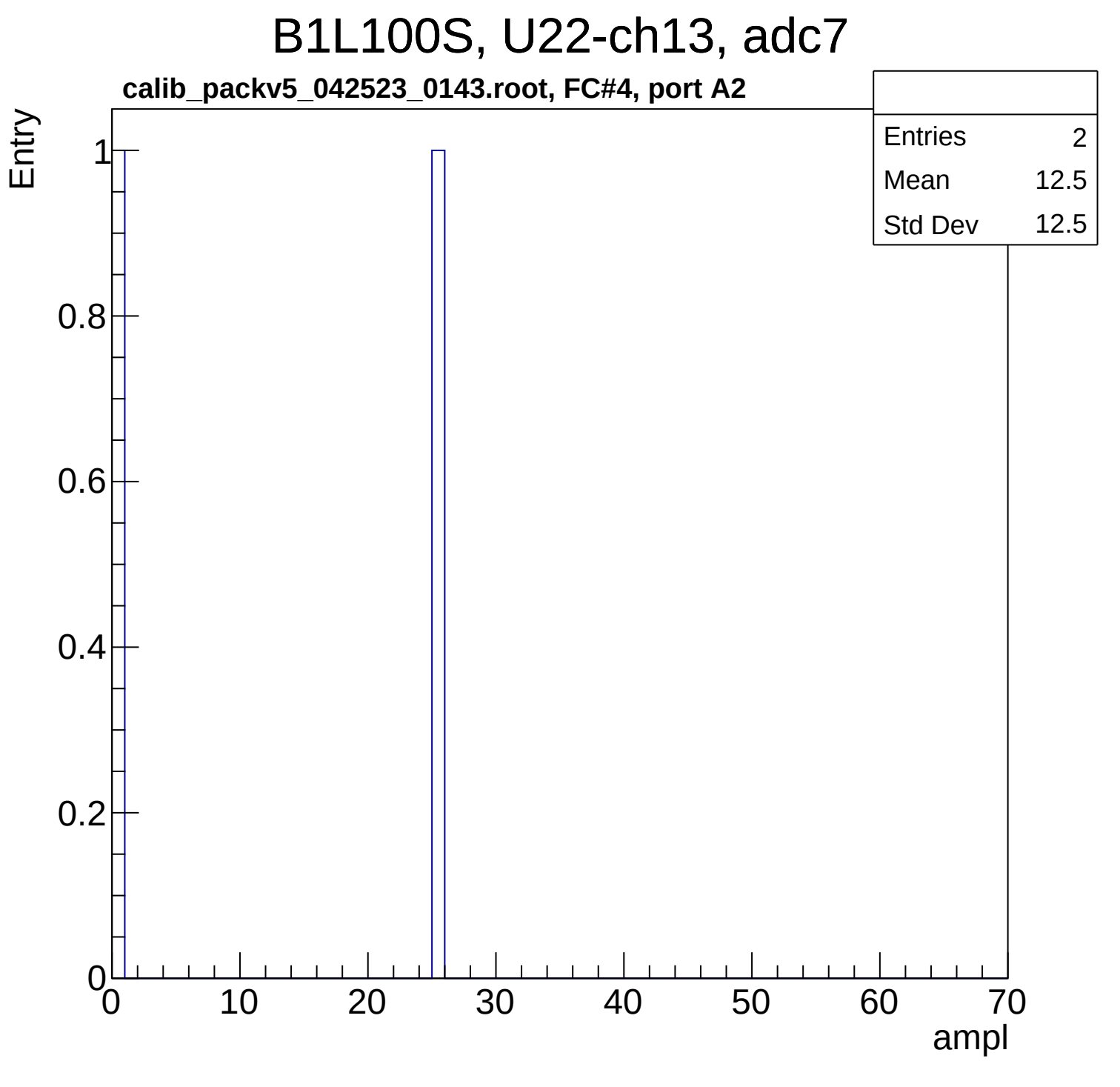
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch13, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
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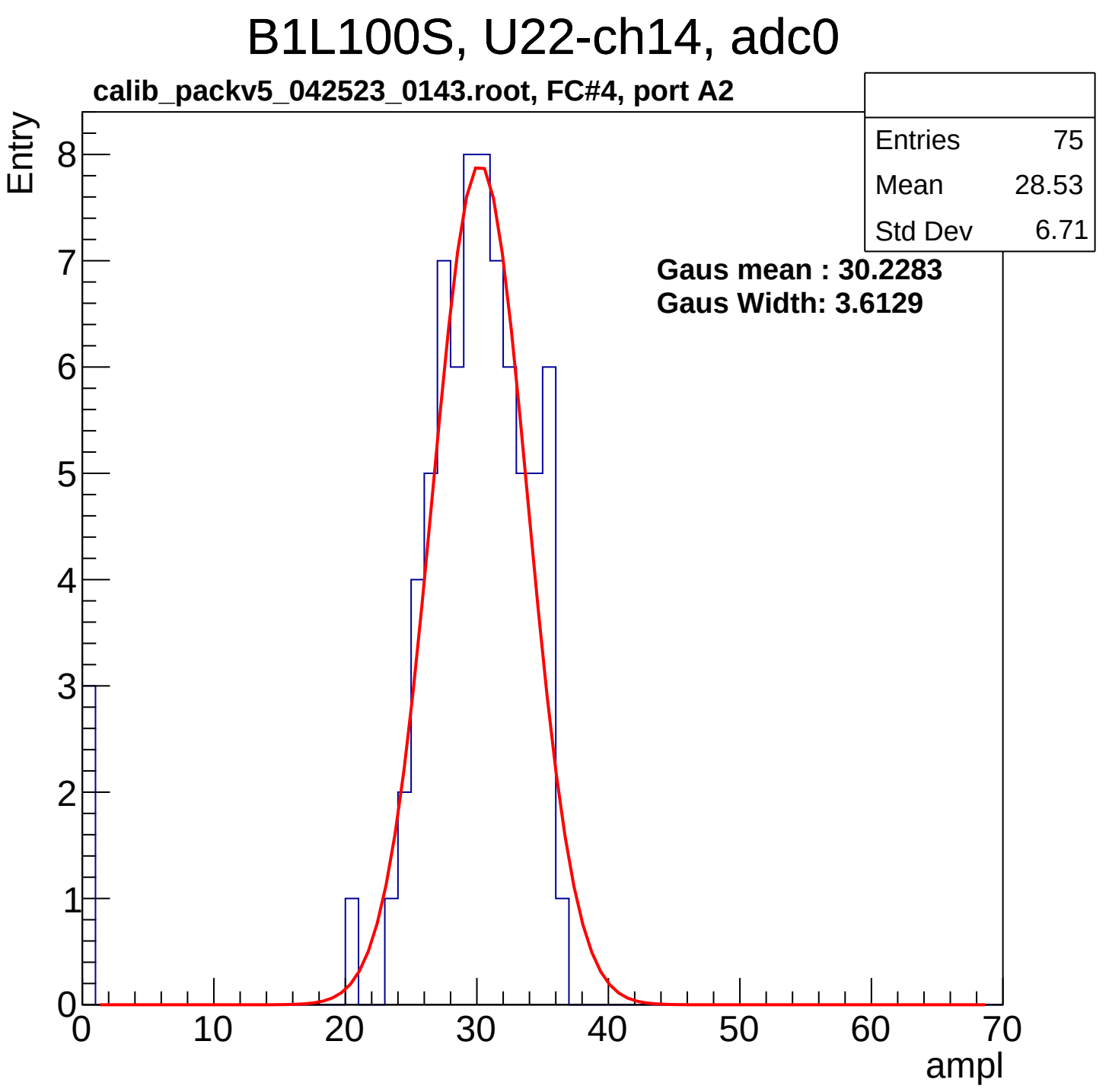
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch14, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.53
Std Dev 6.71
Gaus mean : 30.2283
Gaus Width: 3.6129
Entry
ampl
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2
3
4
5
6
7
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0
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20
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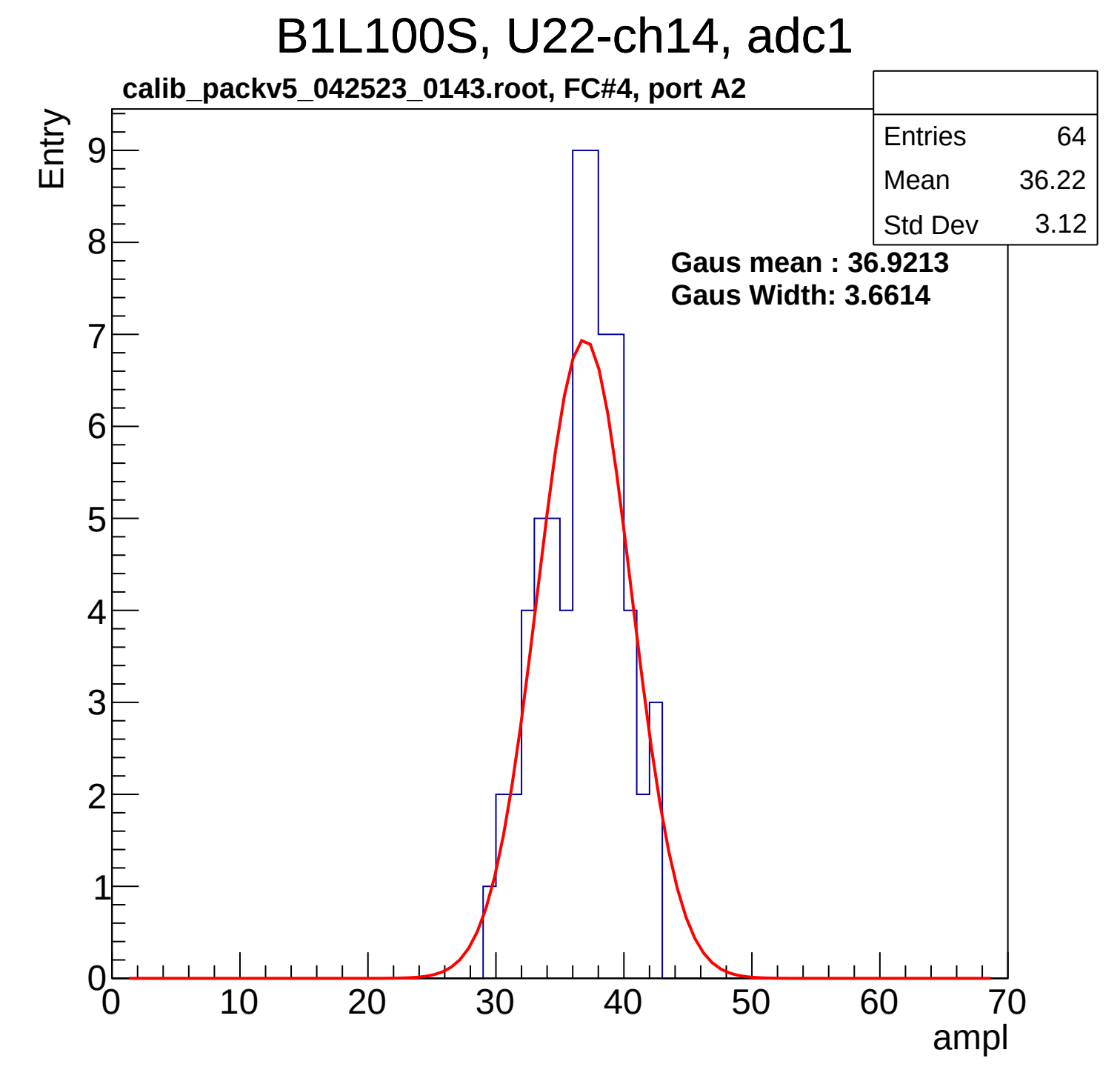
40
50
60
70


B1L100S, U22-ch14, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.22
Std Dev 3.12
Gaus mean : 36.9213
Gaus Width: 3.6614
Entry
ampl
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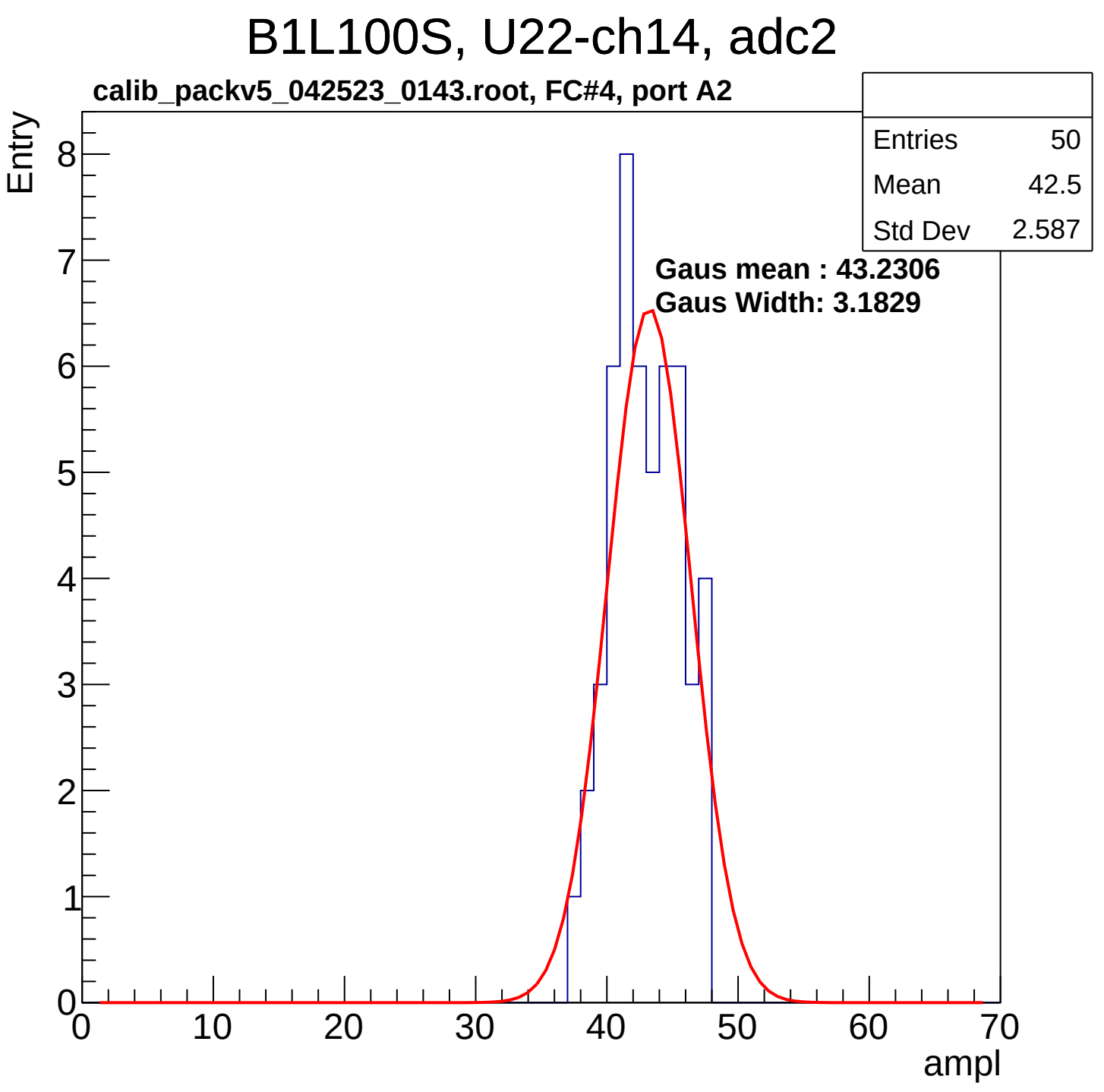
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch14, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 42.5
Std Dev 2.587
Gaus mean : 43.2306
Gaus Width: 3.1829
Entry
ampl
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1
2
3
4
5
6
7
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0
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60
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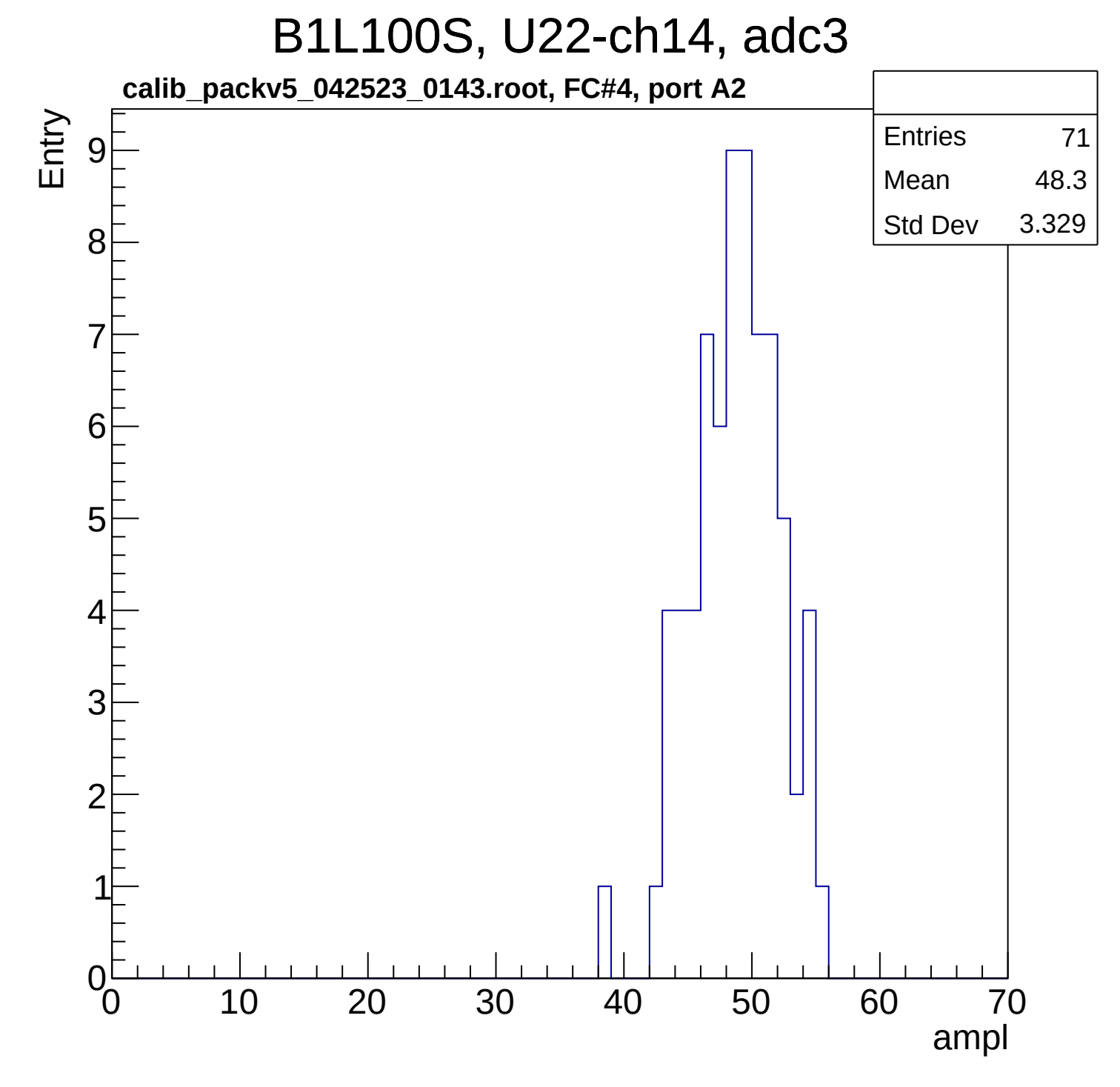
B1L100S, U22-ch14, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.3
Std Dev 3.329
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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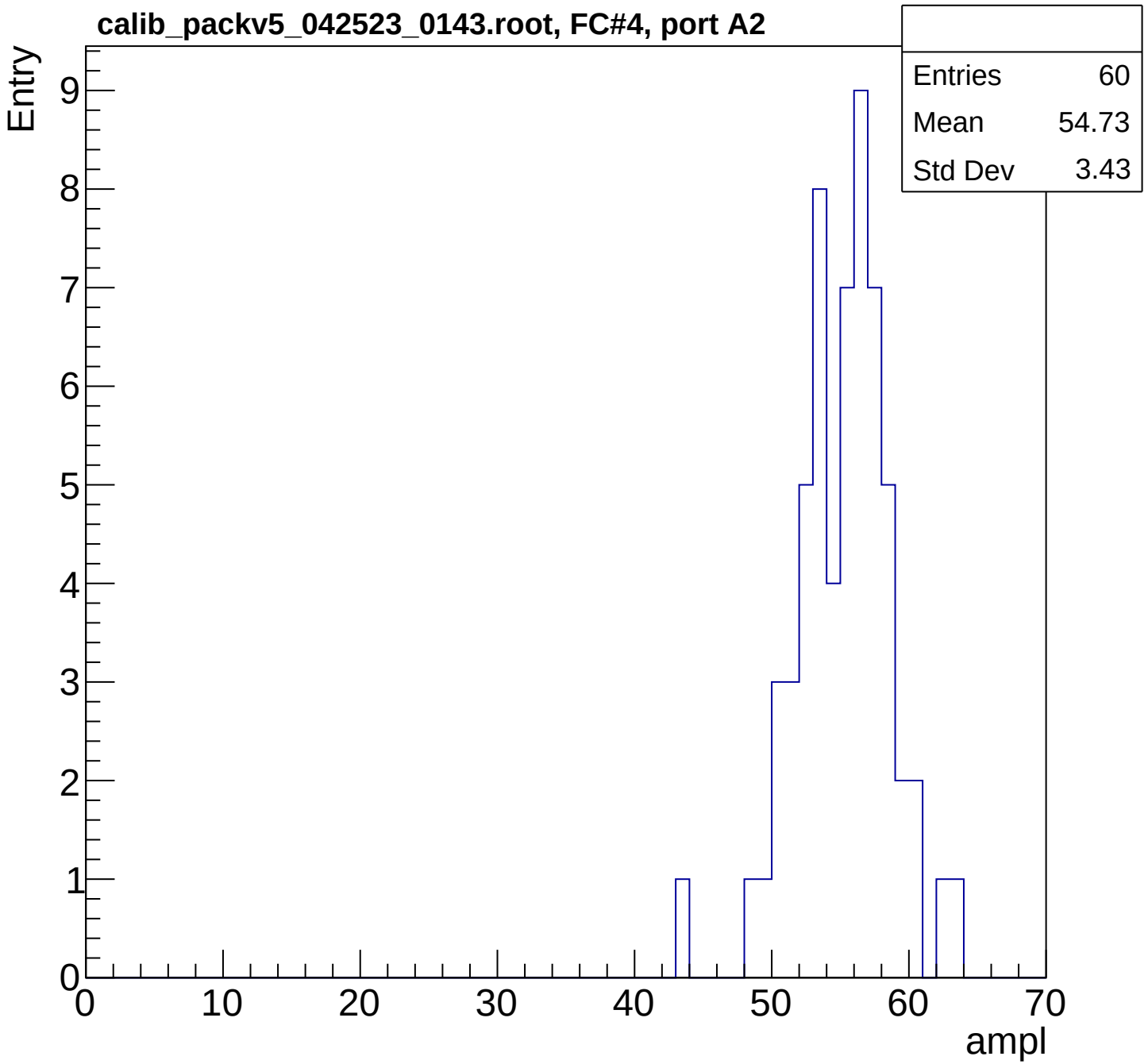
40
50
60
70


B1L100S, U22-ch14, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 54.73
Std Dev 3.43
Entry
ampl
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3
4
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7
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0
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20
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40
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60
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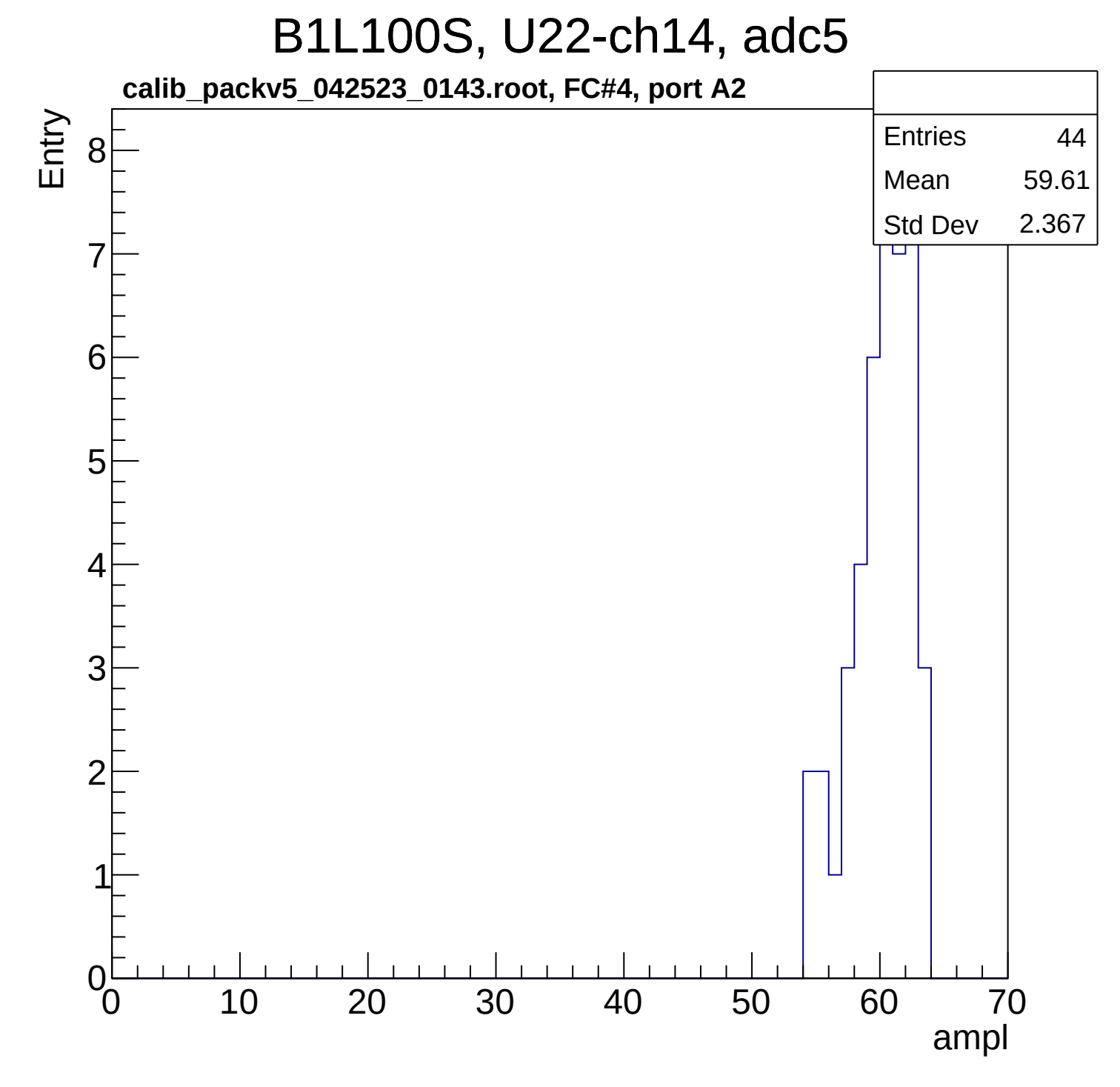
B1L100S, U22-ch14, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 59.61
Std Dev 2.367
Entry
ampl
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1
2
3
4
5
6
7
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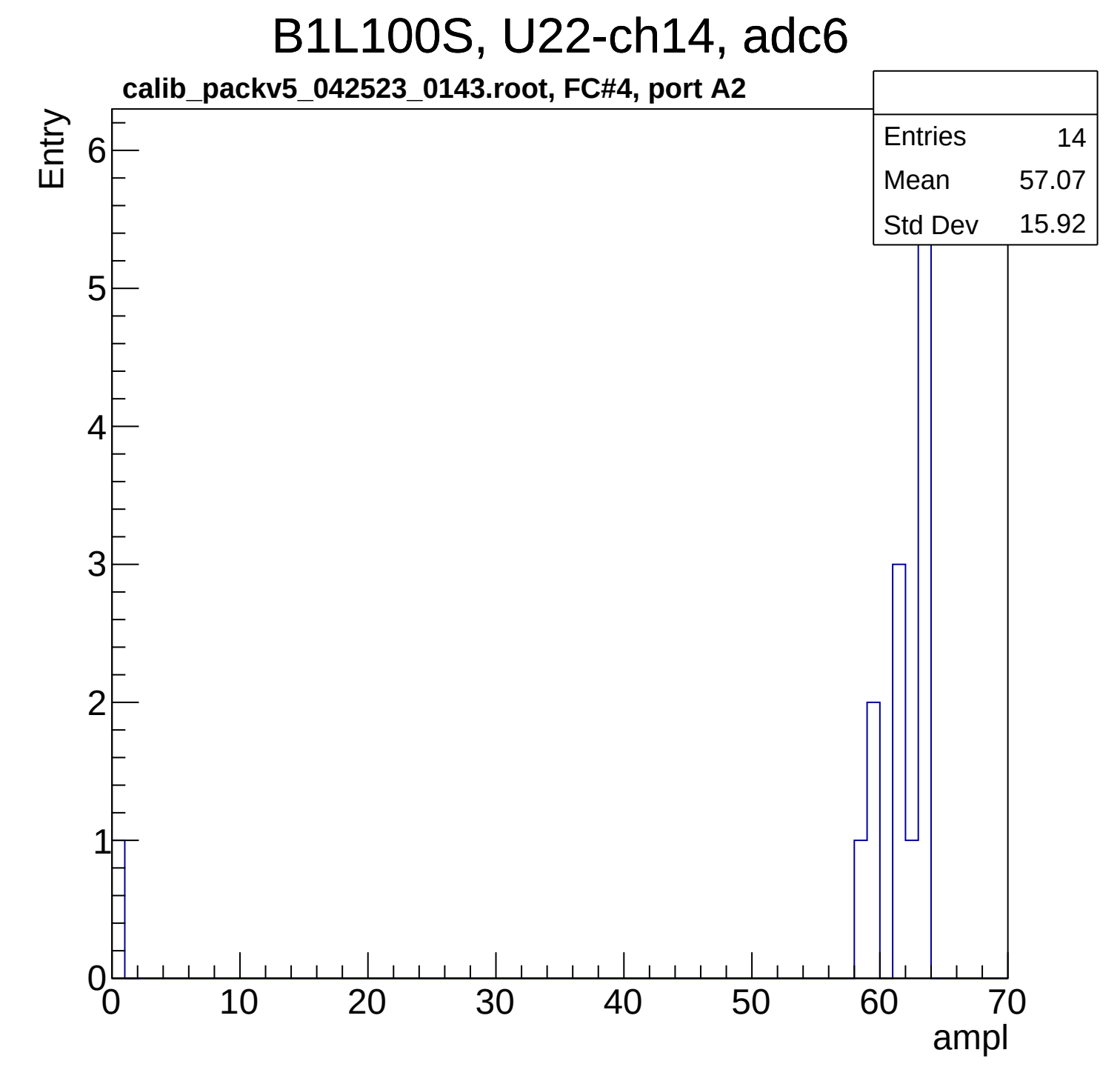
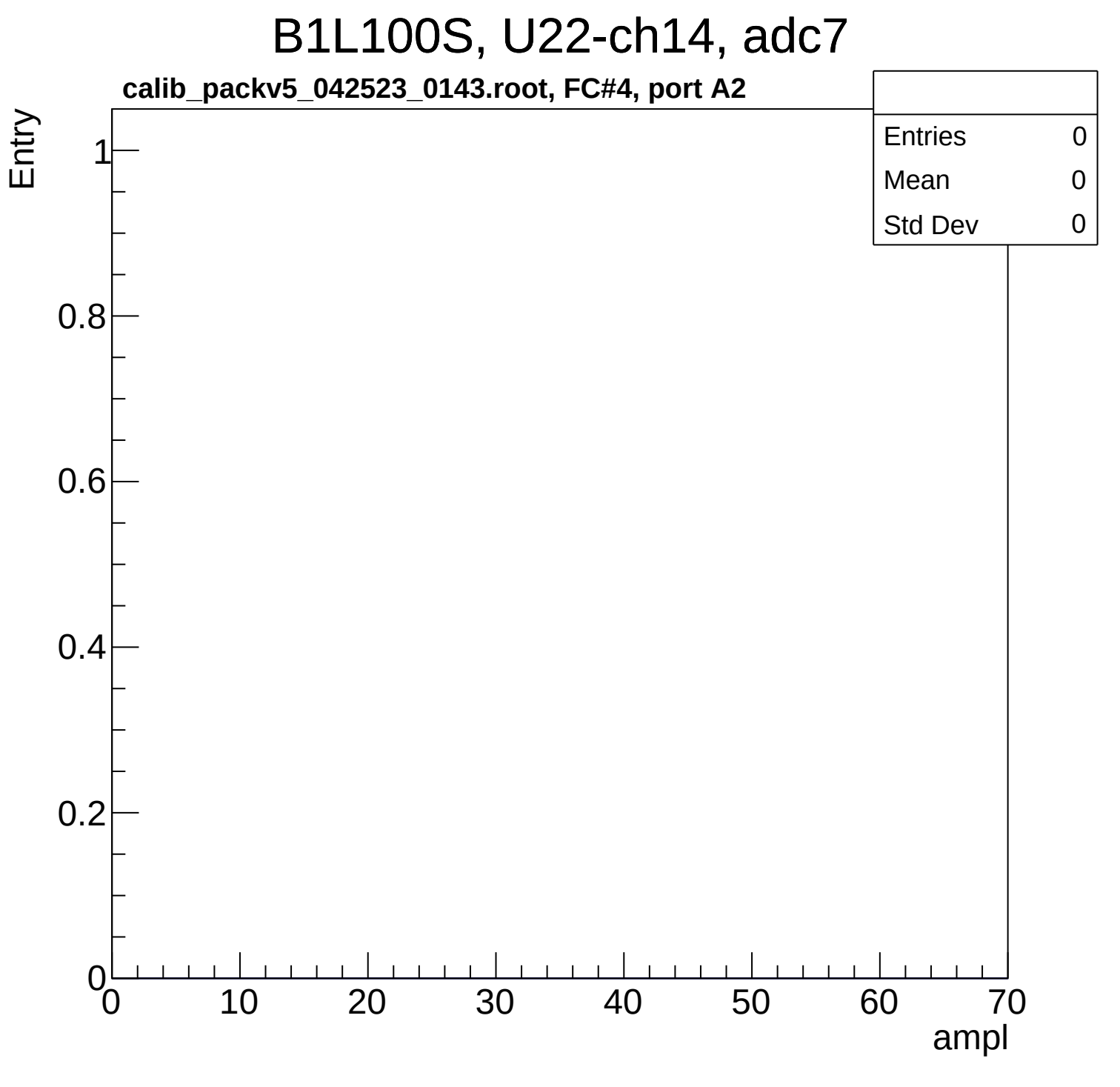
0
10
20
30
40
50
60
70


B1L100S, U22-ch14, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 57.07
Std Dev 15.92
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch14, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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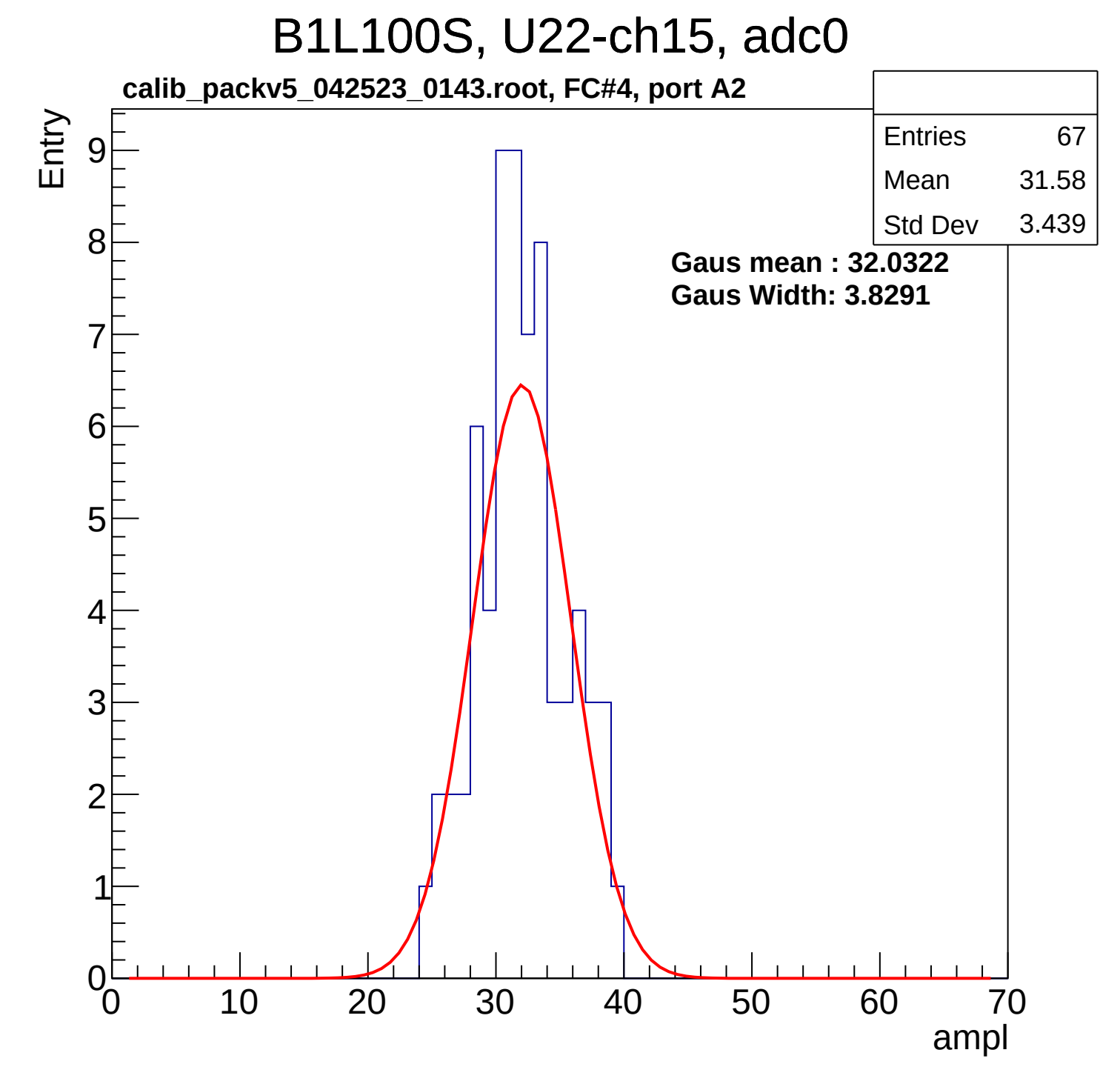
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch15, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 31.58
Std Dev 3.439
Gaus mean : 32.0322
Gaus Width: 3.8291
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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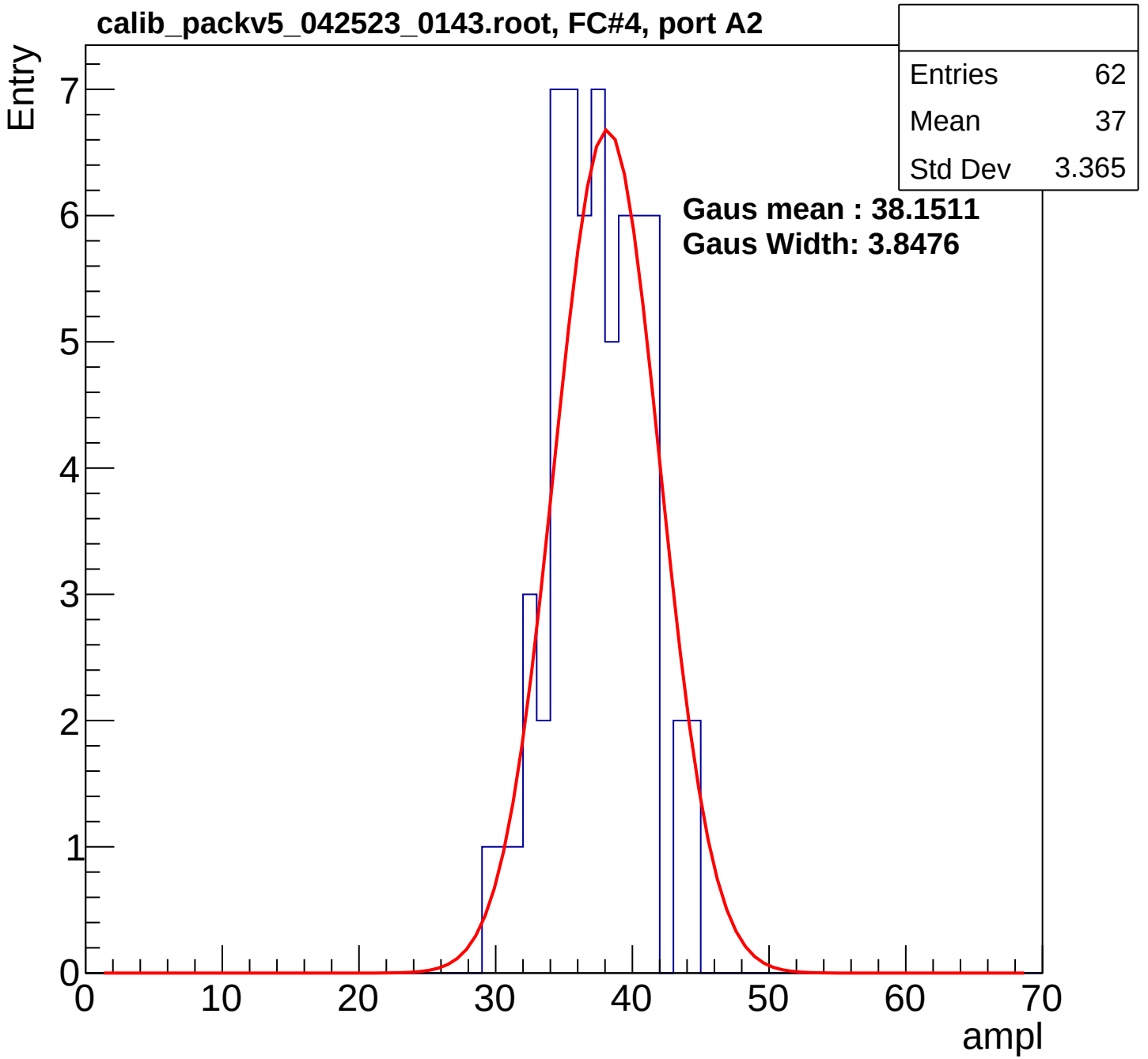
B1L100S, U22-ch15, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 37
Std Dev 3.365
Gaus mean : 38.1511
Gaus Width: 3.8476
Entry
ampl
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2
3
4
5
6
7
0
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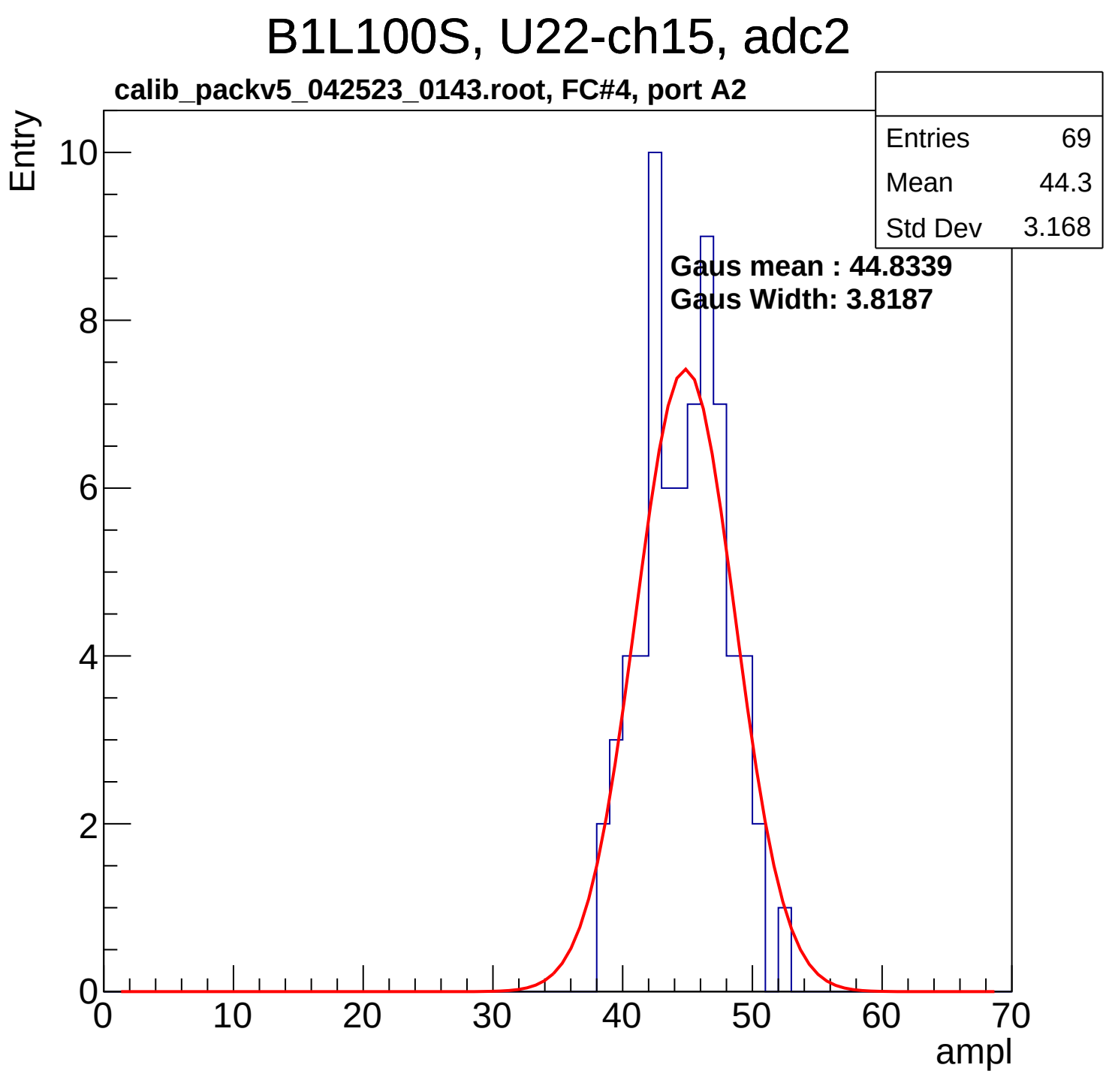
B1L100S, U22-ch15, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.3
Std Dev 3.168
Gaus mean : 44.8339
Gaus Width: 3.8187
Entry
ampl
0
2
4
6
8
10
0
10
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40
50
60
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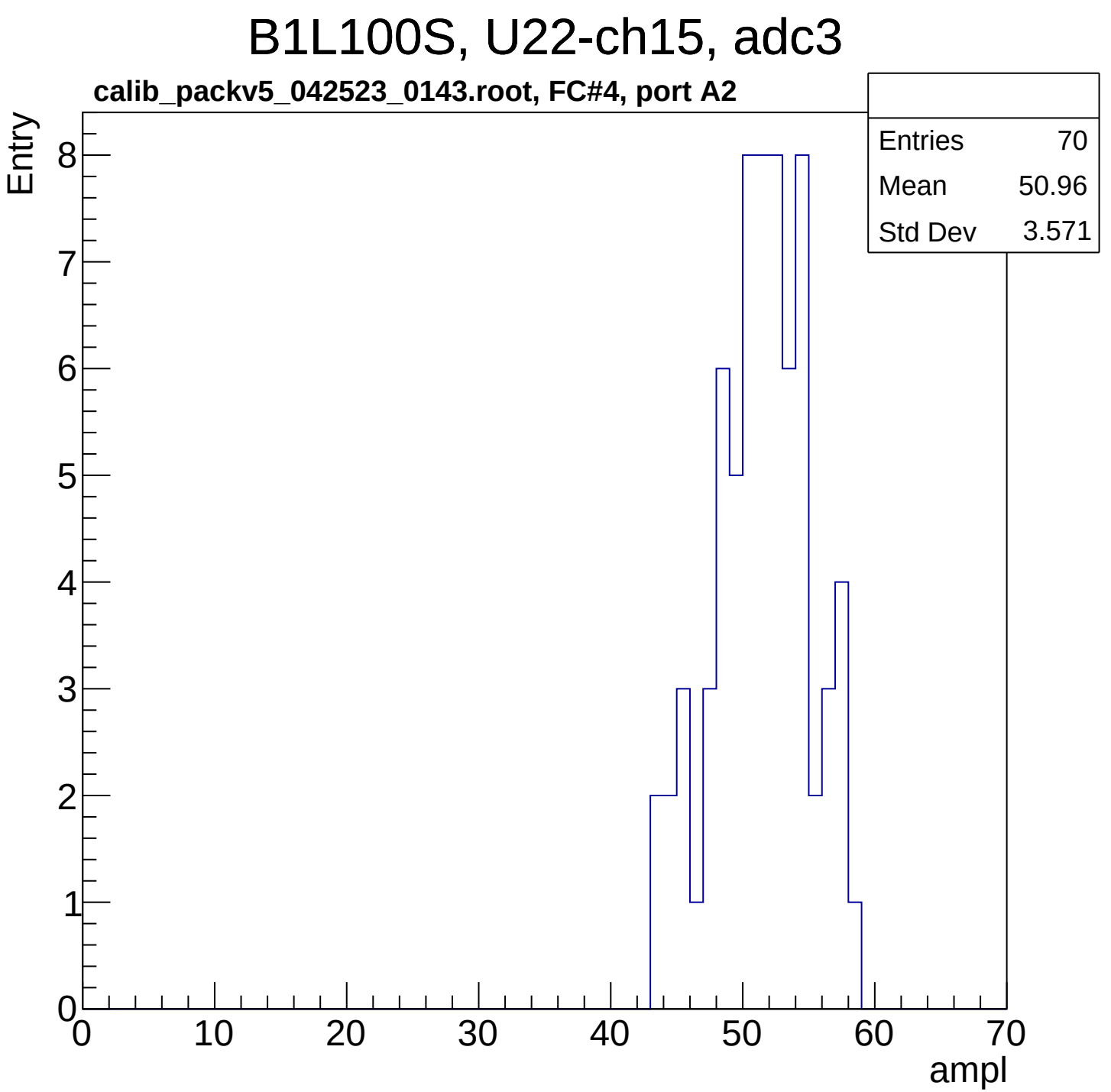
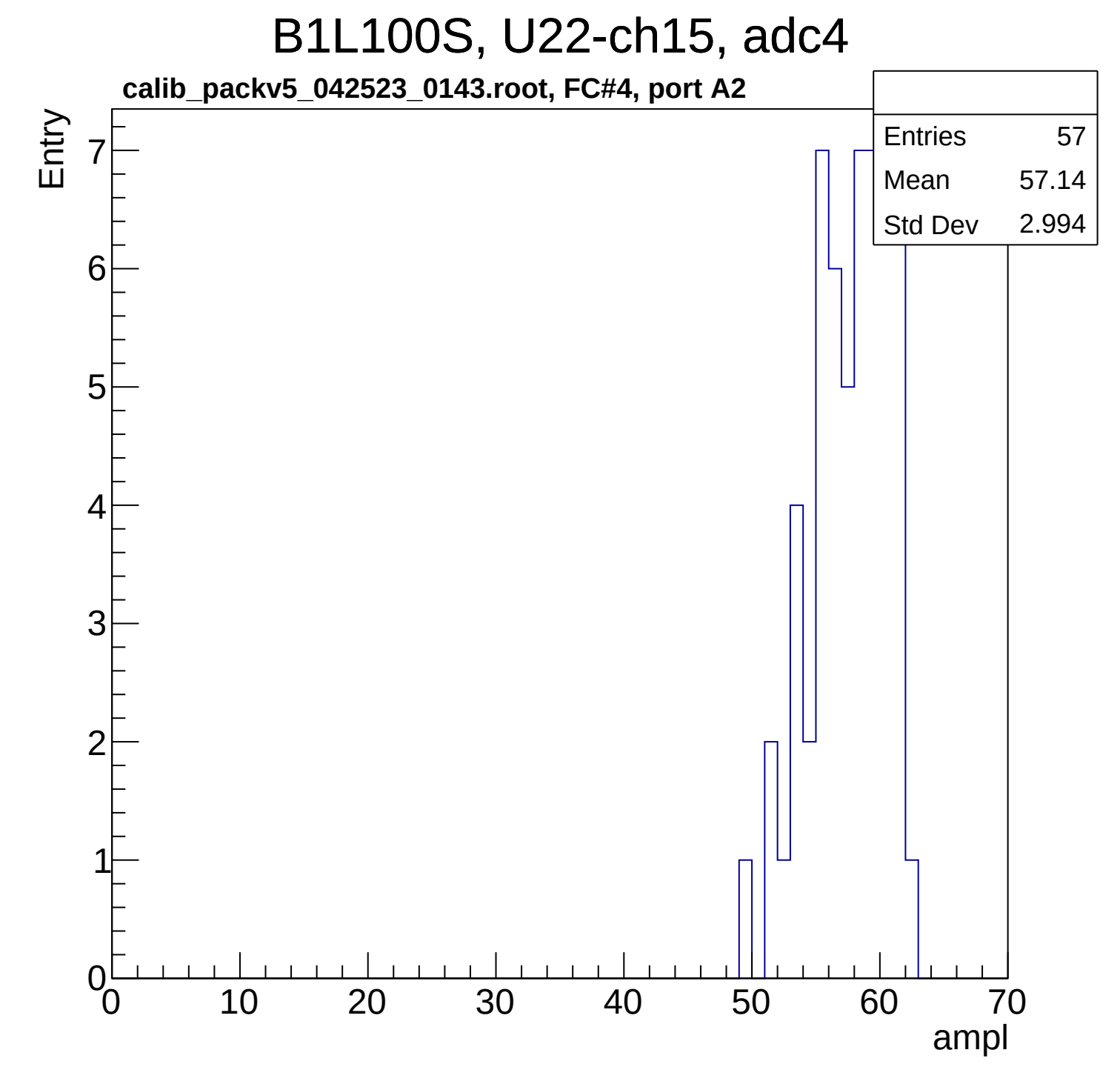
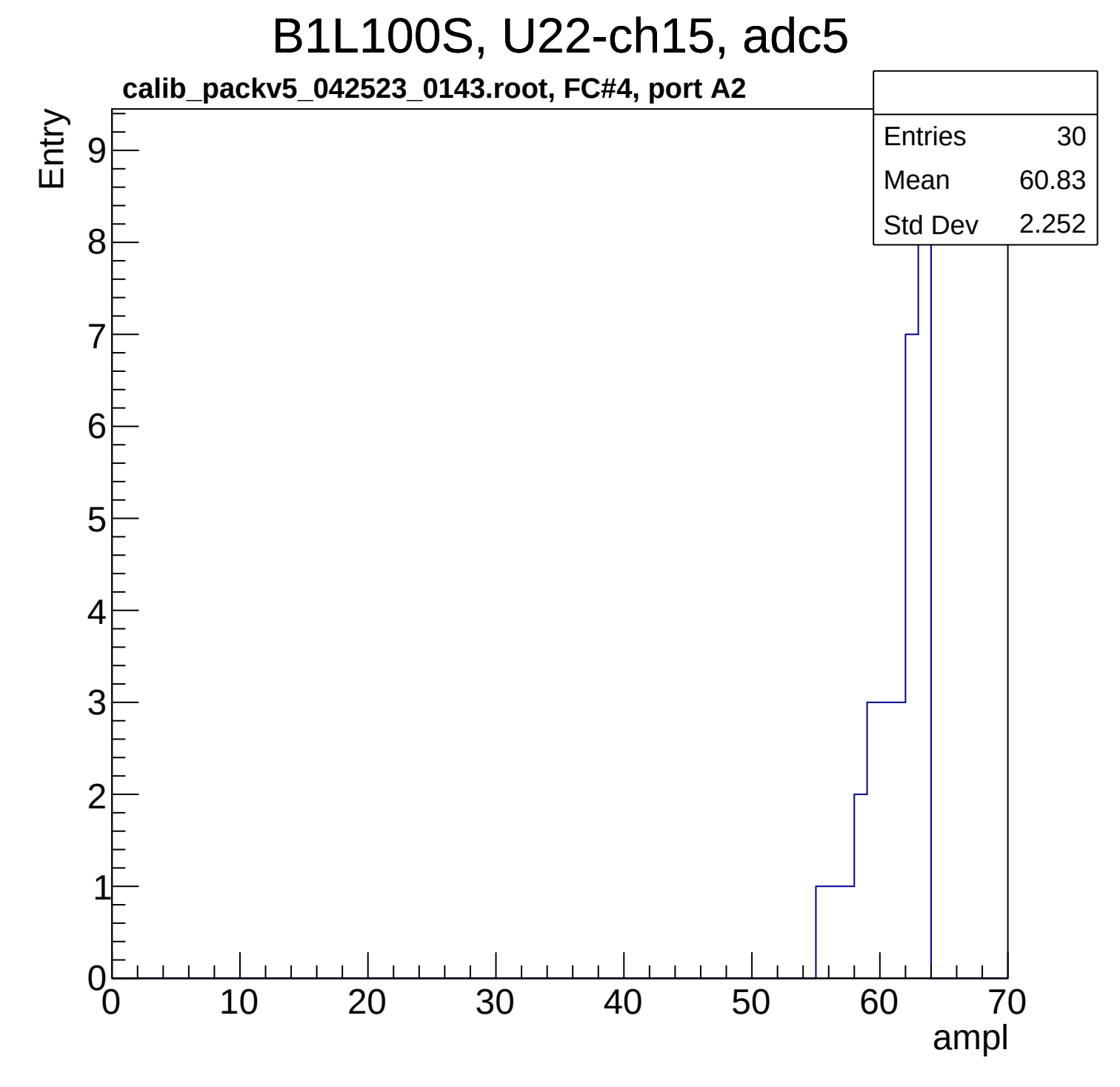
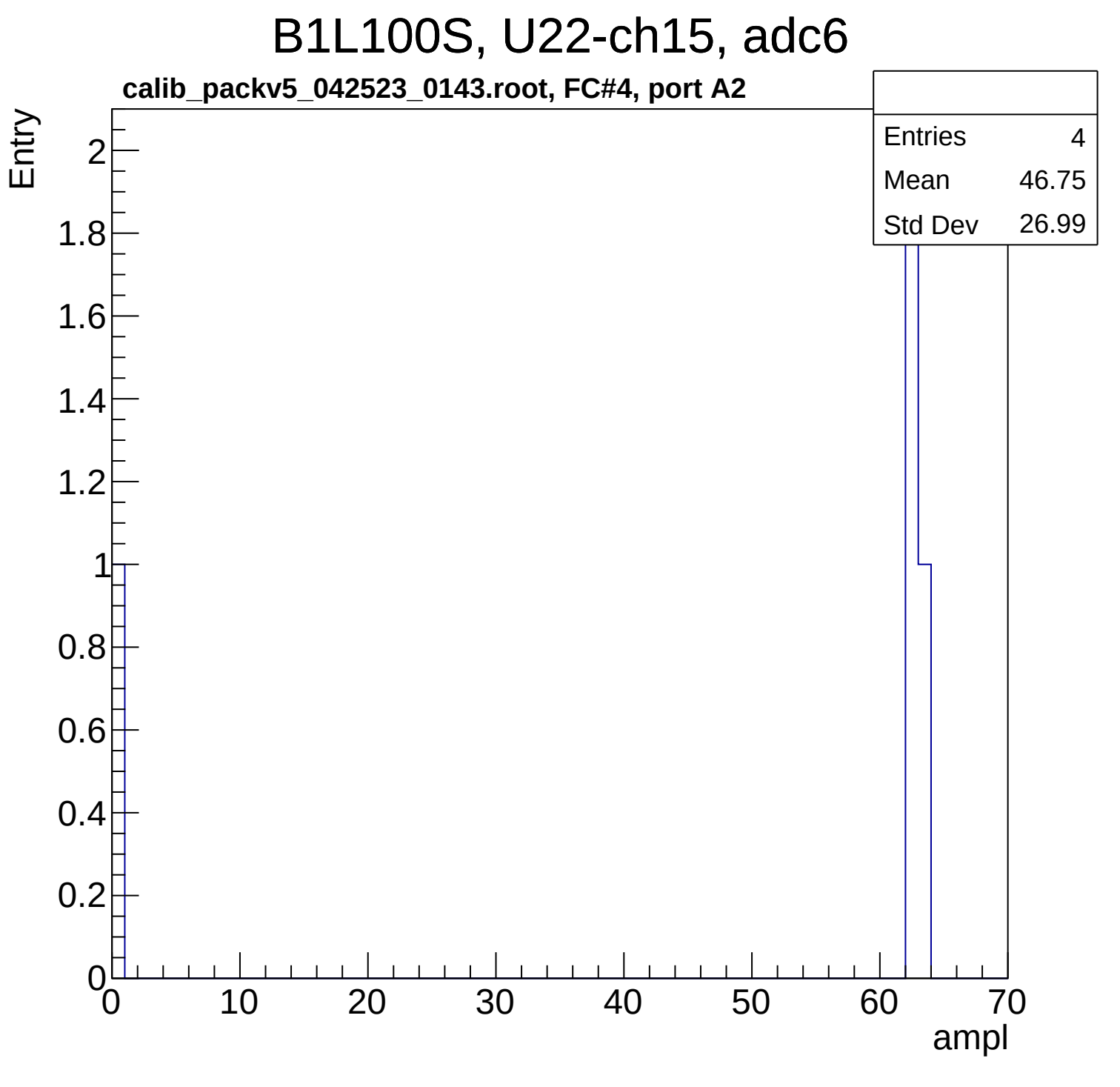
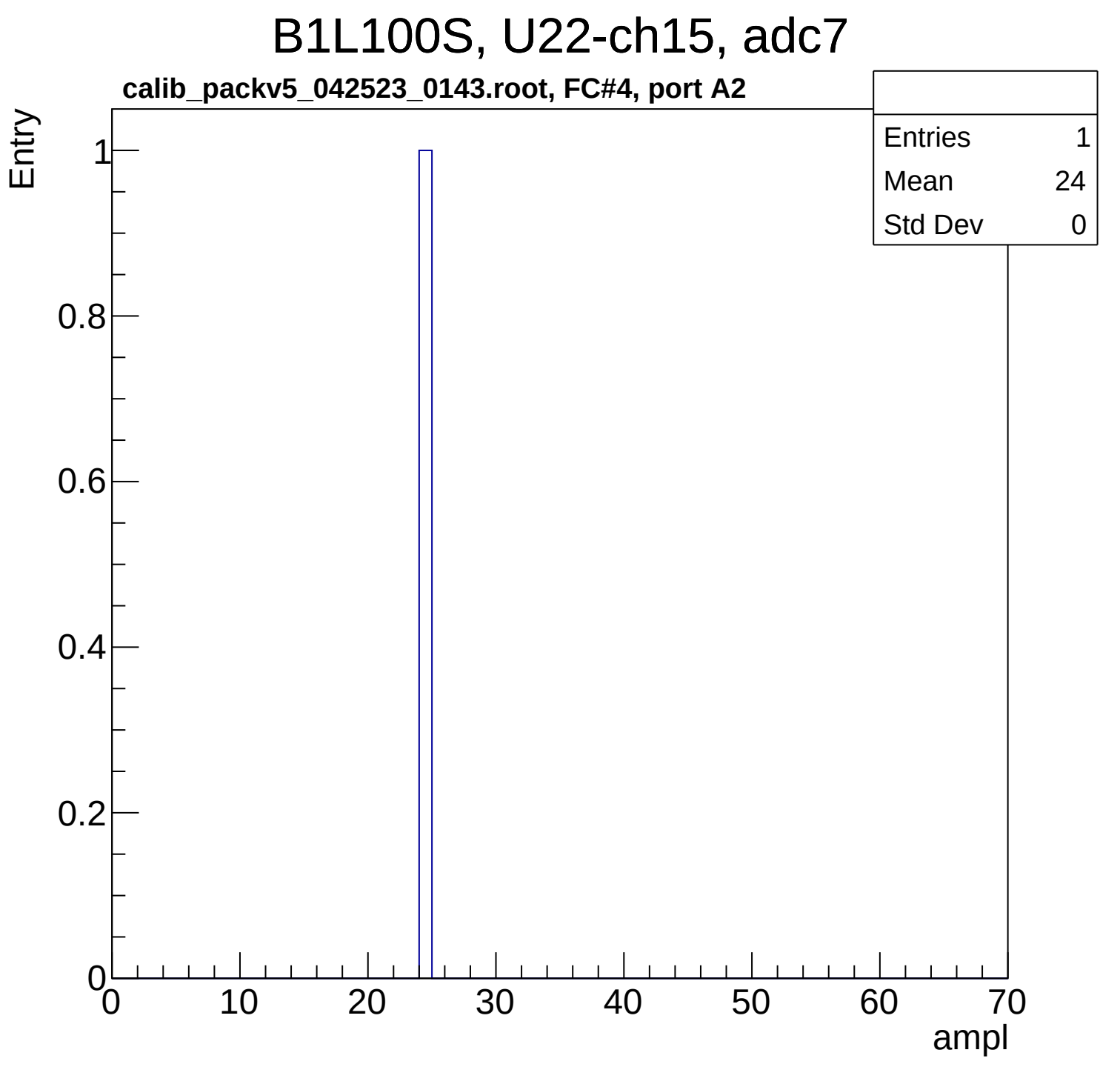
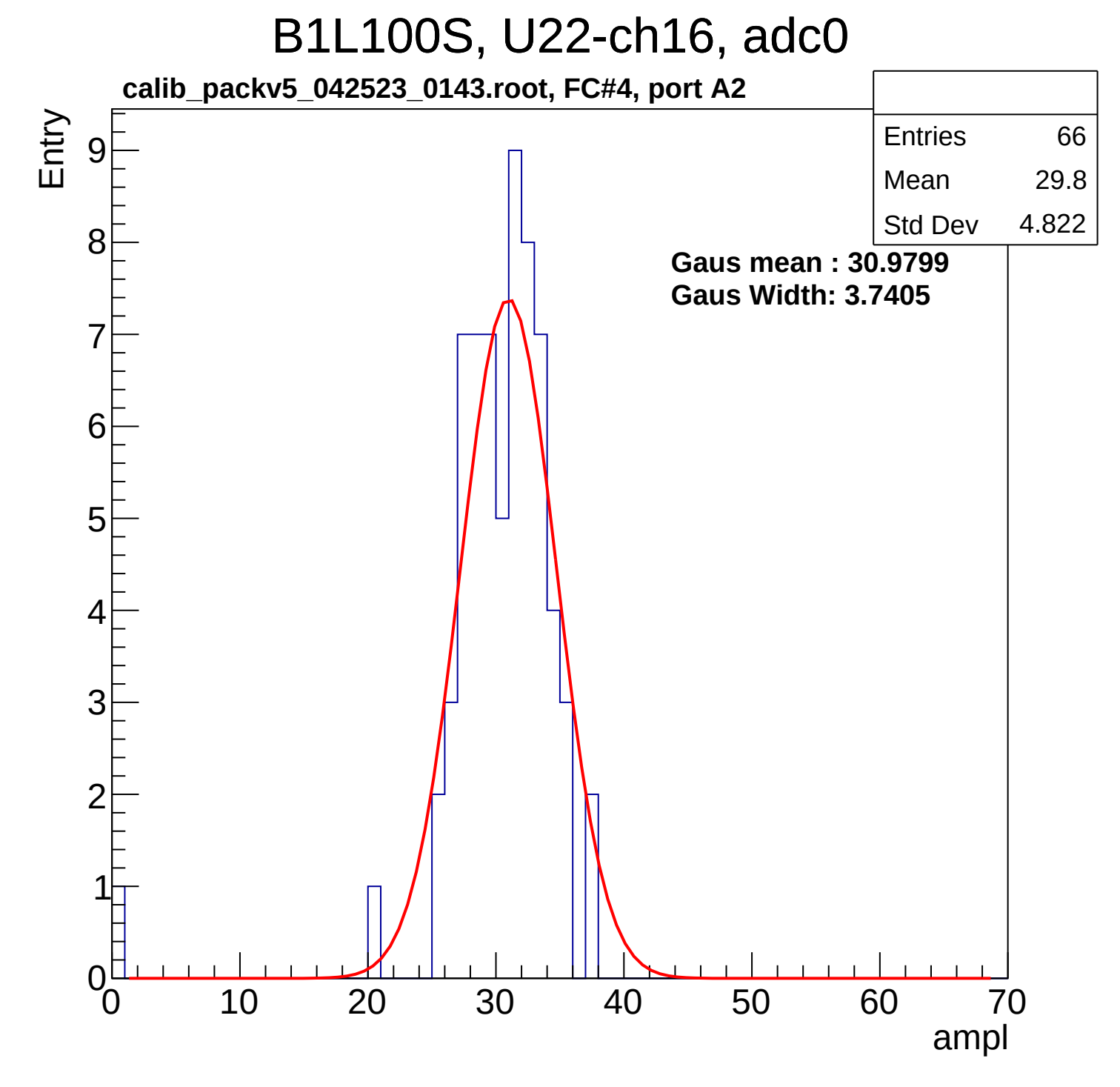

B1L100S, U22-ch15, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 50.96
Std Dev 3.571
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

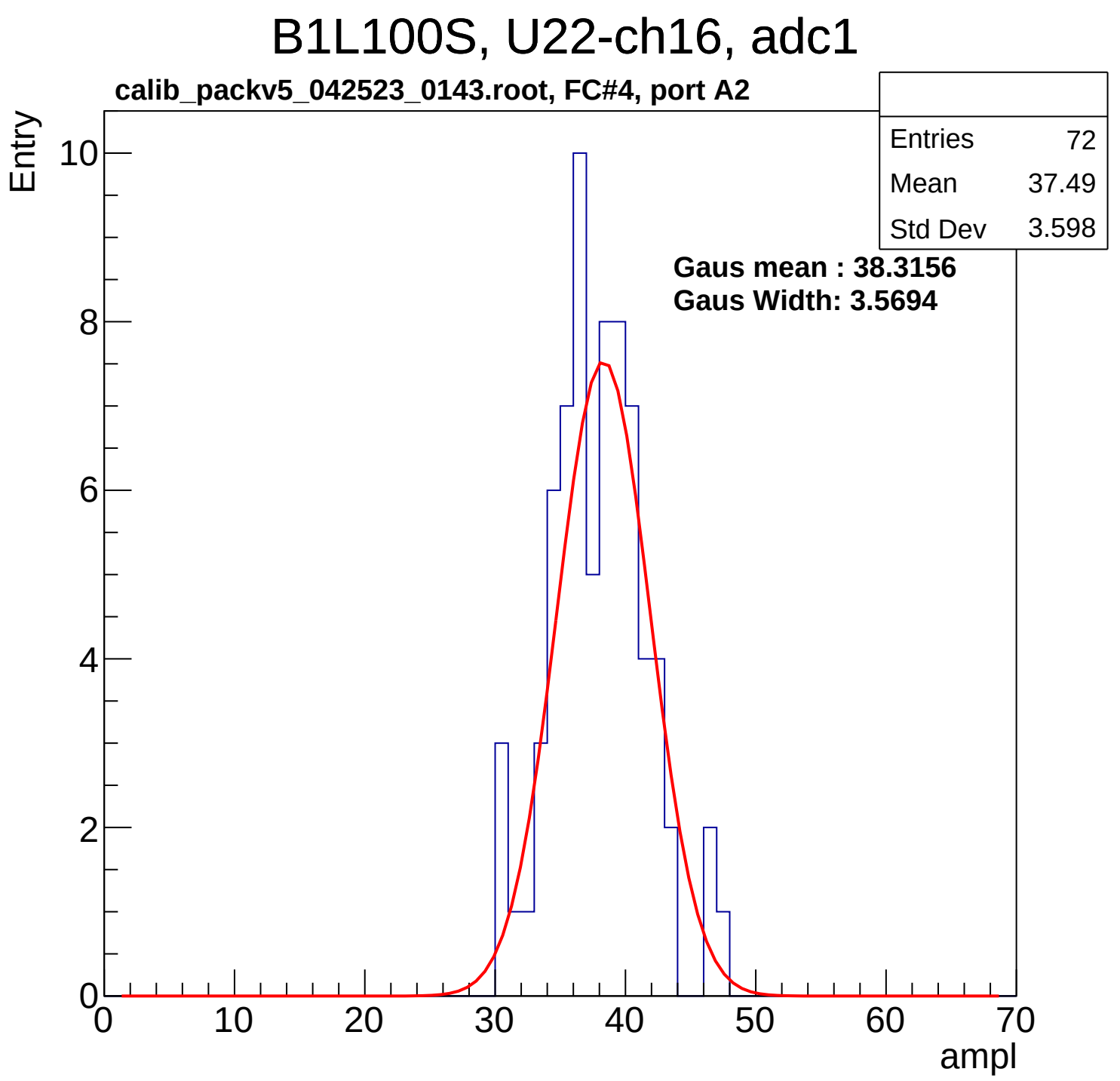

B1L100S, U22-ch15, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.14
Std Dev 2.994
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

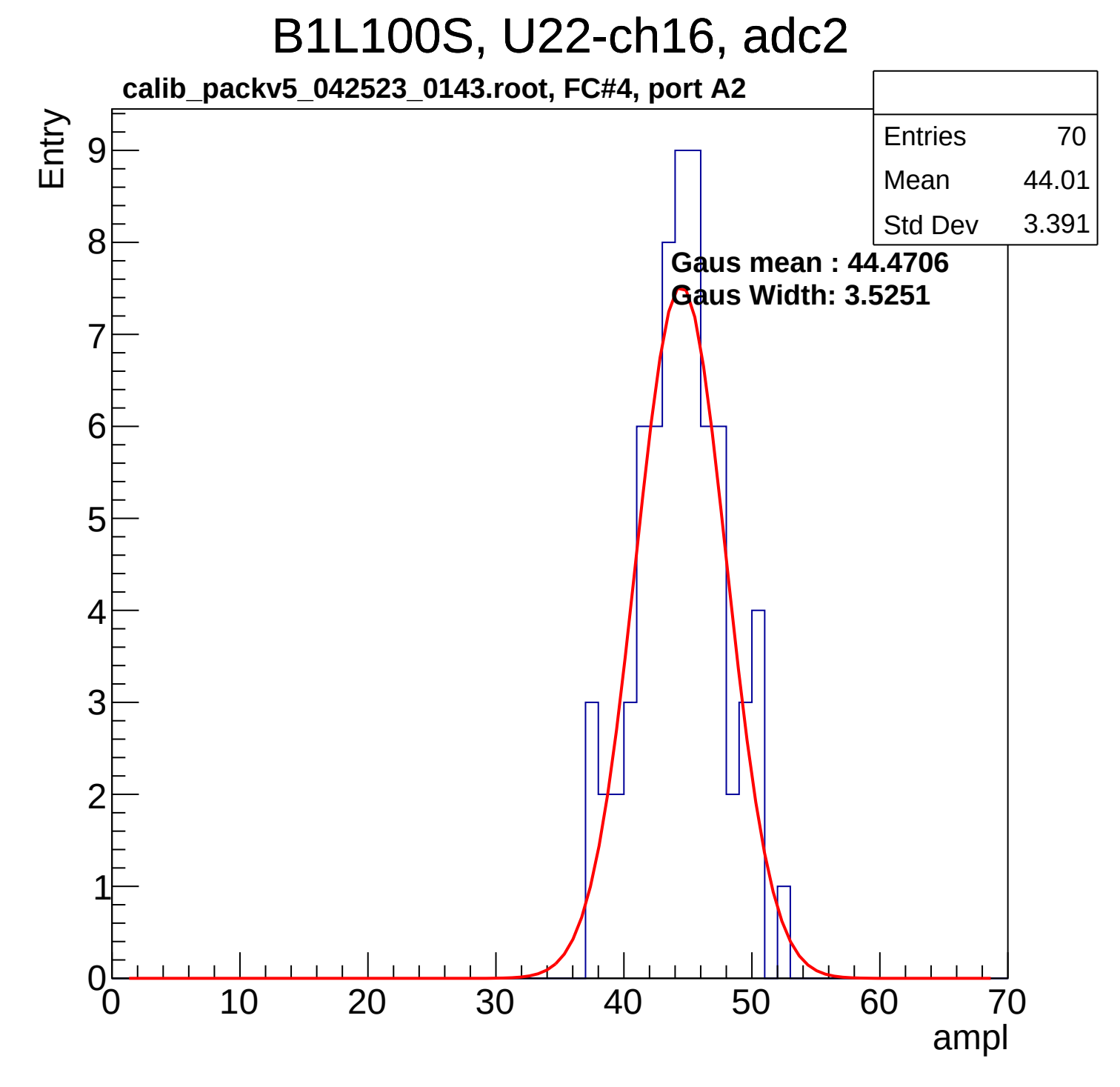

B1L100S, U22-ch15, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 60.83
Std Dev 2.252
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

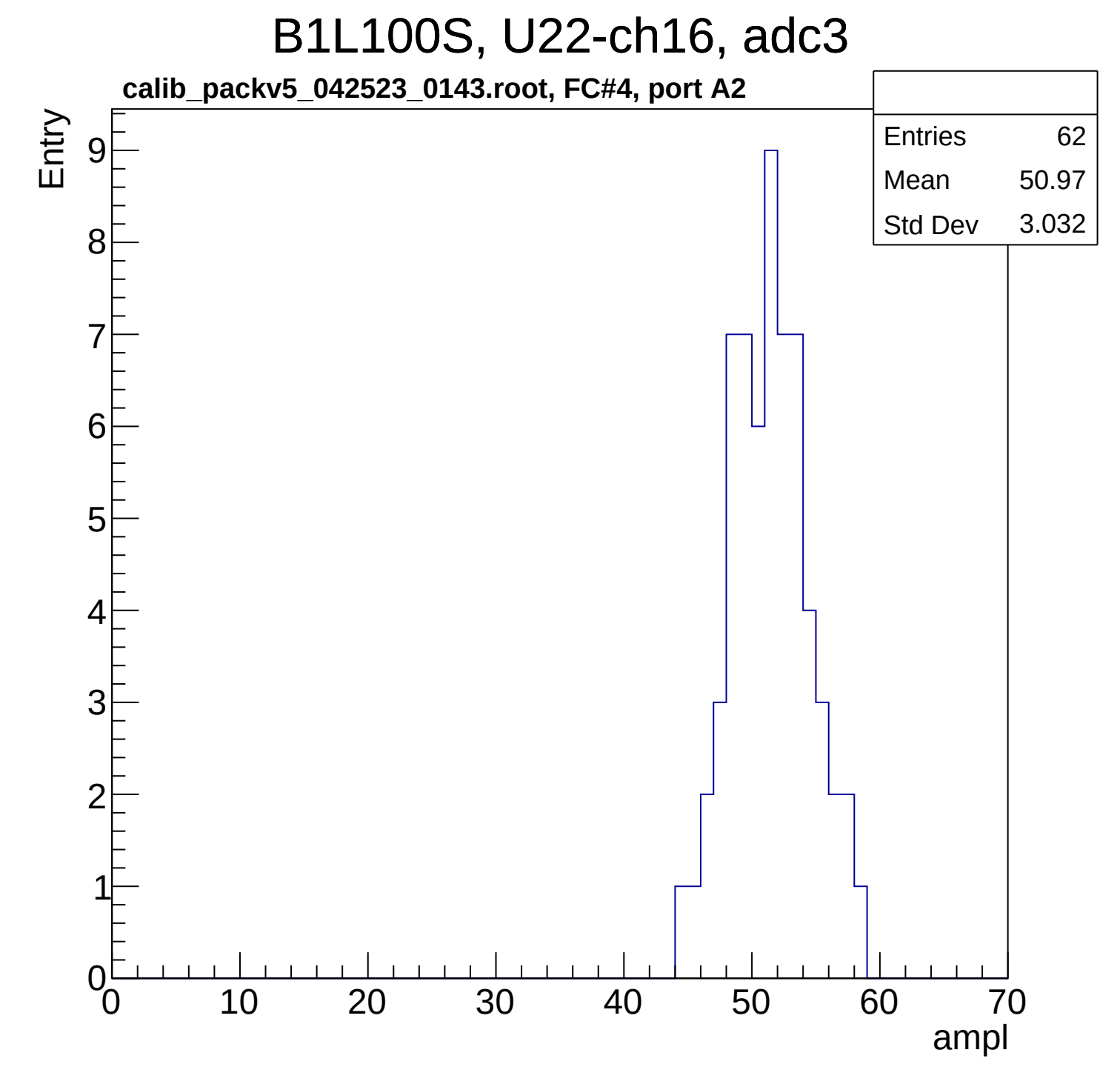

B1L100S, U22-ch15, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 46.75
Std Dev 26.99
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

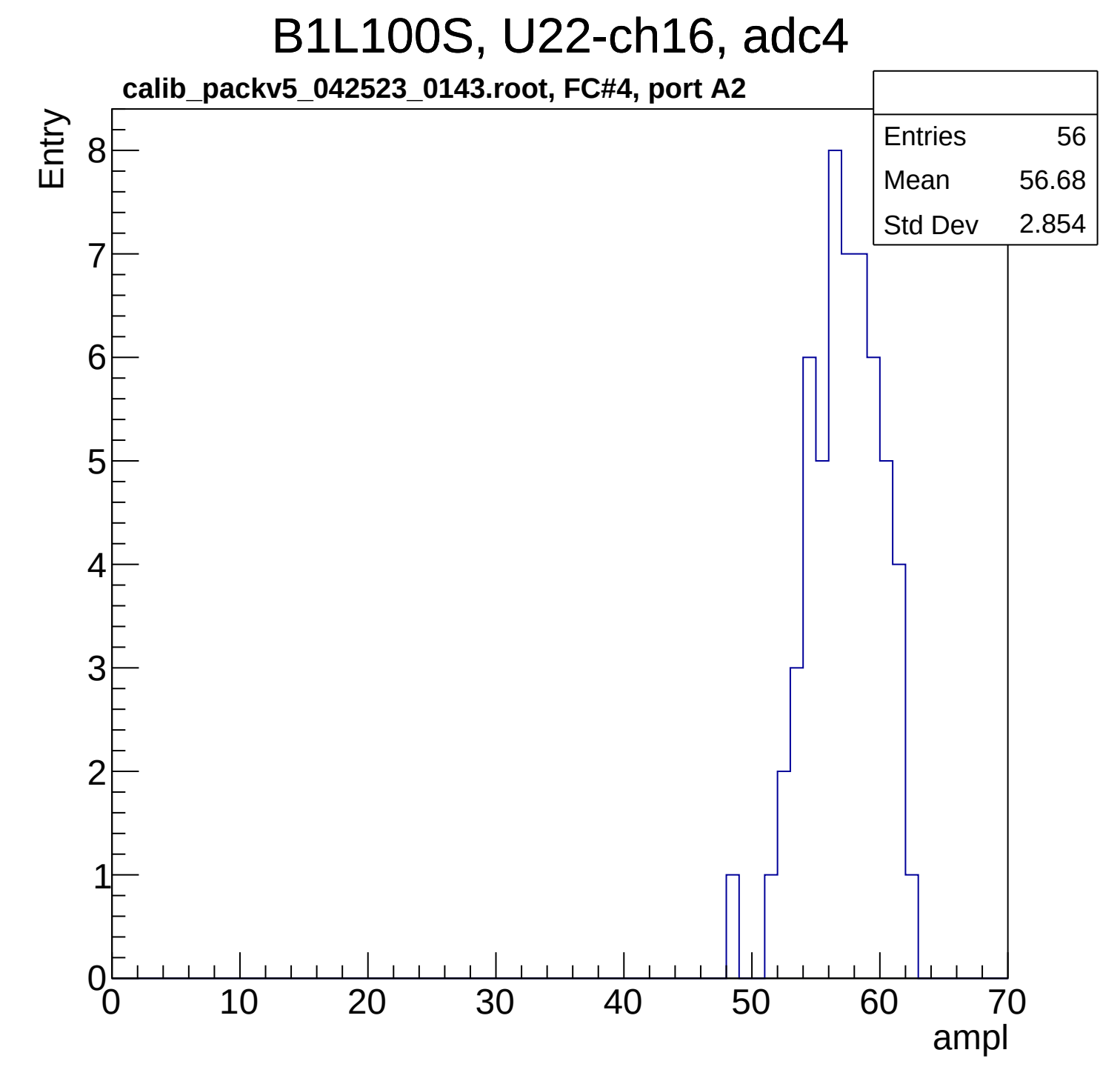

B1L100S, U22-ch15, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

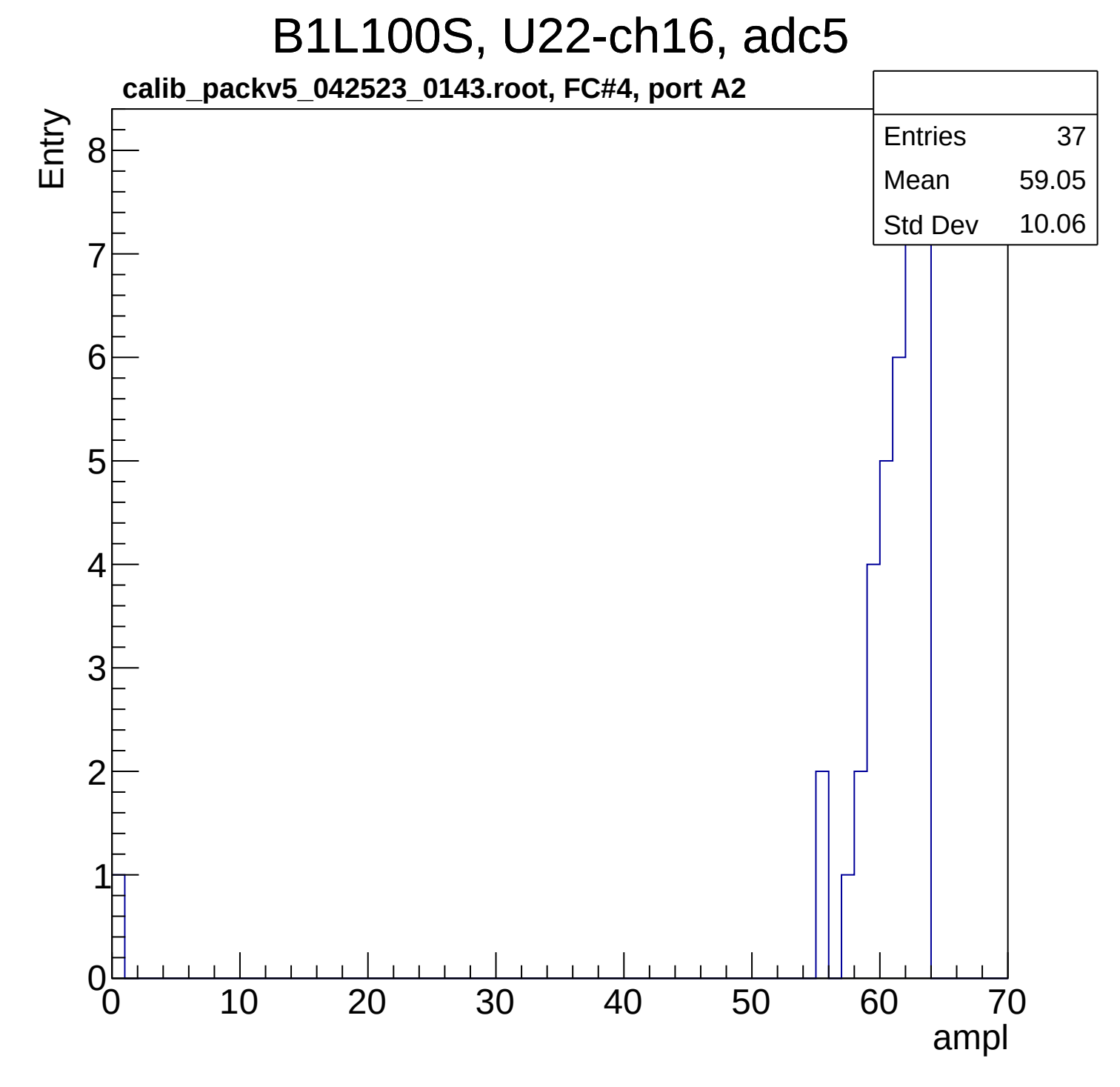

B1L100S, U22-ch16, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.8
Std Dev 4.822
Gaus mean : 30.9799
Gaus Width: 3.7405
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

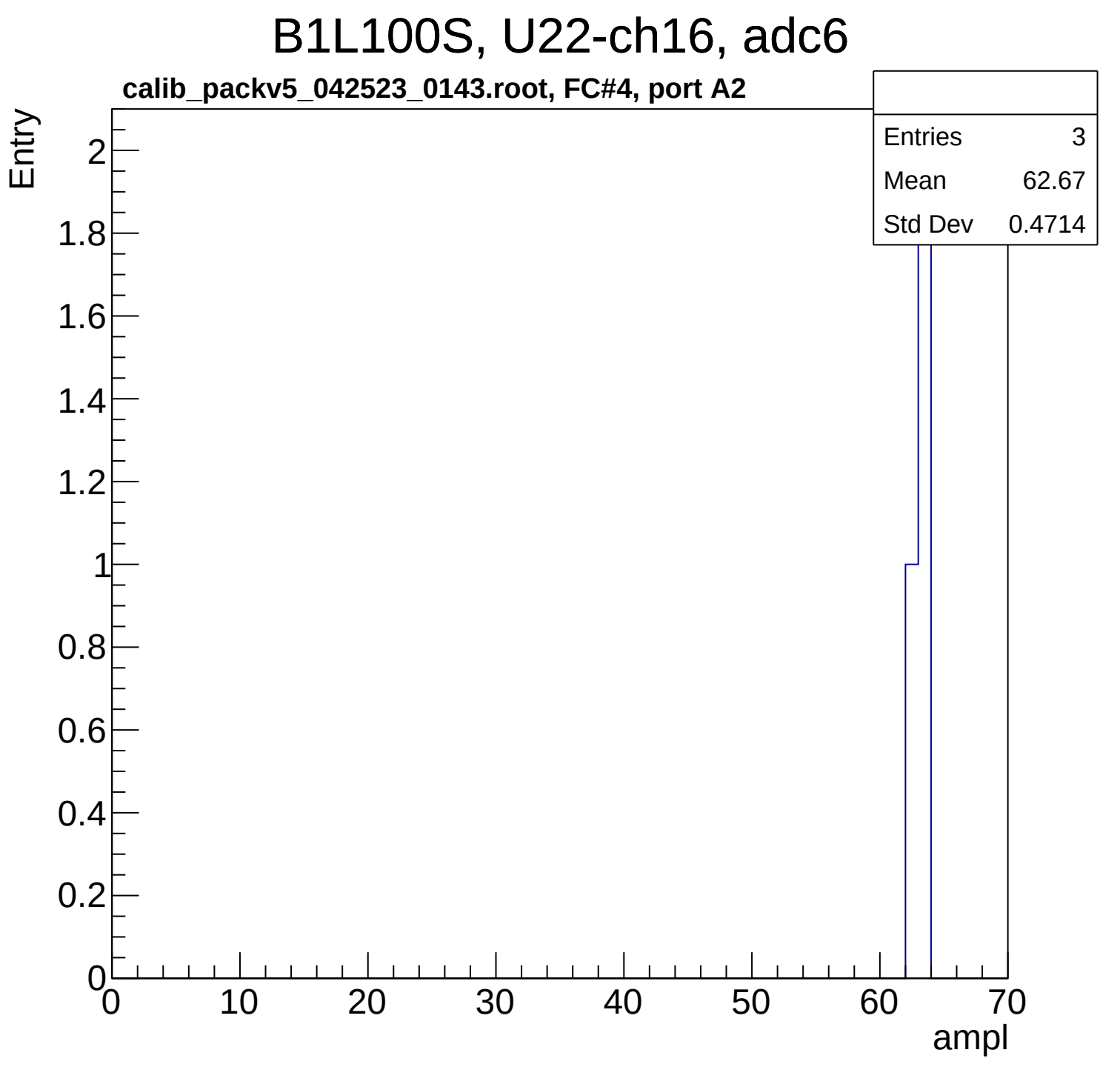

B1L100S, U22-ch16, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 37.49
Std Dev 3.598
Gaus mean : 38.3156
Gaus Width: 3.5694
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

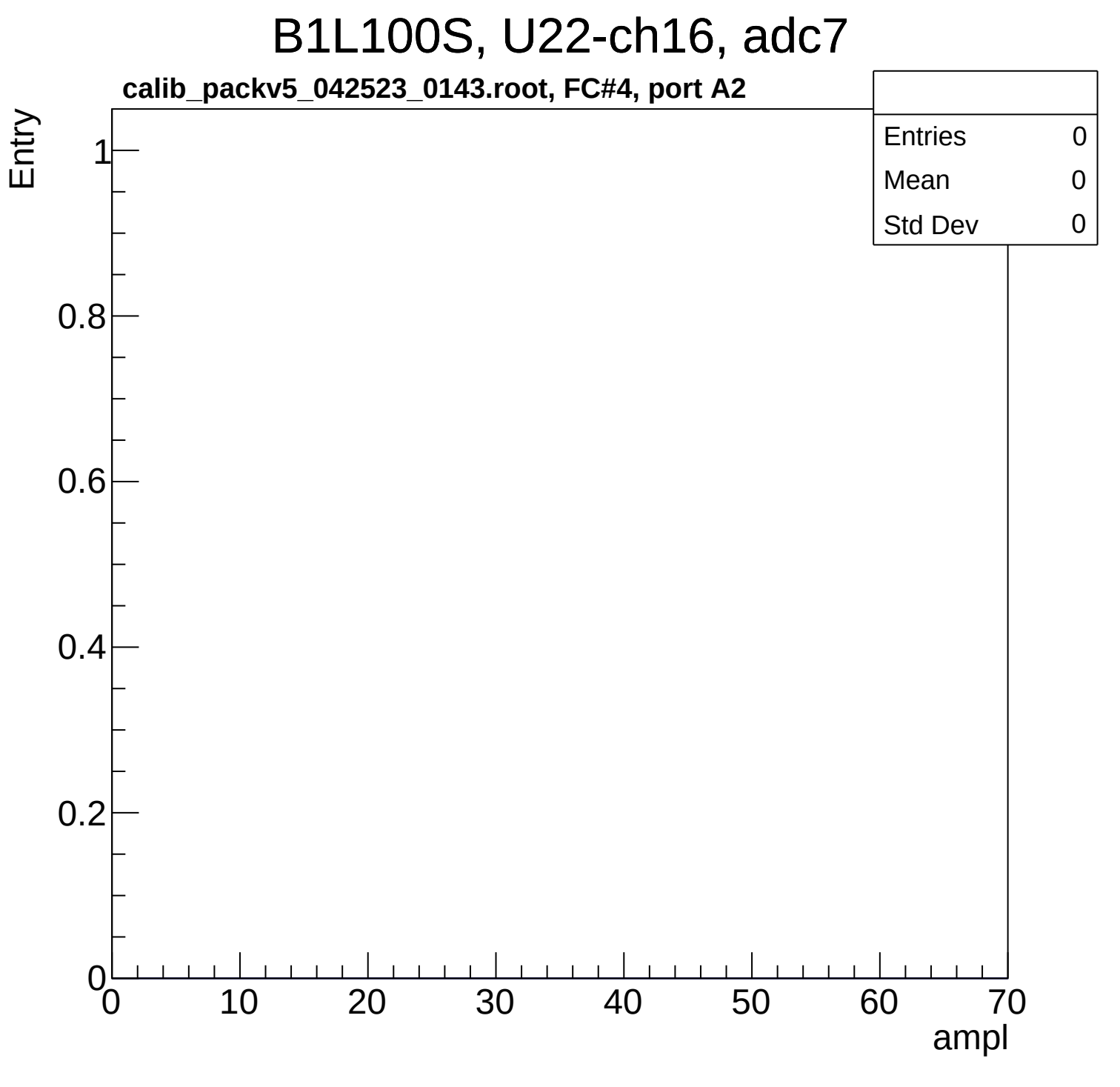

B1L100S, U22-ch16, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.01
Std Dev 3.391
Gaus mean : 44.4706
Gaus Width: 3.5251
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

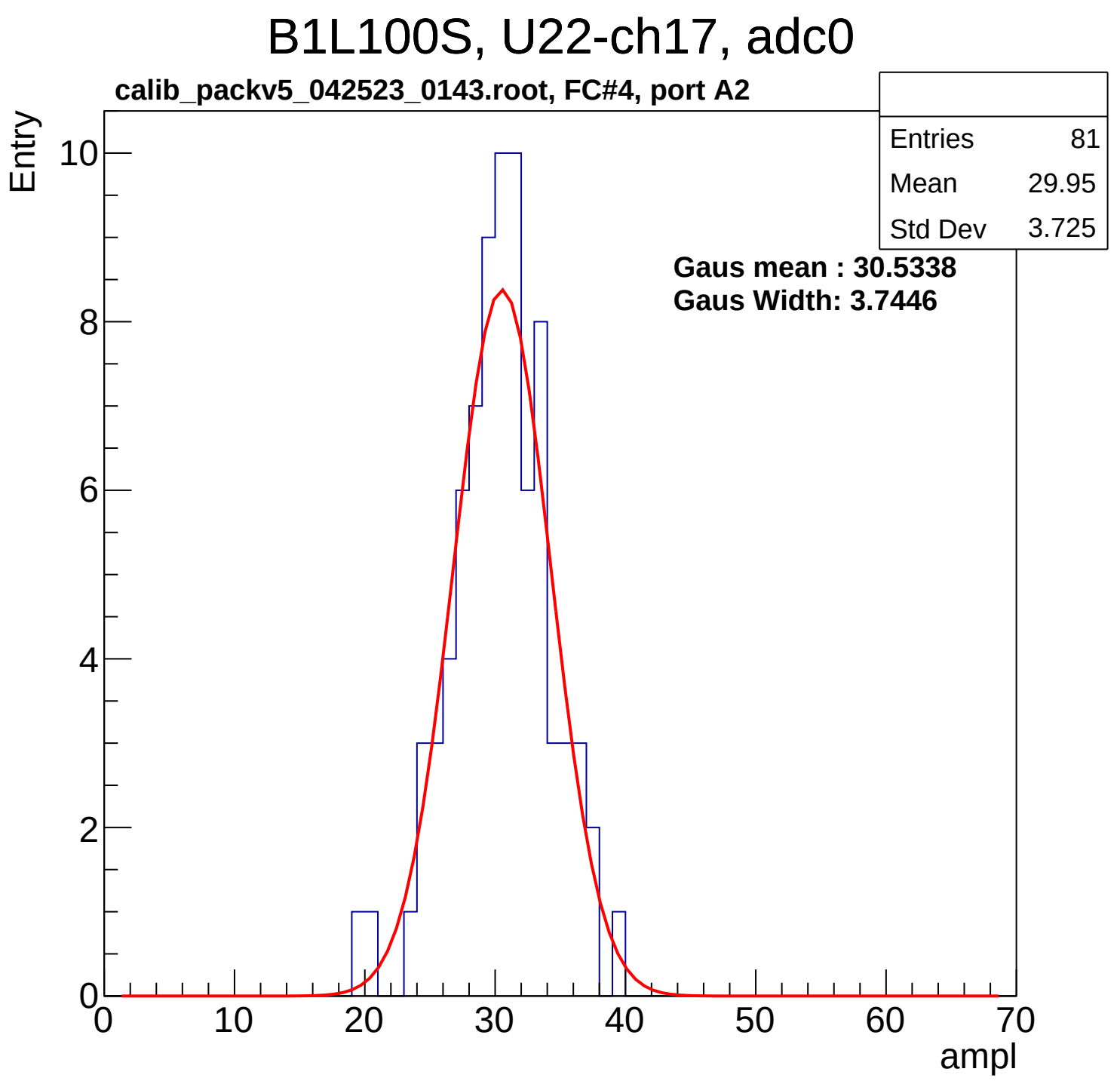

B1L100S, U22-ch16, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 50.97
Std Dev 3.032
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

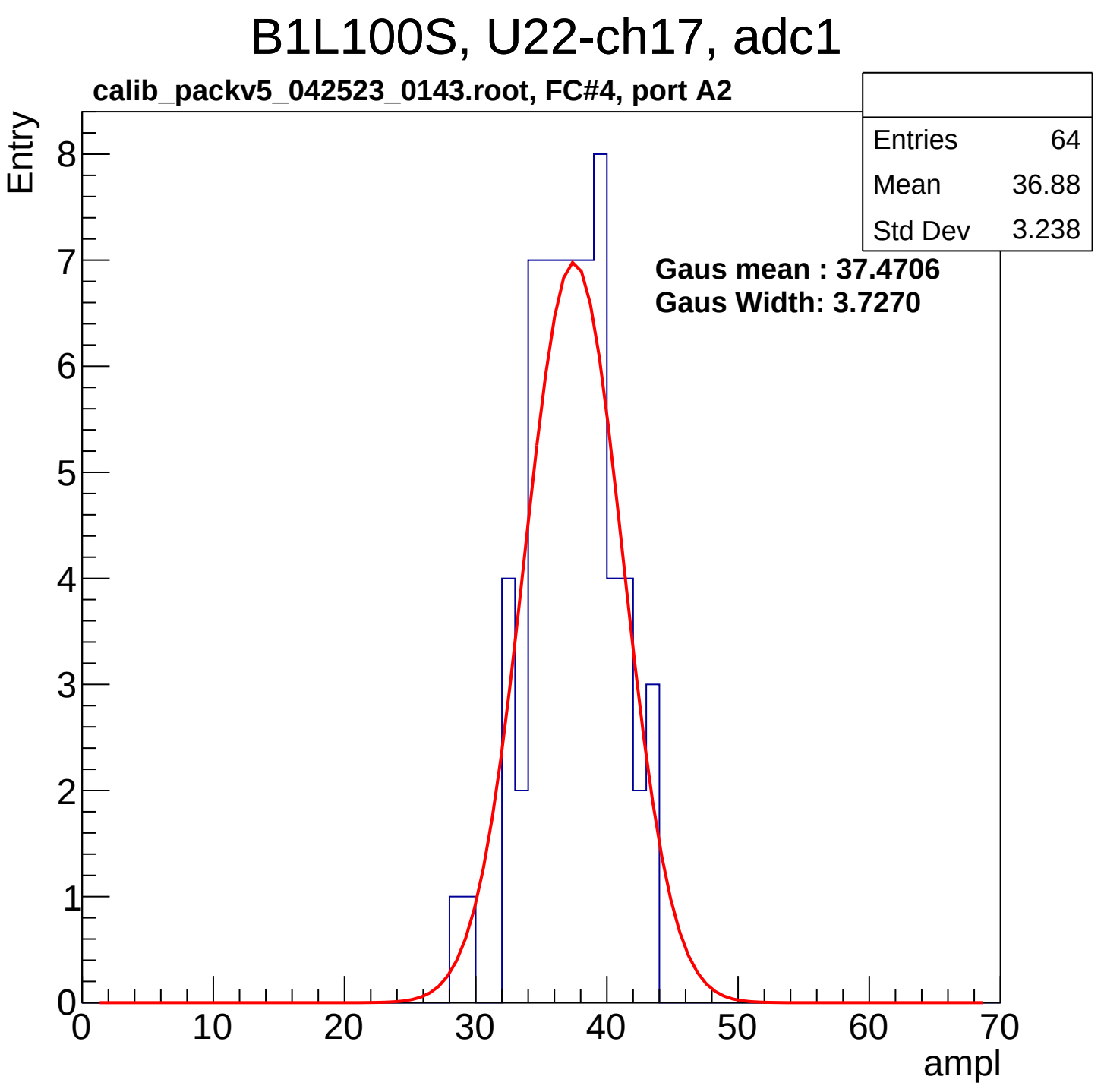

B1L100S, U22-ch16, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.68
Std Dev 2.854
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

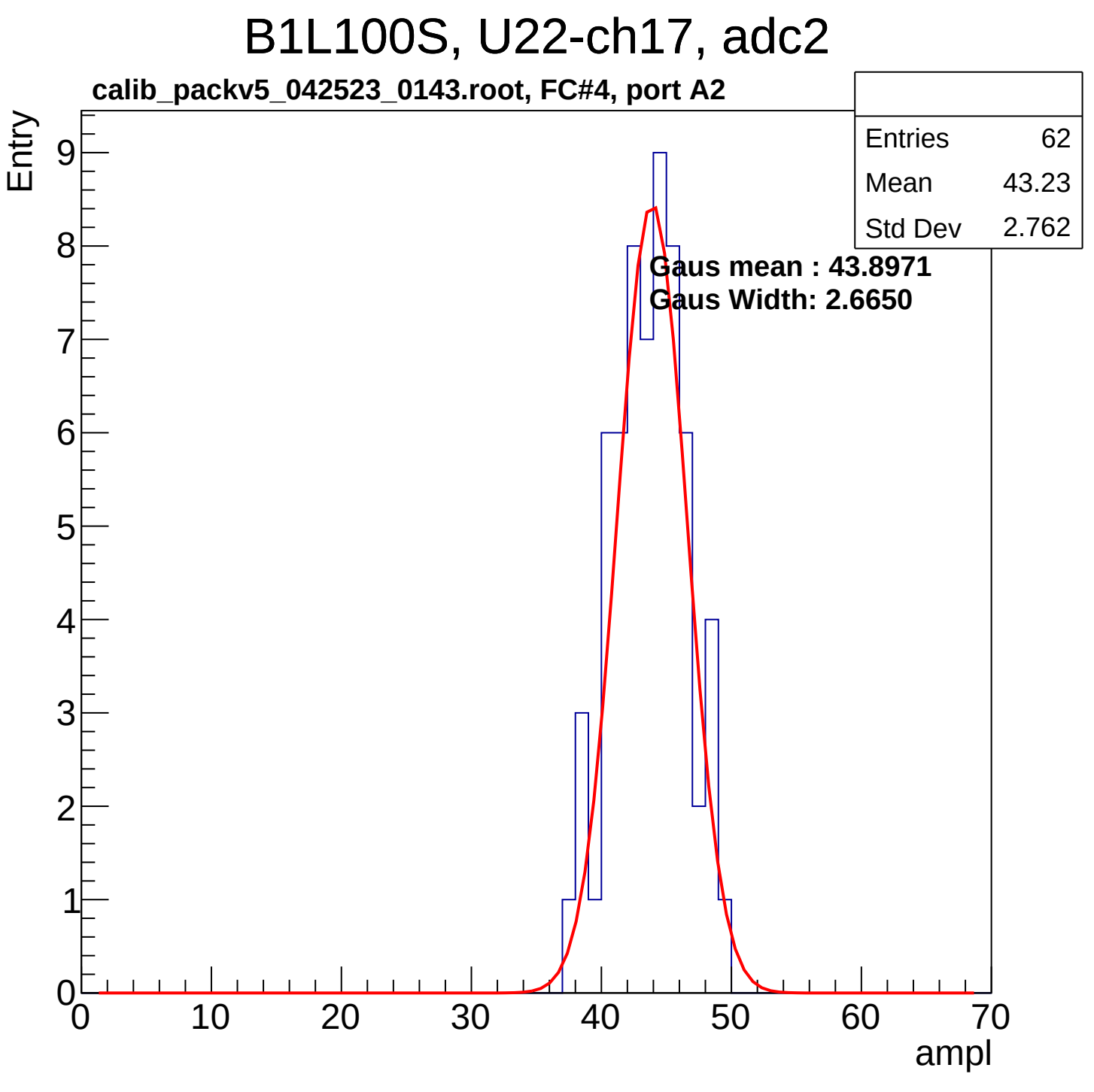

B1L100S, U22-ch16, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.05
Std Dev 10.06
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

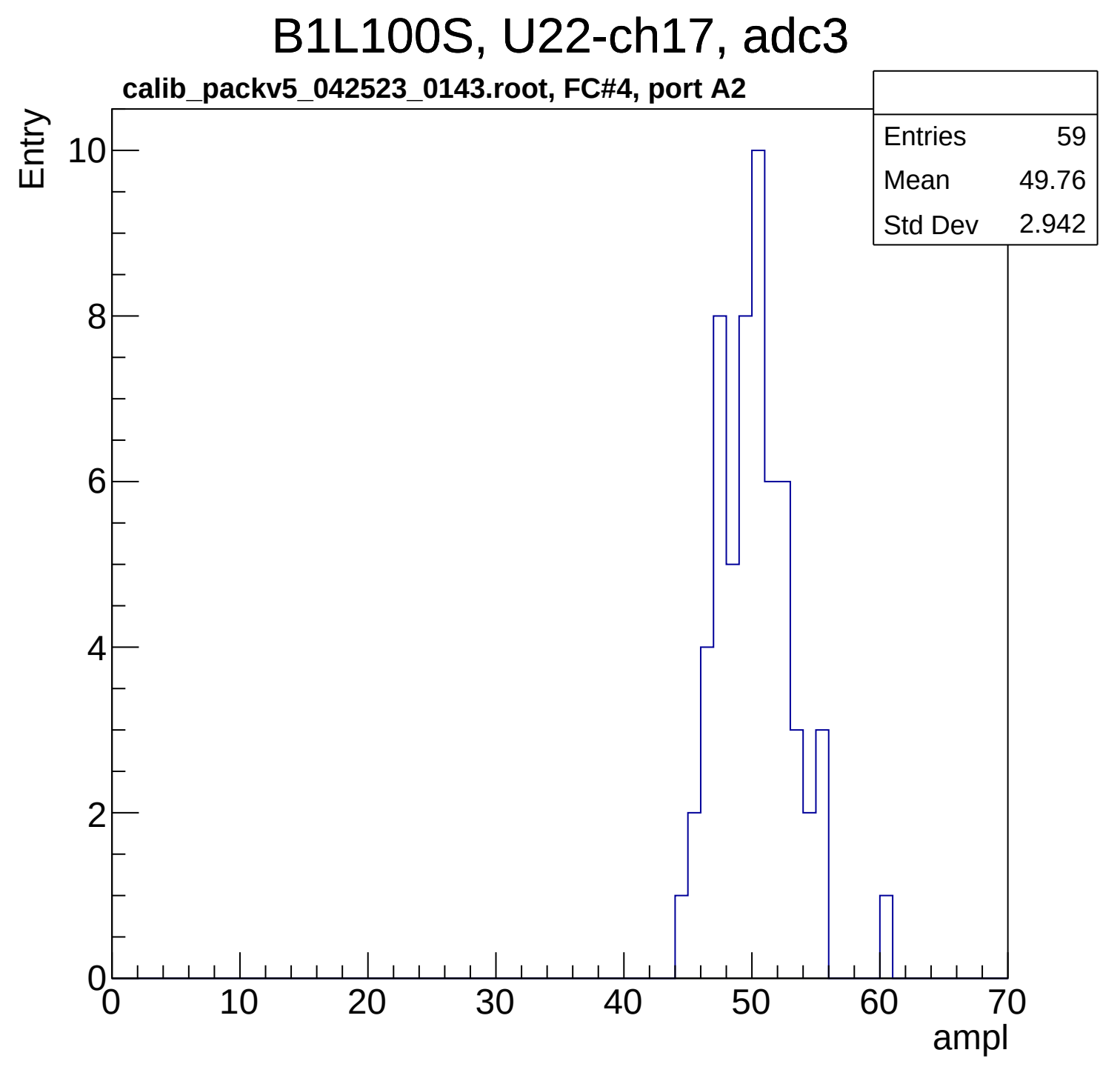

B1L100S, U22-ch16, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

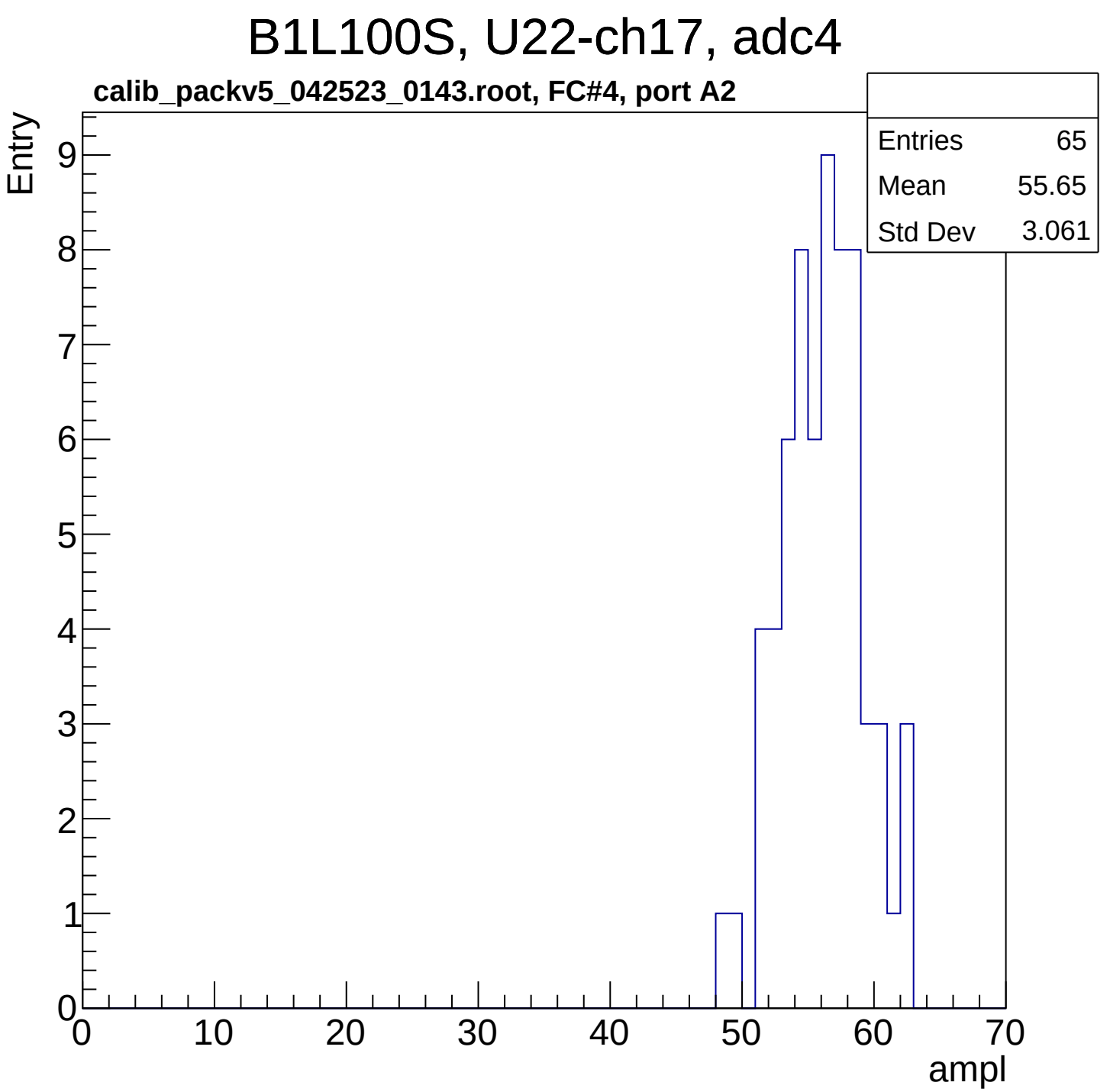

B1L100S, U22-ch16, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

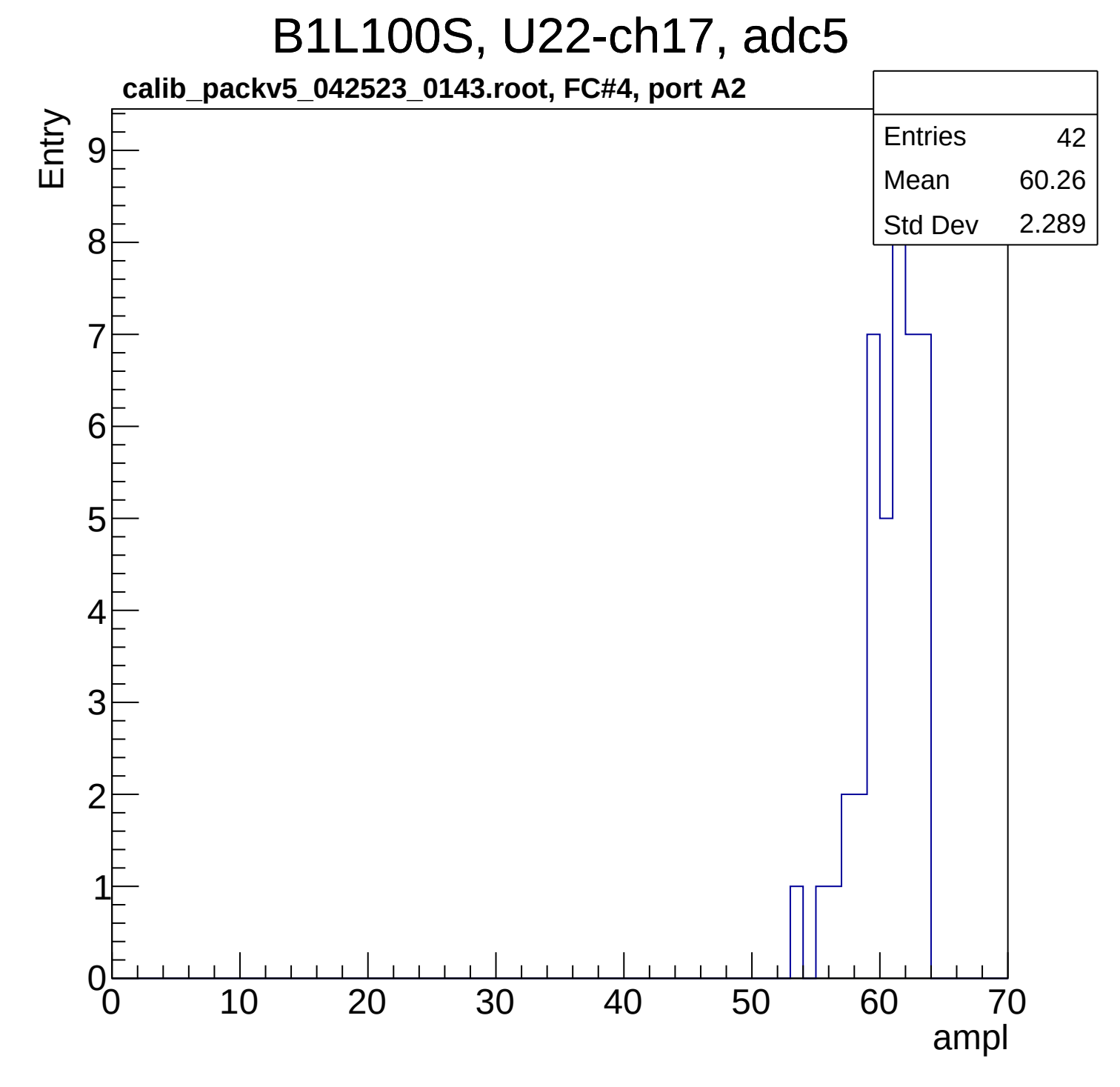

B1L100S, U22-ch17, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.95
Std Dev 3.725
Gaus mean : 30.5338
Gaus Width: 3.7446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch17, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.88
Std Dev 3.238
Gaus mean : 37.4706
Gaus Width: 3.7270
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch17, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.23
Std Dev 2.762
Gaus mean : 43.8971
Gaus Width: 2.6650
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch17, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 49.76
Std Dev 2.942
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch17, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.65
Std Dev 3.061
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch17, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 60.26
Std Dev 2.289
Entry
ampl
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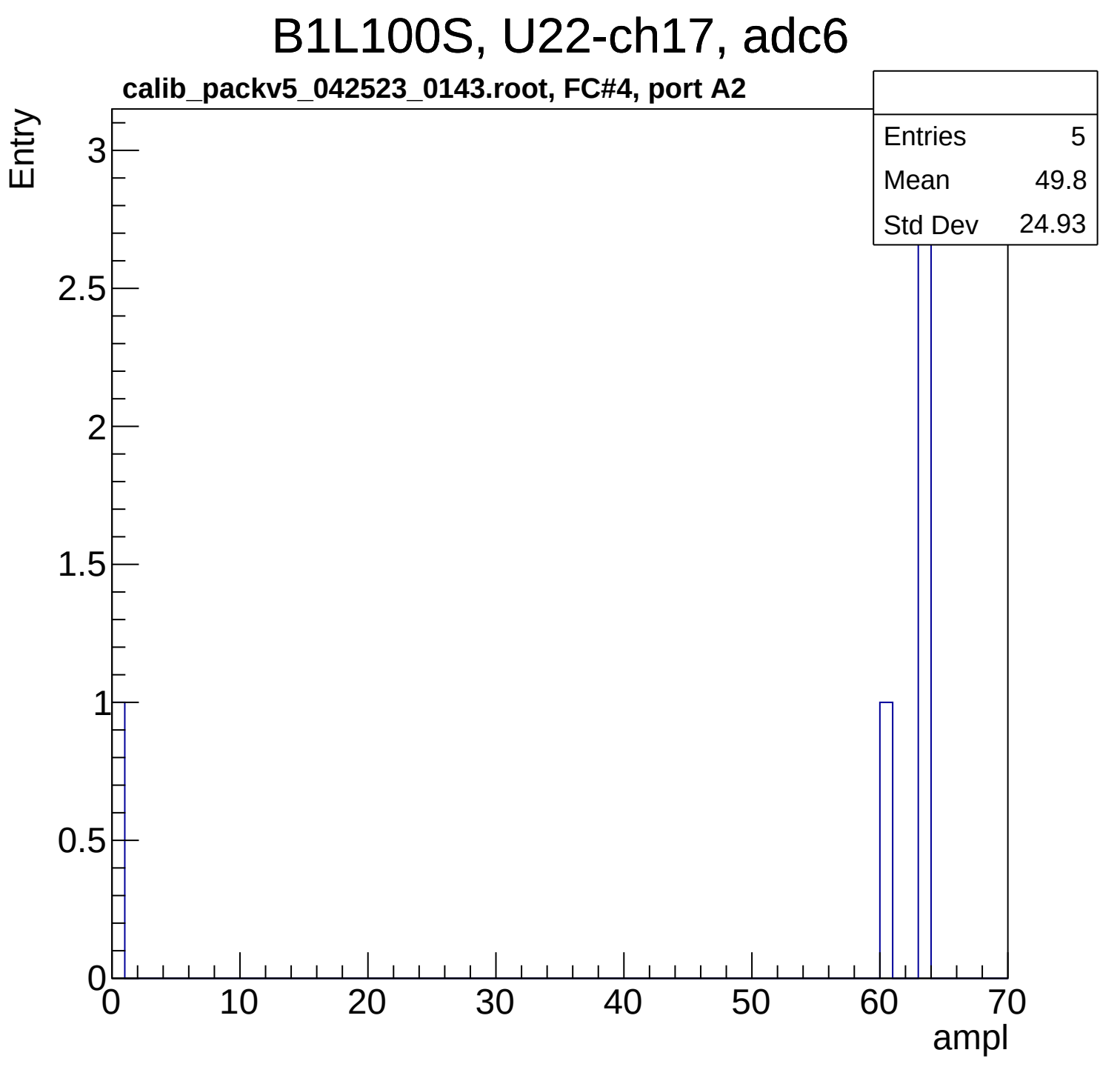
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch17, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 49.8
Std Dev 24.93
Entry
ampl
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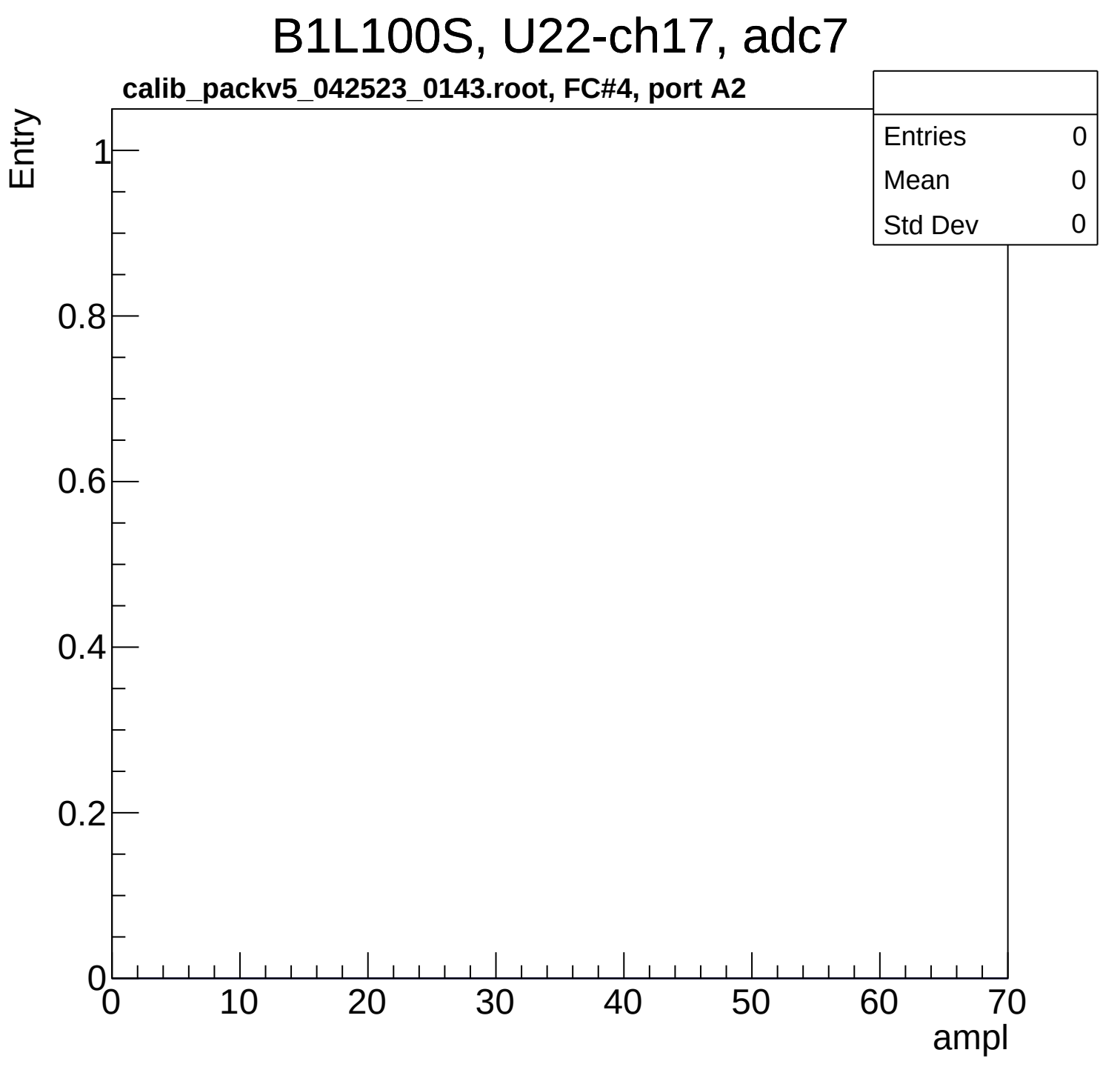
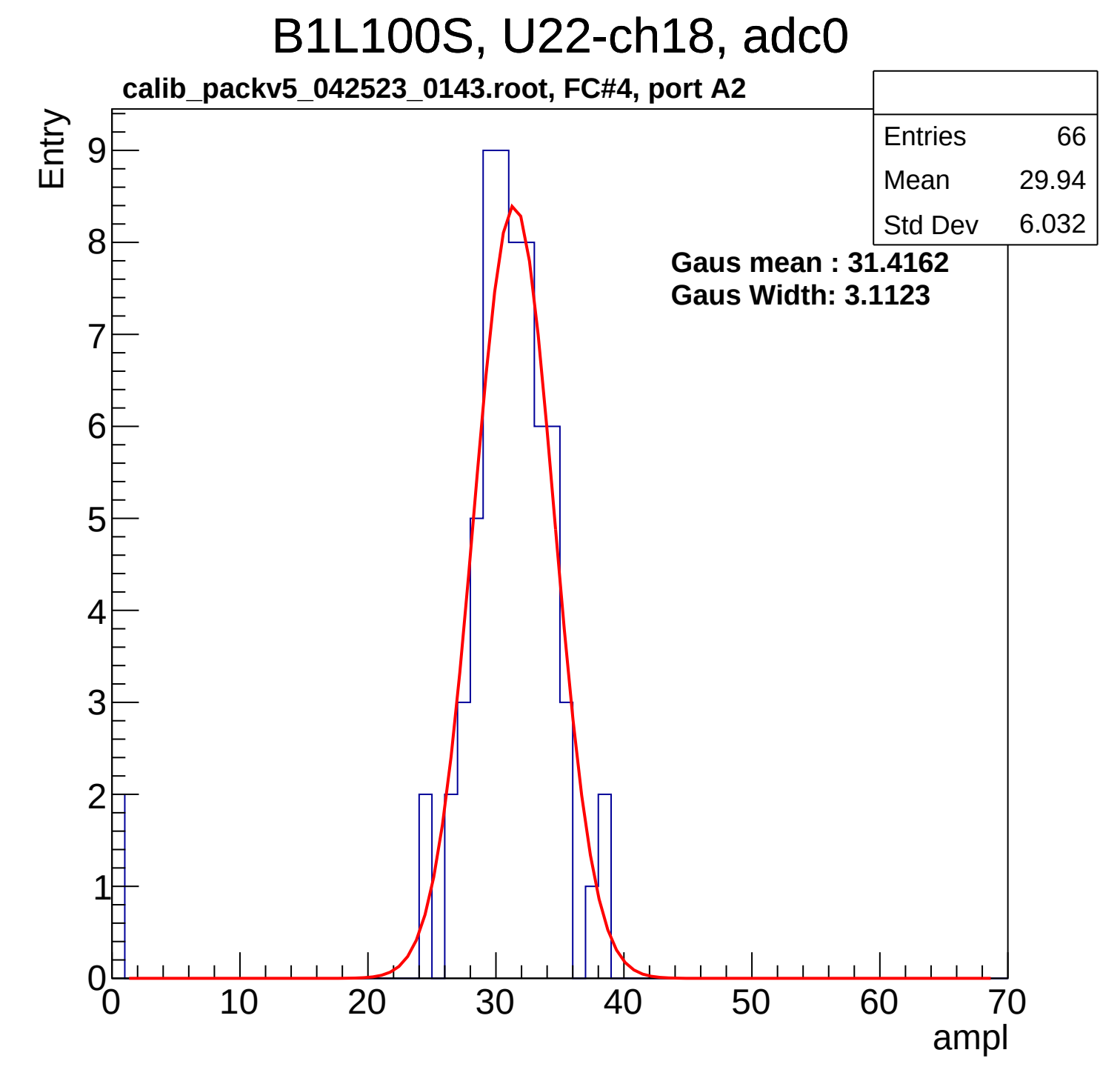
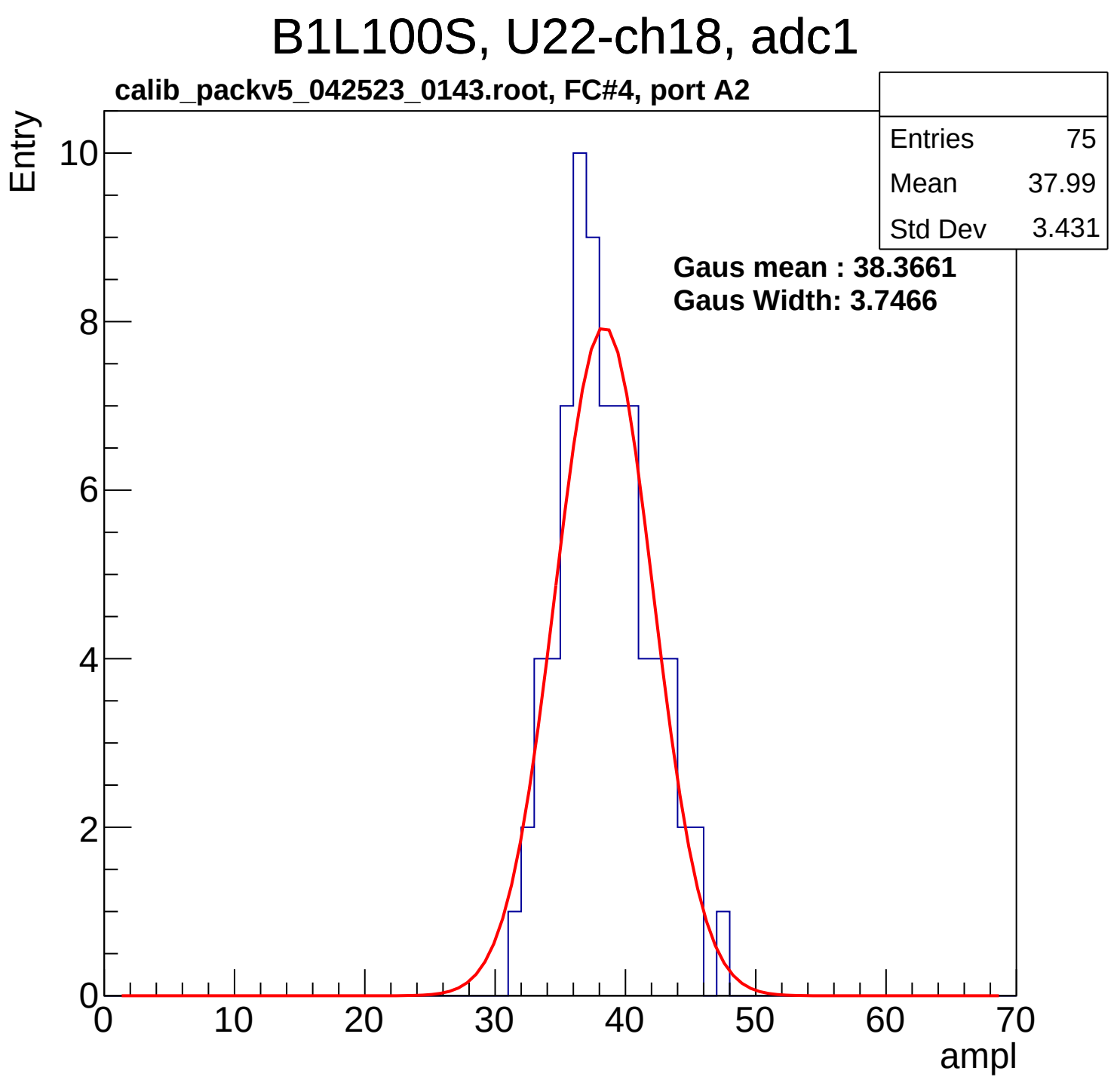
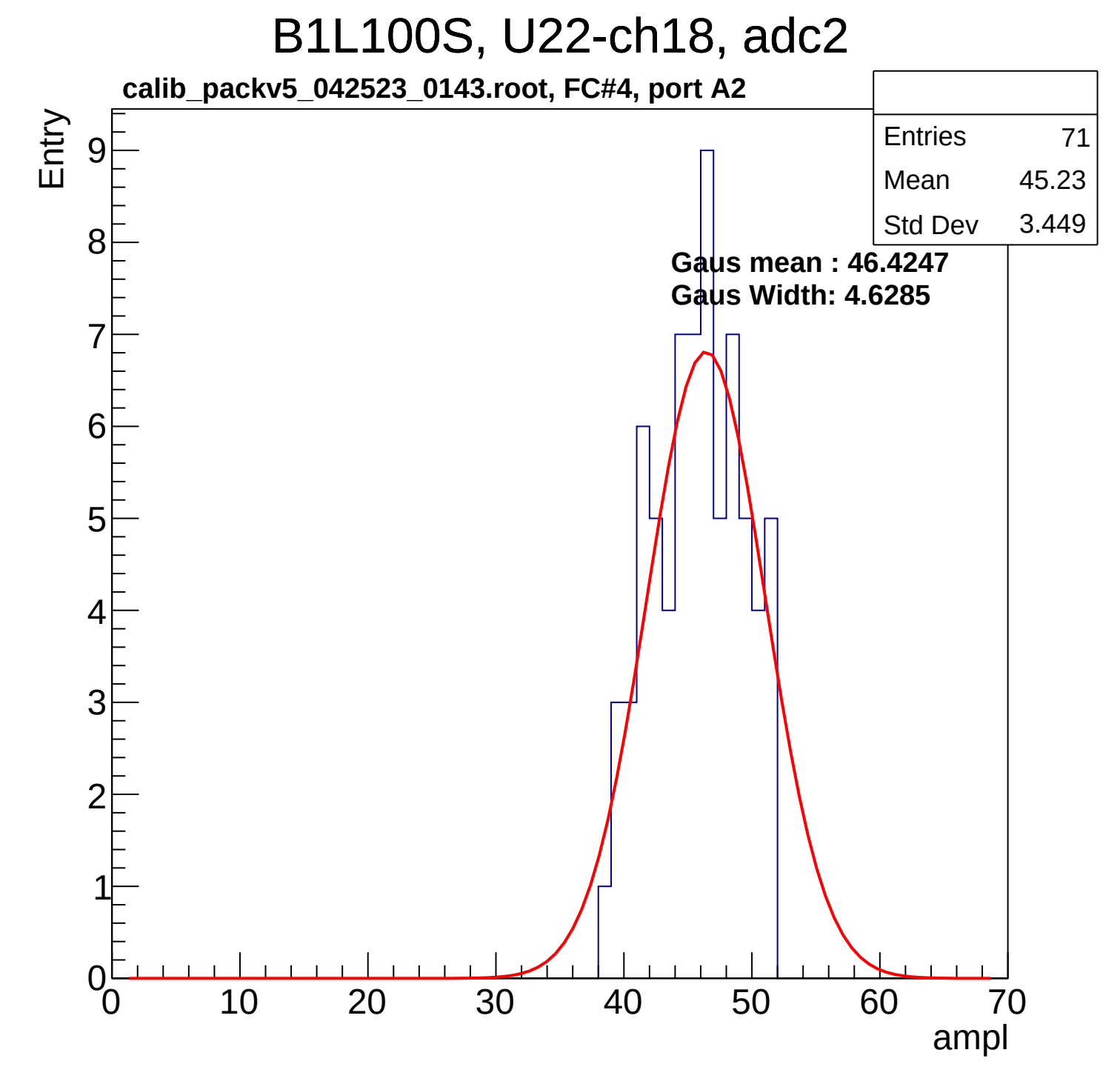
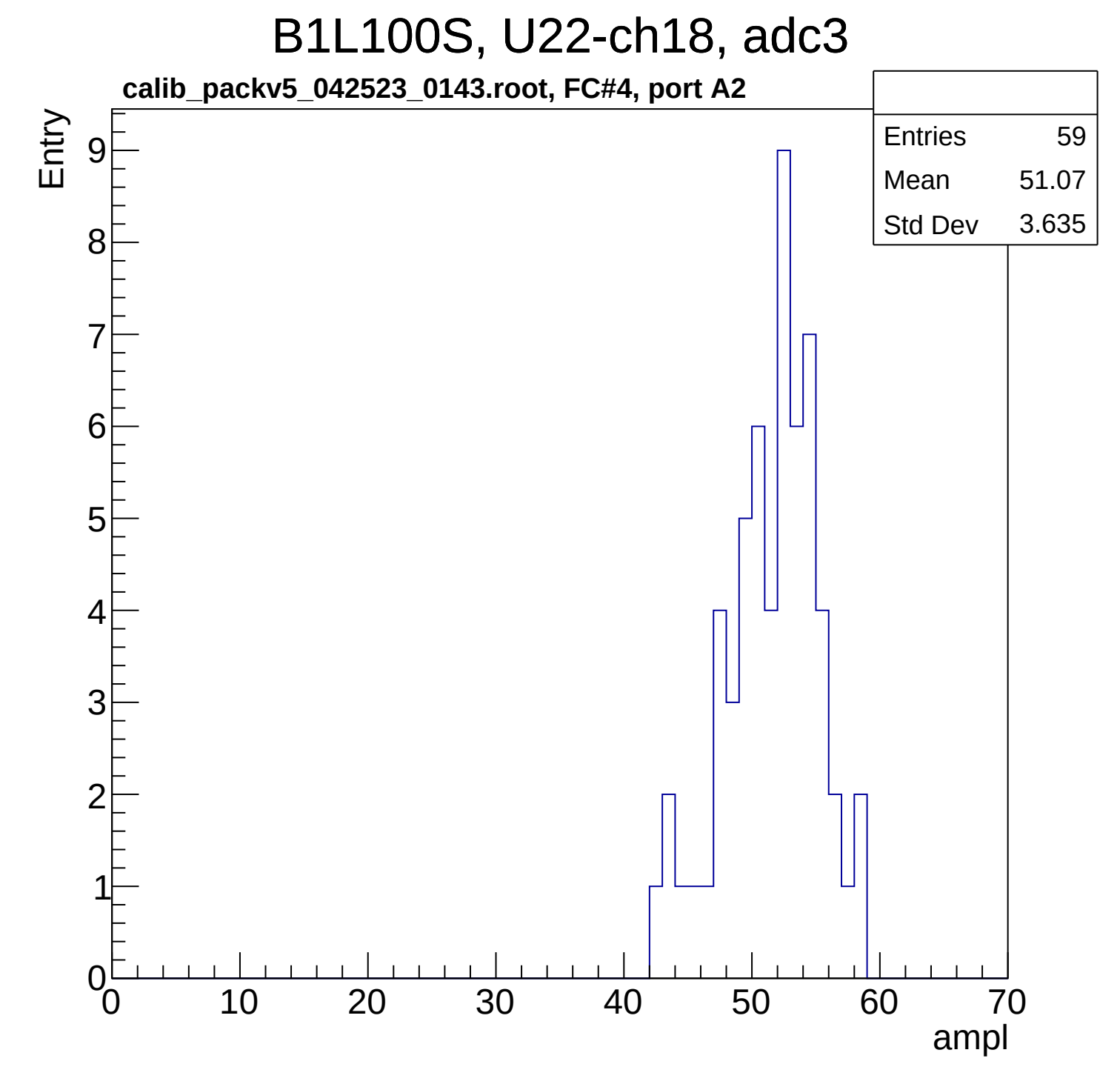
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch17, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch18, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.94
Std Dev 6.032
Gaus mean : 31.4162
Gaus Width: 3.1123
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch18, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.99
Std Dev 3.431
Gaus mean : 38.3661
Gaus Width: 3.7466
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch18, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 45.23
Std Dev 3.449
Gaus mean : 46.4247
Gaus Width: 4.6285
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch18, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 51.07
Std Dev 3.635
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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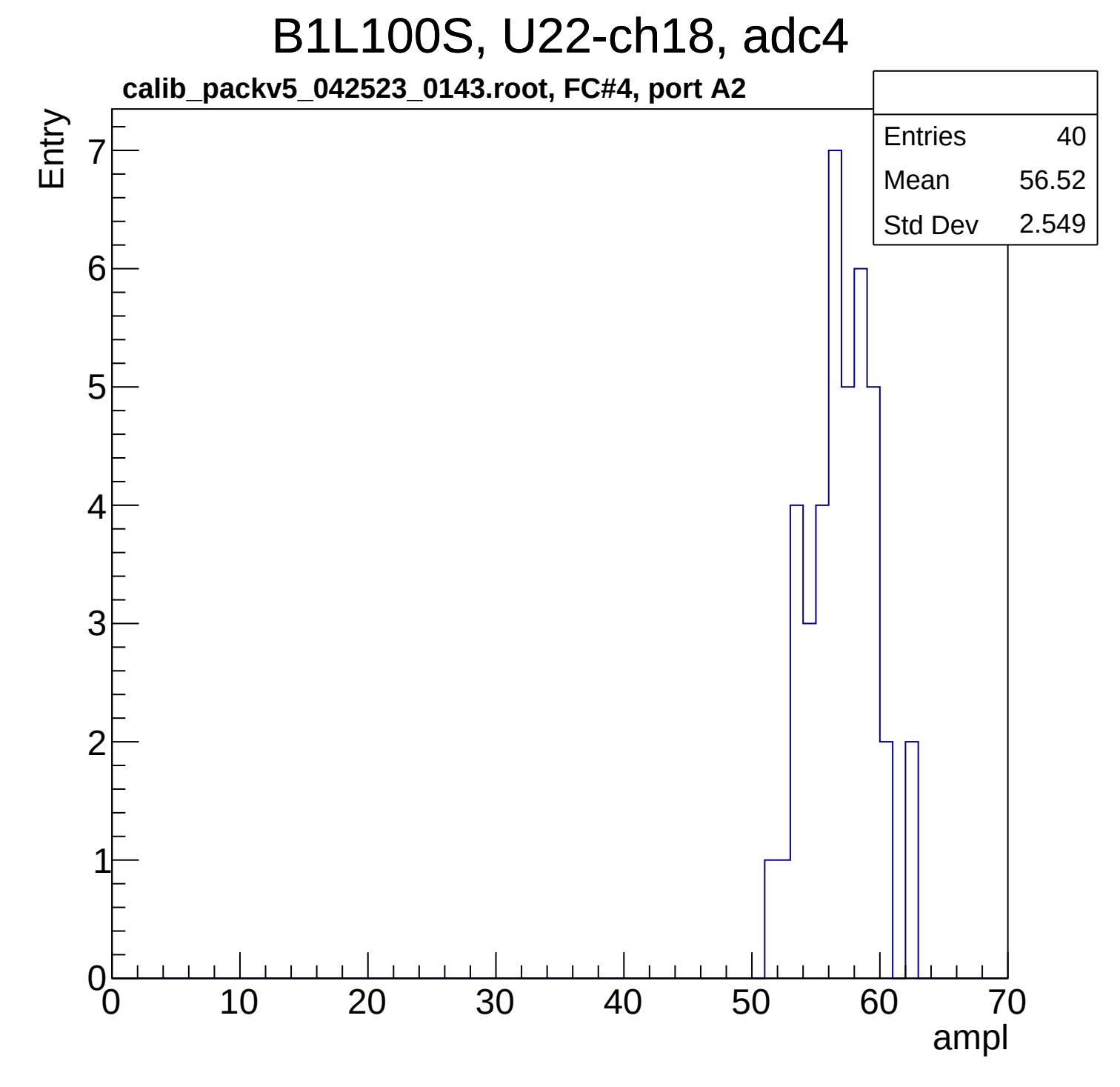
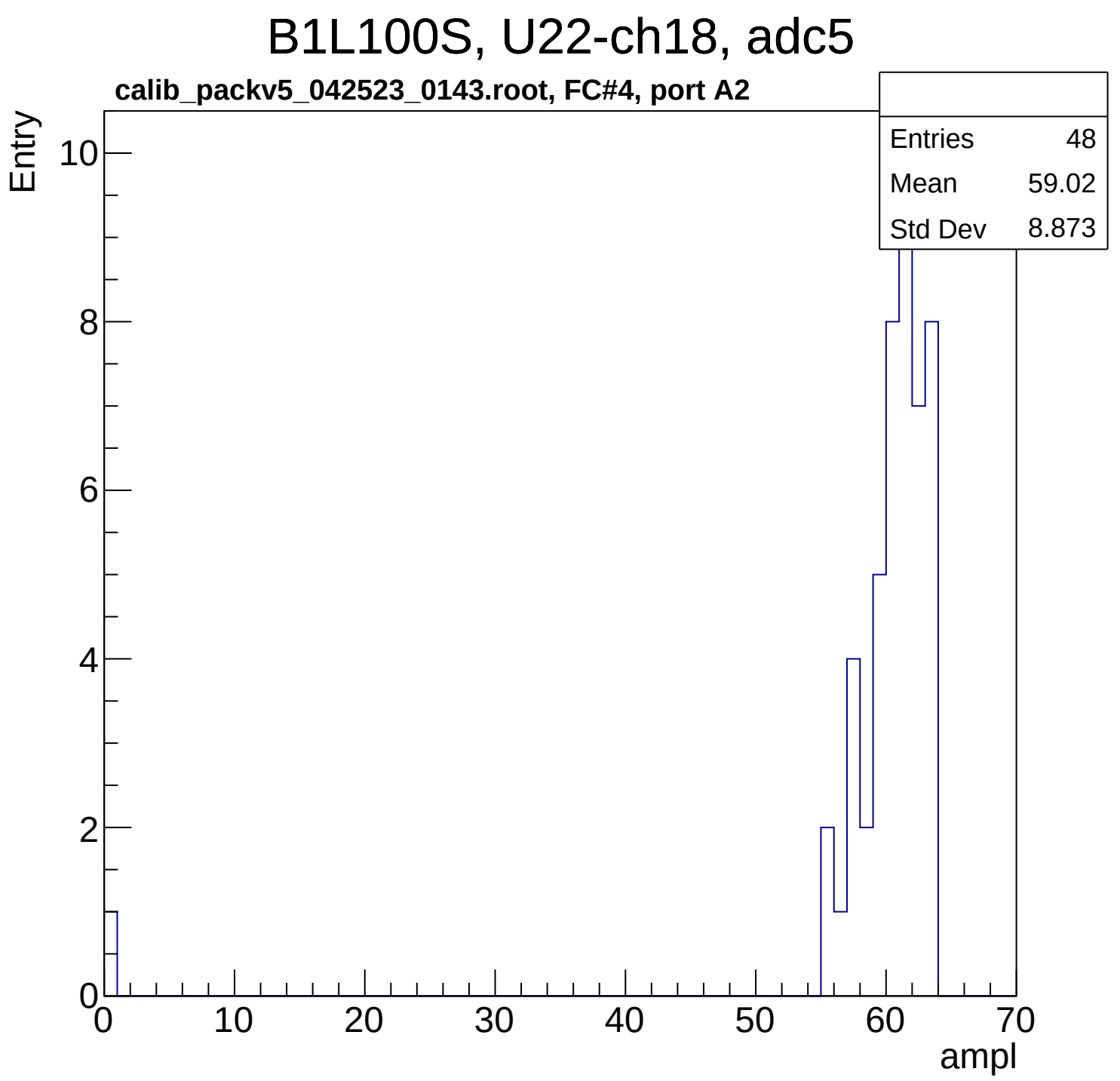
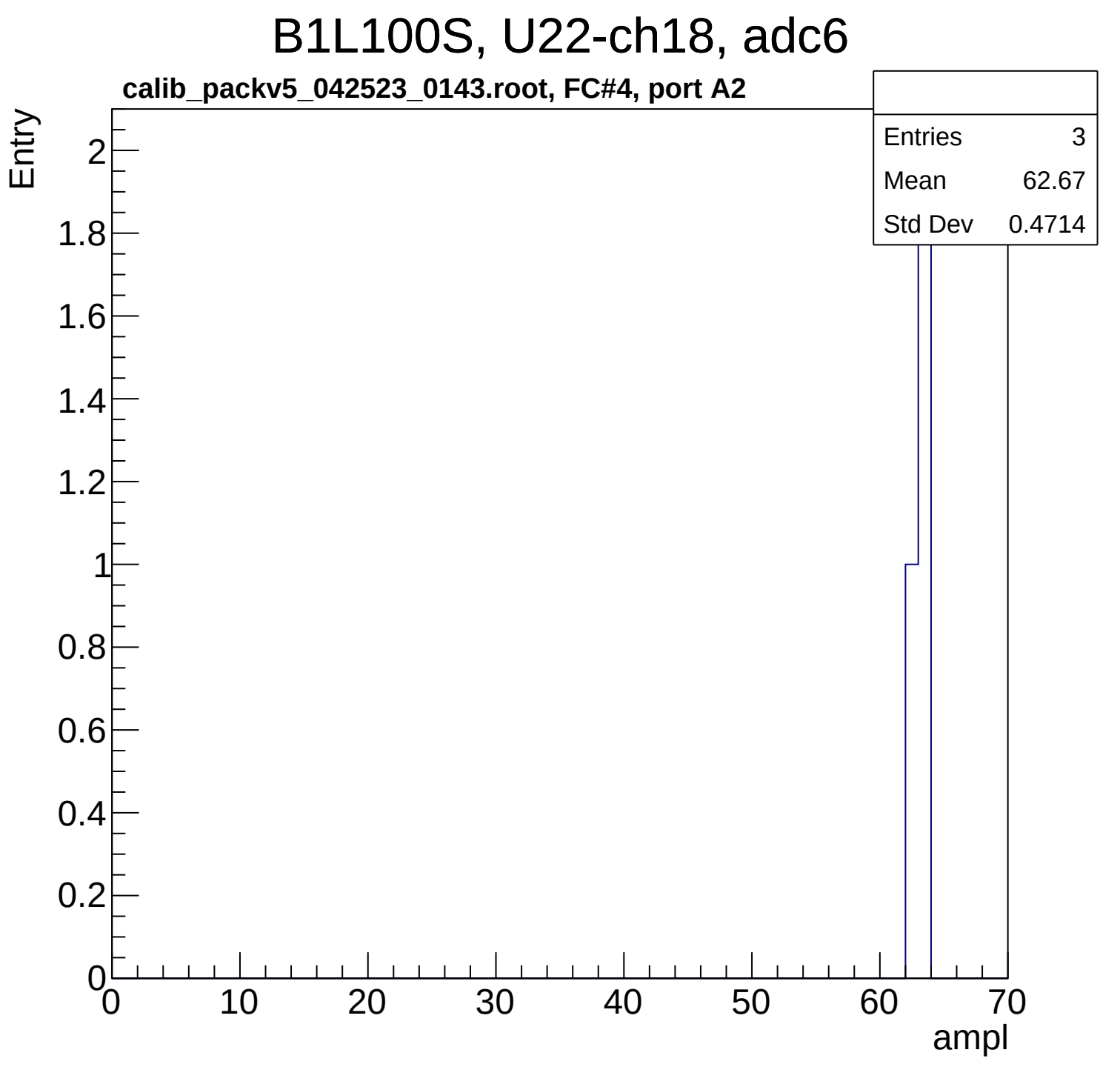
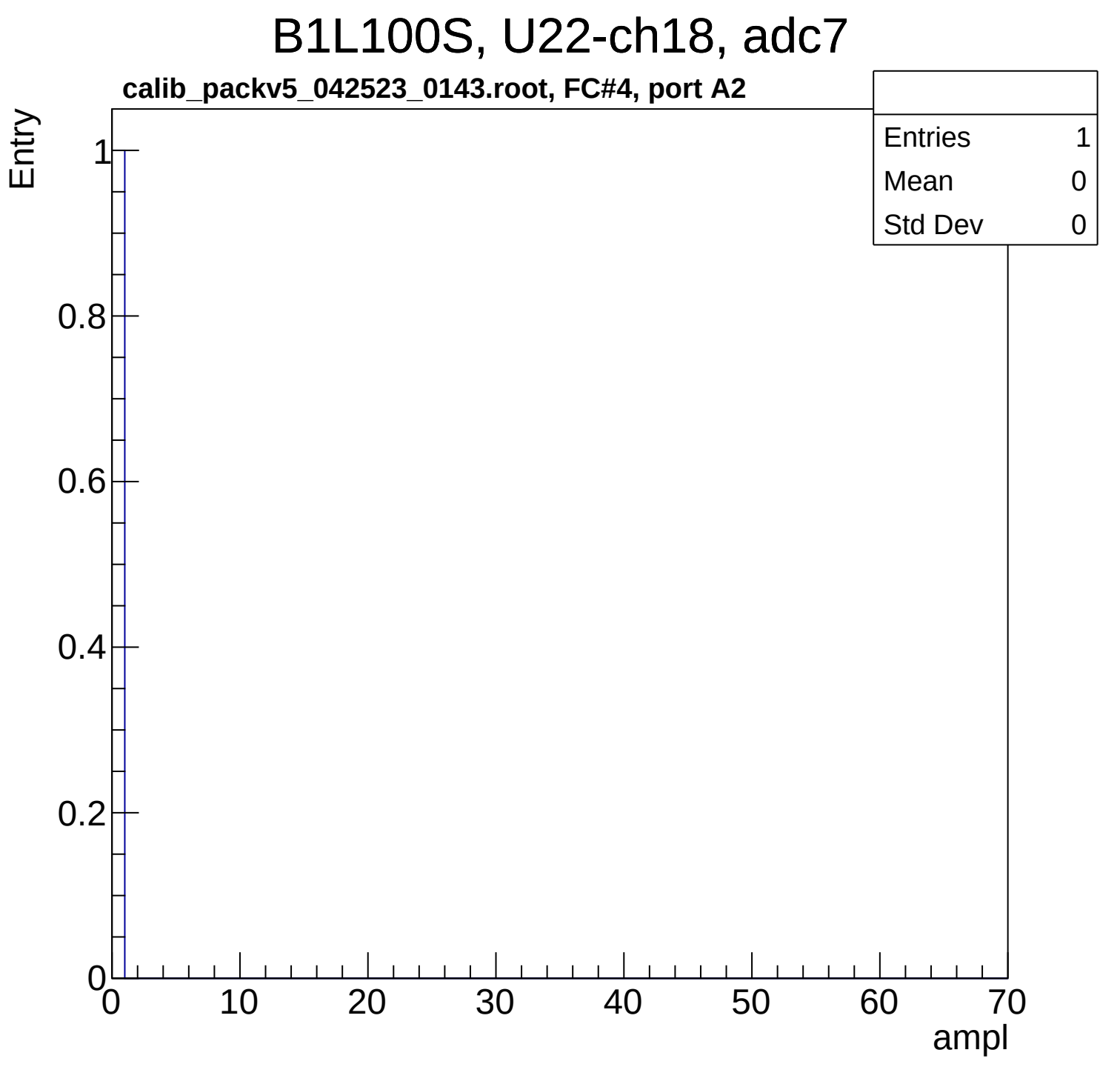
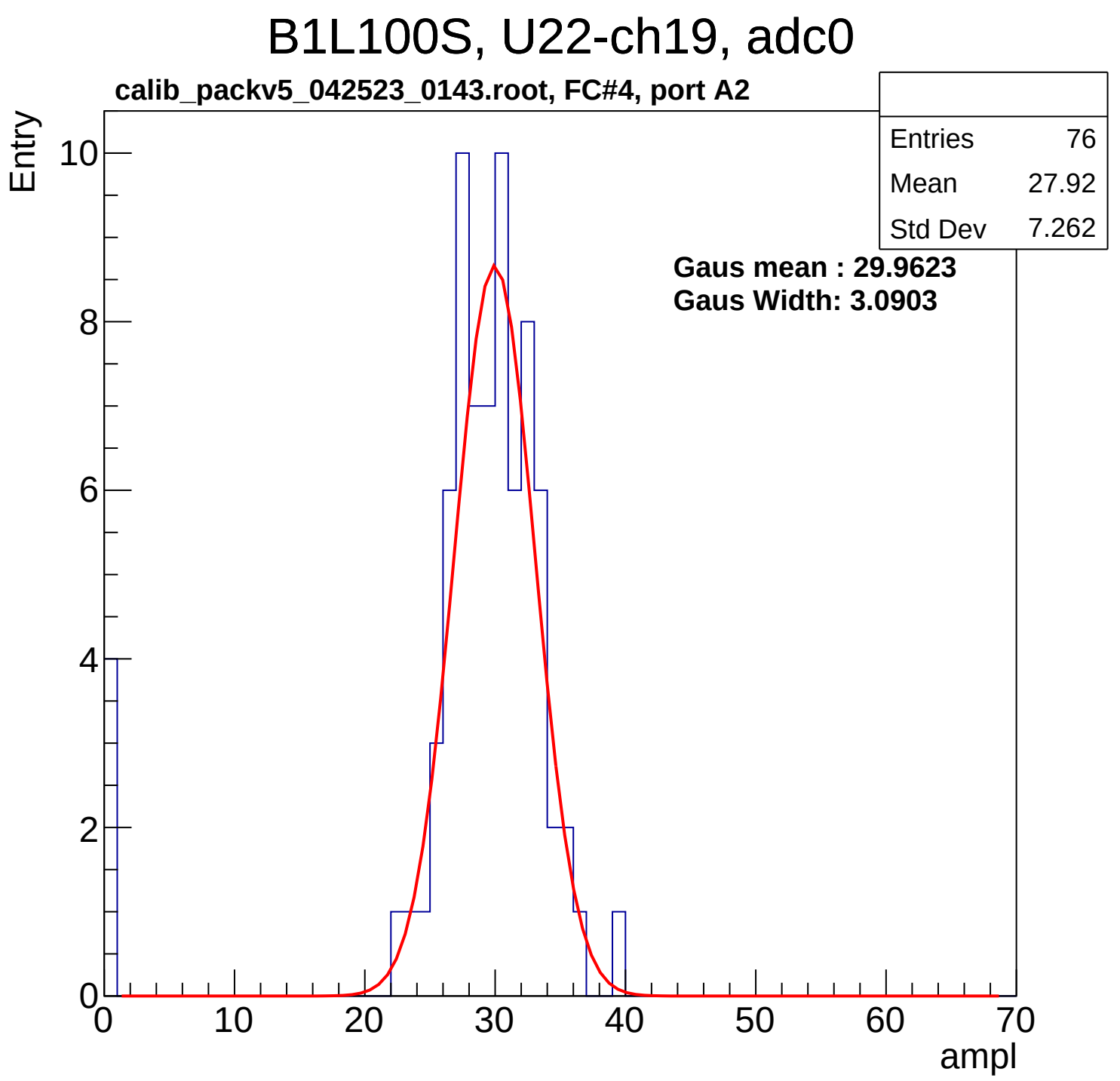
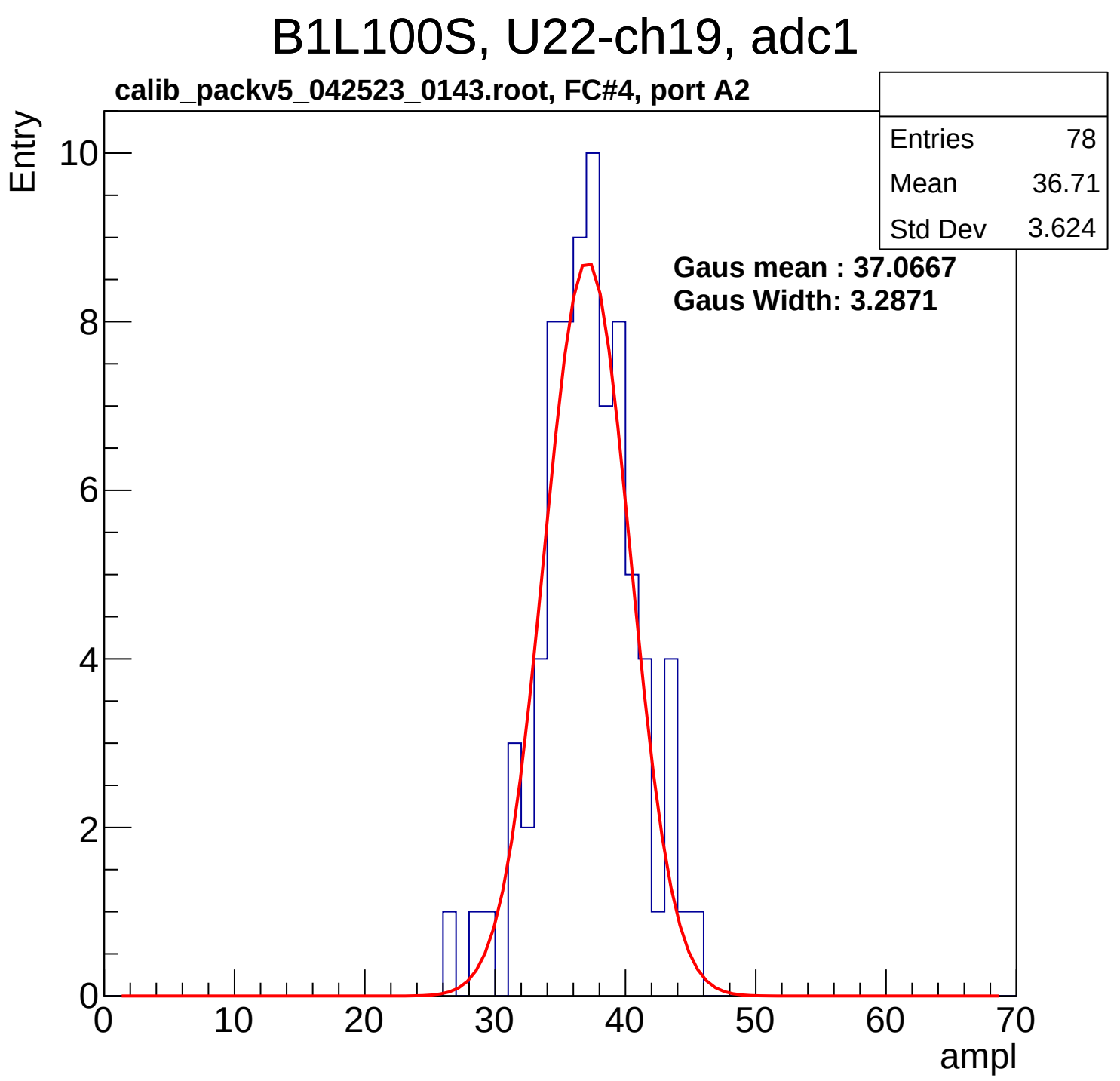

B1L100S, U22-ch18, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 56.52
Std Dev 2.549
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

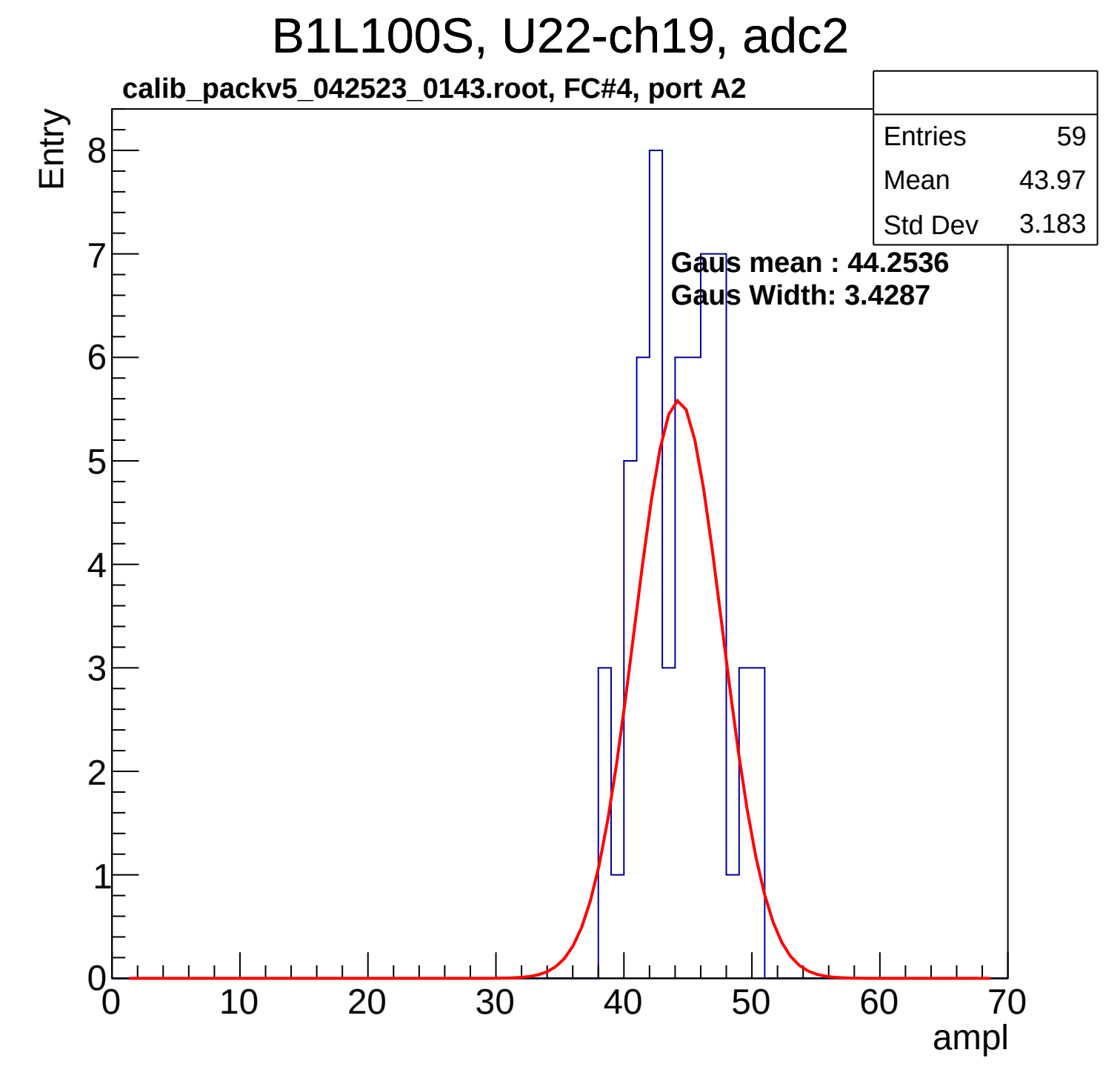

B1L100S, U22-ch18, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.02
Std Dev 8.873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch18, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch18, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch19, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 27.92
Std Dev 7.262
Gaus mean : 29.9623
Gaus Width: 3.0903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch19, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.71
Std Dev 3.624
Gaus mean : 37.0667
Gaus Width: 3.2871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch19, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.97
Std Dev 3.183
Gaus mean : 44.2536
Gaus Width: 3.4287
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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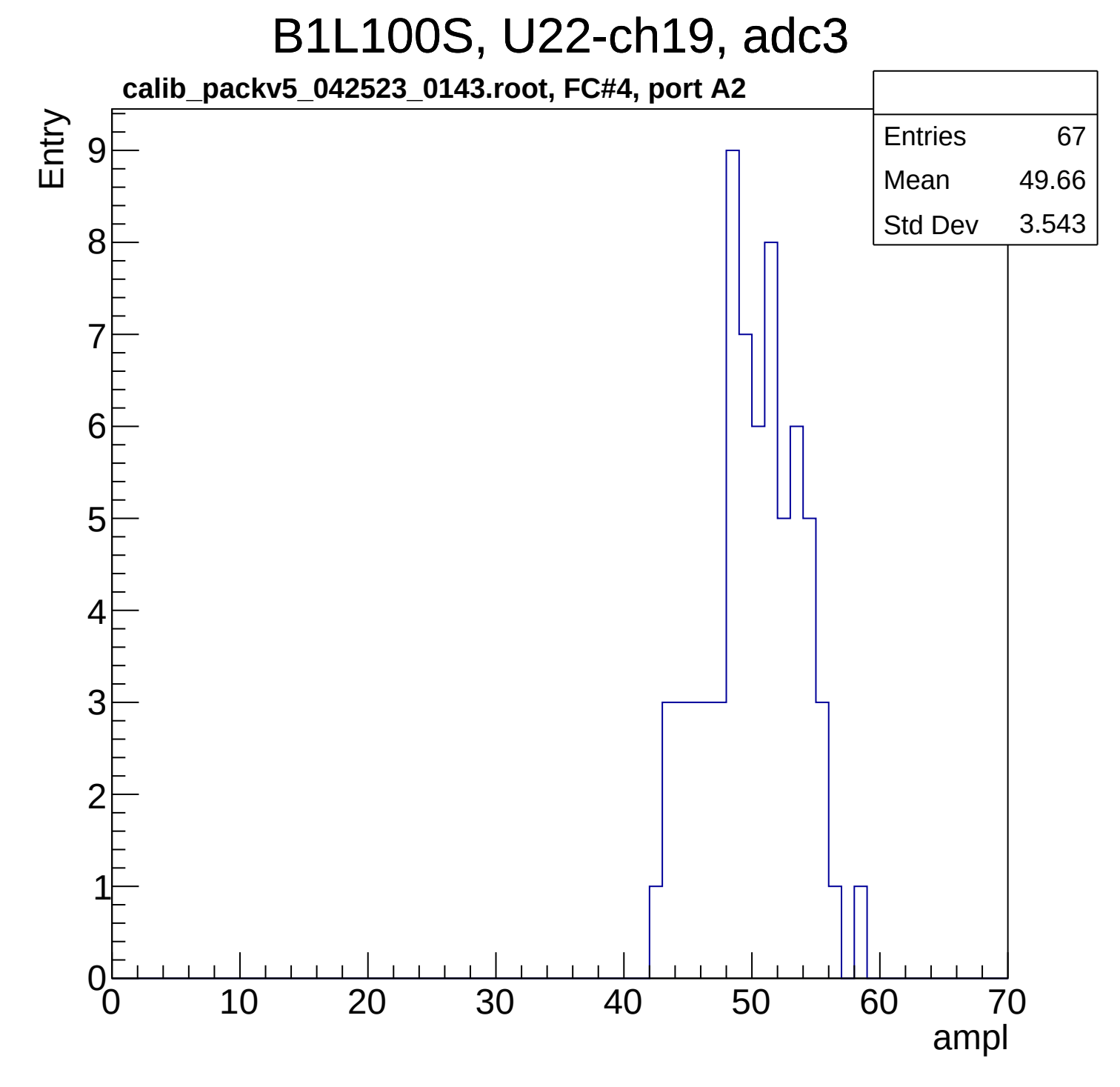
B1L100S, U22-ch19, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.66
Std Dev 3.543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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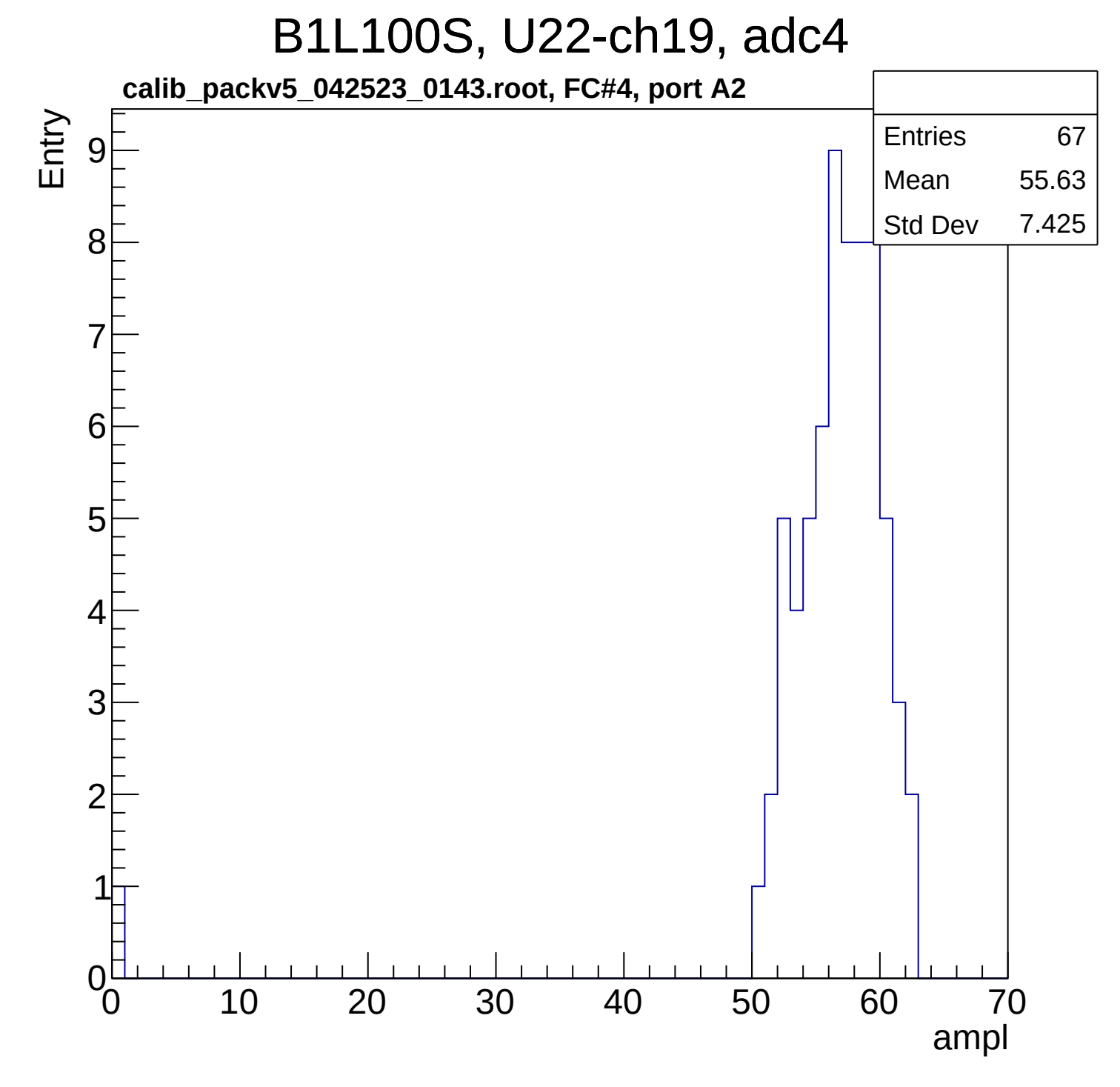
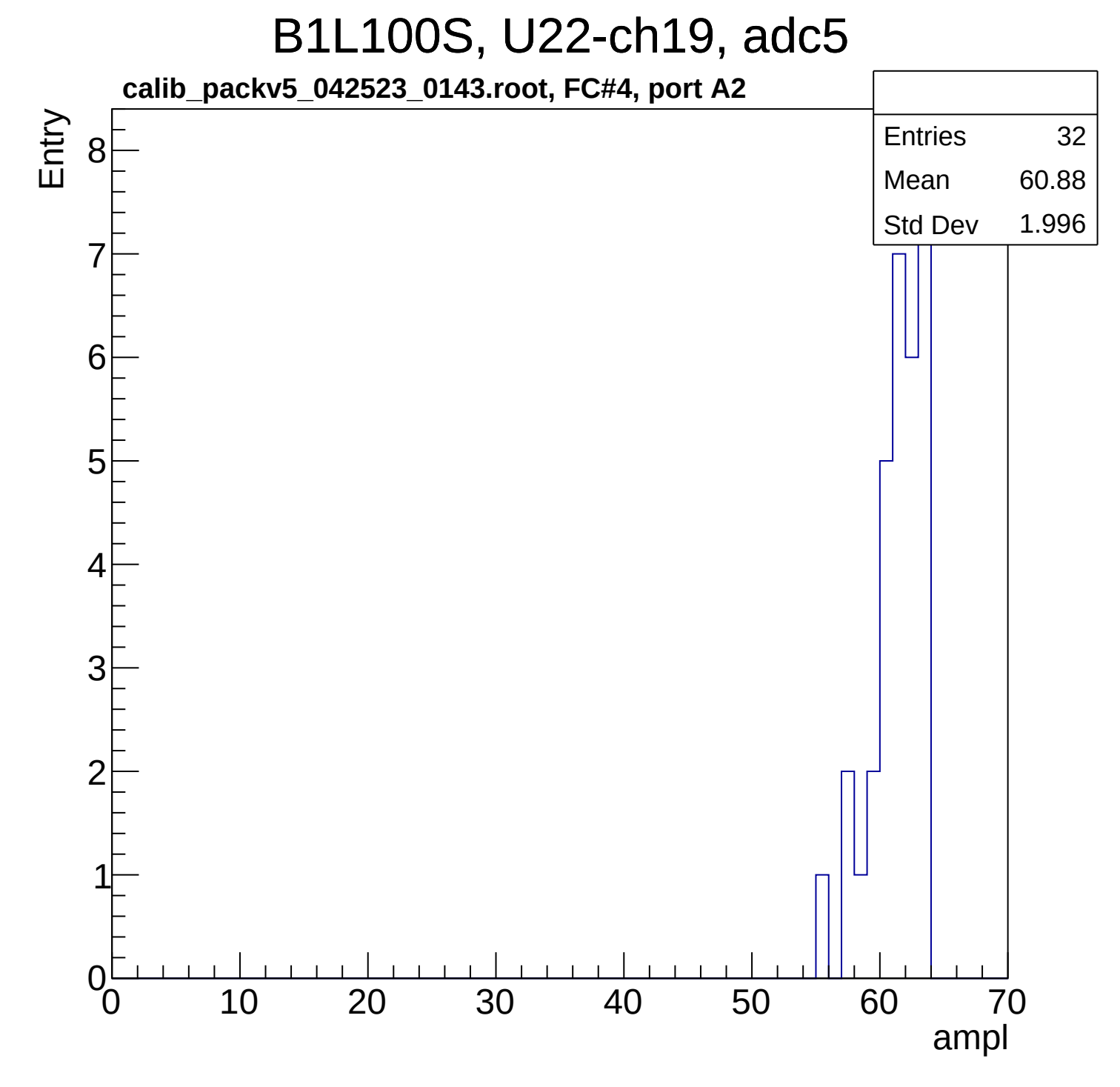

B1L100S, U22-ch19, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.63
Std Dev 7.425
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch19, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 60.88
Std Dev 1.996
Entry
ampl
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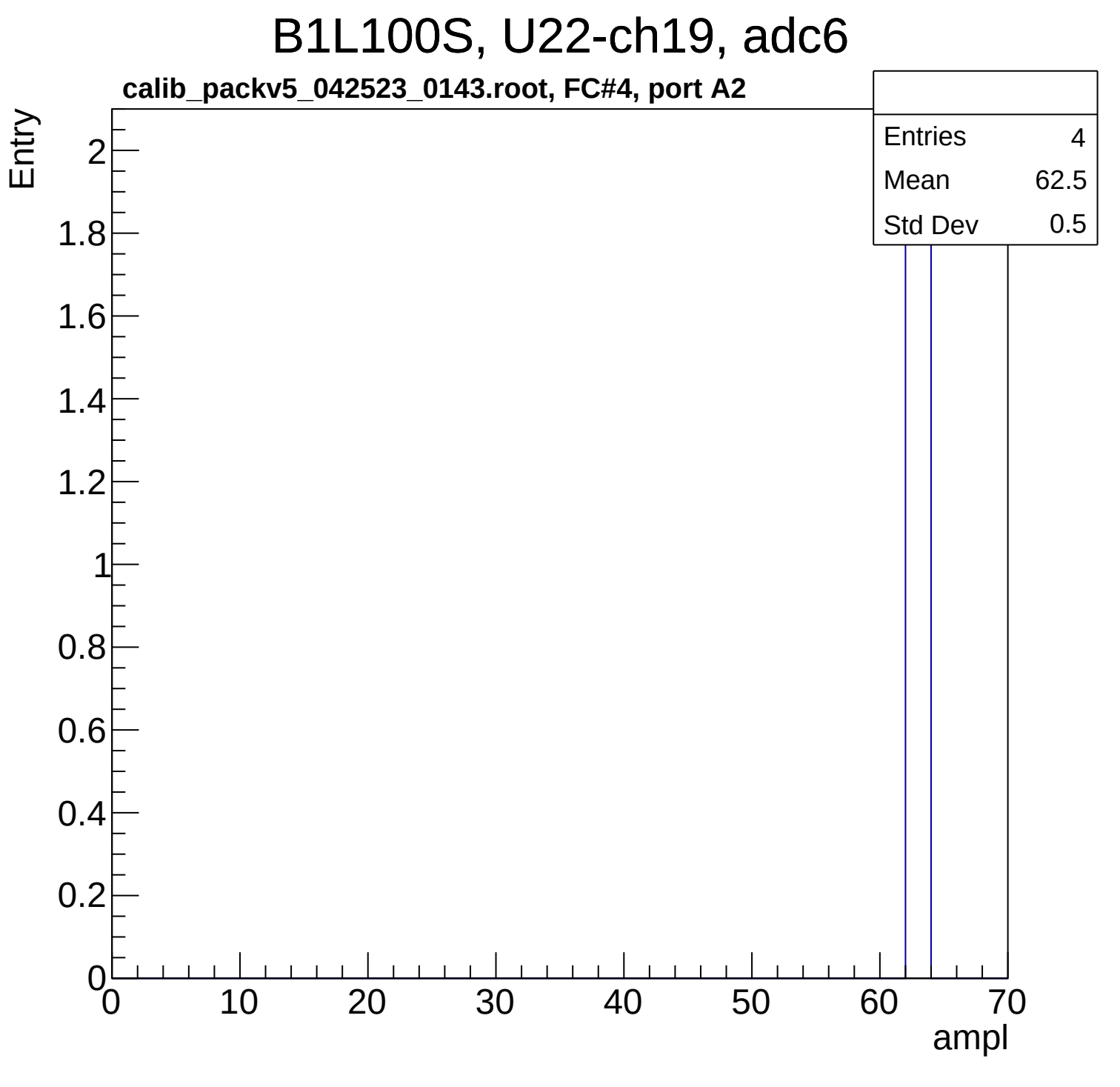
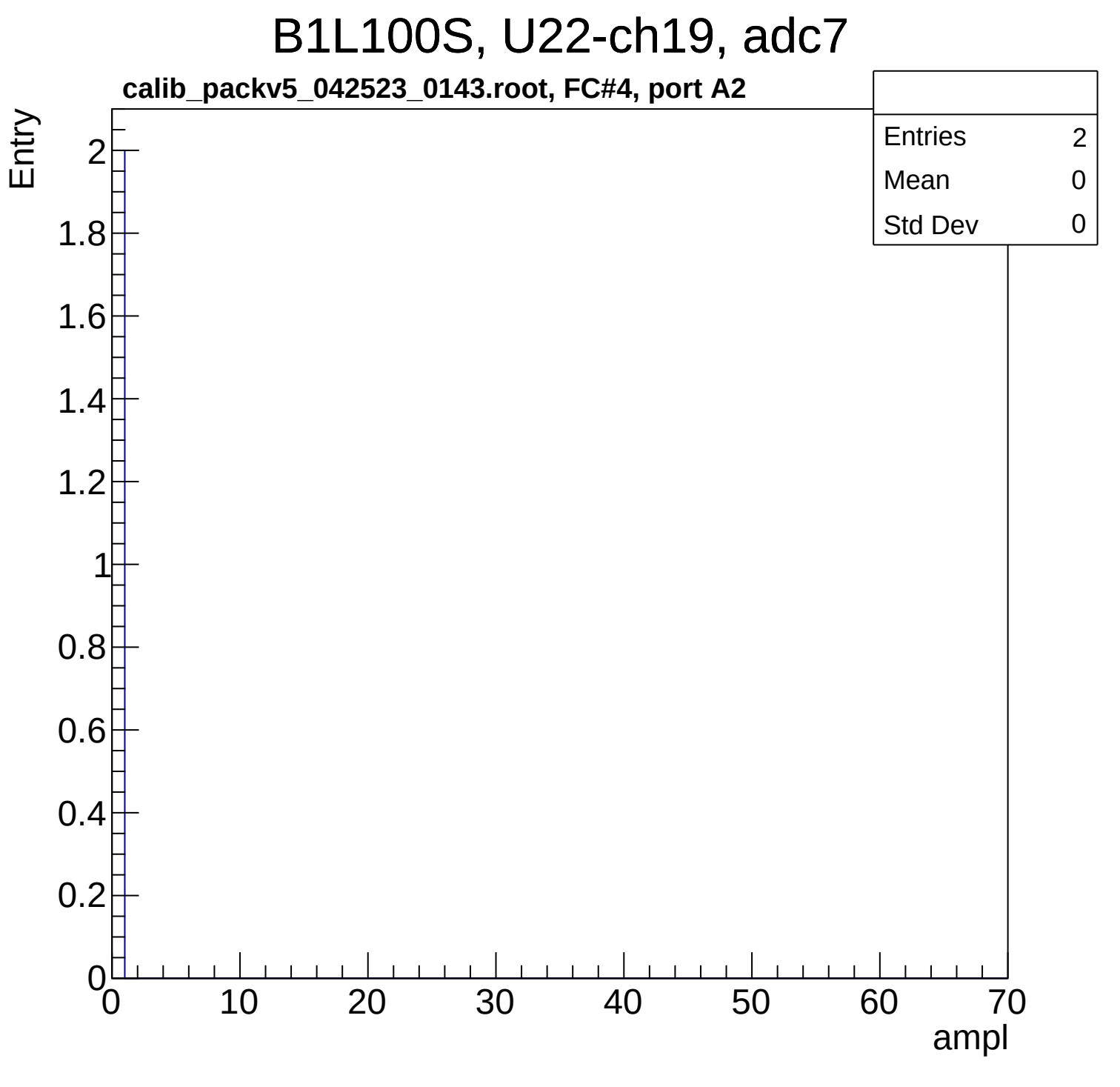
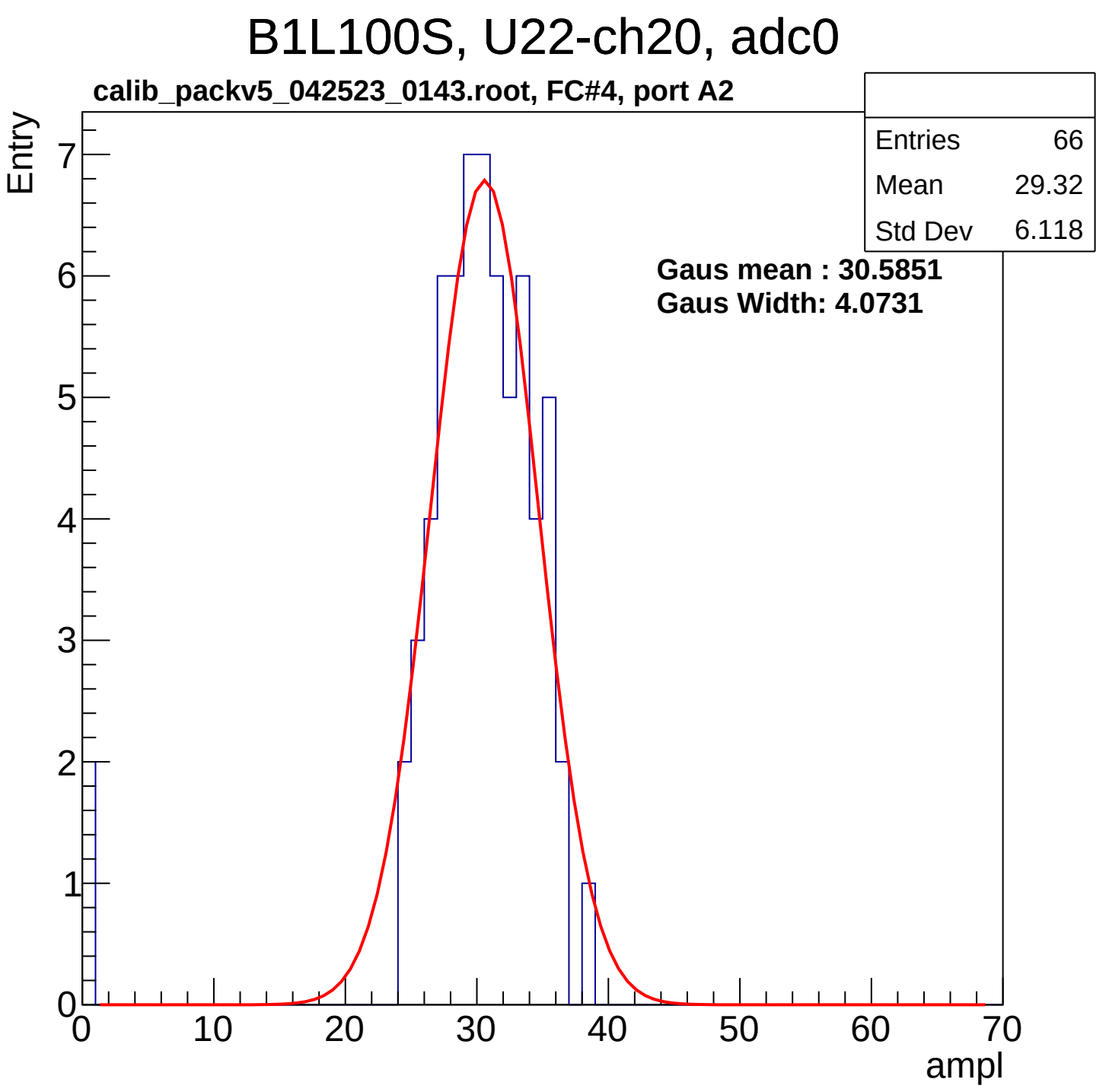
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch19, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch19, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch20, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.32
Std Dev 6.118
Gaus mean : 30.5851
Gaus Width: 4.0731
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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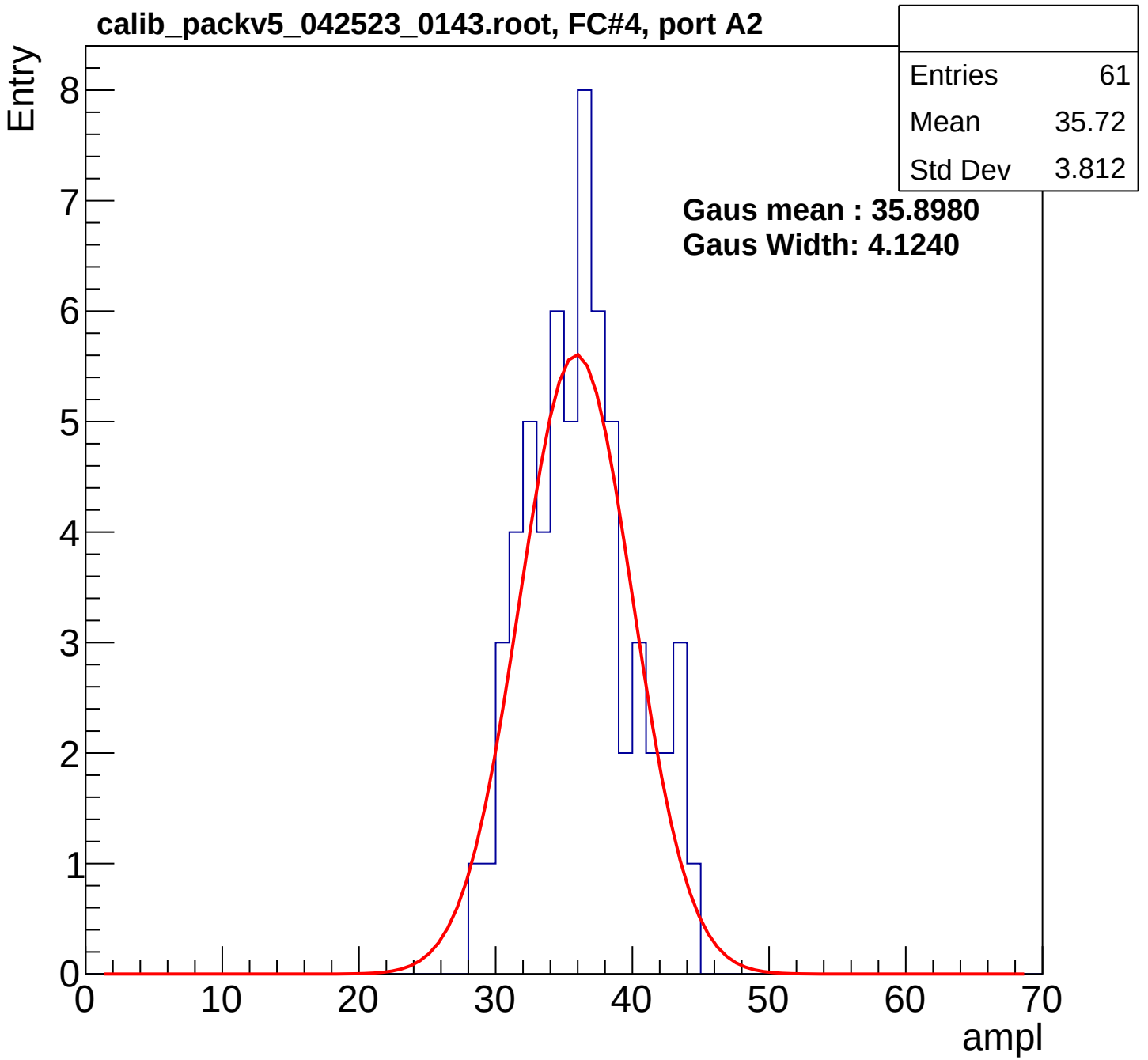
B1L100S, U22-ch20, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 35.72
Std Dev 3.812
Gaus mean : 35.8980
Gaus Width: 4.1240
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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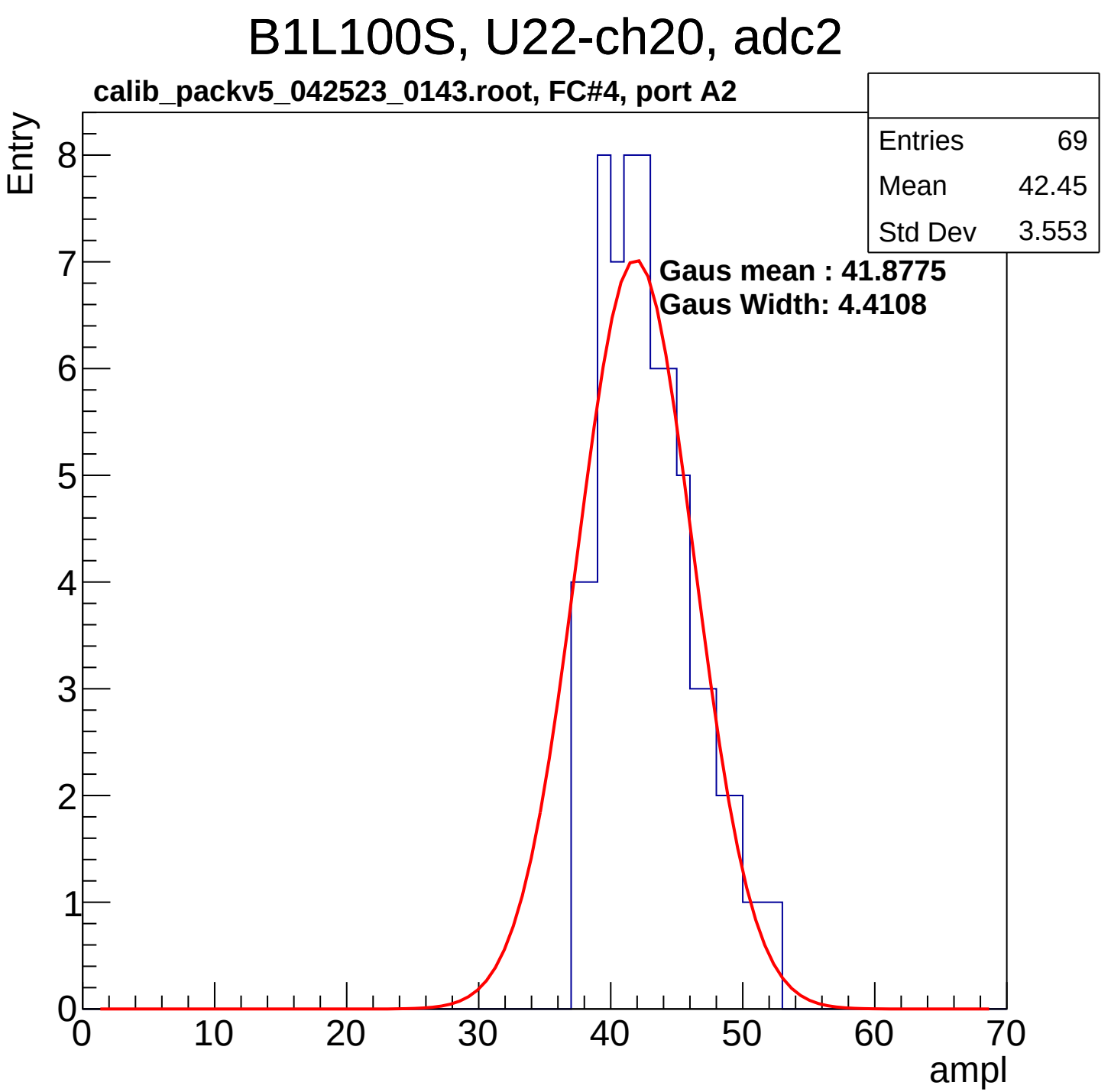
B1L100S, U22-ch20, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.45
Std Dev 3.553
Gaus mean : 41.8775
Gaus Width: 4.4108
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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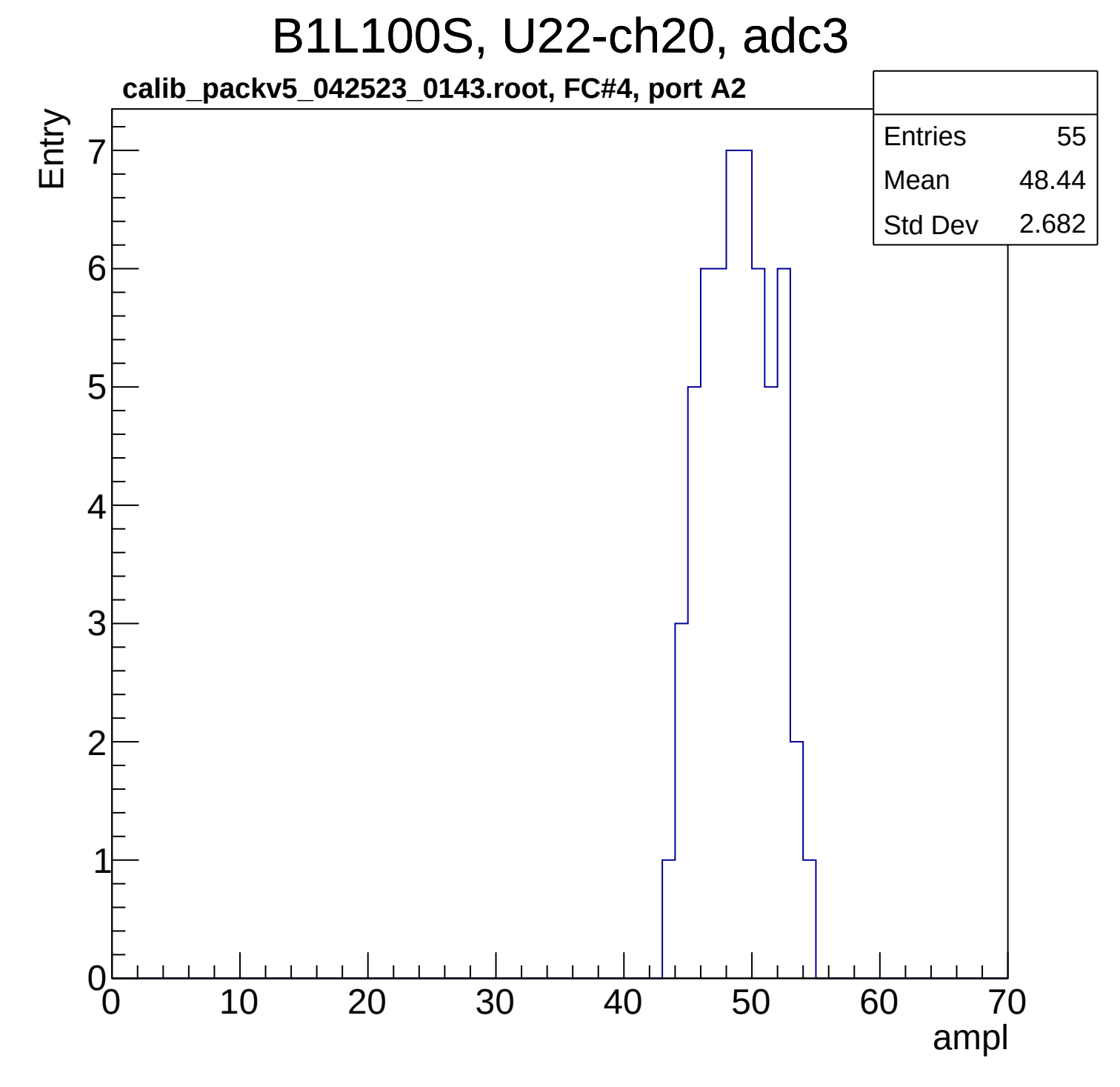
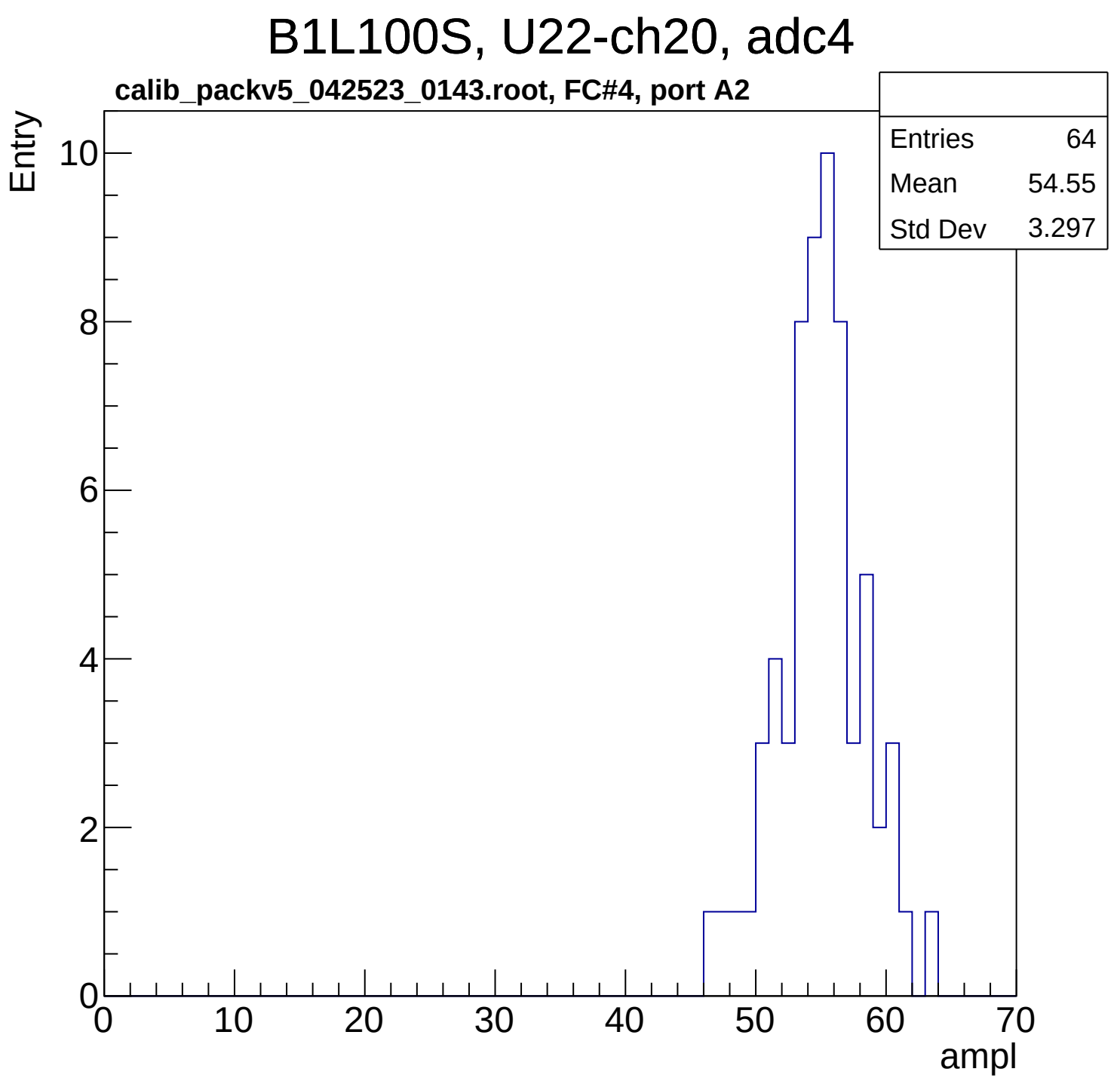
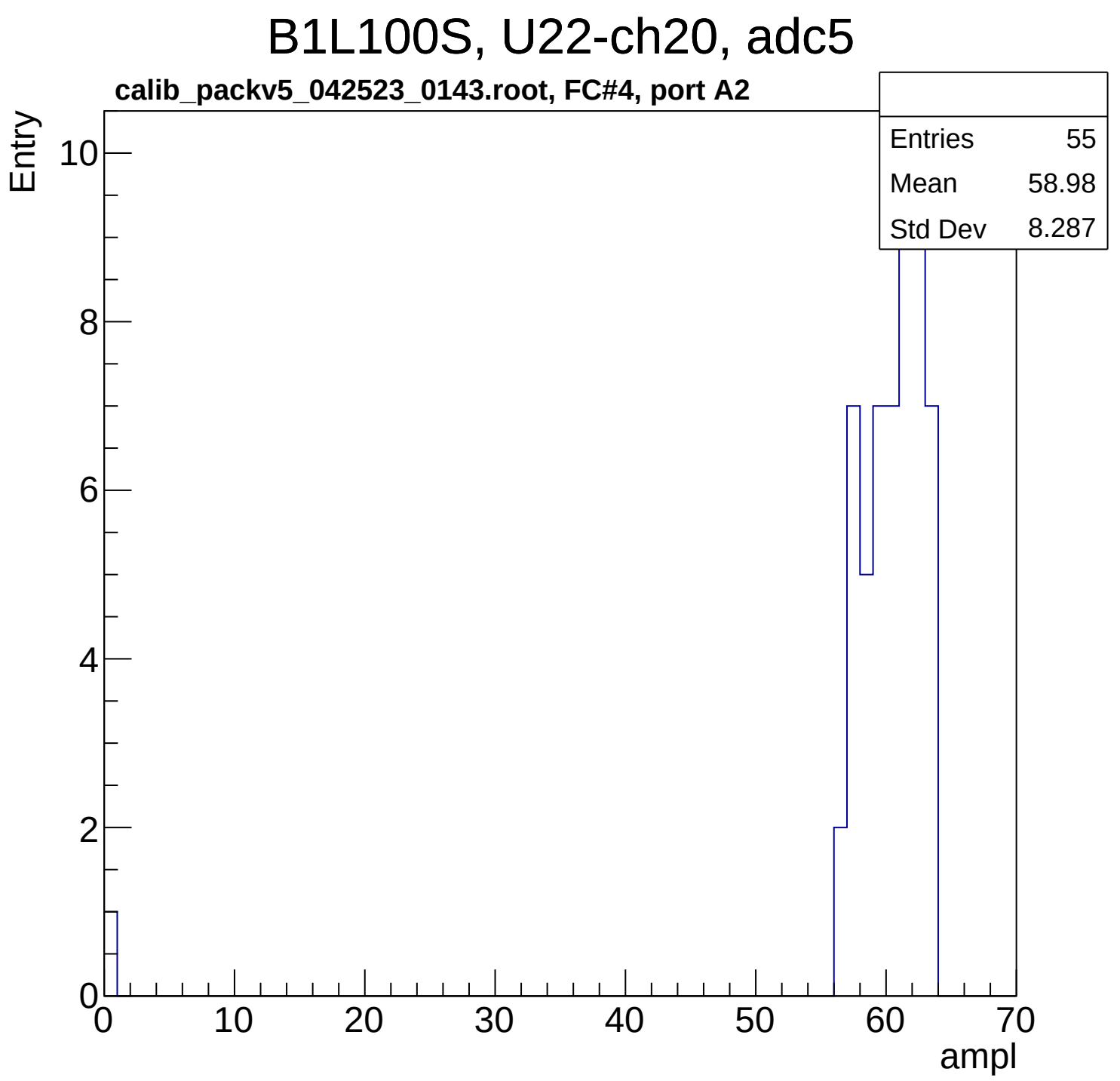

B1L100S, U22-ch20, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 48.44
Std Dev 2.682
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch20, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 54.55
Std Dev 3.297
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch20, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 58.98
Std Dev 8.287
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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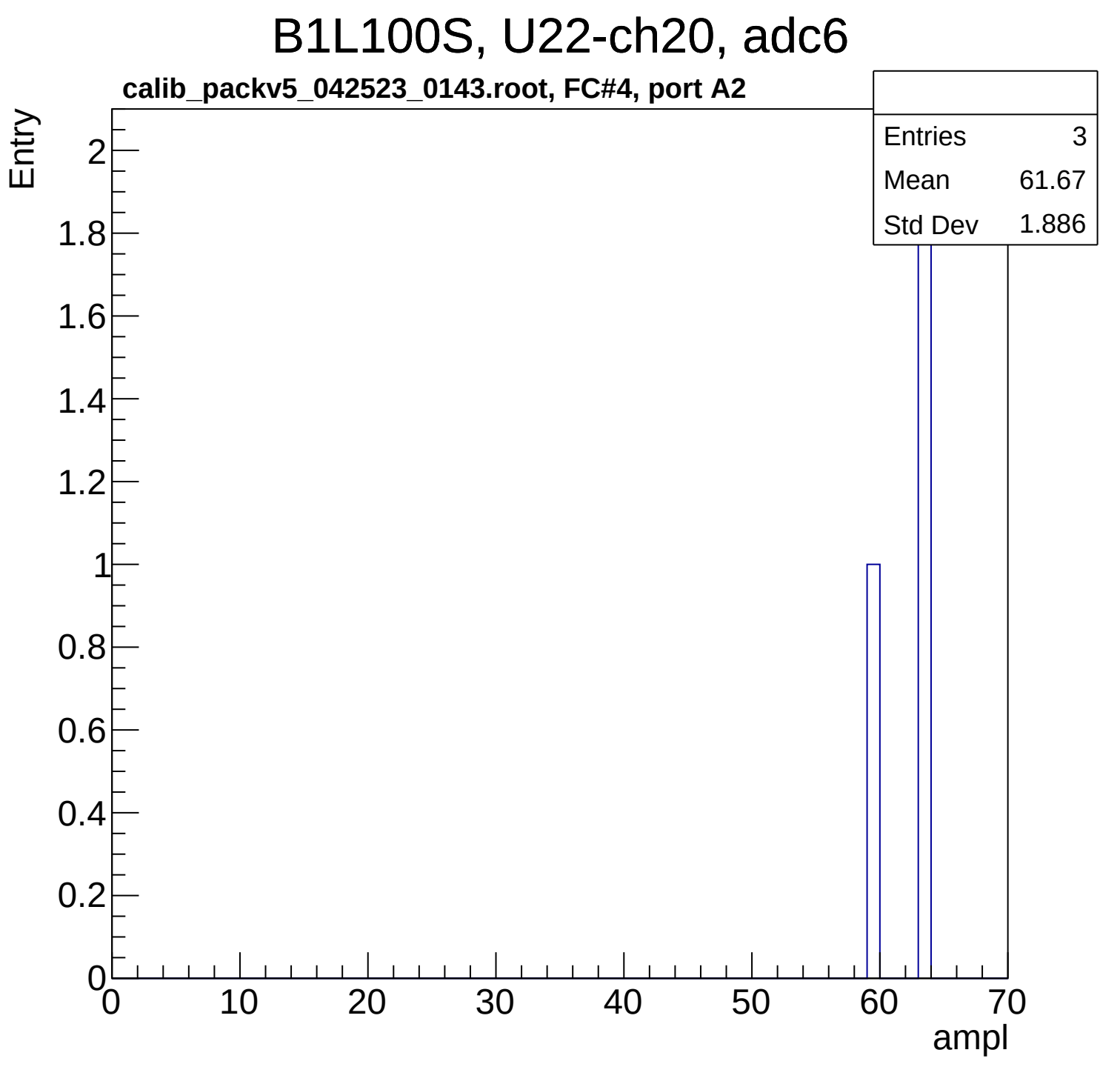
B1L100S, U22-ch20, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 1.886
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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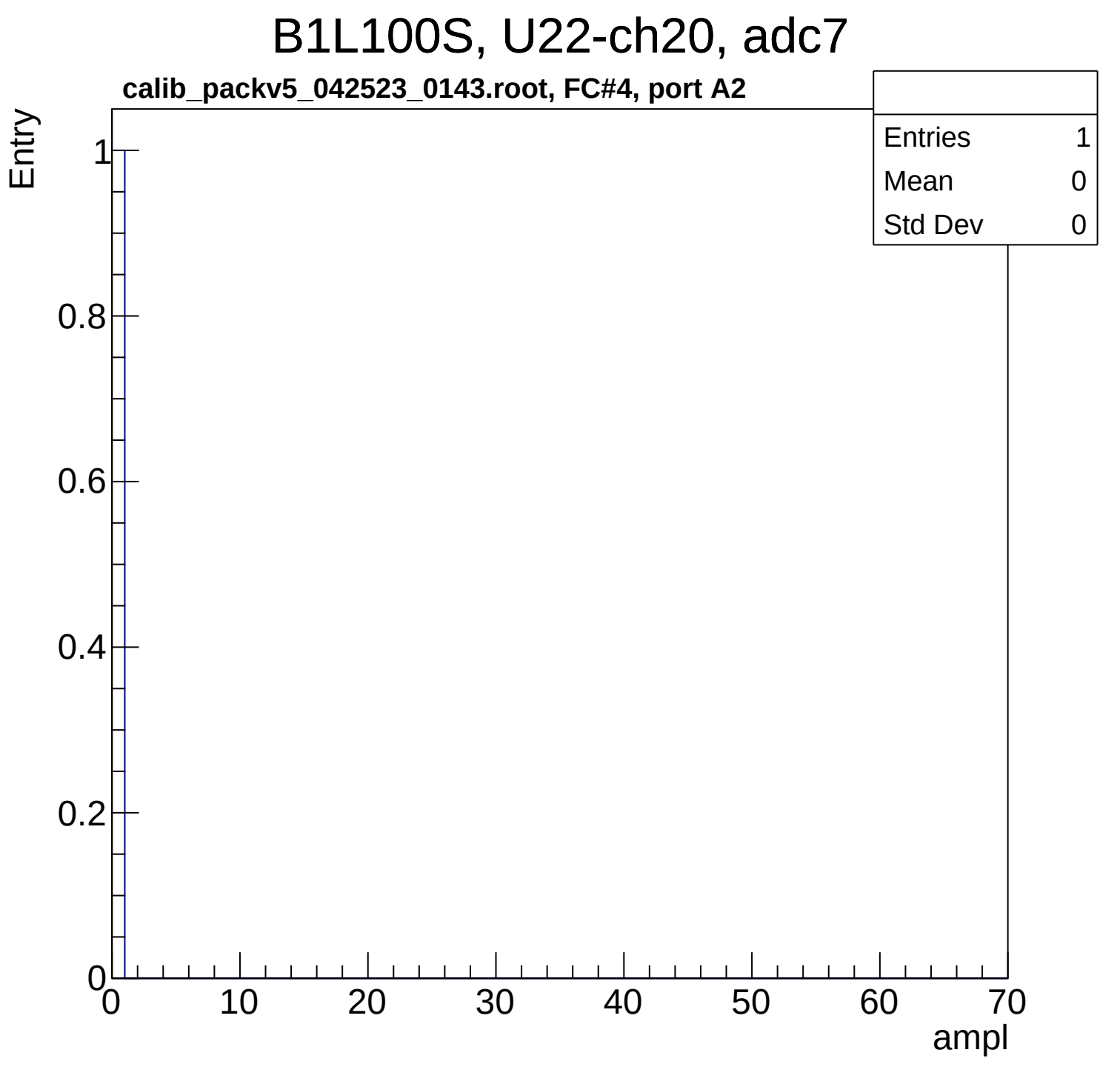
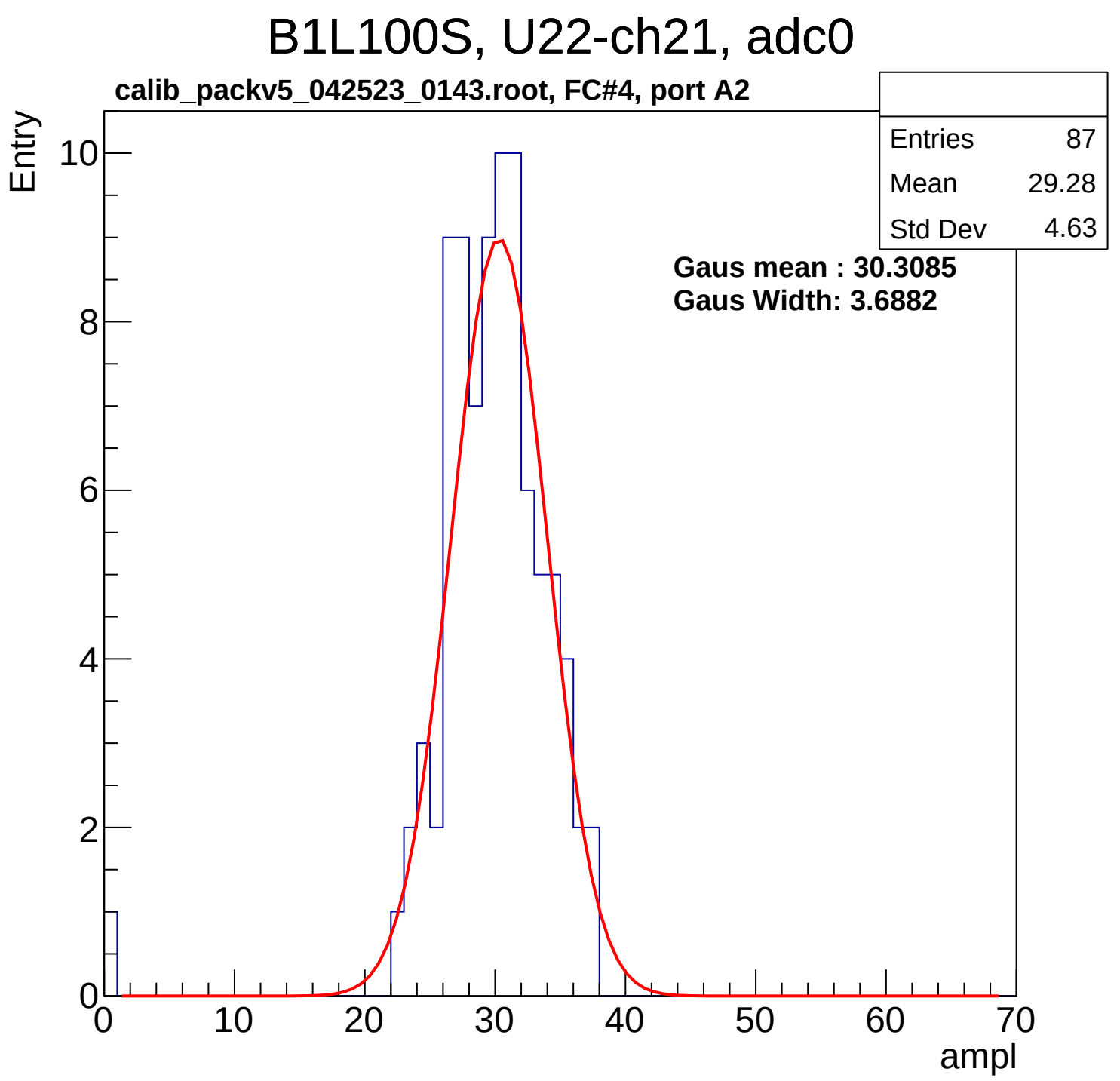

B1L100S, U22-ch20, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch21, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 87
Mean 29.28
Std Dev 4.63
Gaus mean : 30.3085
Gaus Width: 3.6882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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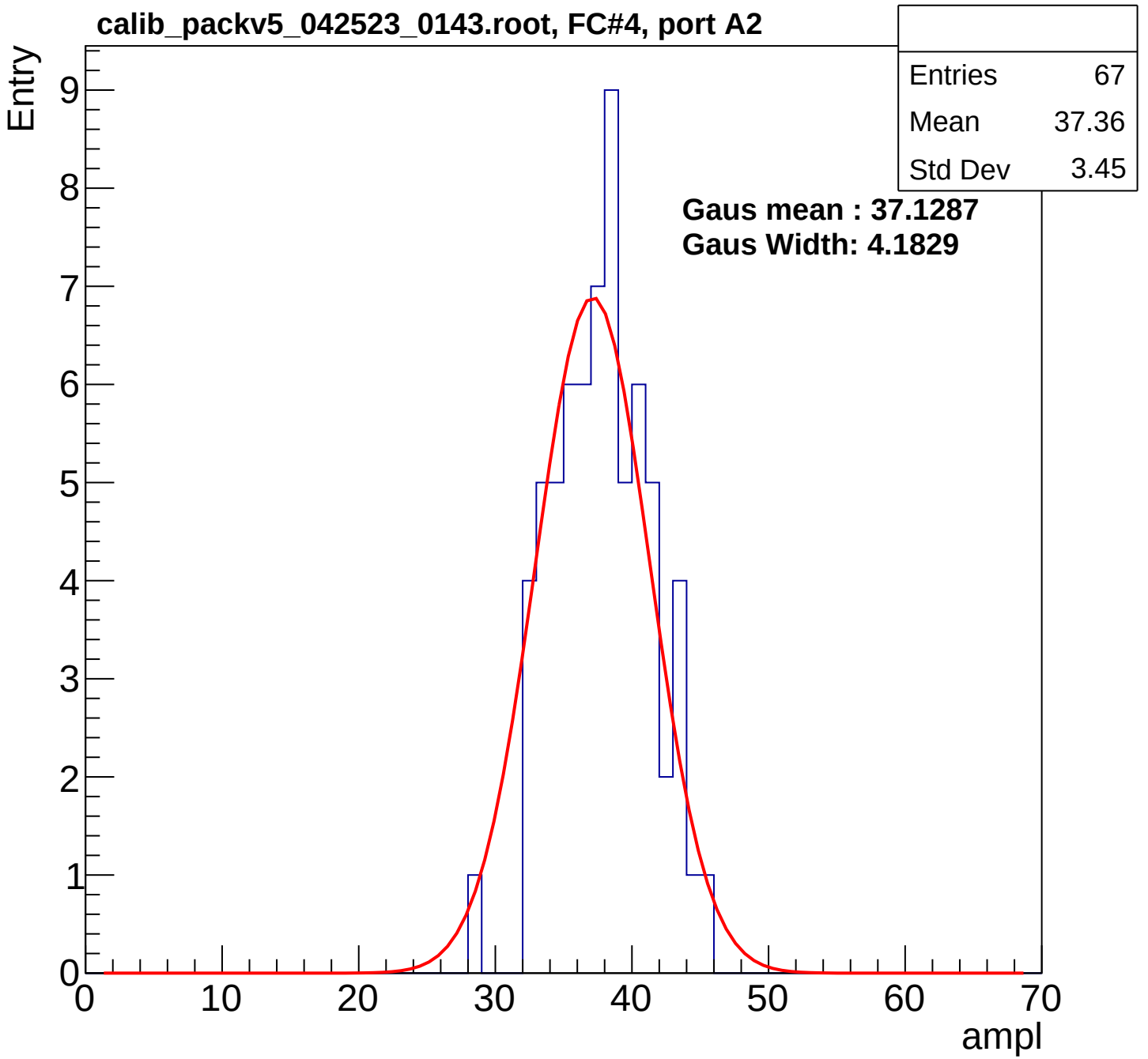
B1L100S, U22-ch21, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.36
Std Dev 3.45
Gaus mean : 37.1287
Gaus Width: 4.1829
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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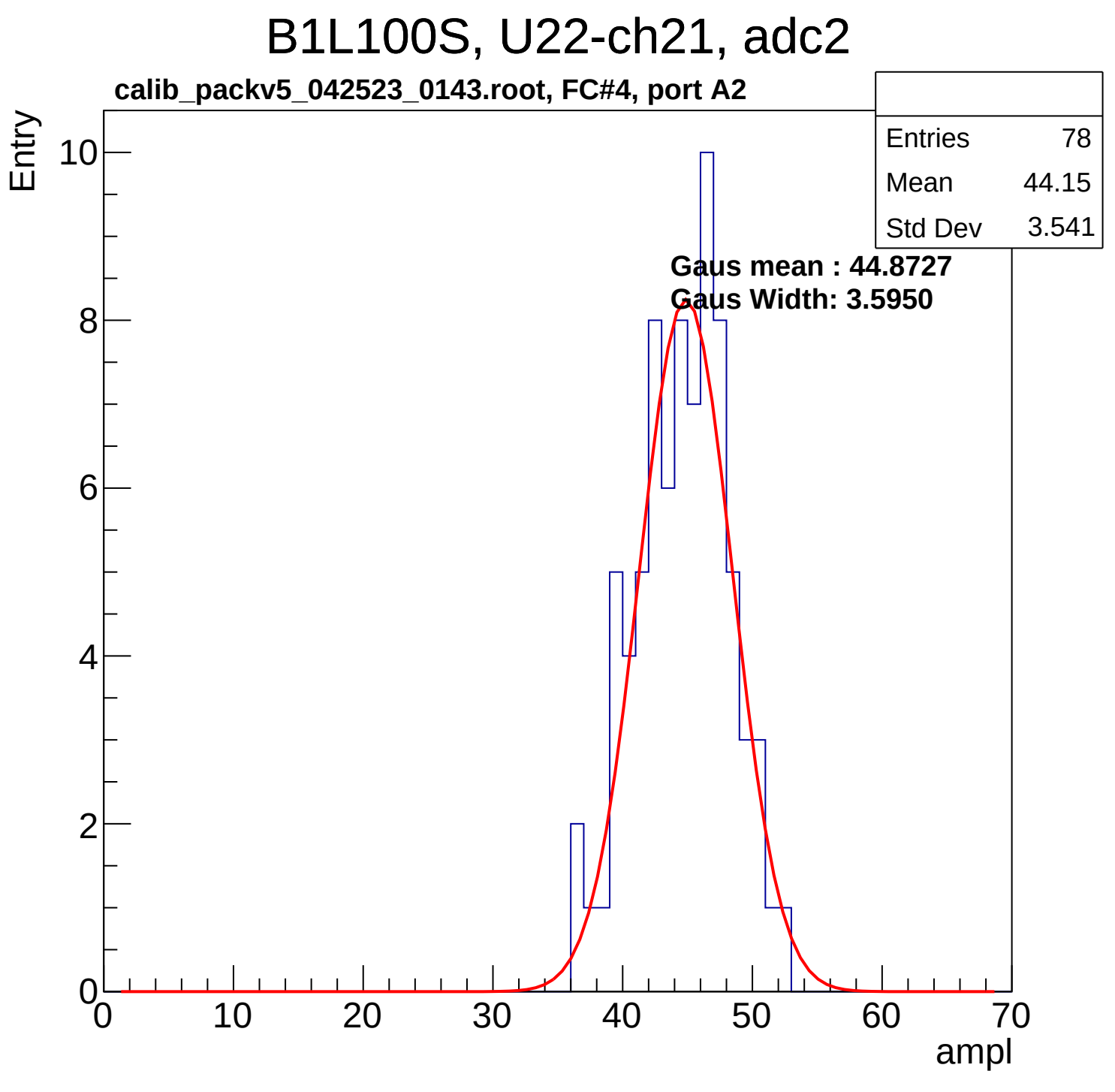
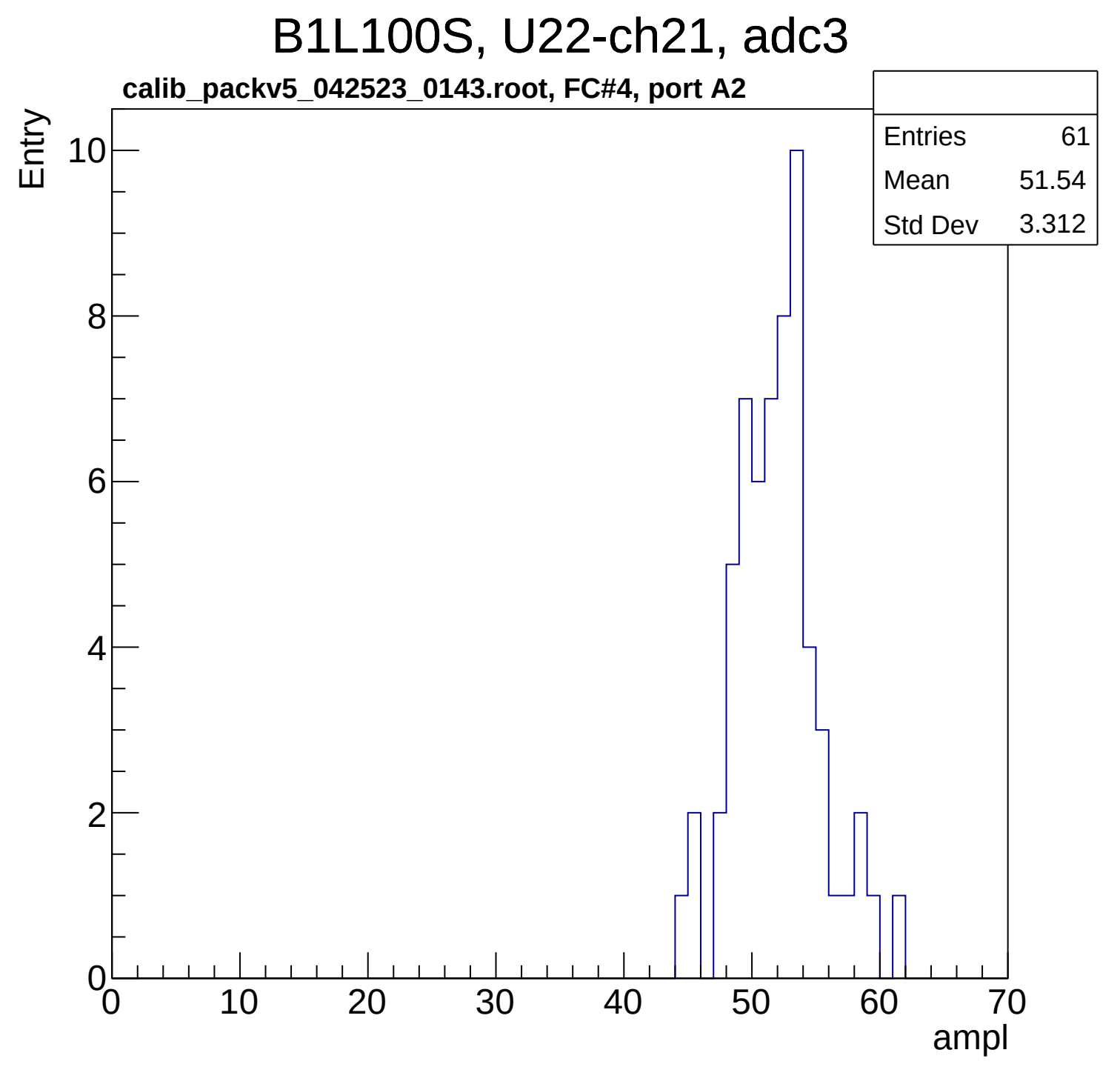
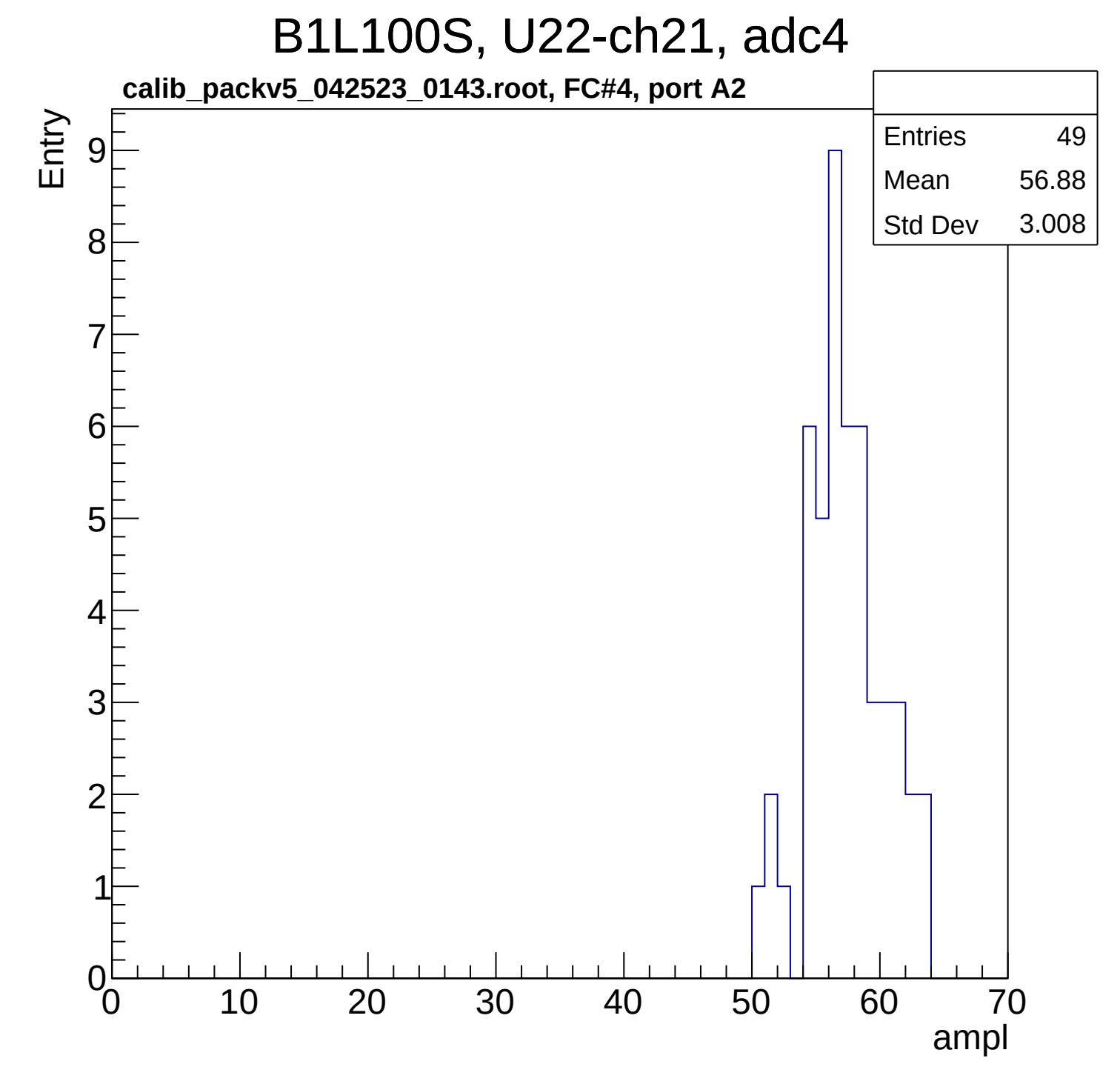
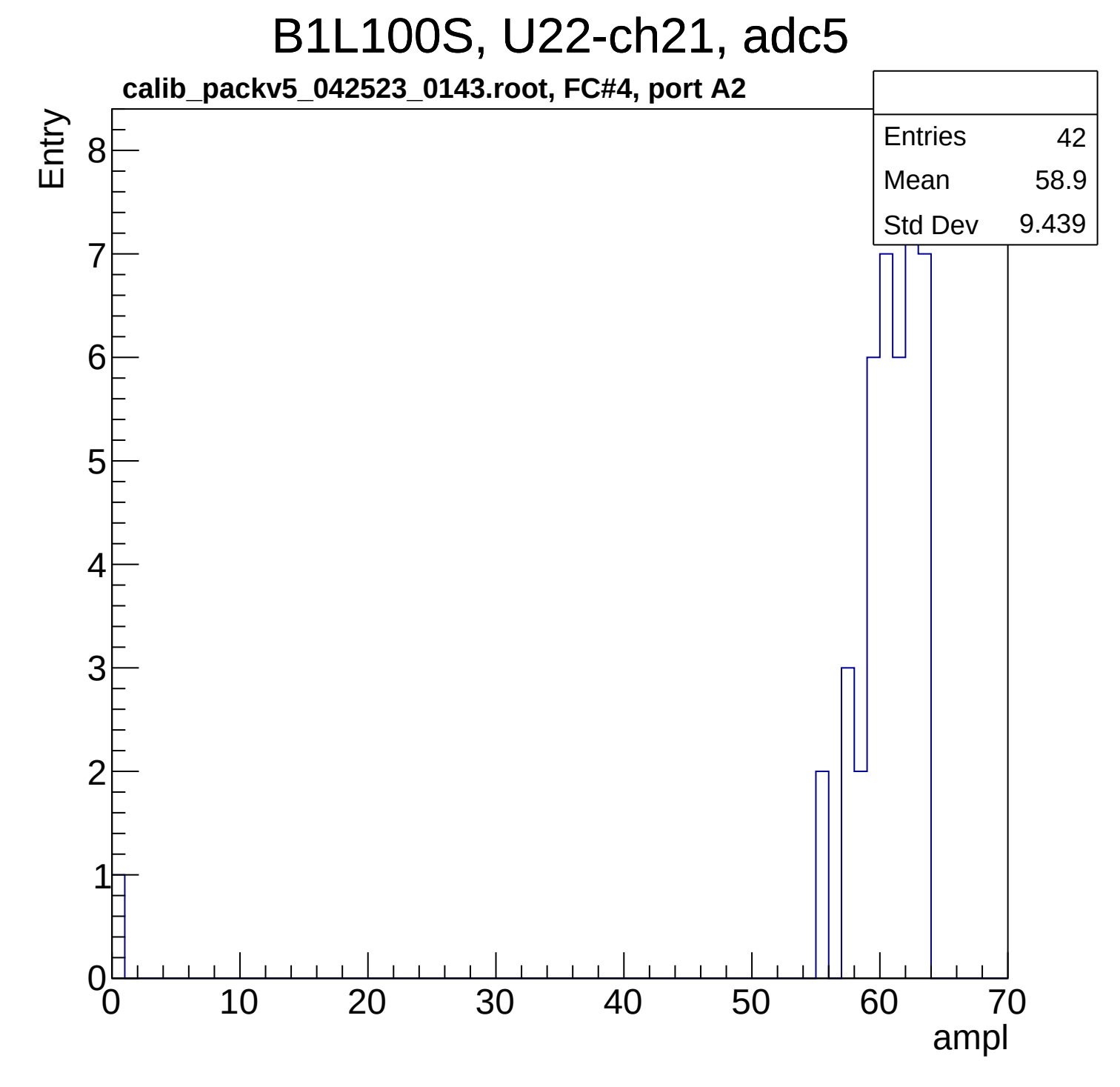

B1L100S, U22-ch21, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 44.15
Std Dev 3.541
Gaus mean : 44.8727
Gaus Width: 3.5950
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch21, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 51.54
Std Dev 3.312
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch21, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.88
Std Dev 3.008
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch21, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.9
Std Dev 9.439
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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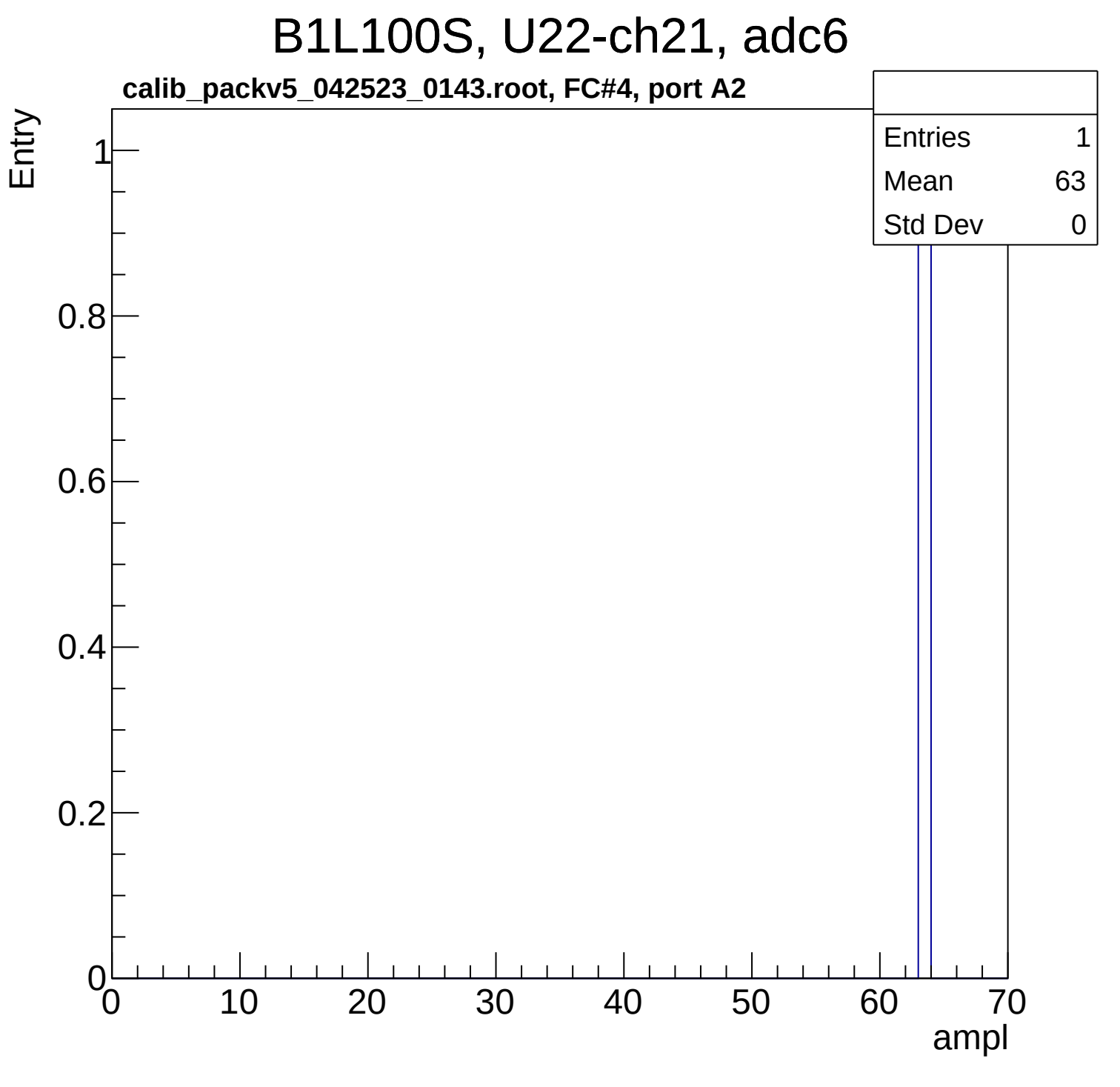
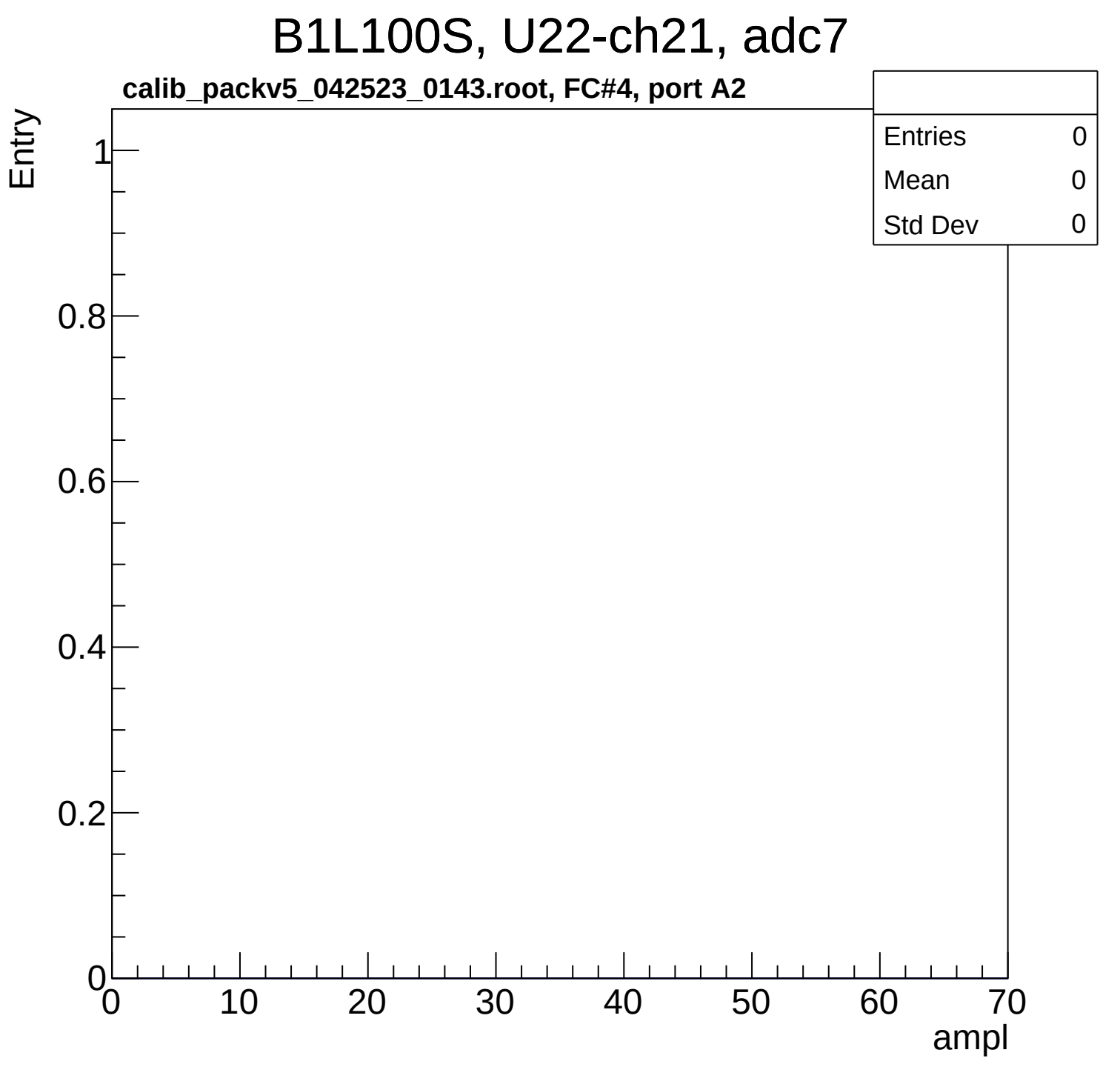
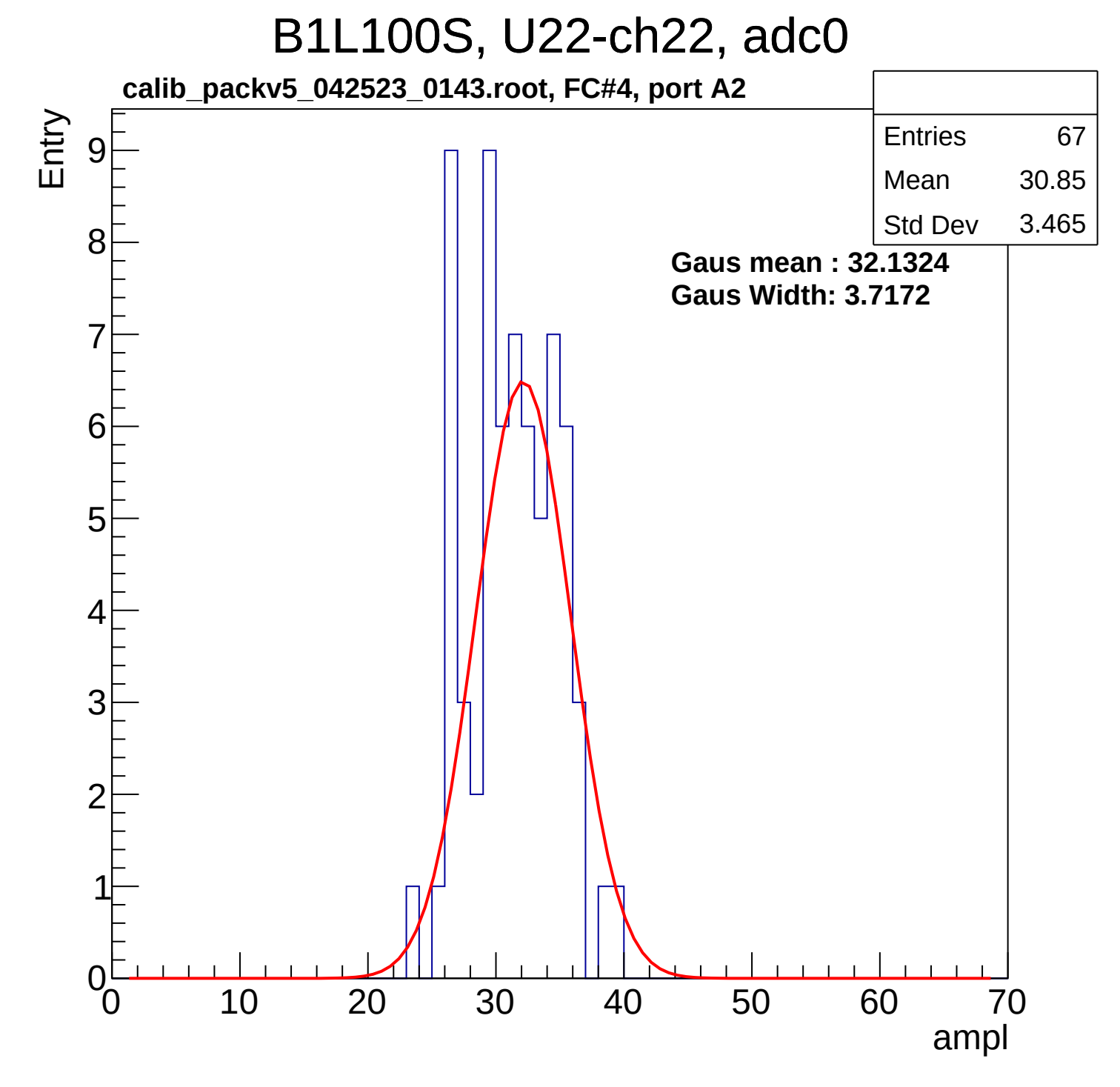
40
50
60
70


B1L100S, U22-ch21, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch21, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch22, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.85
Std Dev 3.465
Gaus mean : 32.1324
Gaus Width: 3.7172
Entry
ampl
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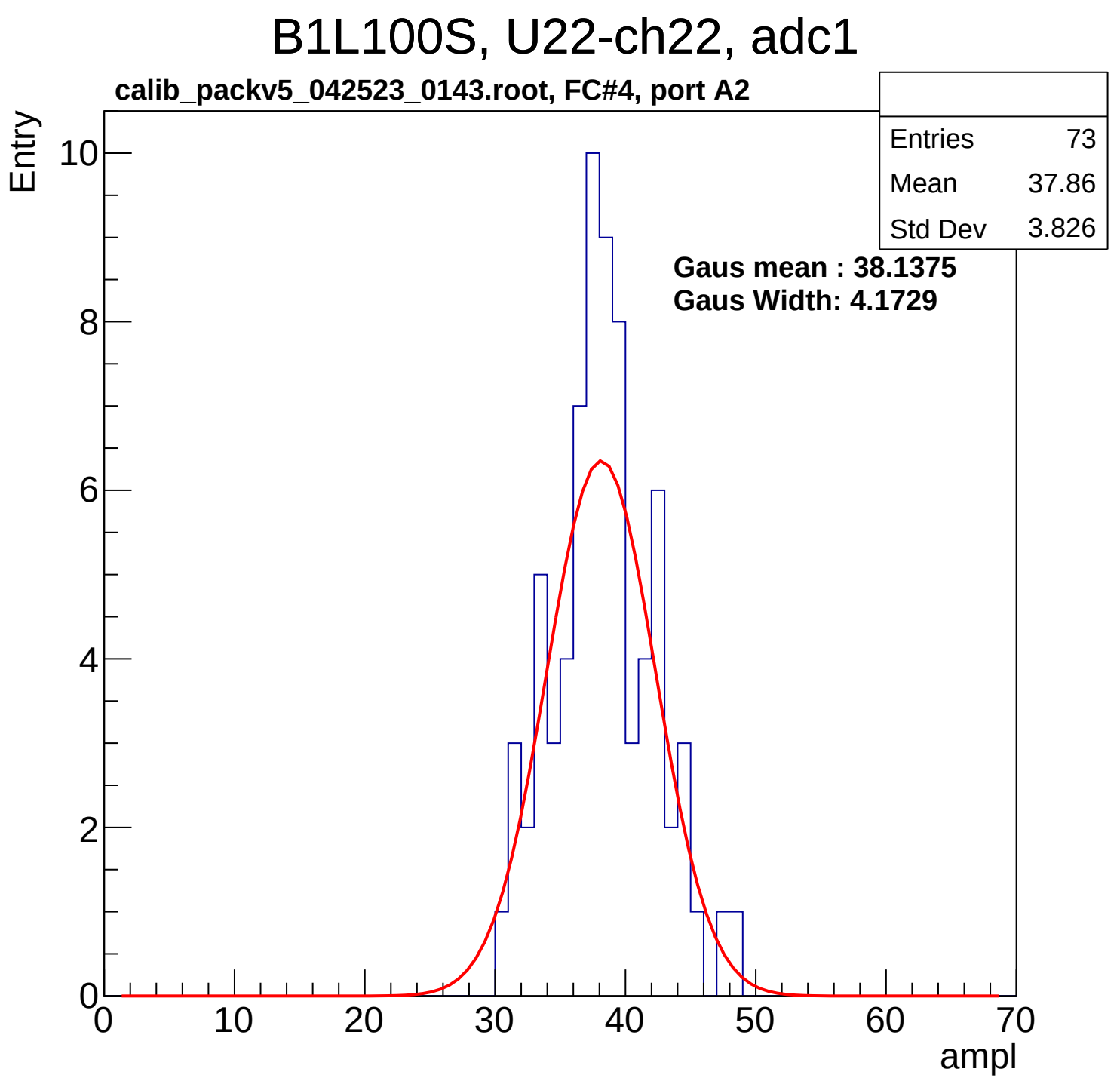
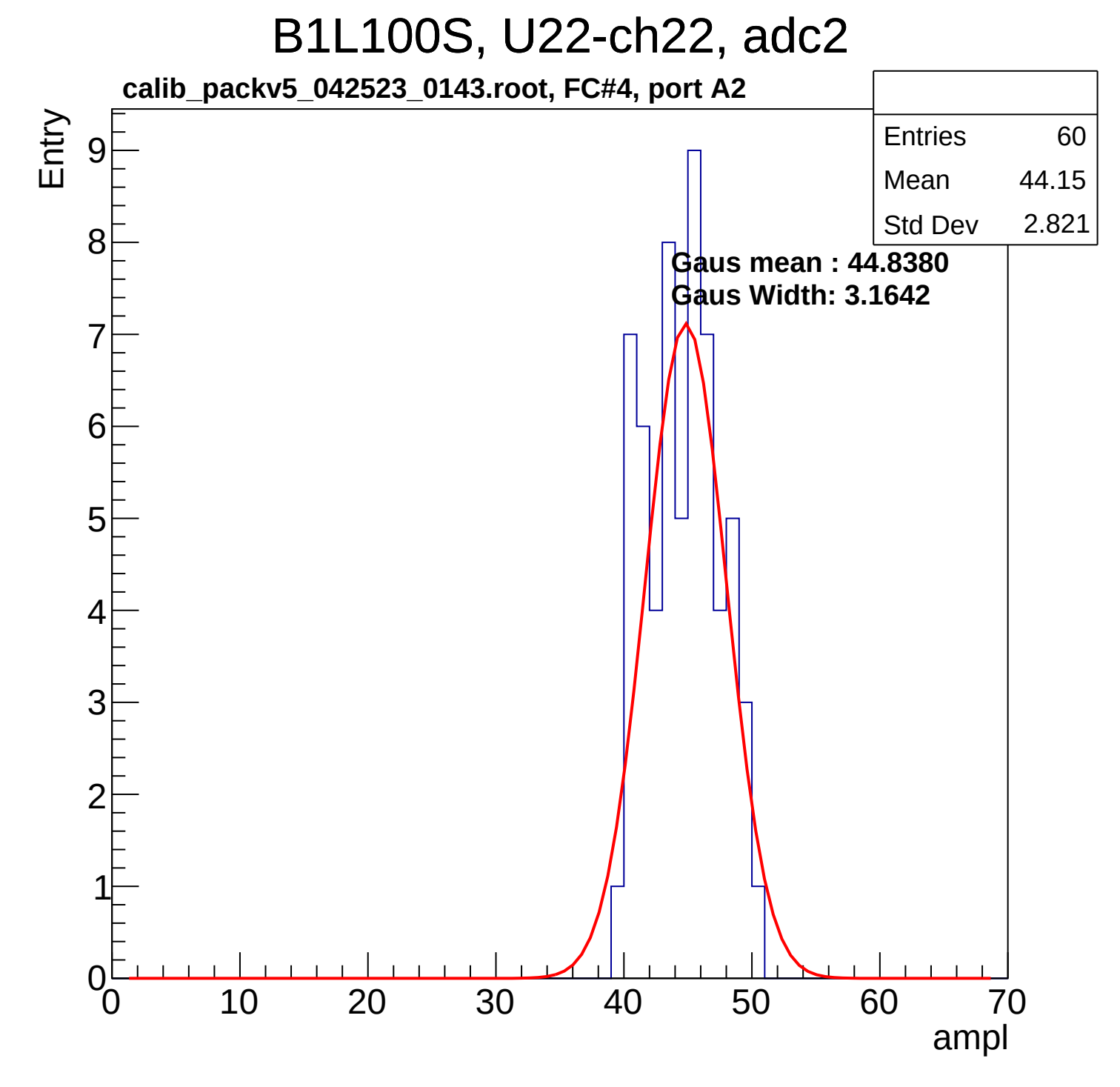
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch22, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.86
Std Dev 3.826
Gaus mean : 38.1375
Gaus Width: 4.1729
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch22, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 44.15
Std Dev 2.821
Gaus mean : 44.8380
Gaus Width: 3.1642
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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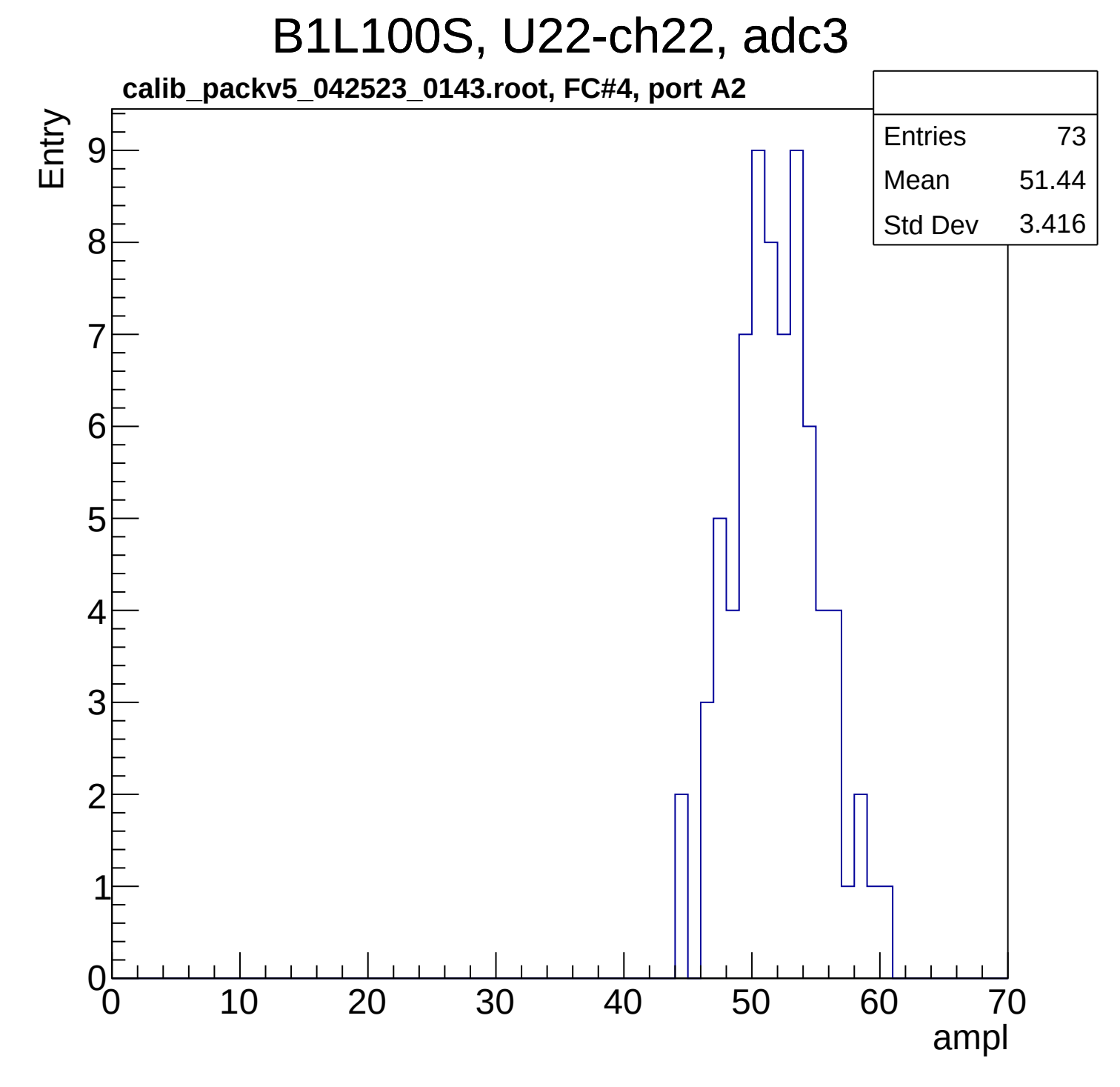
B1L100S, U22-ch22, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 51.44
Std Dev 3.416
Entry
ampl
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2
3
4
5
6
7
8
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0
10
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40
50
60
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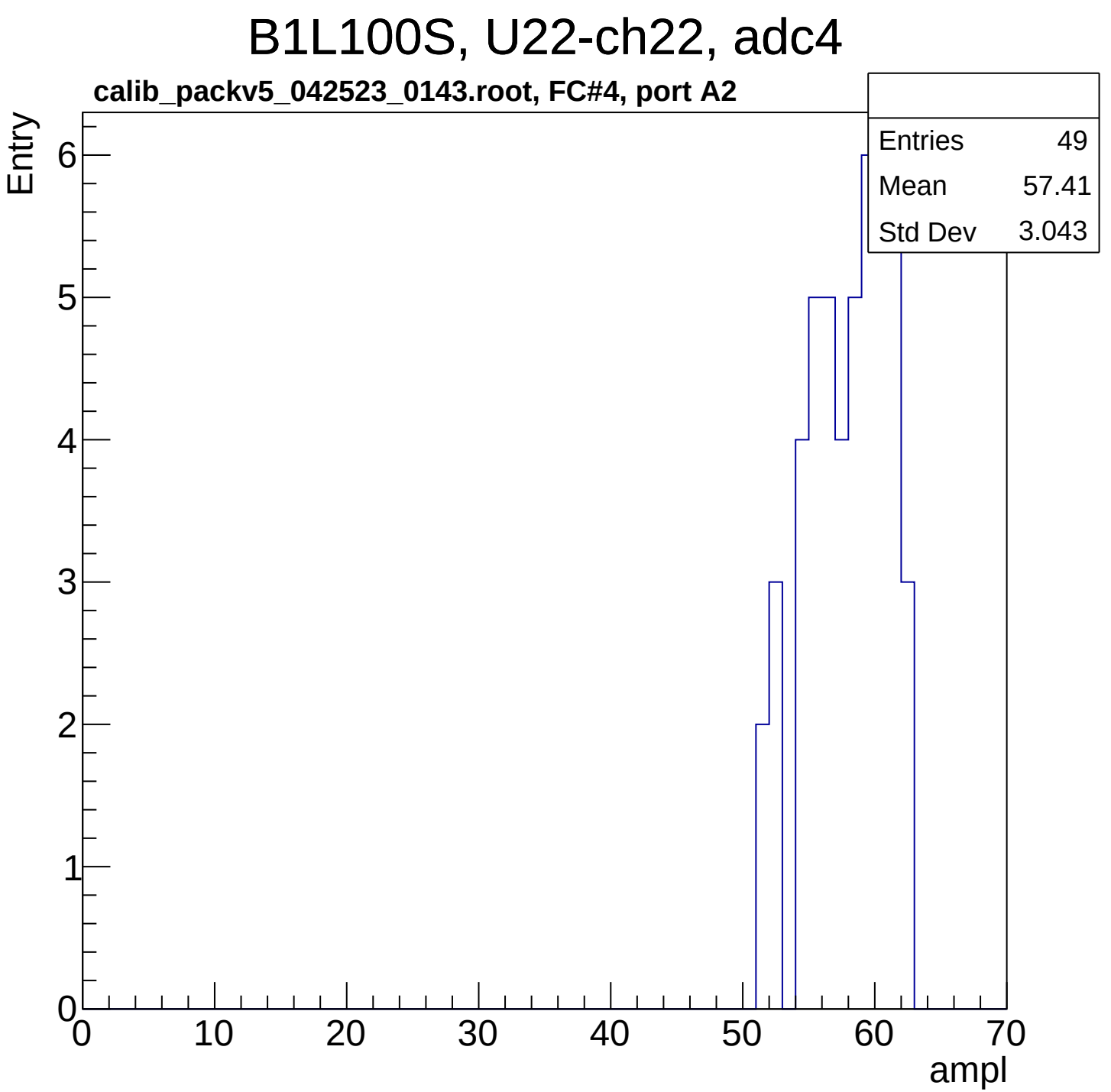
B1L100S, U22-ch22, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 57.41
Std Dev 3.043
Entry
ampl
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1
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3
4
5
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0
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40
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60
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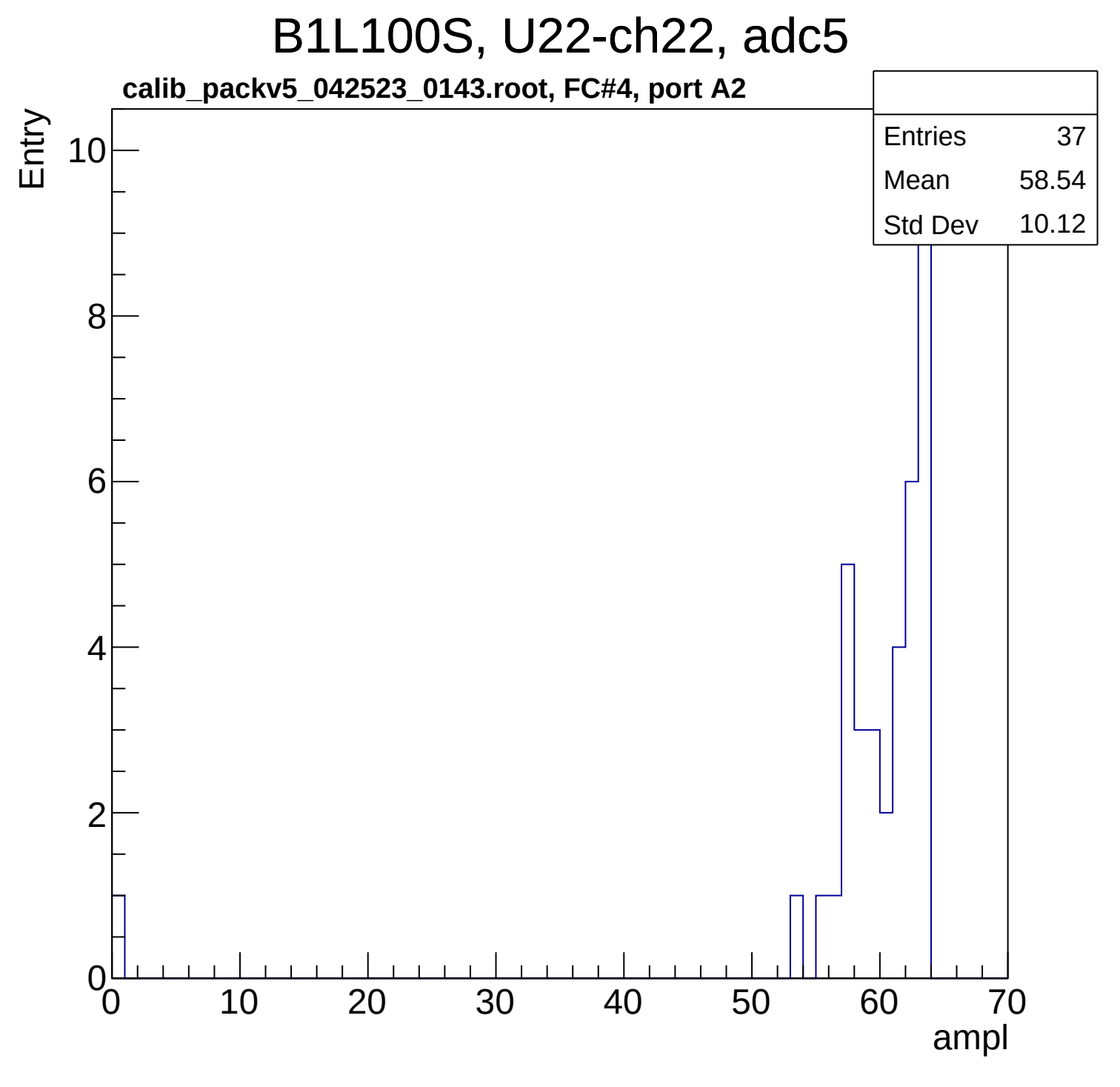
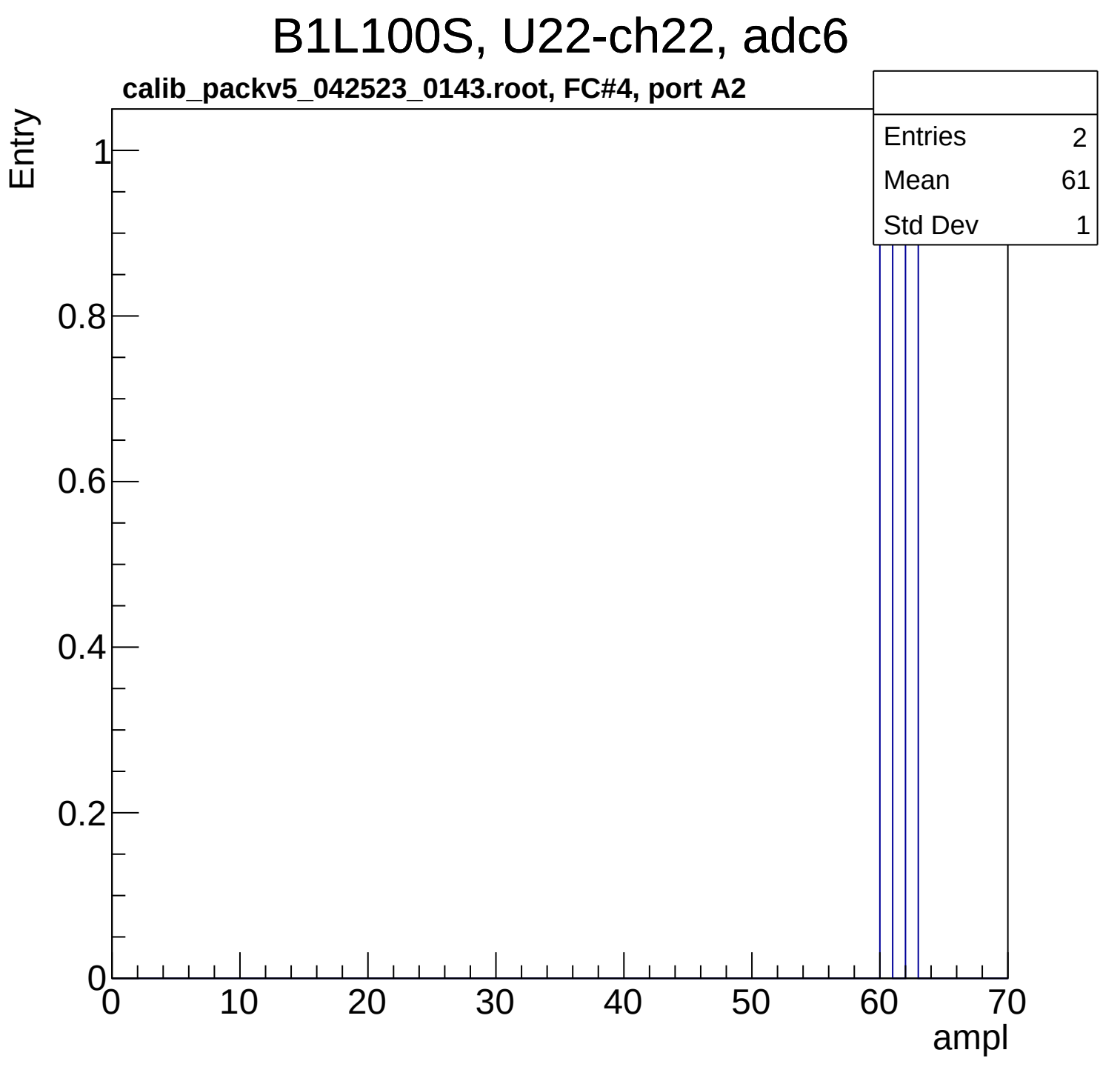
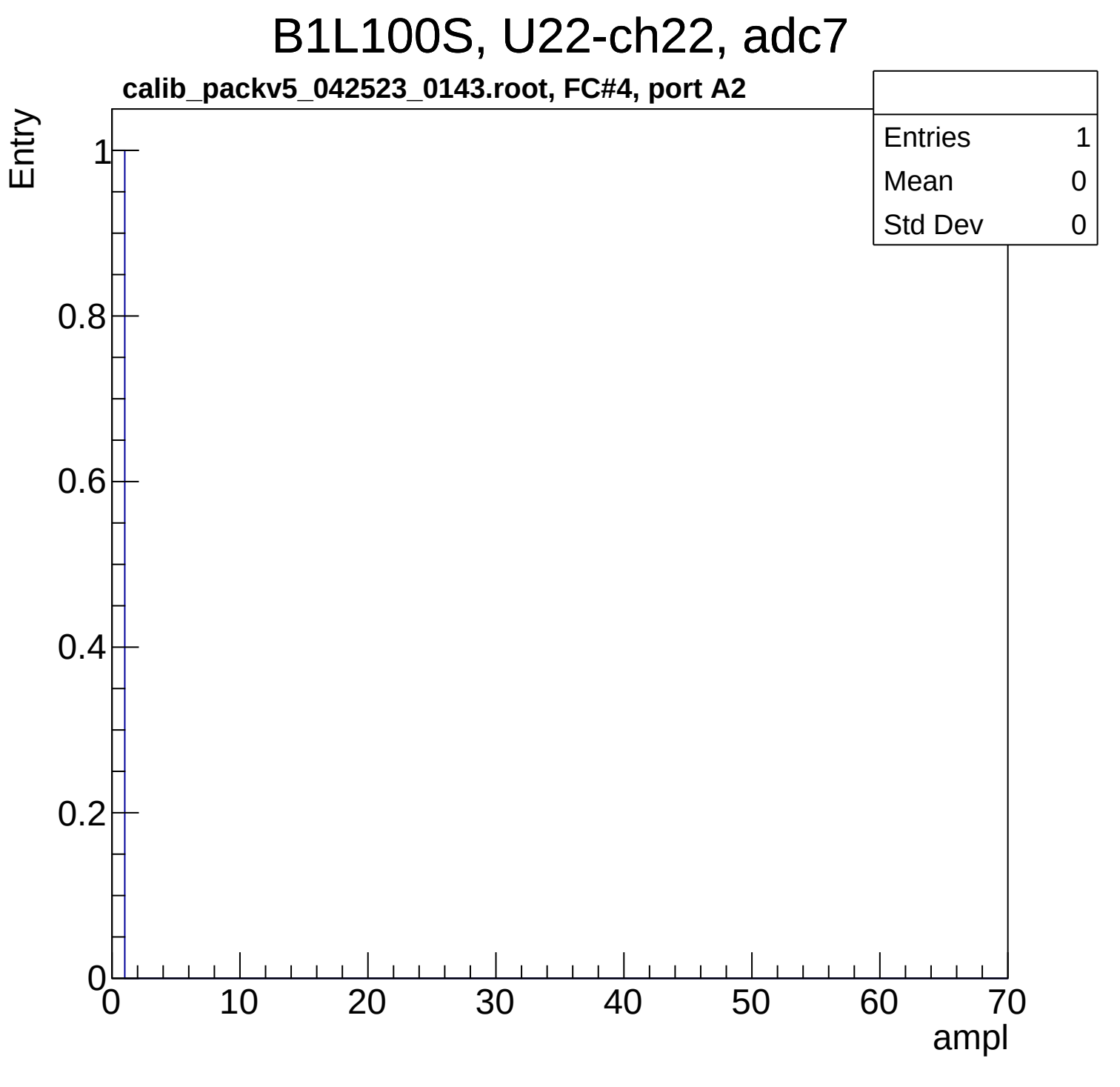

B1L100S, U22-ch22, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.54
Std Dev 10.12
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch22, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch22, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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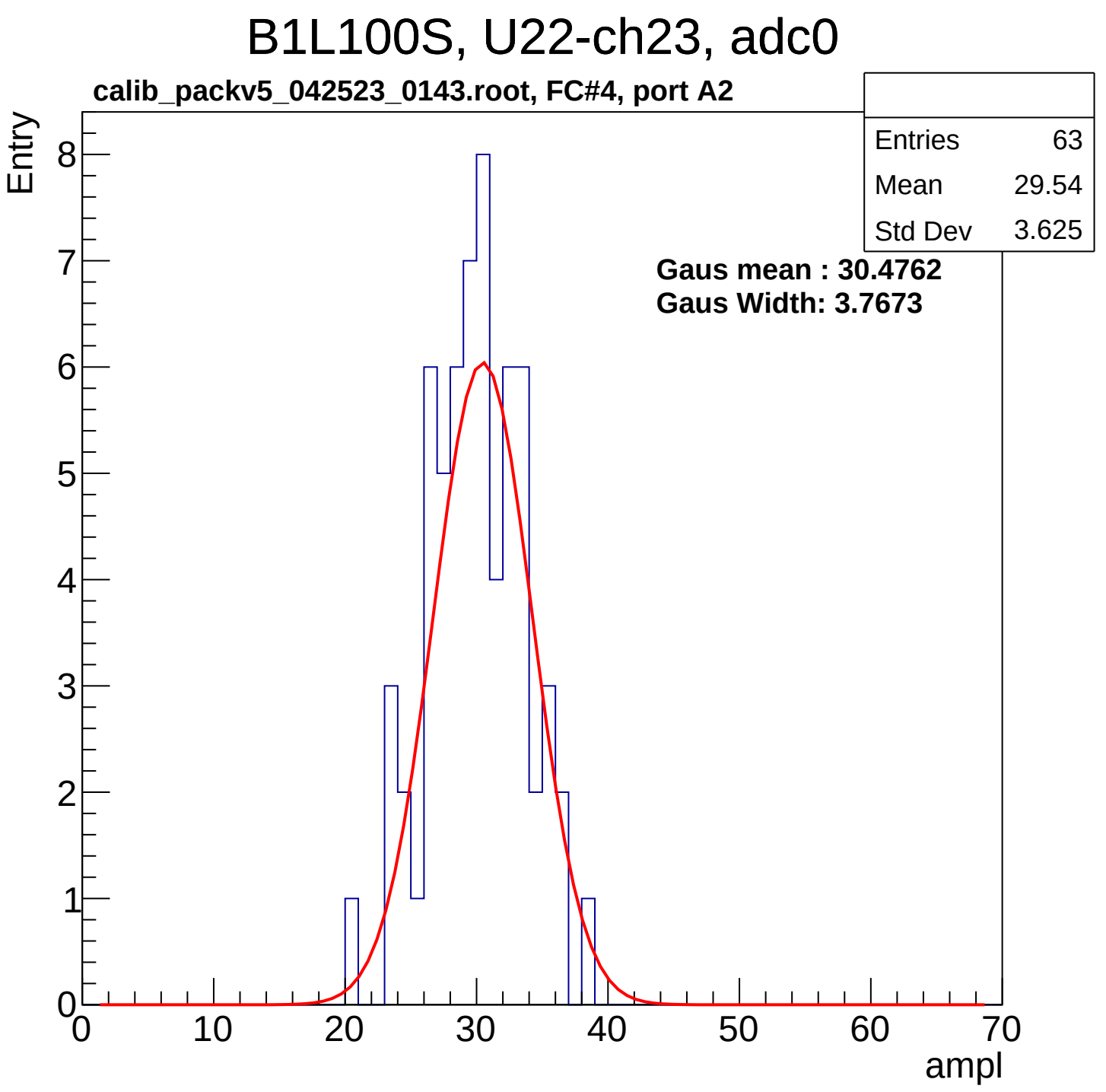
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch23, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 29.54
Std Dev 3.625
Gaus mean : 30.4762
Gaus Width: 3.7673
Entry
ampl
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1
2
3
4
5
6
7
8
0
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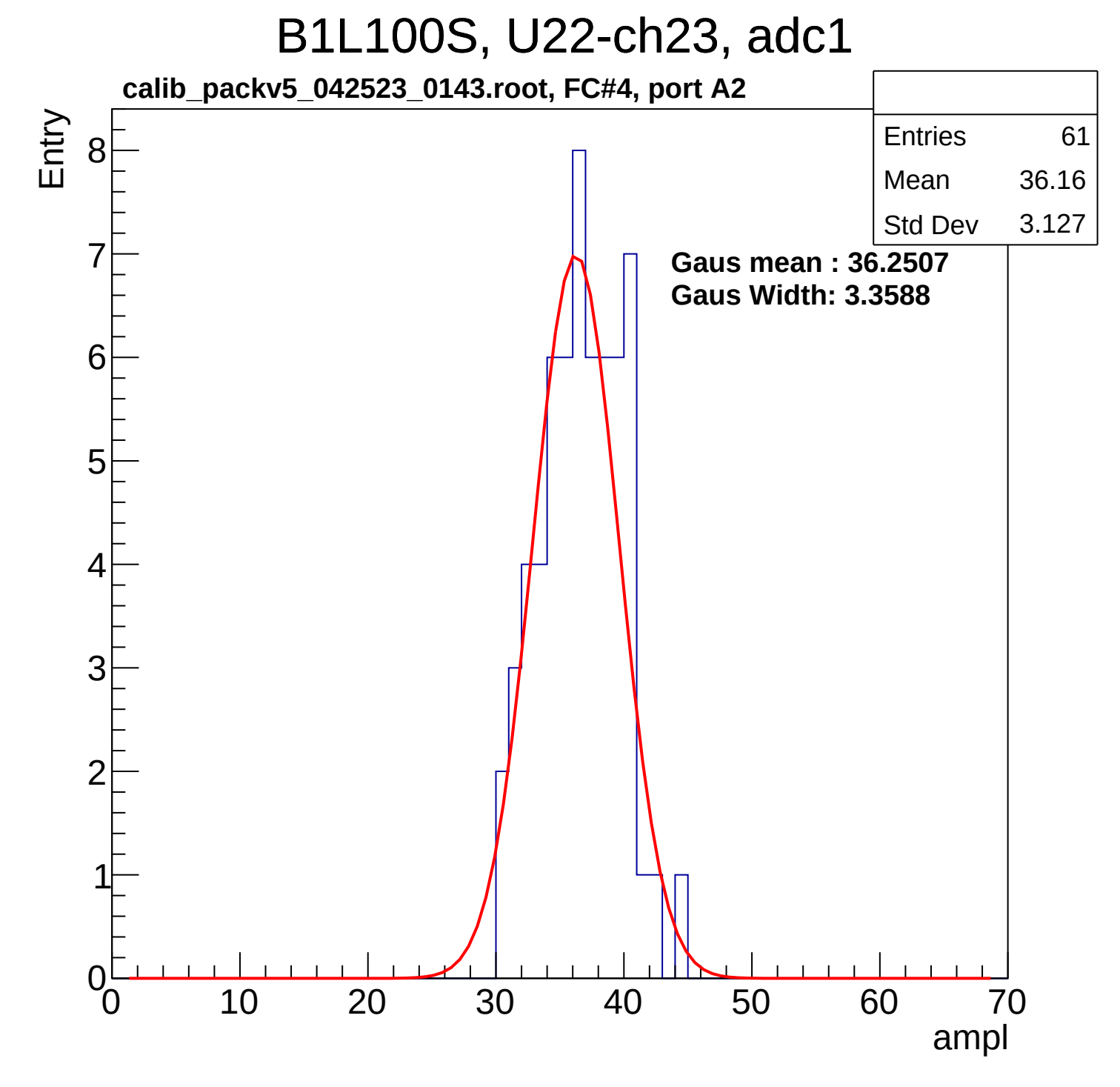
B1L100S, U22-ch23, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 36.16
Std Dev 3.127
Gaus mean : 36.2507
Gaus Width: 3.3588
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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40
50
60
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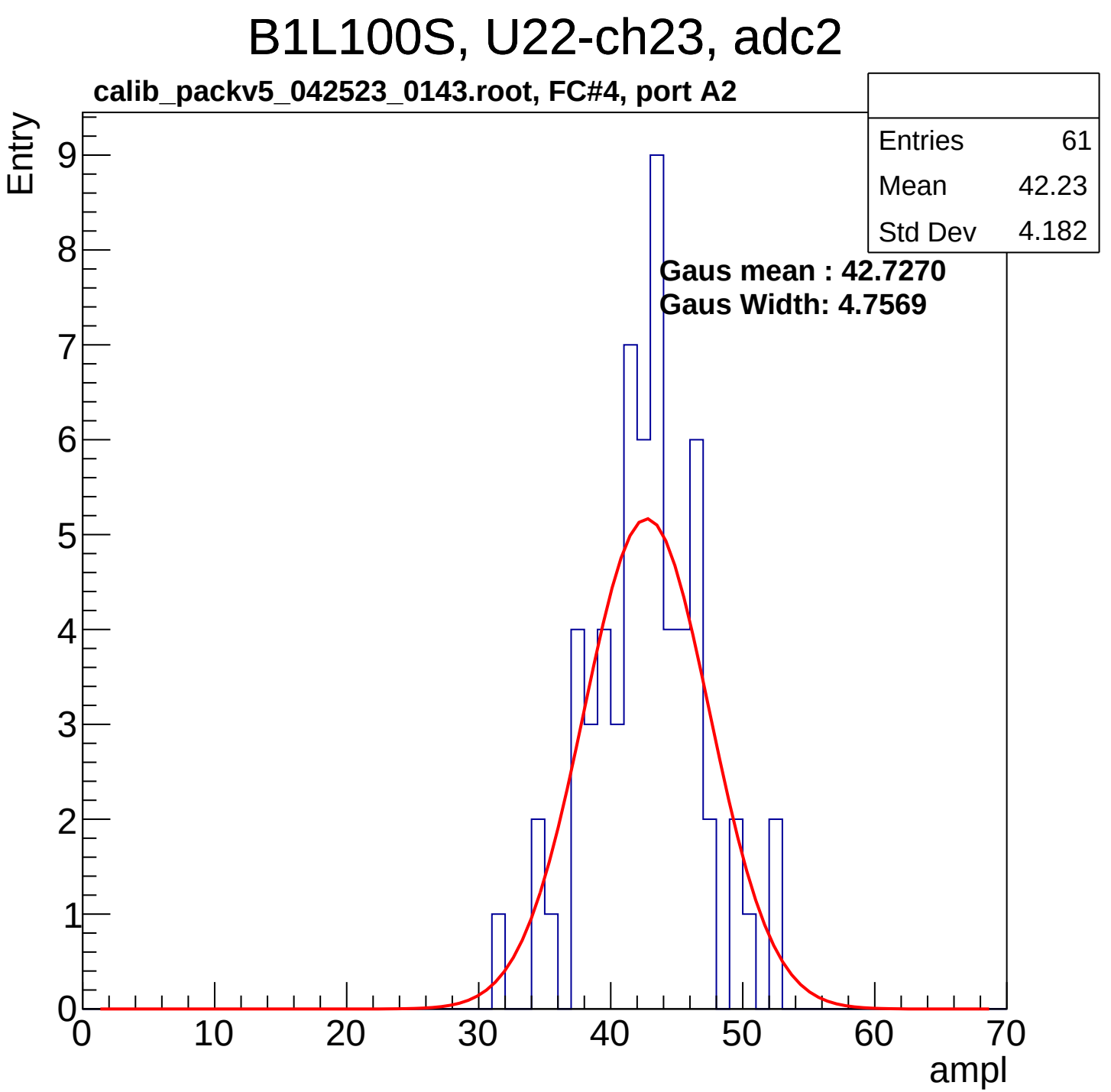
B1L100S, U22-ch23, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.23
Std Dev 4.182
Gaus mean : 42.7270
Gaus Width: 4.7569
Entry
ampl
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2
3
4
5
6
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0
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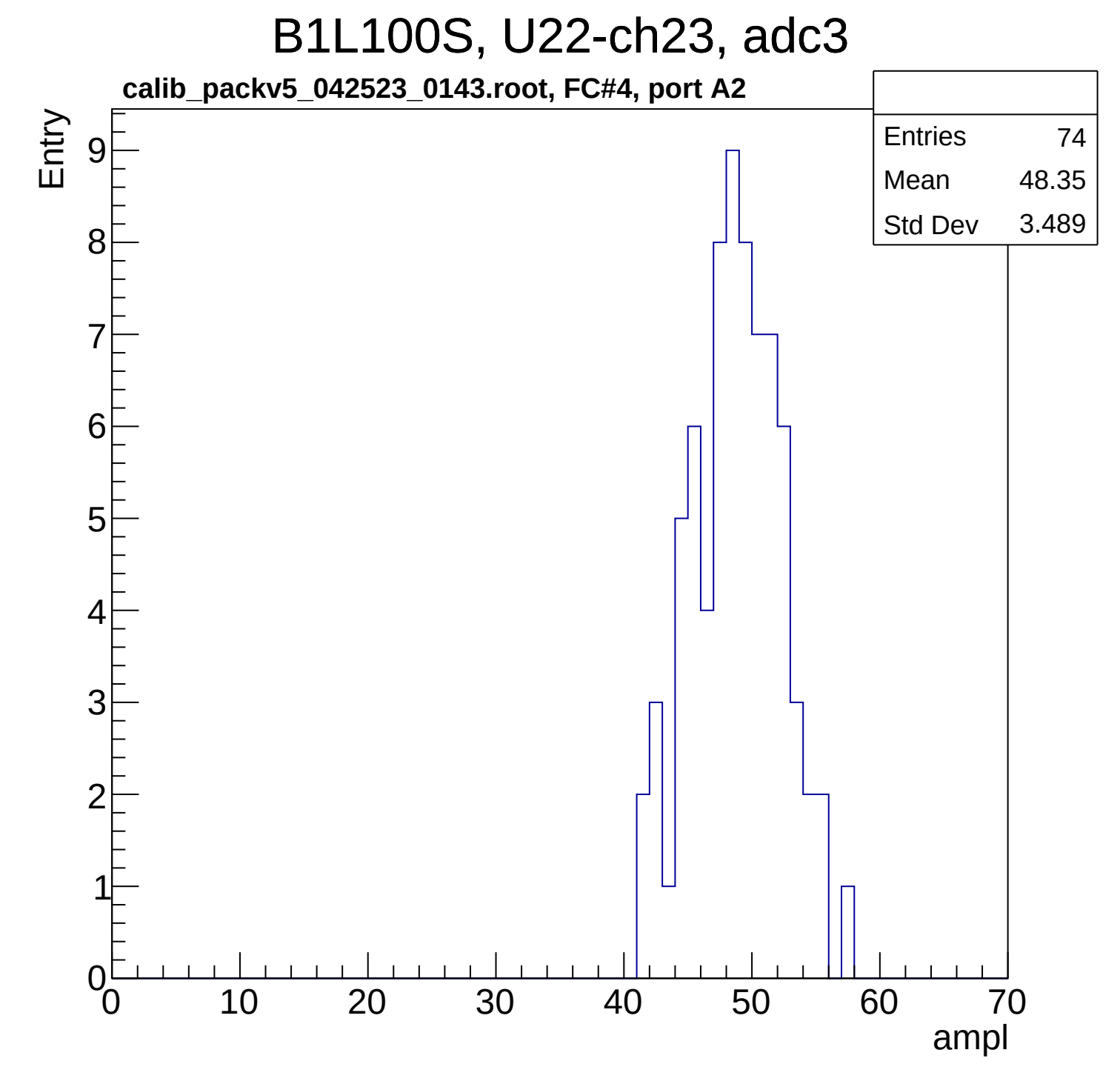
B1L100S, U22-ch23, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 48.35
Std Dev 3.489
Entry
ampl
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2
3
4
5
6
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0
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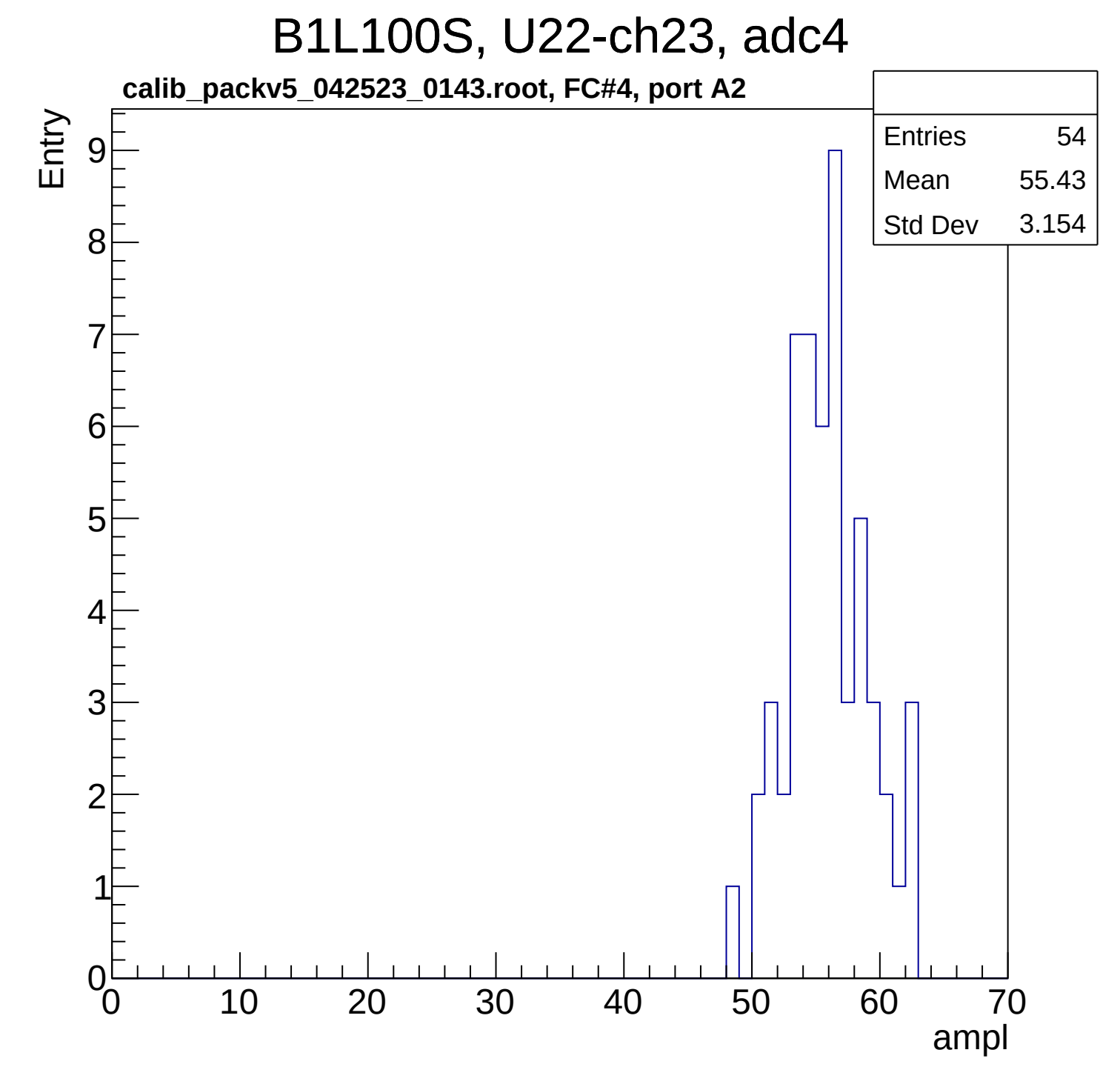
B1L100S, U22-ch23, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.43
Std Dev 3.154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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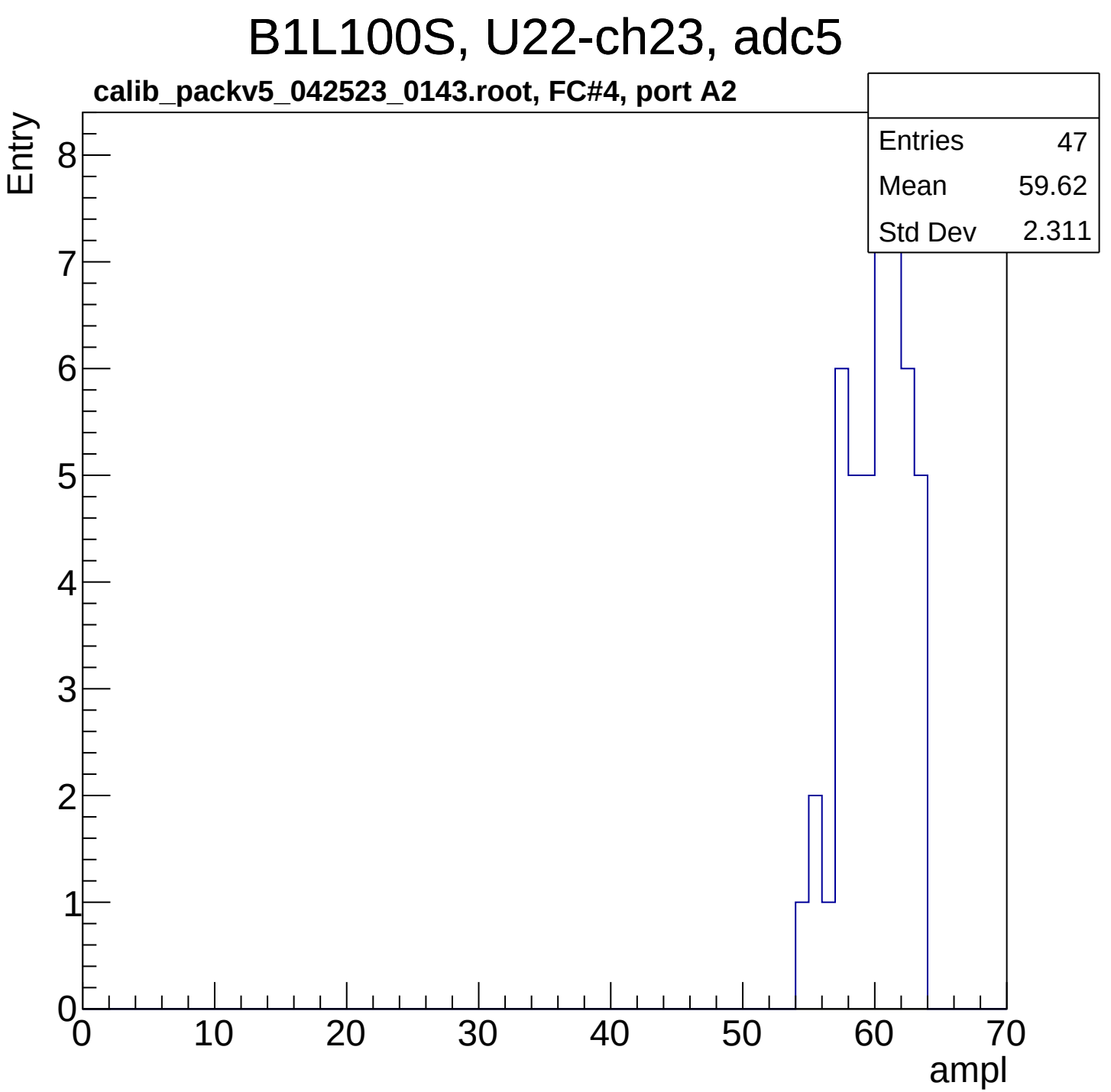
B1L100S, U22-ch23, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.62
Std Dev 2.311
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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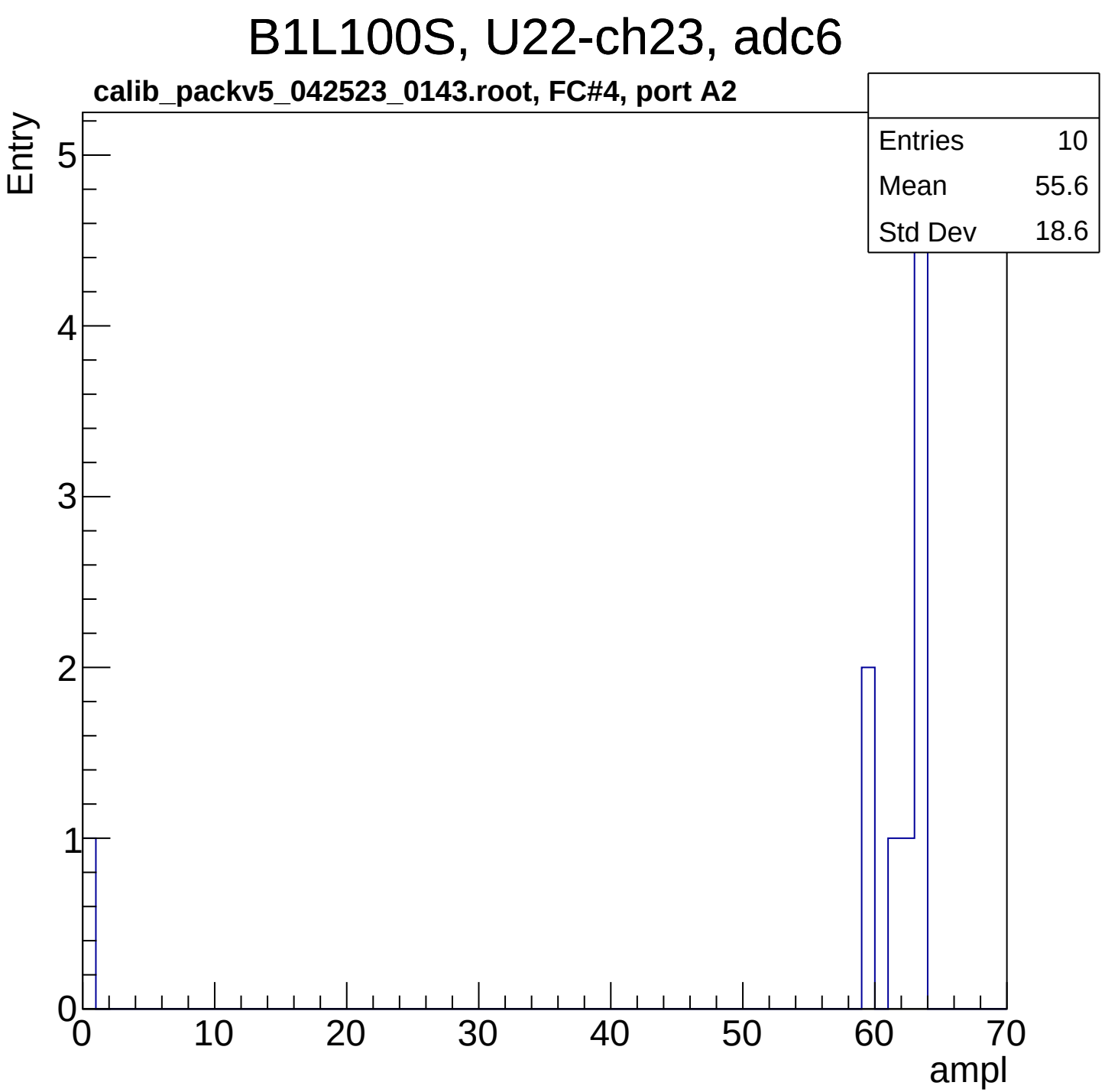
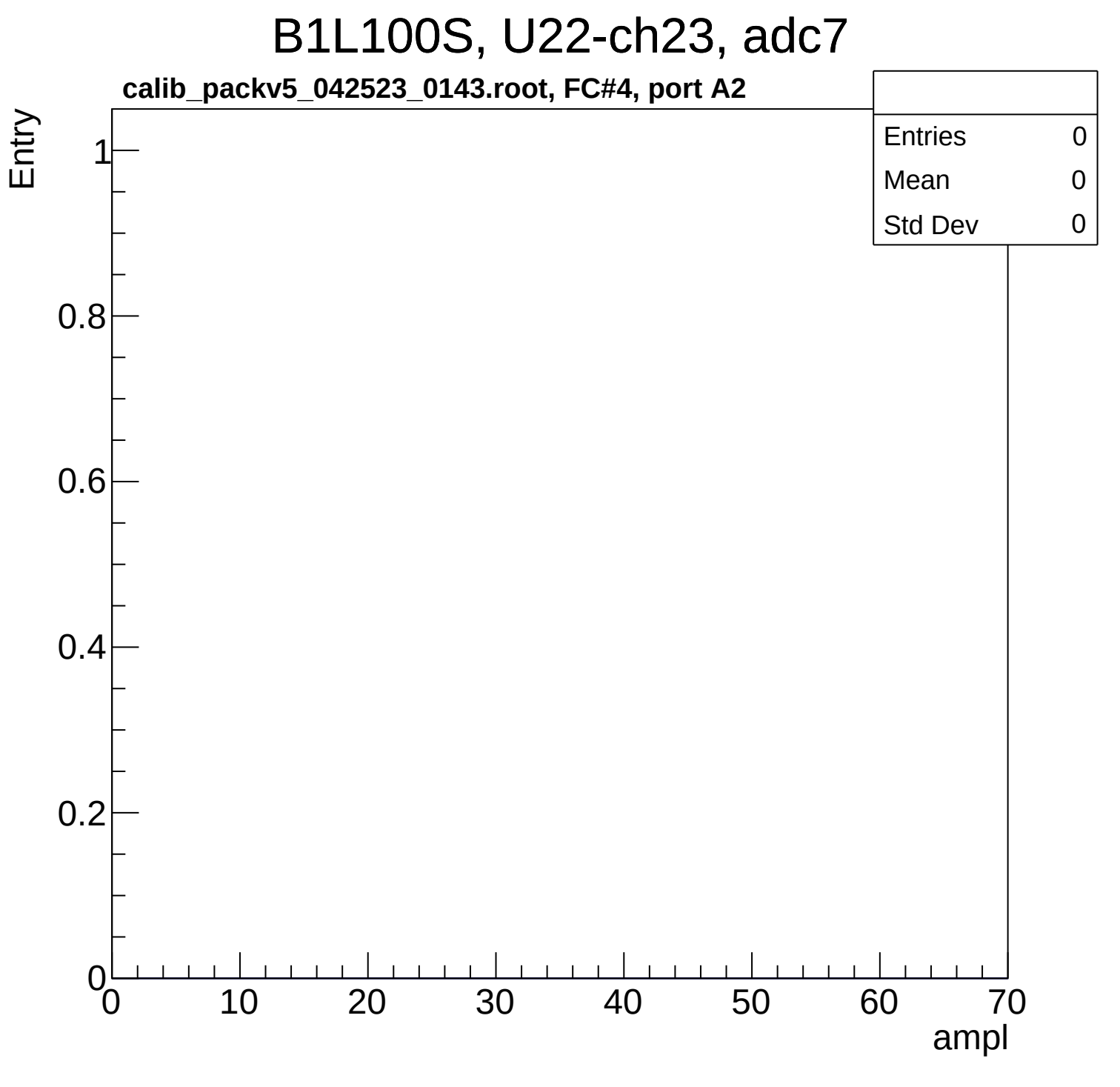
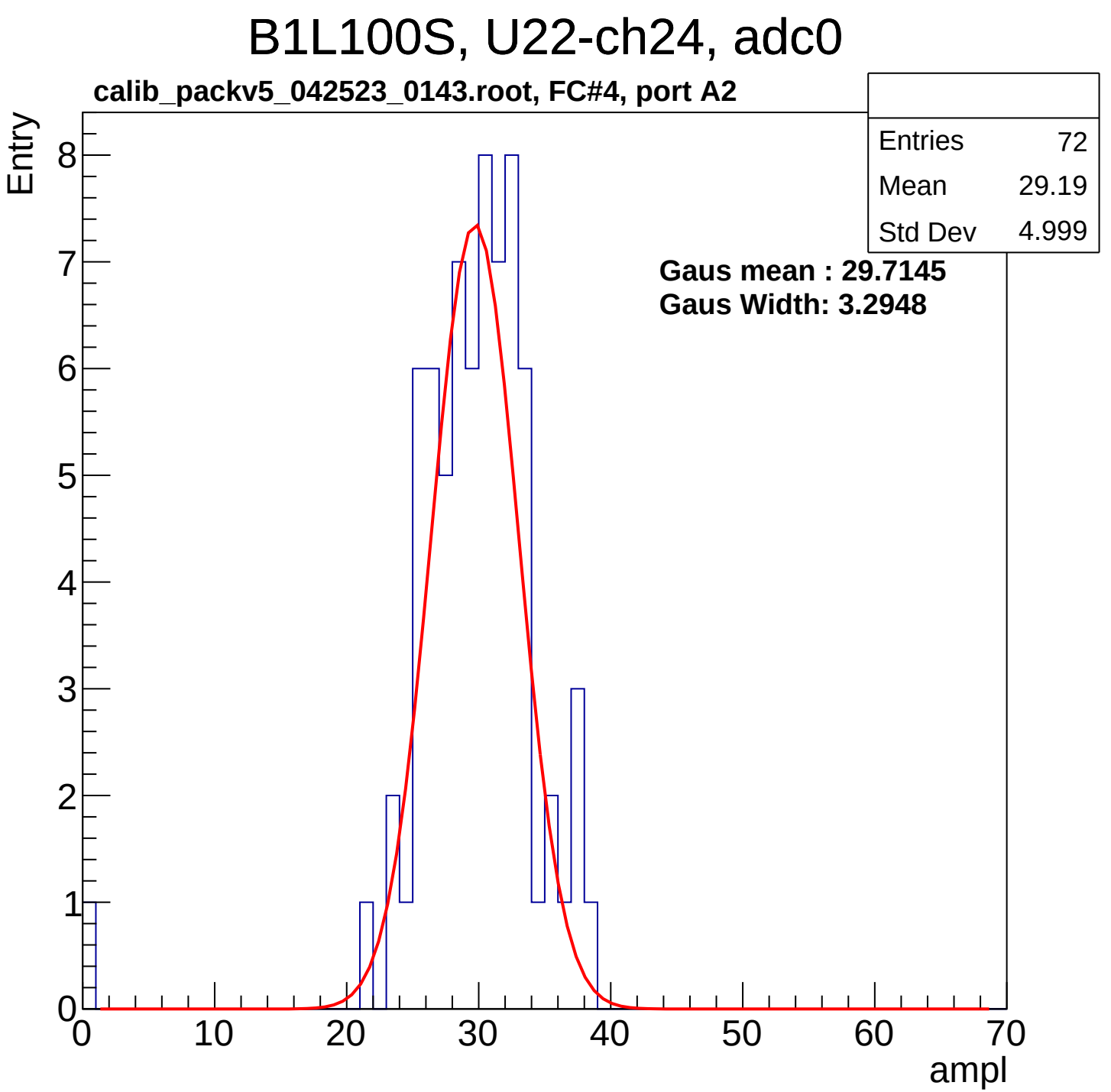

B1L100S, U22-ch23, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 55.6
Std Dev 18.6
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch23, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch24, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 29.19
Std Dev 4.999
Gaus mean : 29.7145
Gaus Width: 3.2948
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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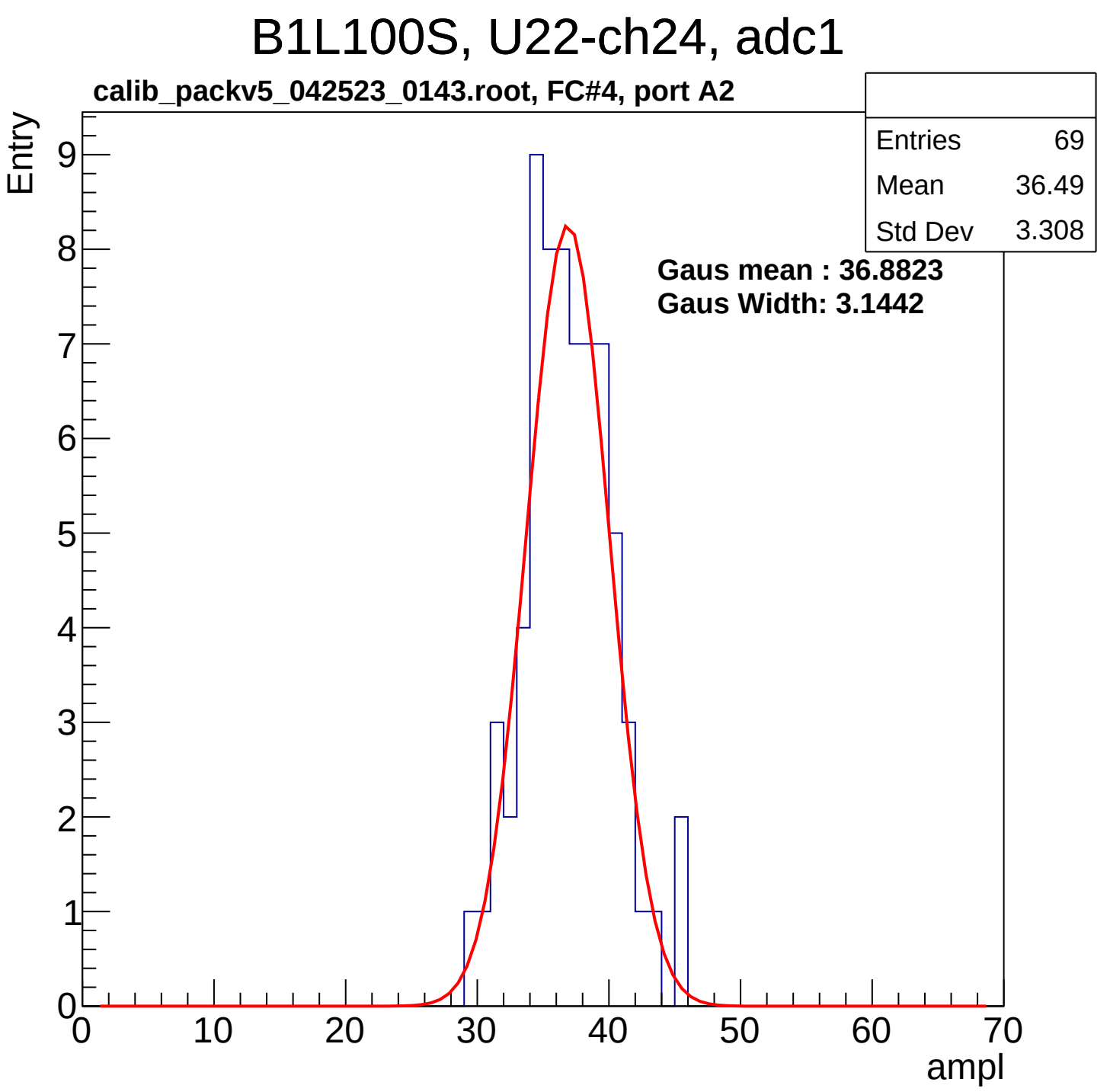
B1L100S, U22-ch24, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 36.49
Std Dev 3.308
Gaus mean : 36.8823
Gaus Width: 3.1442
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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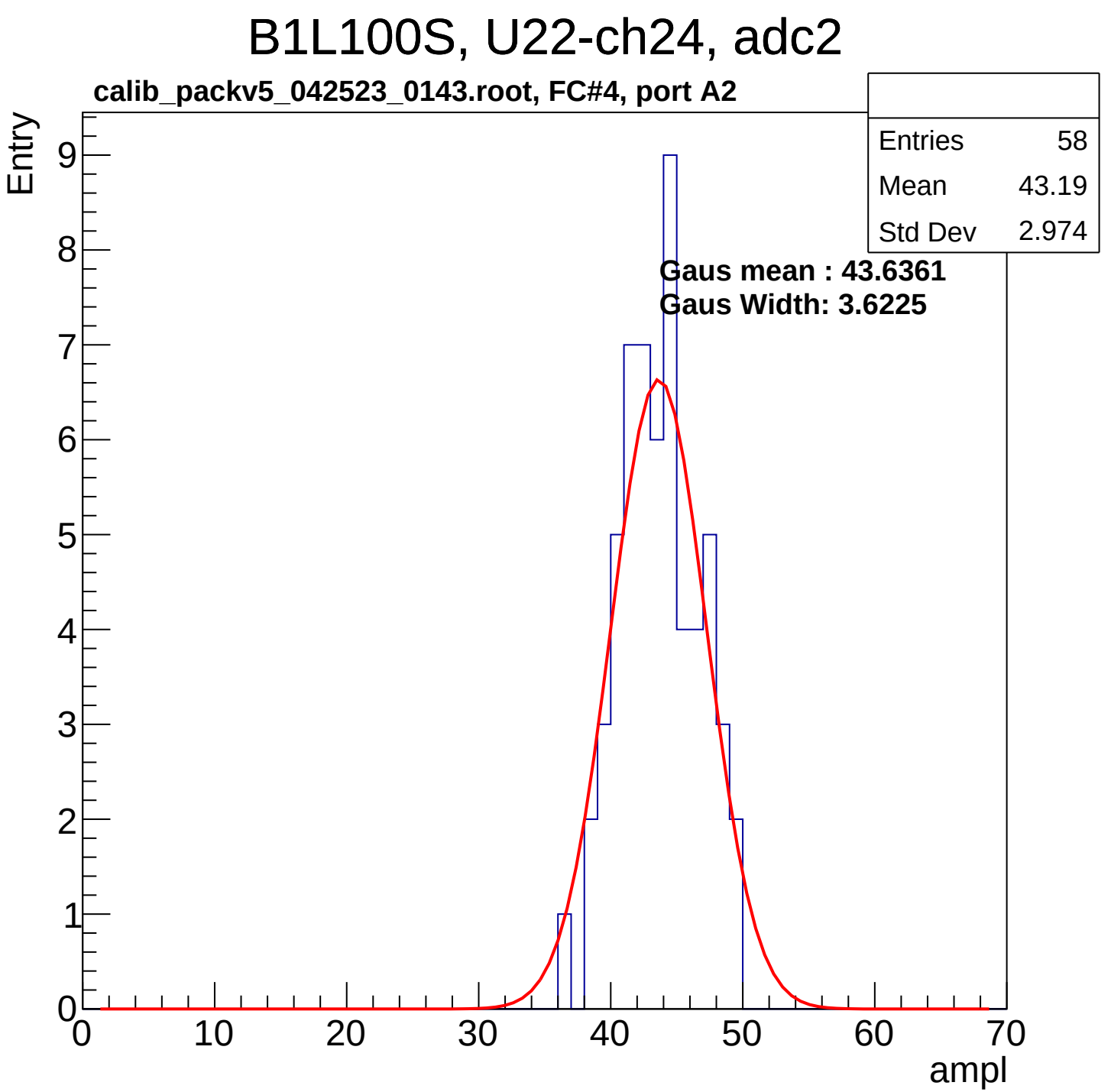
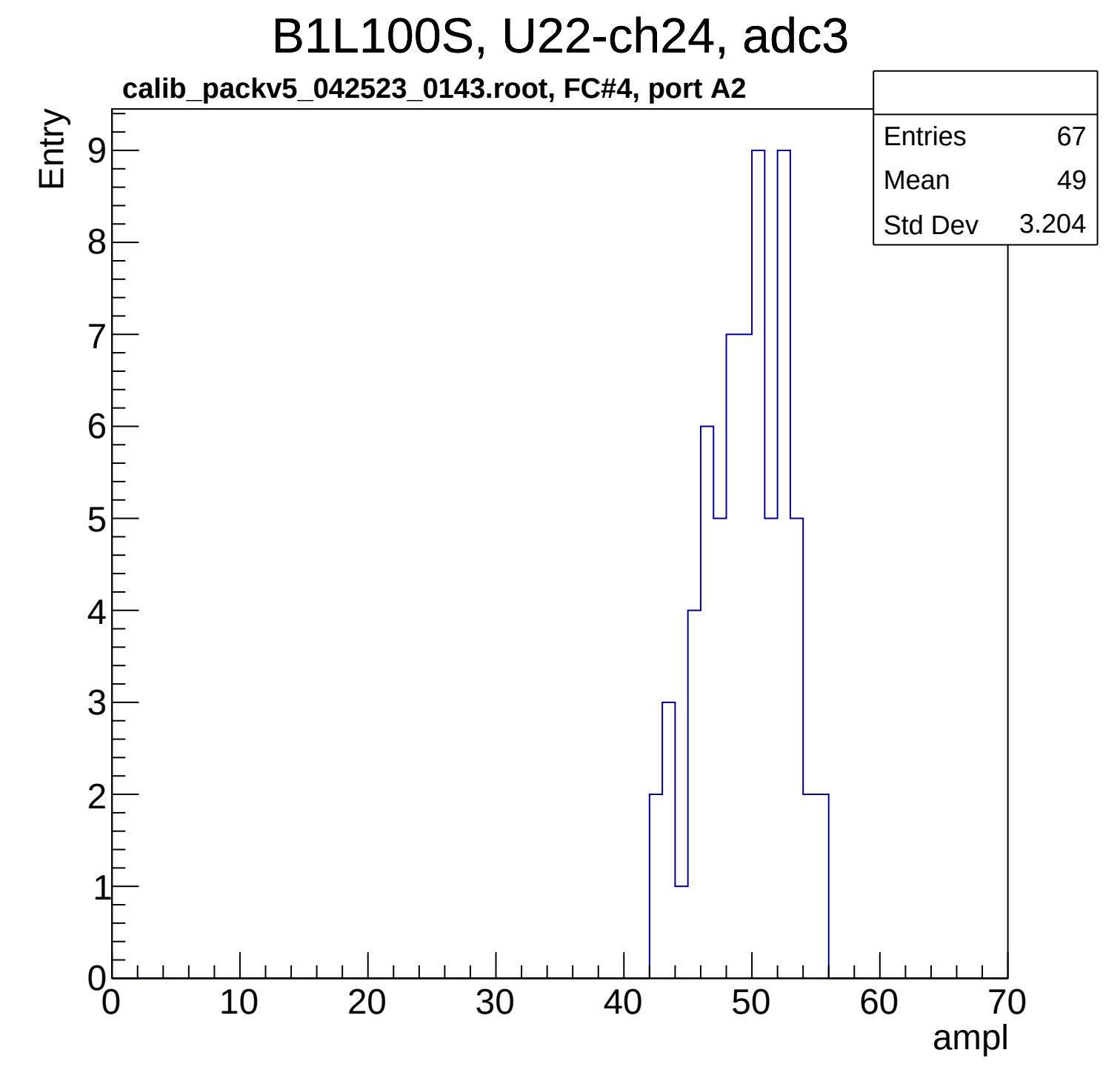

B1L100S, U22-ch24, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 43.19
Std Dev 2.974
Gaus mean : 43.6361
Gaus Width: 3.6225
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch24, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49
Std Dev 3.204
Entry
ampl
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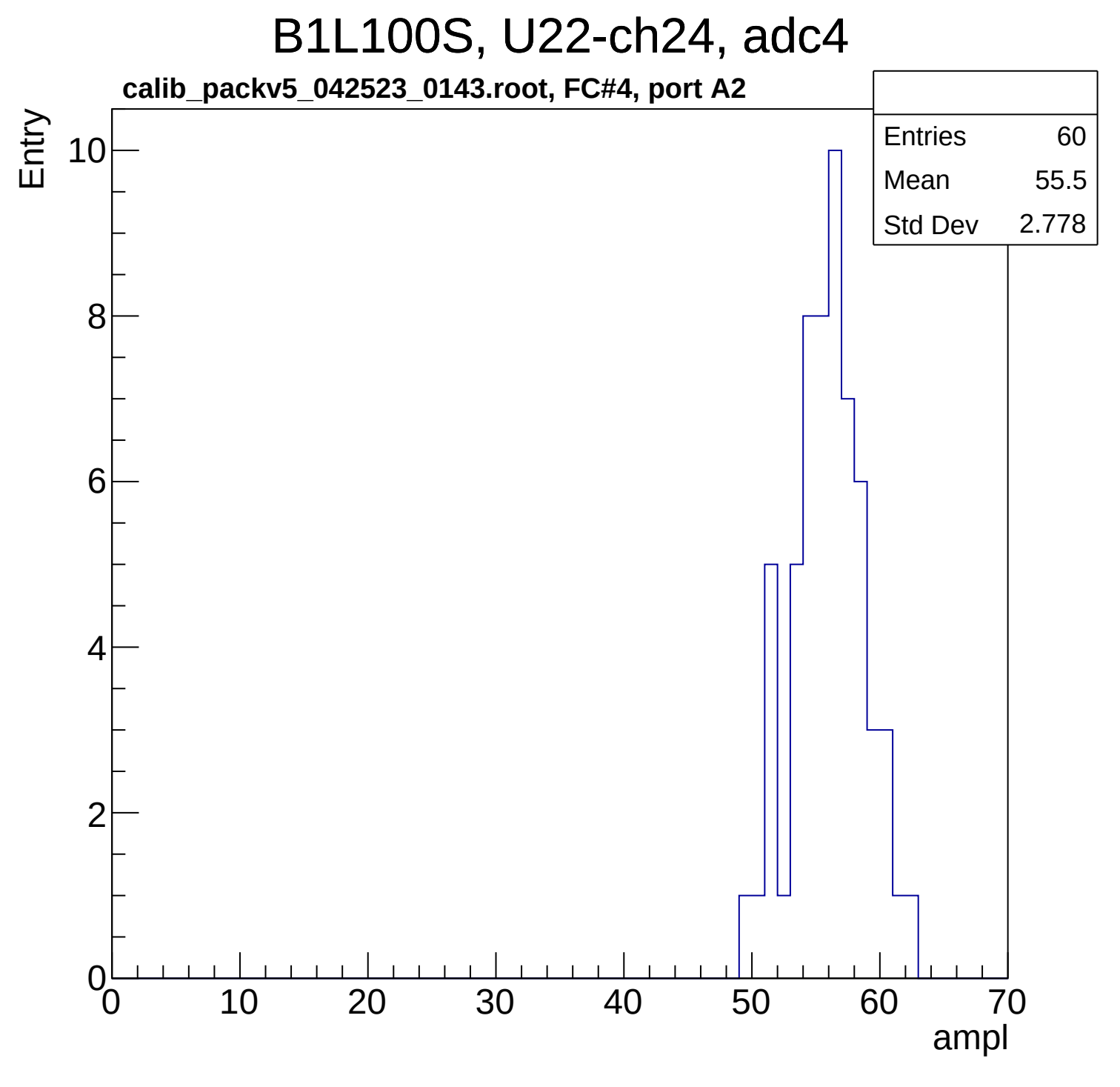
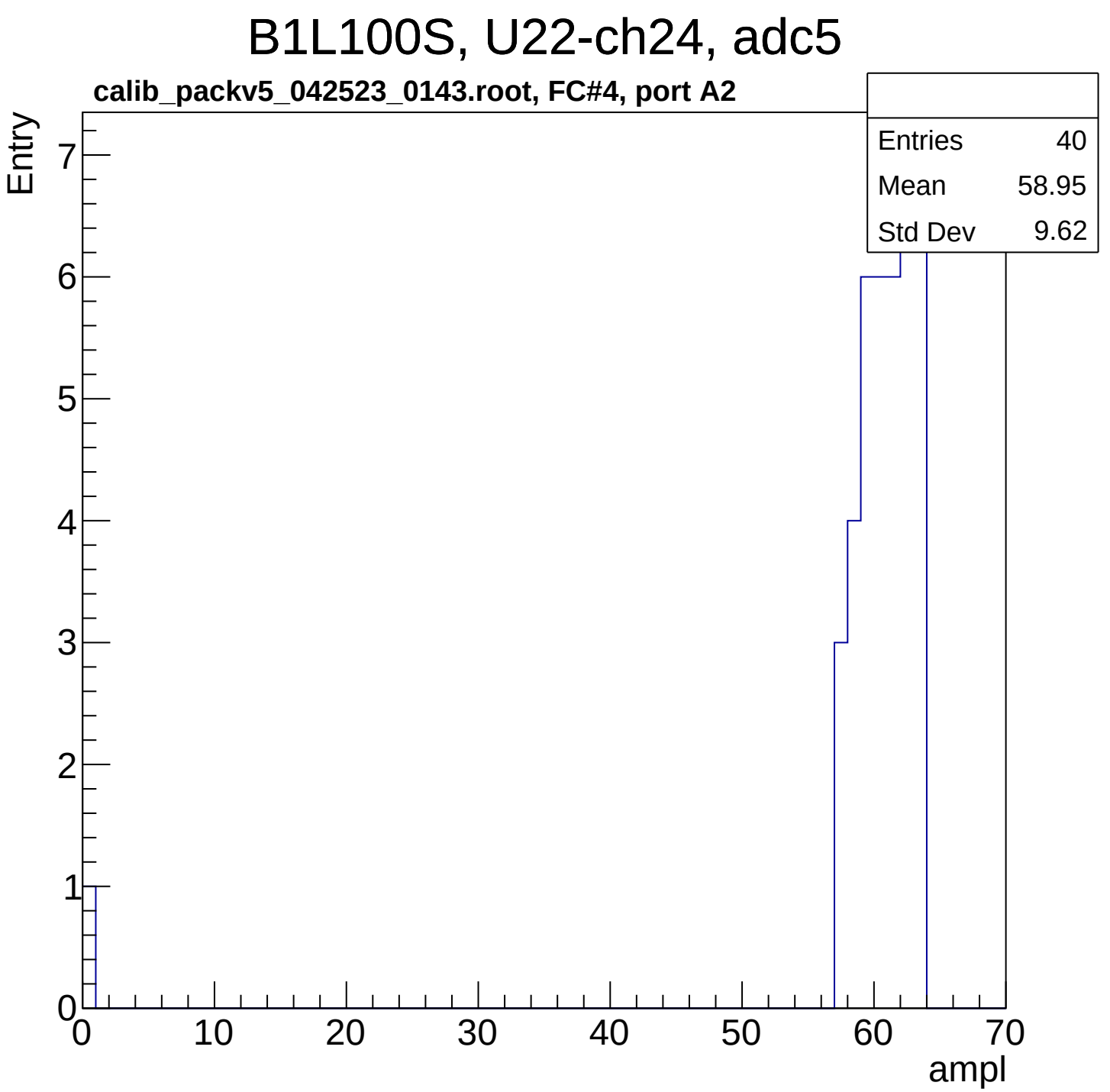
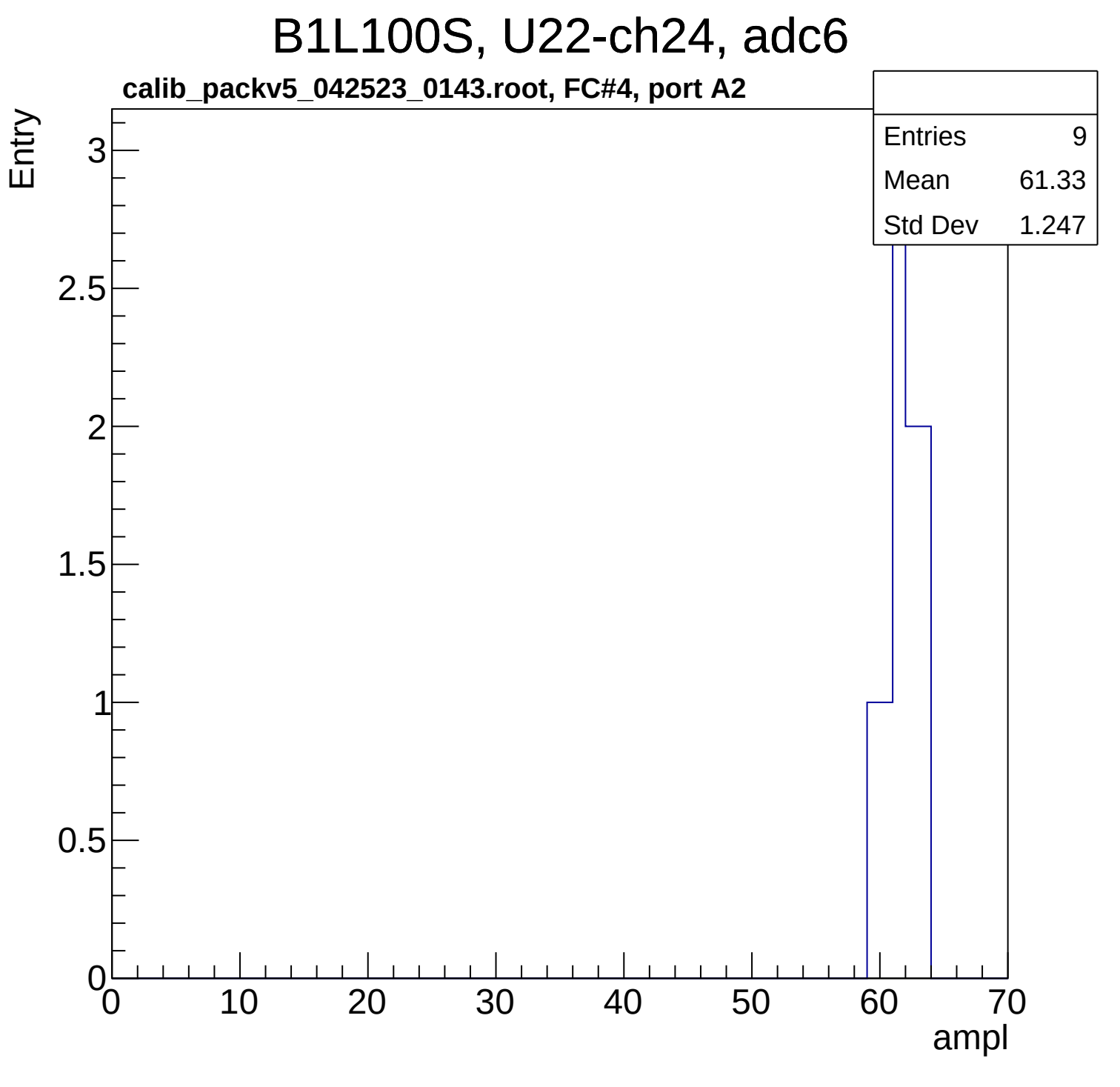
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch24, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.5
Std Dev 2.778
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch24, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.95
Std Dev 9.62
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch24, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.33
Std Dev 1.247
Entry
ampl
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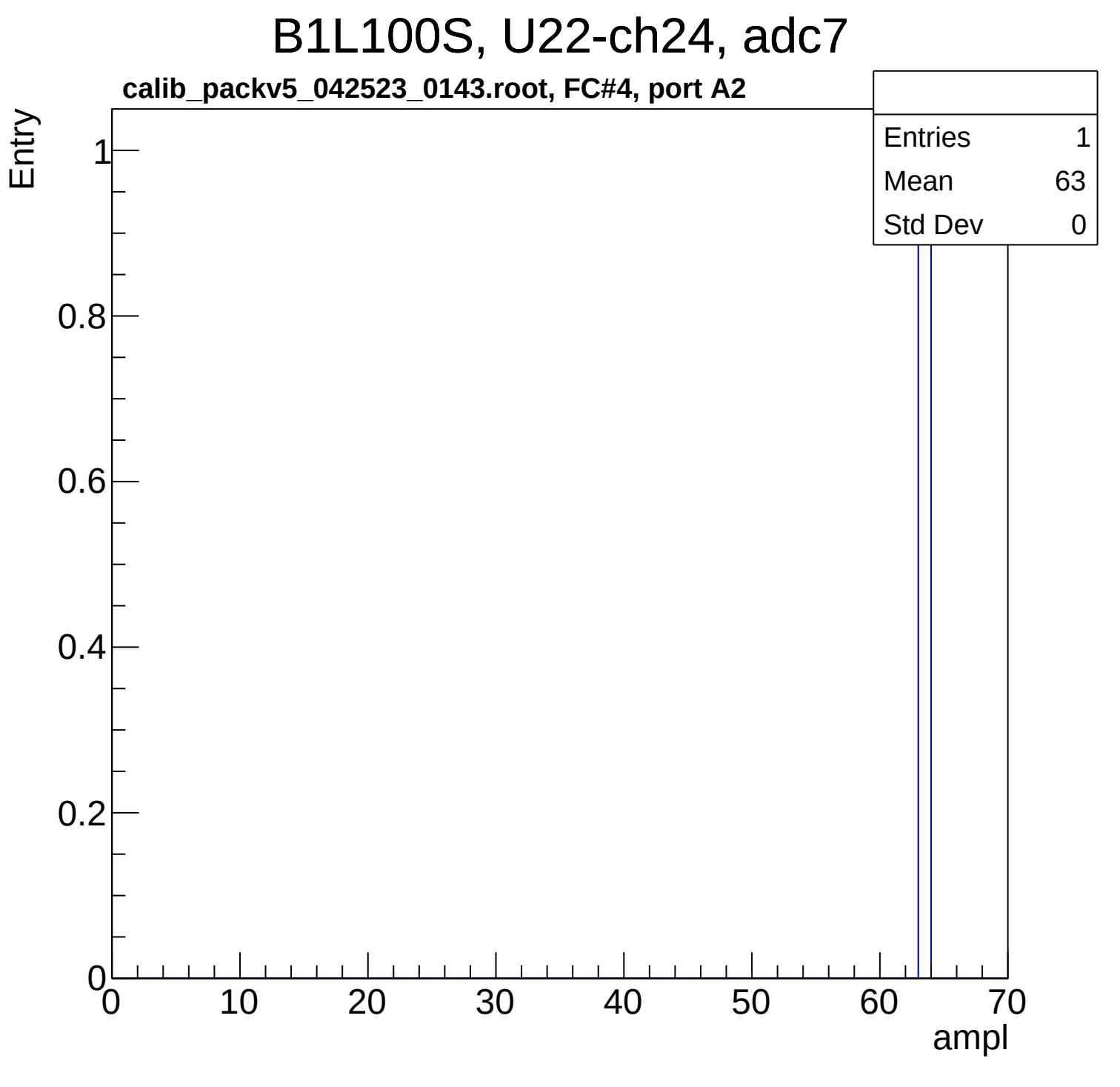
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch24, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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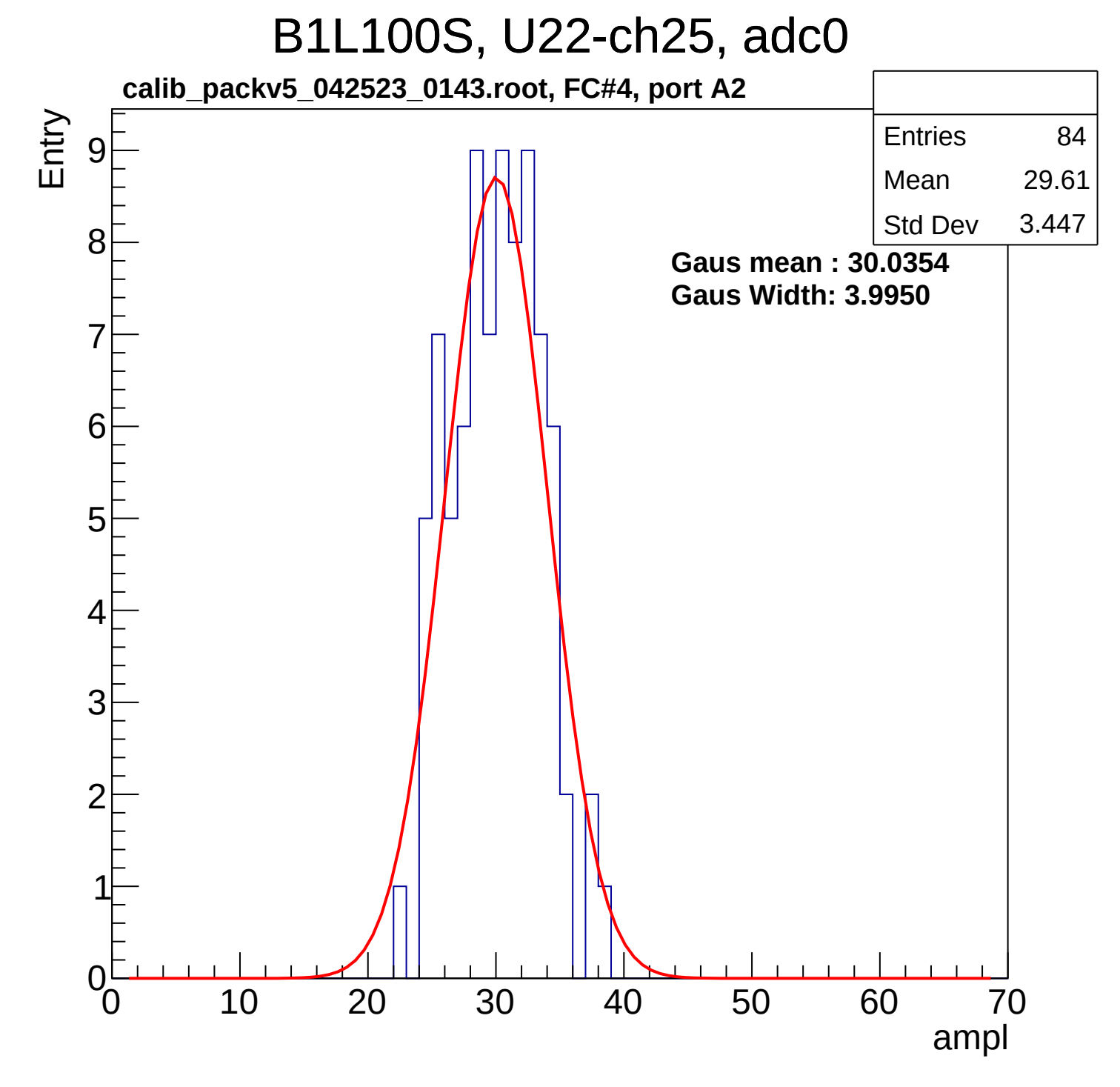
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch25, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 29.61
Std Dev 3.447
Gaus mean : 30.0354
Gaus Width: 3.9950
Entry
ampl
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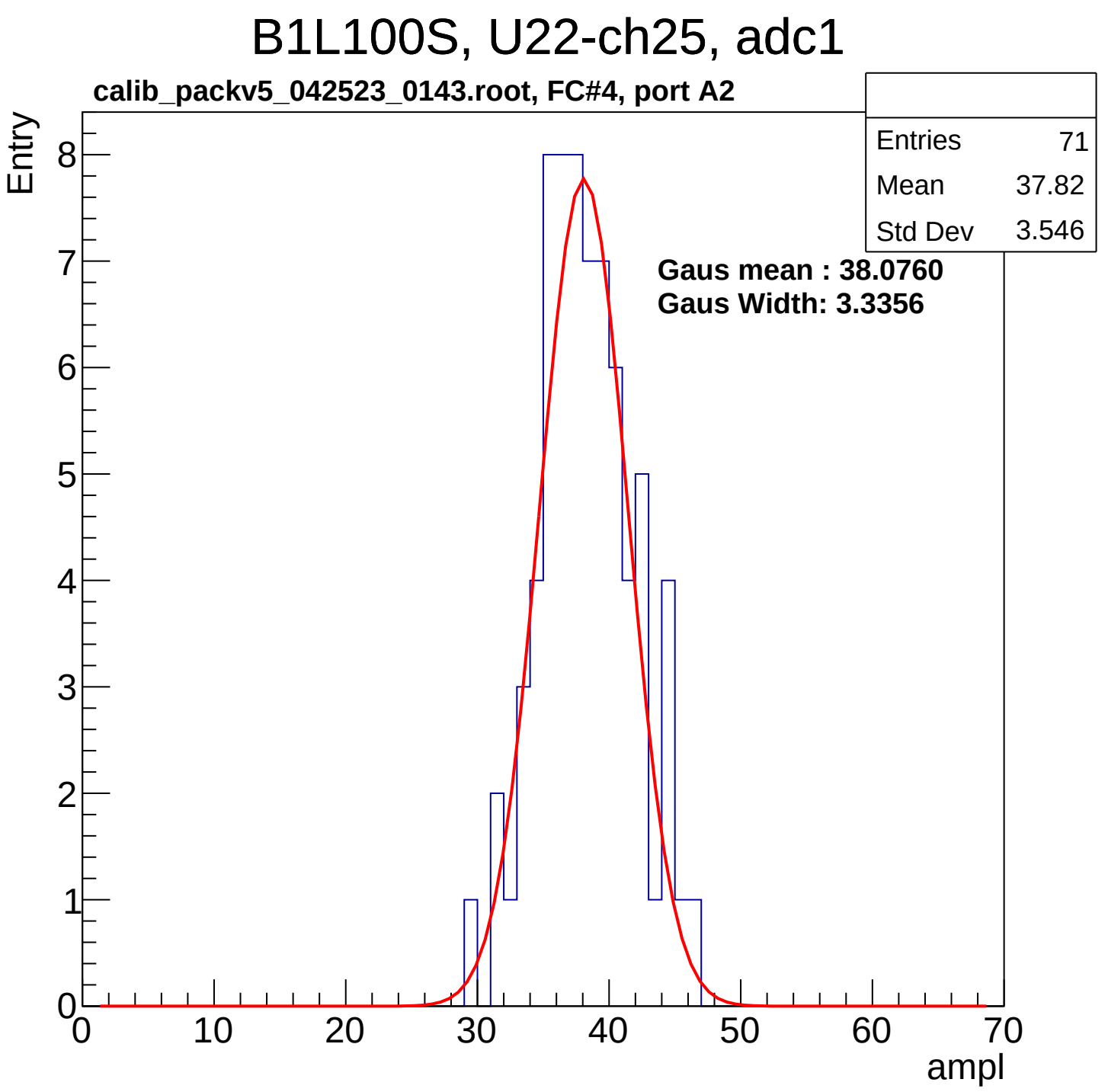
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch25, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.82
Std Dev 3.546
Gaus mean : 38.0760
Gaus Width: 3.3356
Entry
ampl
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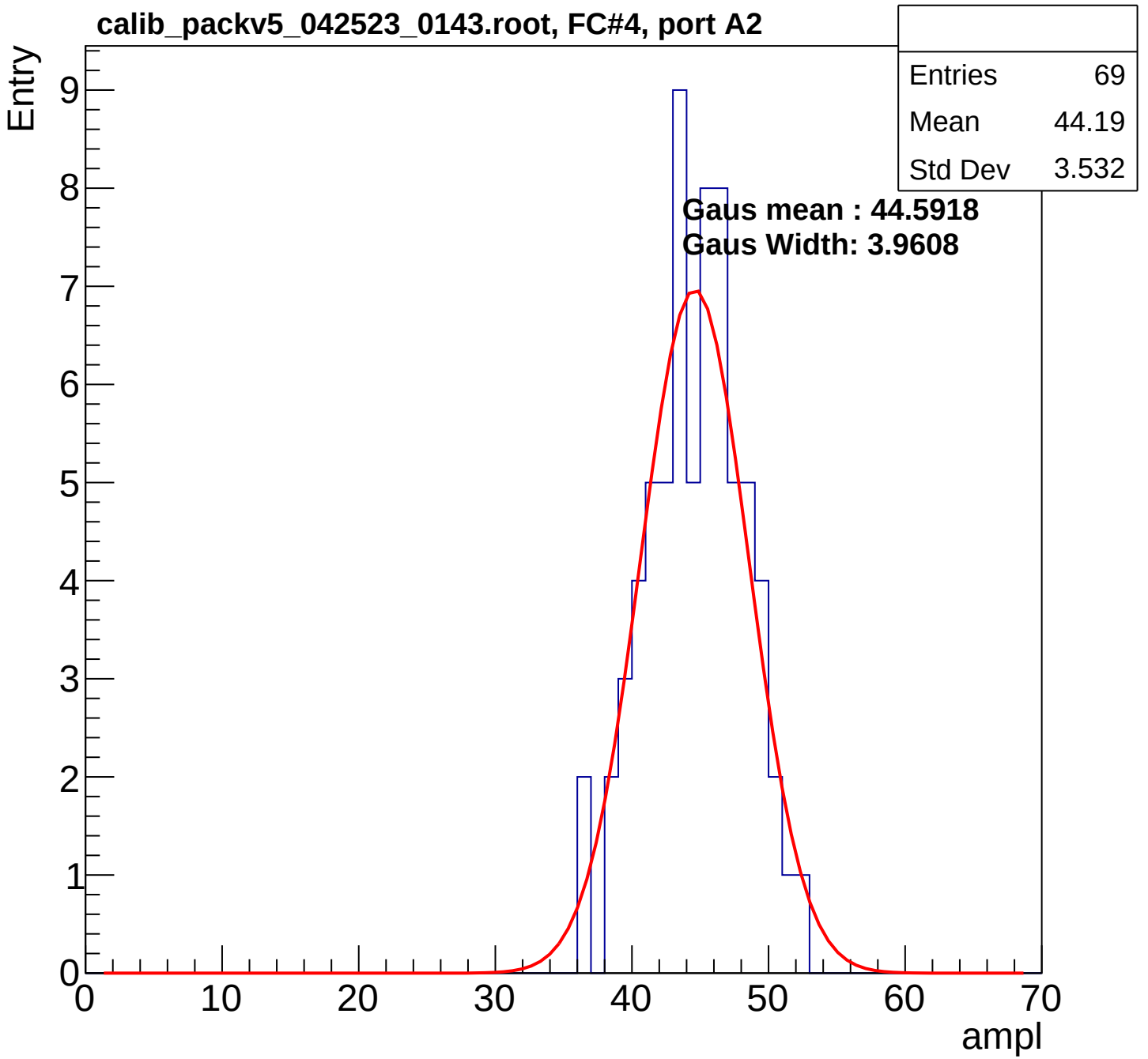
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch25, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 44.19
Std Dev 3.532
Gaus mean : 44.5918
Gaus Width: 3.9608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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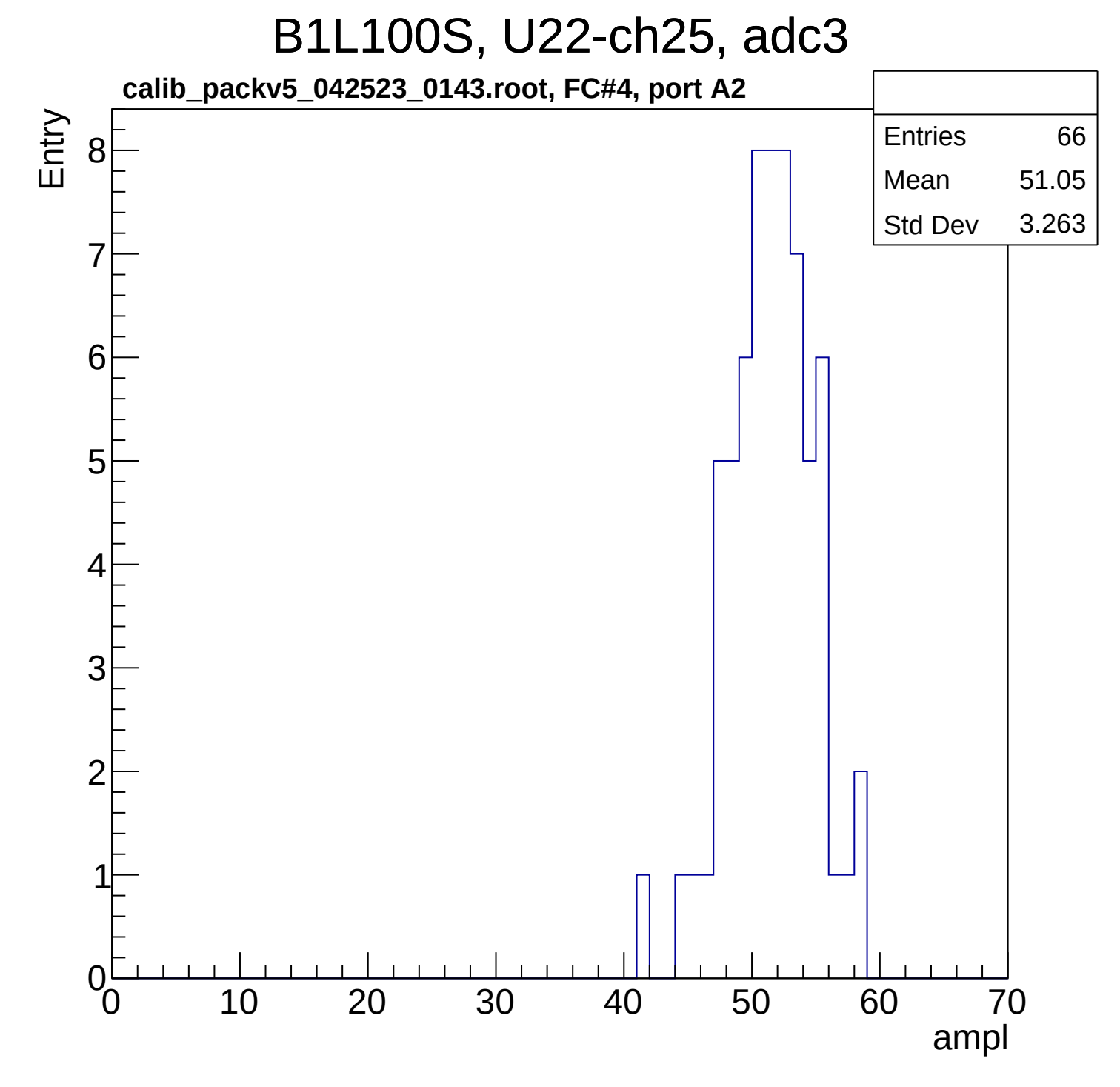
B1L100S, U22-ch25, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 51.05
Std Dev 3.263
Entry
ampl
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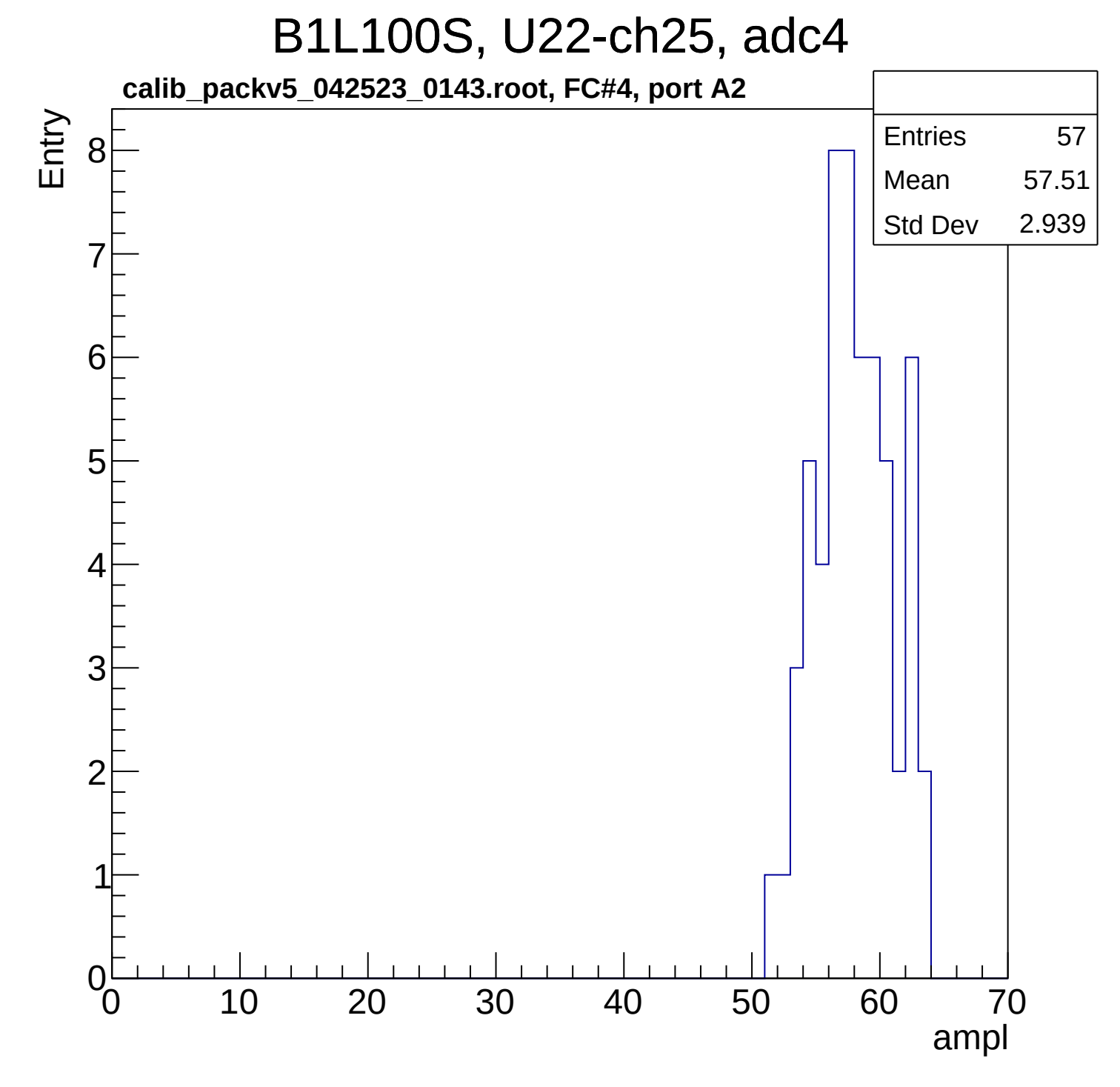
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch25, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 57.51
Std Dev 2.939
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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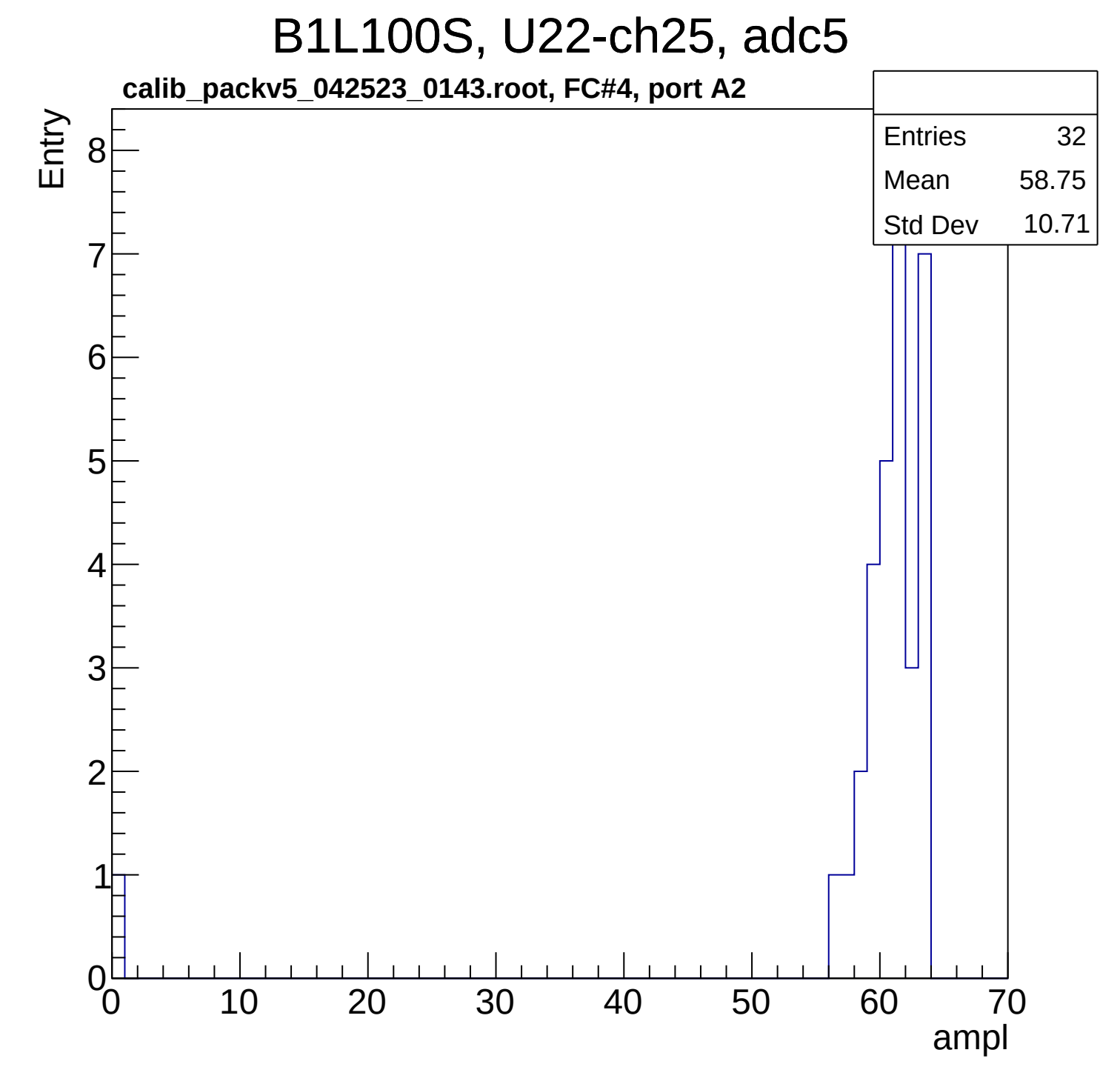
40
50
60
70


B1L100S, U22-ch25, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 58.75
Std Dev 10.71
Entry
ampl
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1
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4
5
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7
8
0
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40
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60
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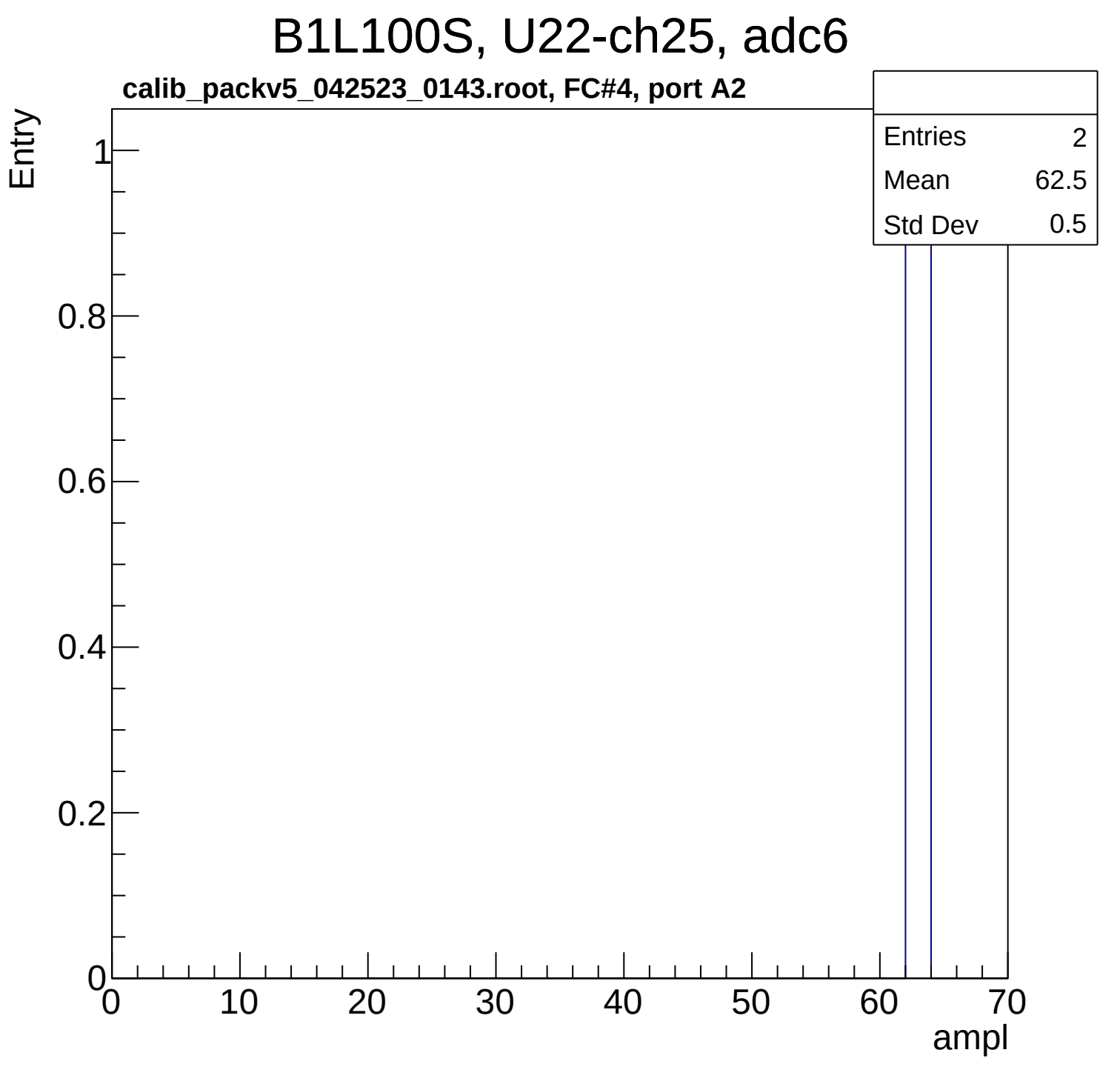
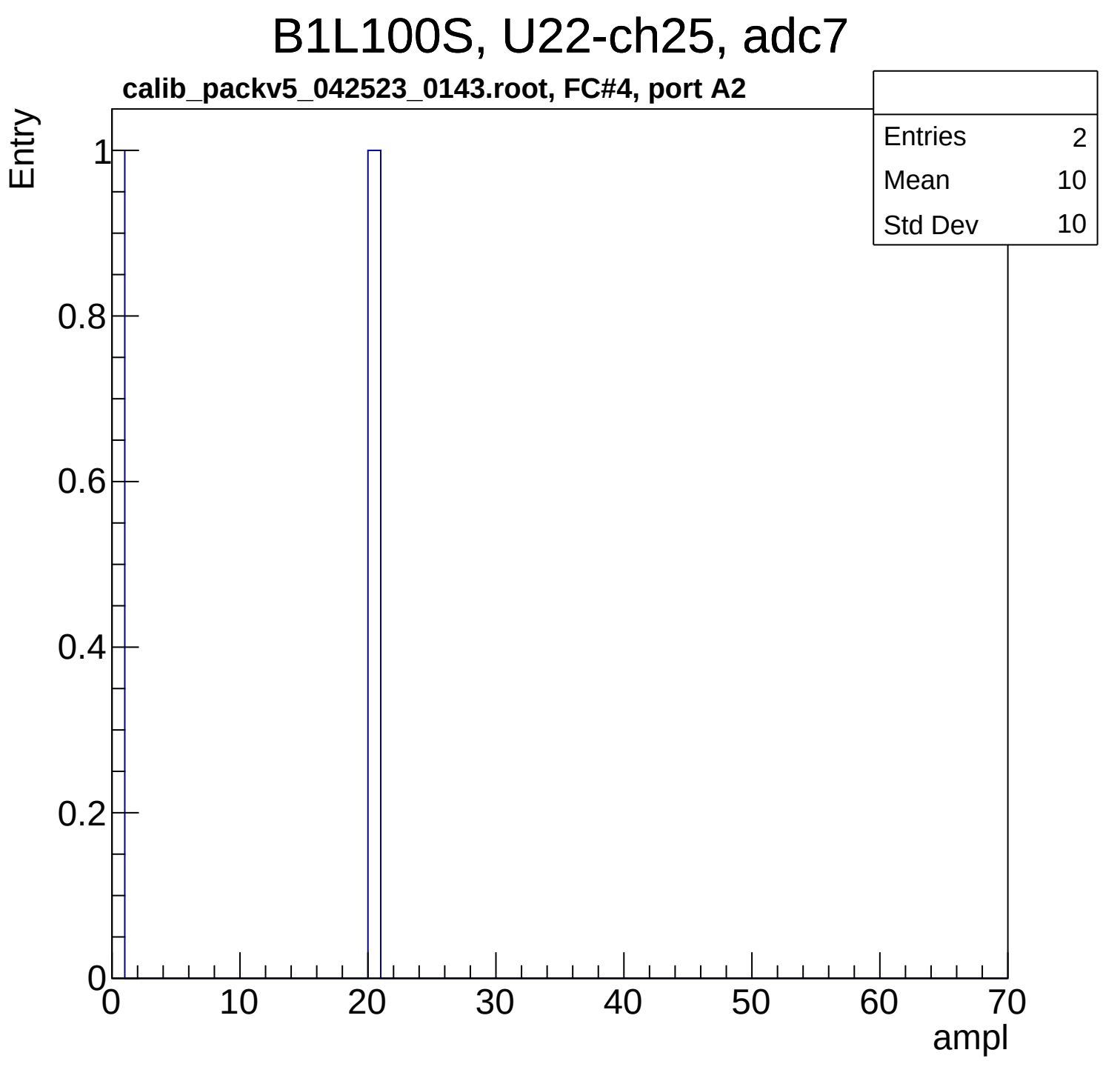

B1L100S, U22-ch25, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch25, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
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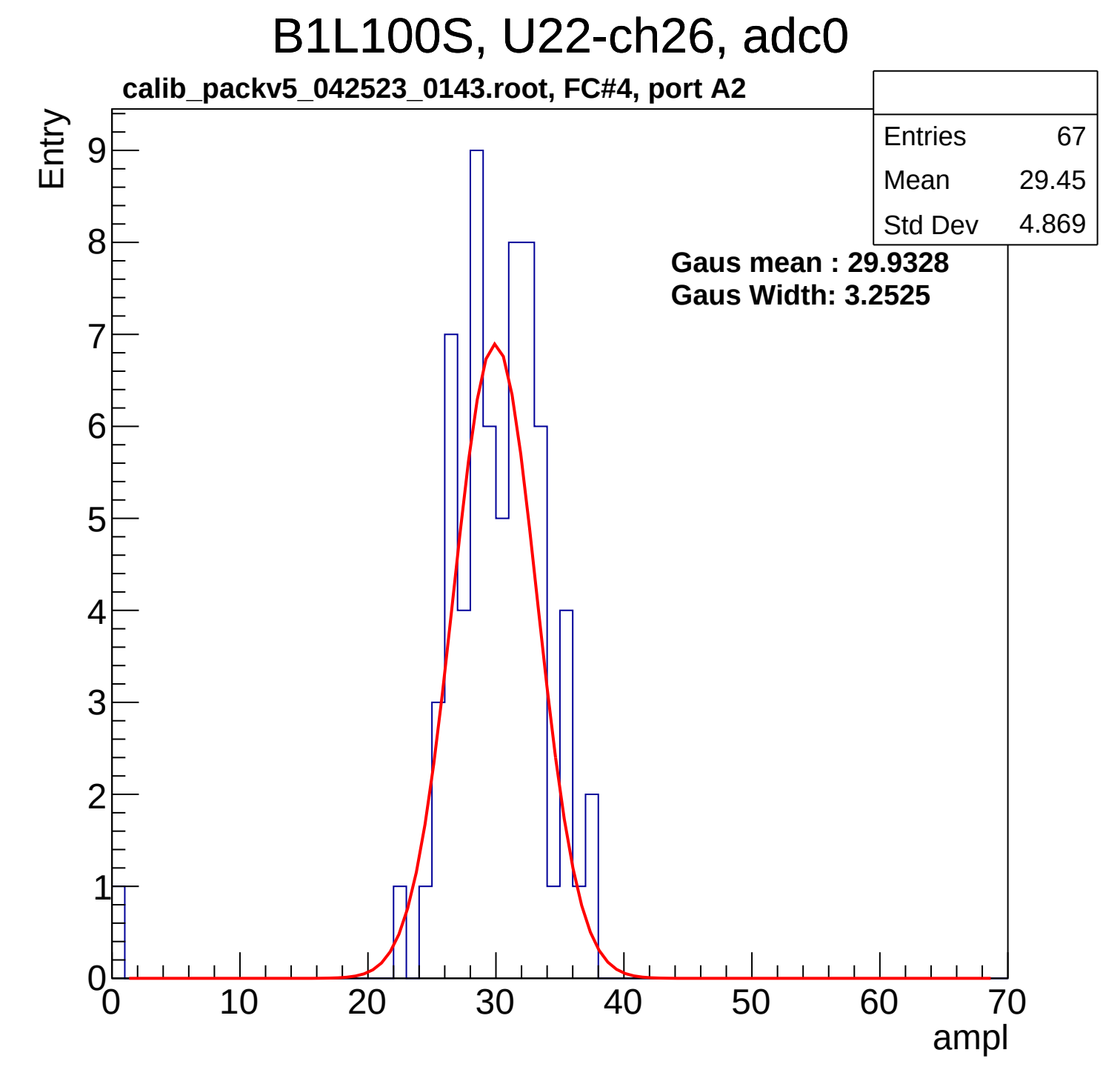
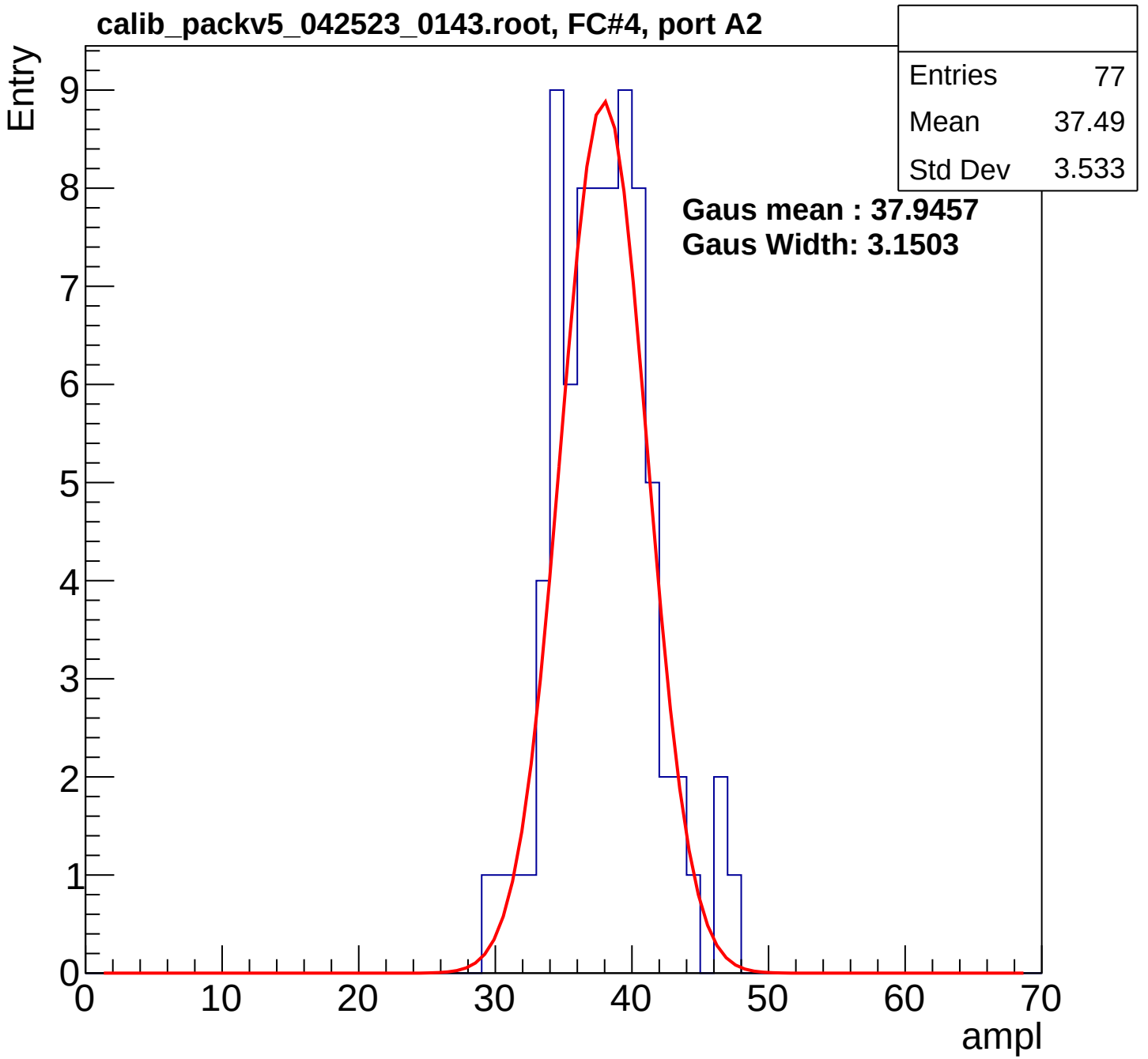
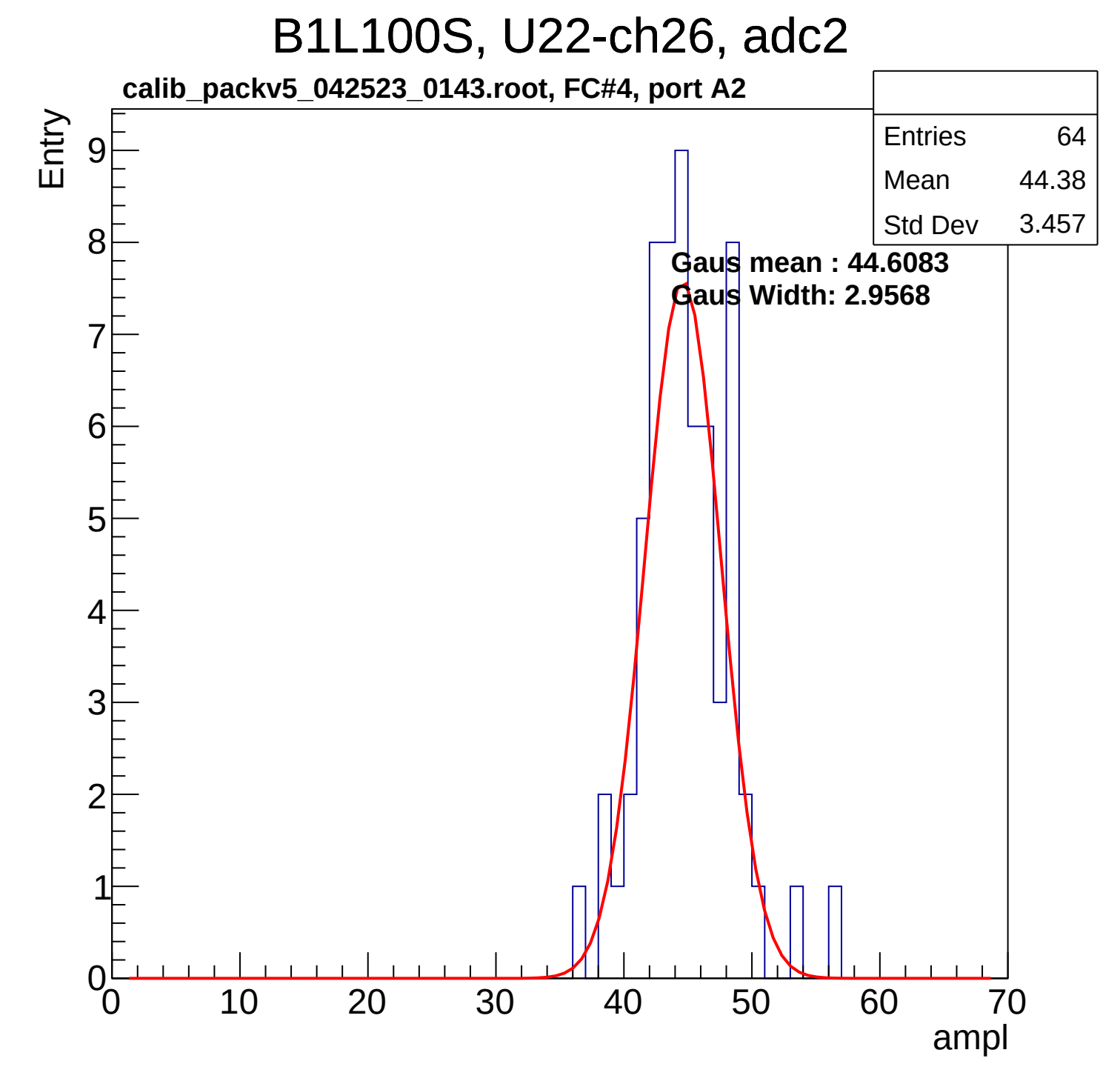
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch26, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.45
Std Dev 4.869
Gaus mean : 29.9328
Gaus Width: 3.2525
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch26, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 37.49
Std Dev 3.533
Gaus mean : 37.9457
Gaus Width: 3.1503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch26, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 44.38
Std Dev 3.457
Gaus mean : 44.6083
Gaus Width: 2.9568
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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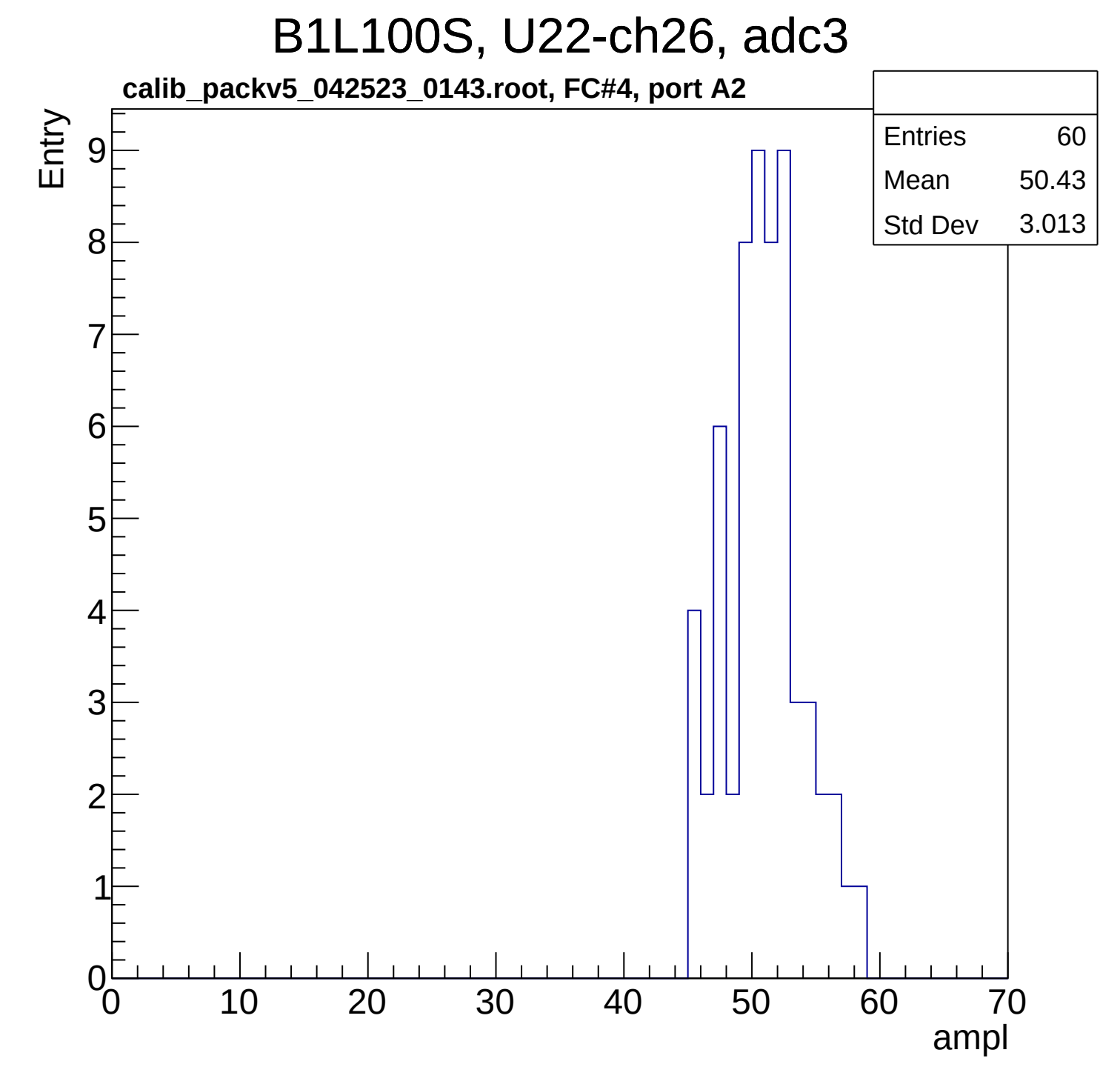
B1L100S, U22-ch26, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 50.43
Std Dev 3.013
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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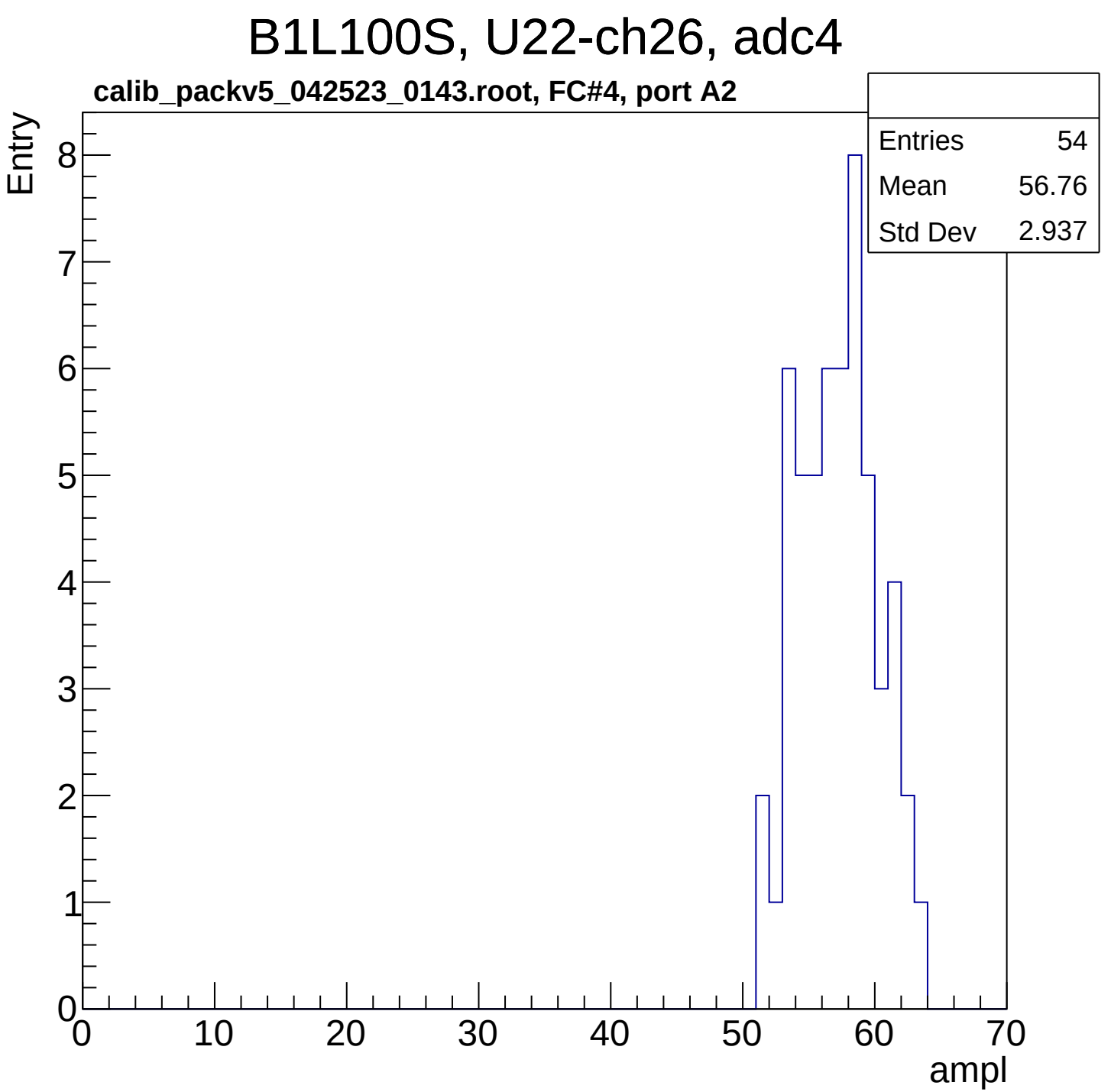
B1L100S, U22-ch26, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.76
Std Dev 2.937
Entry
ampl
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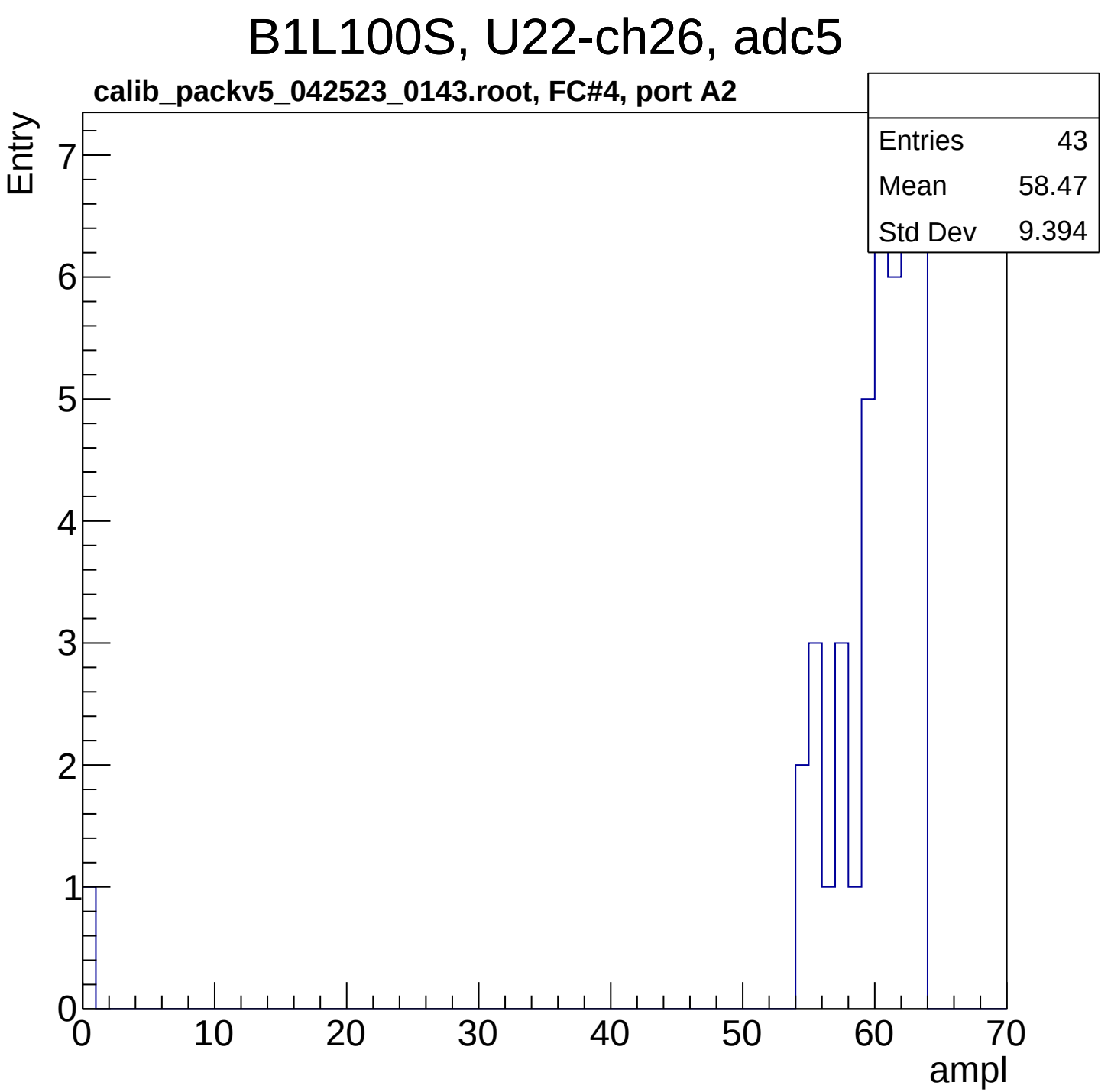
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch26, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.47
Std Dev 9.394
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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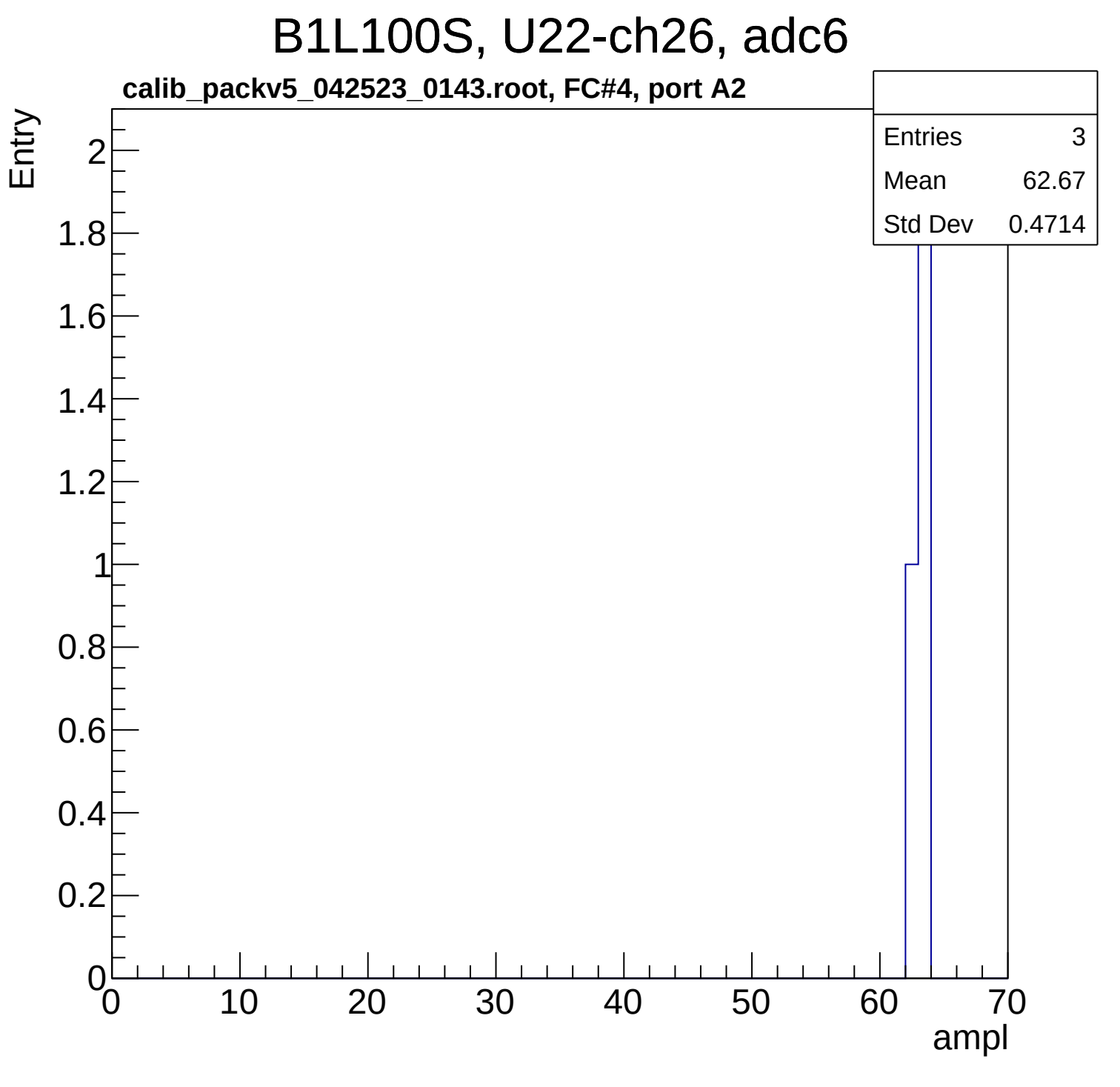
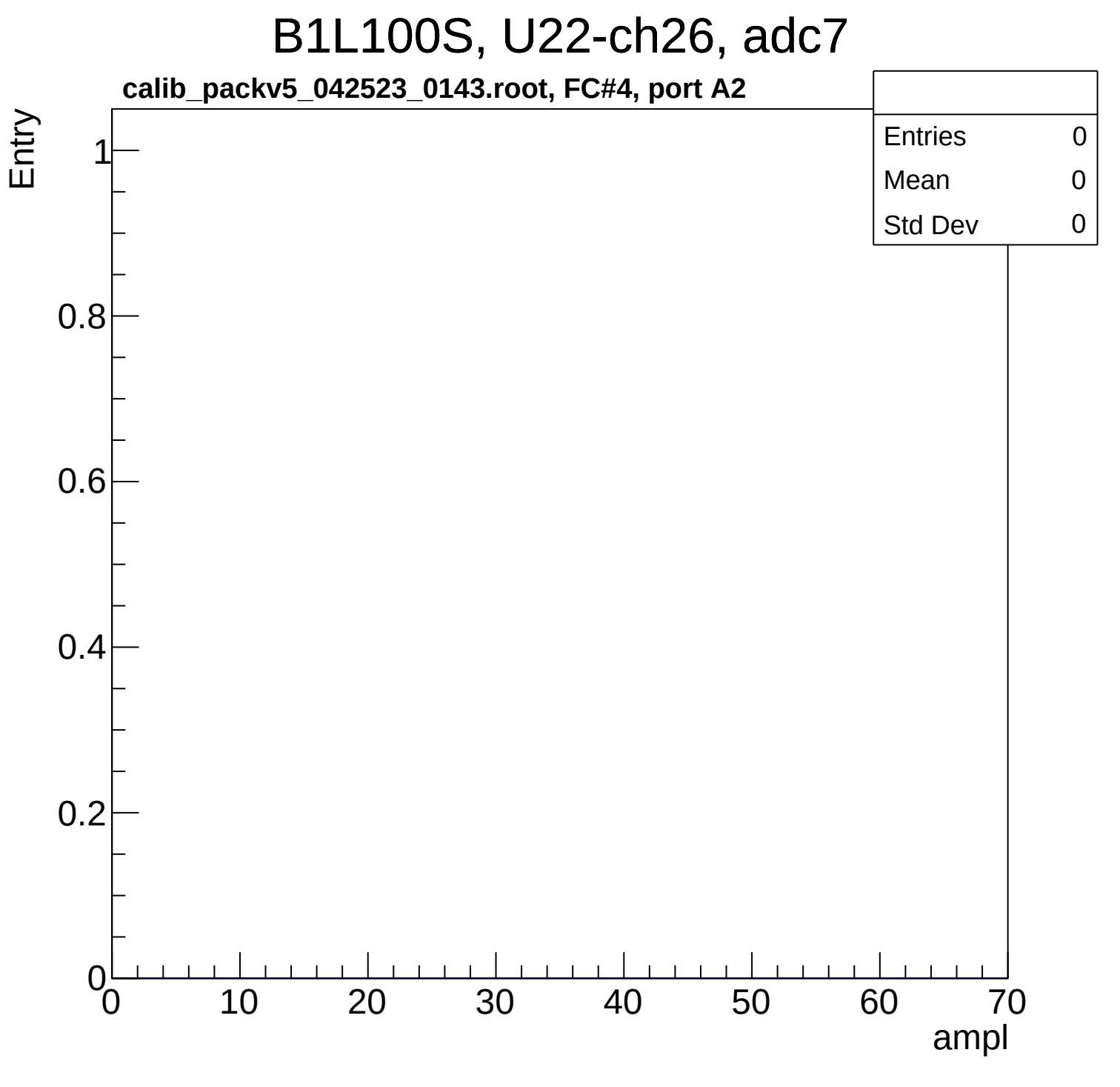

B1L100S, U22-ch26, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch26, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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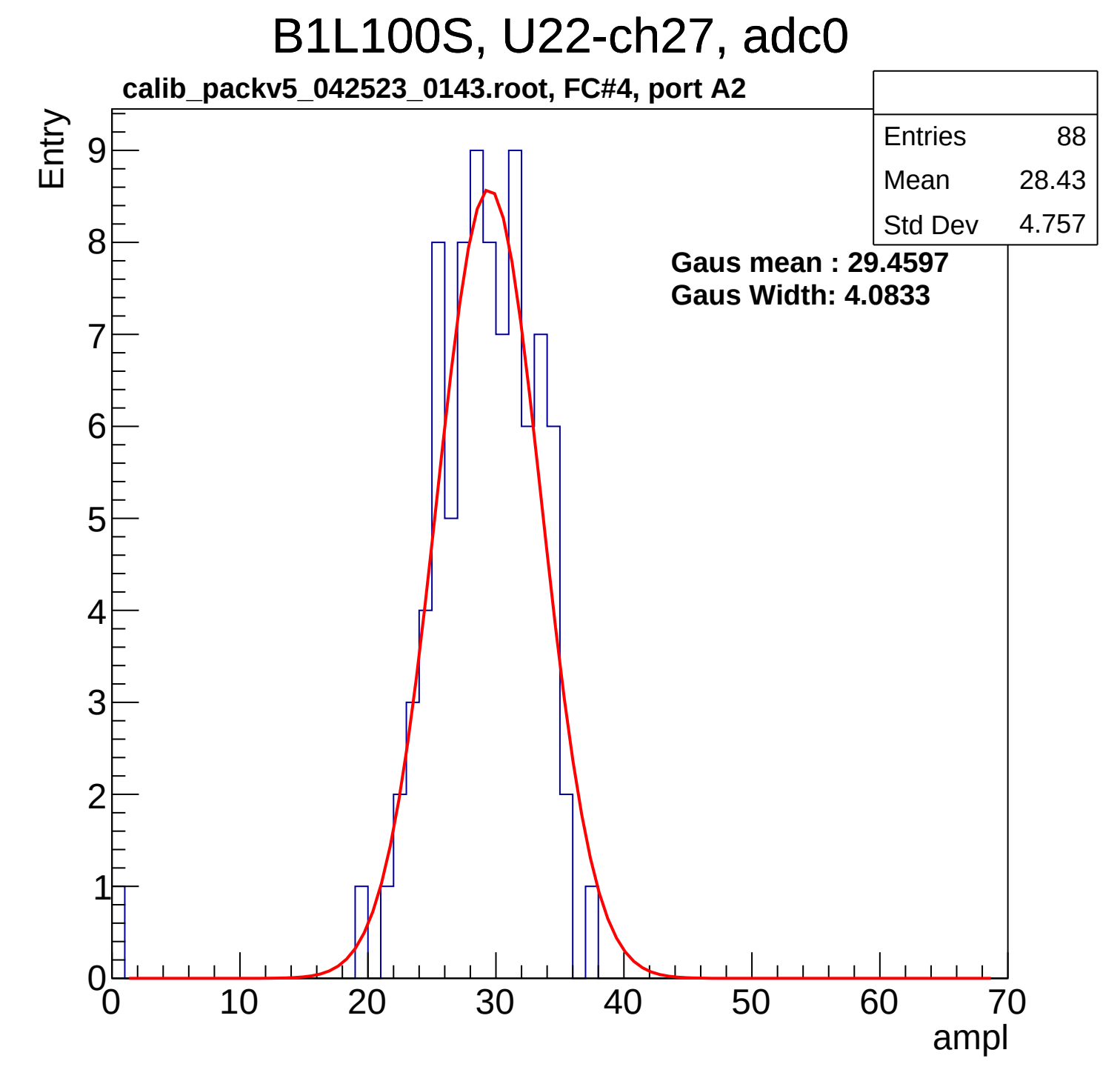
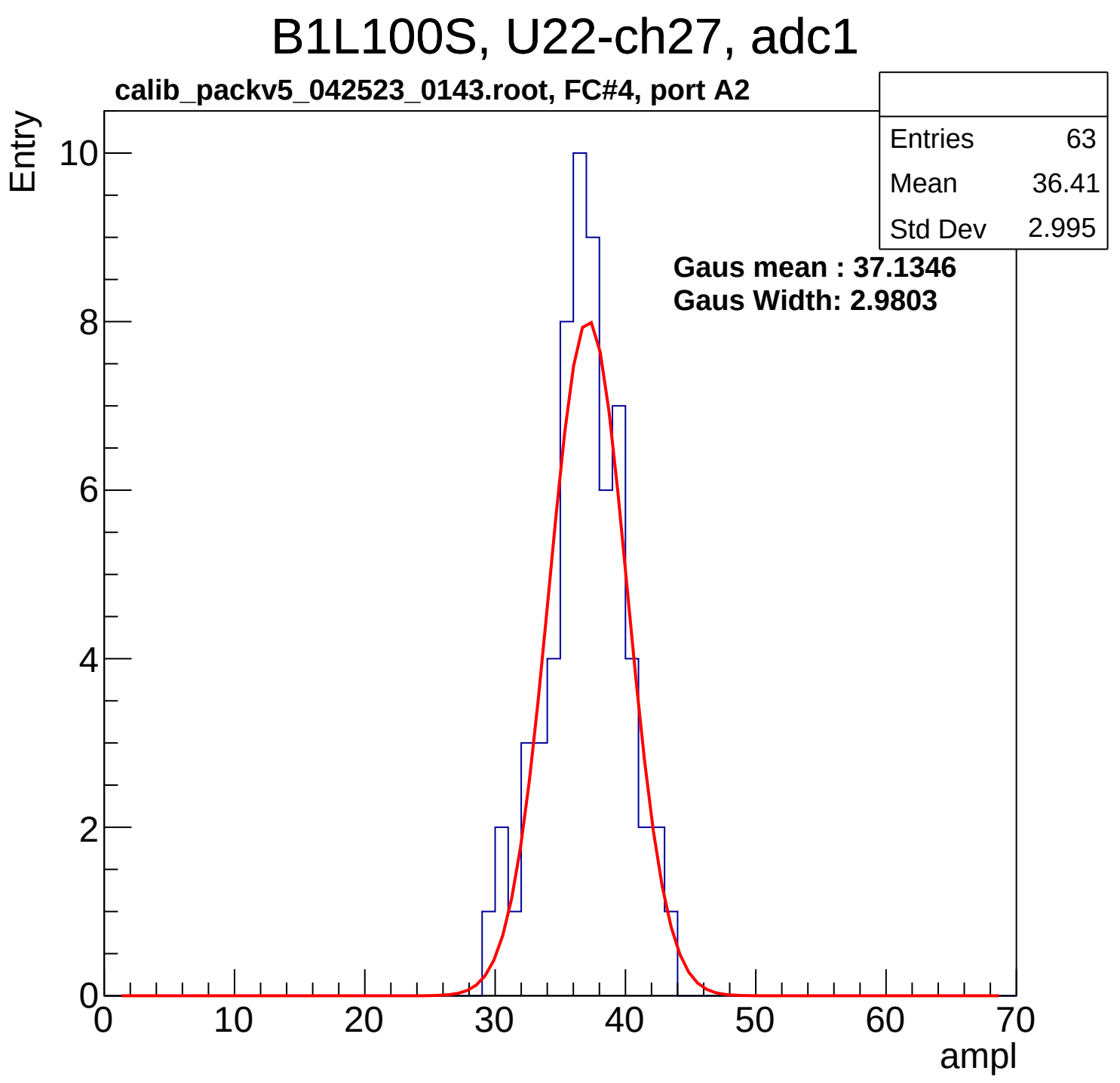
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch27, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 28.43
Std Dev 4.757
Gaus mean : 29.4597
Gaus Width: 4.0833
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch27, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 36.41
Std Dev 2.995
Gaus mean : 37.1346
Gaus Width: 2.9803
Entry
ampl
0
2
4
6
8
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0
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40
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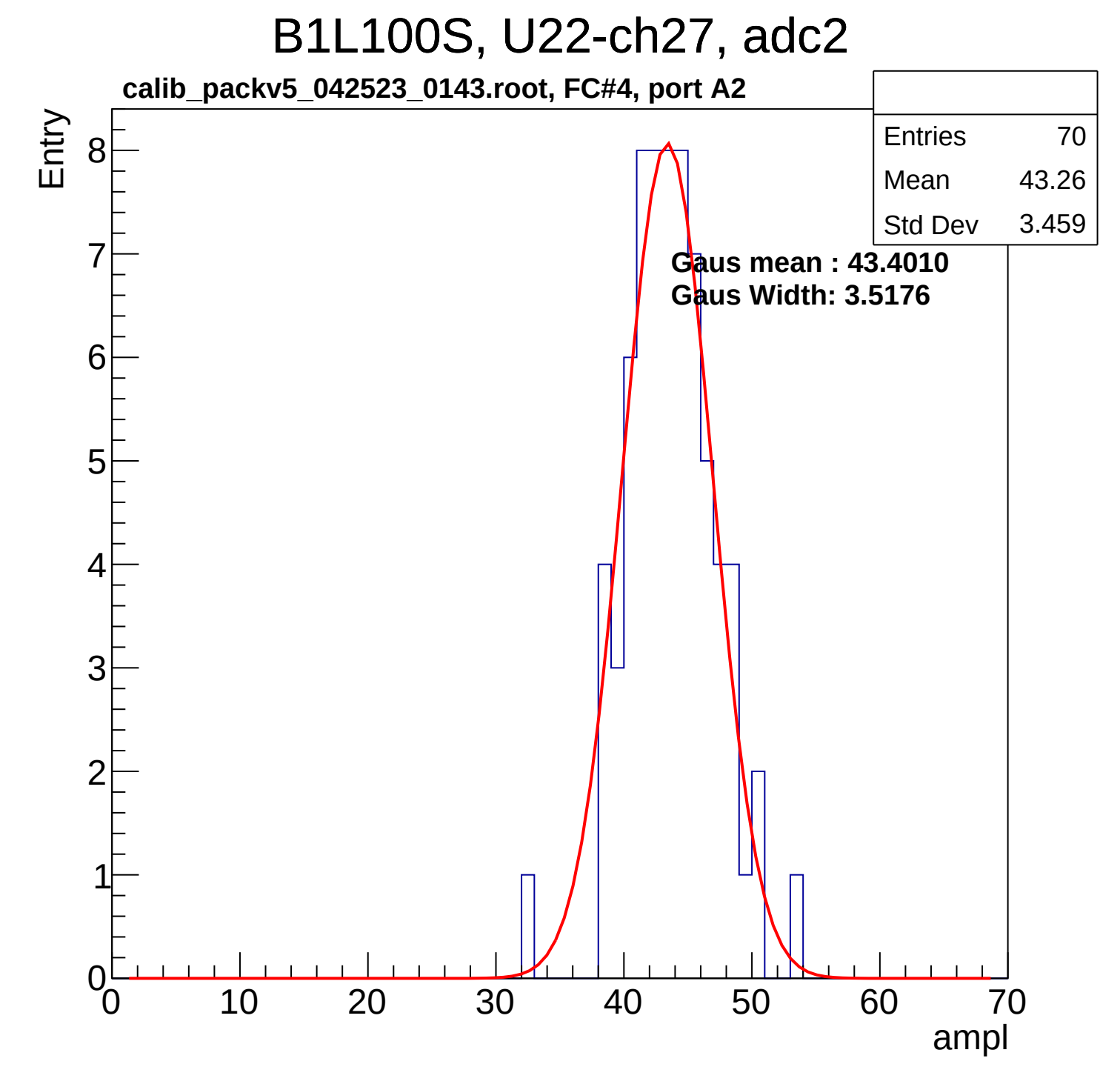
B1L100S, U22-ch27, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 43.26
Std Dev 3.459
Gaus mean : 43.4010
Gaus Width: 3.5176
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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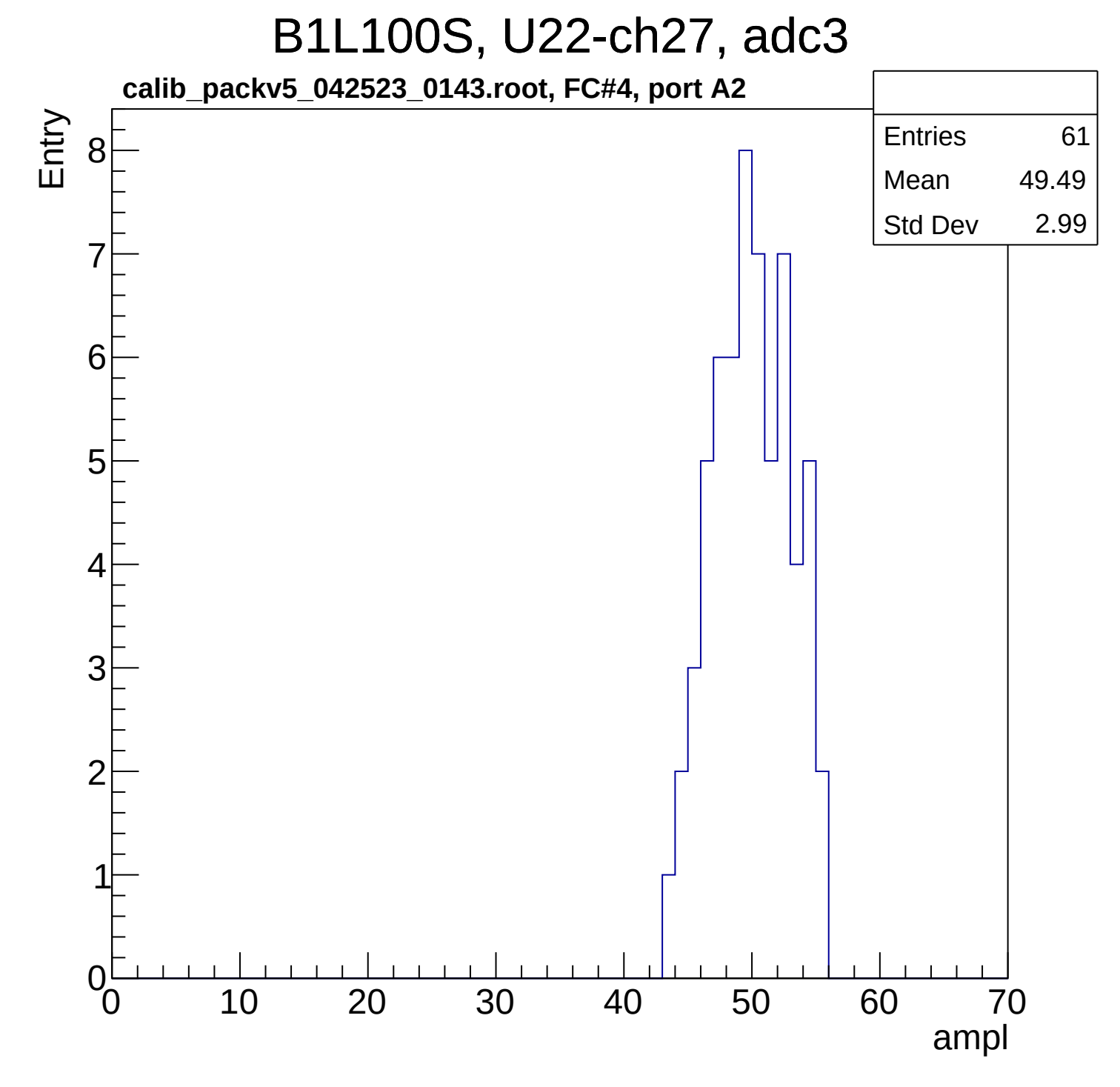
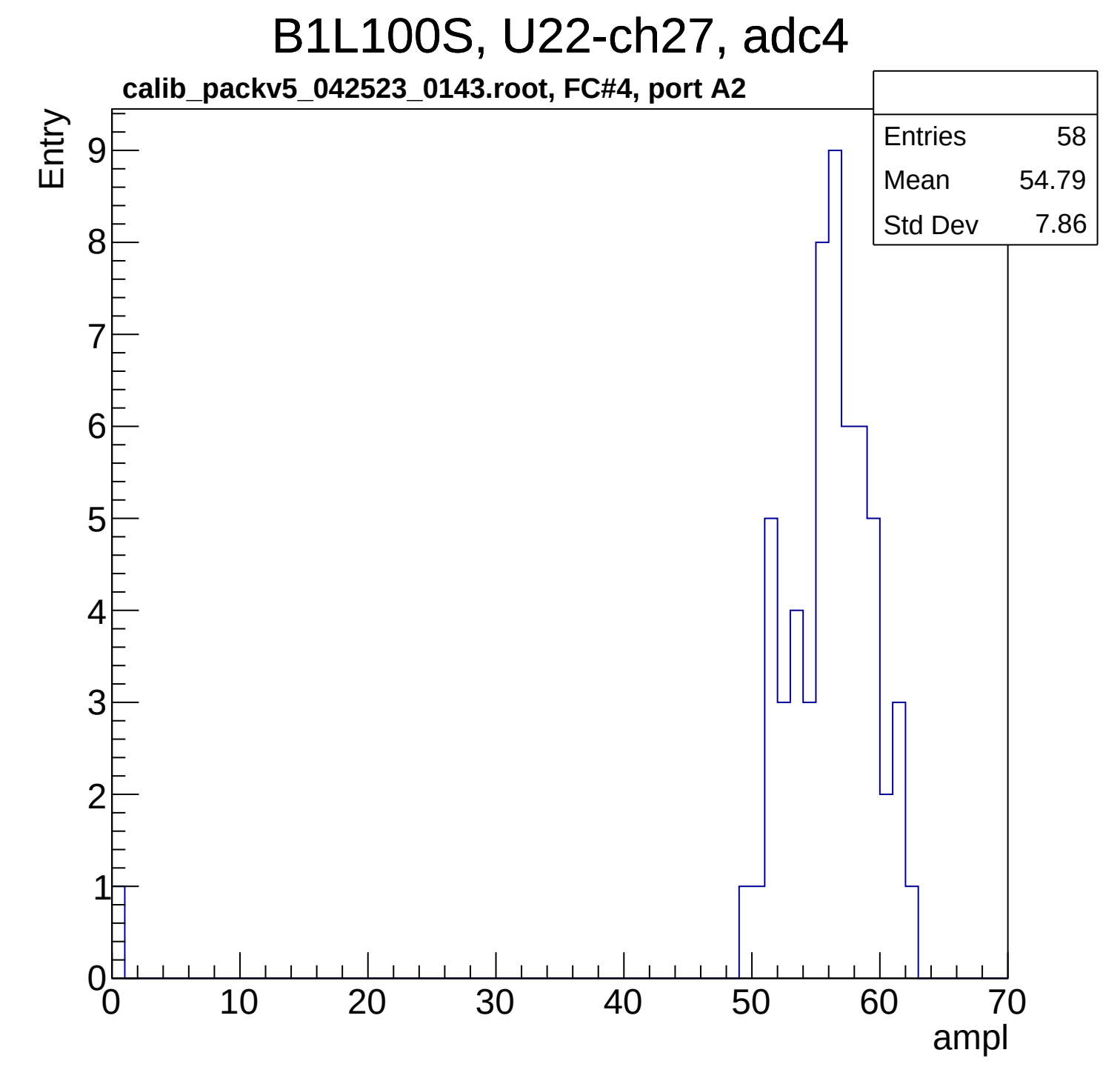

B1L100S, U22-ch27, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.49
Std Dev 2.99
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch27, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 54.79
Std Dev 7.86
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
50
60
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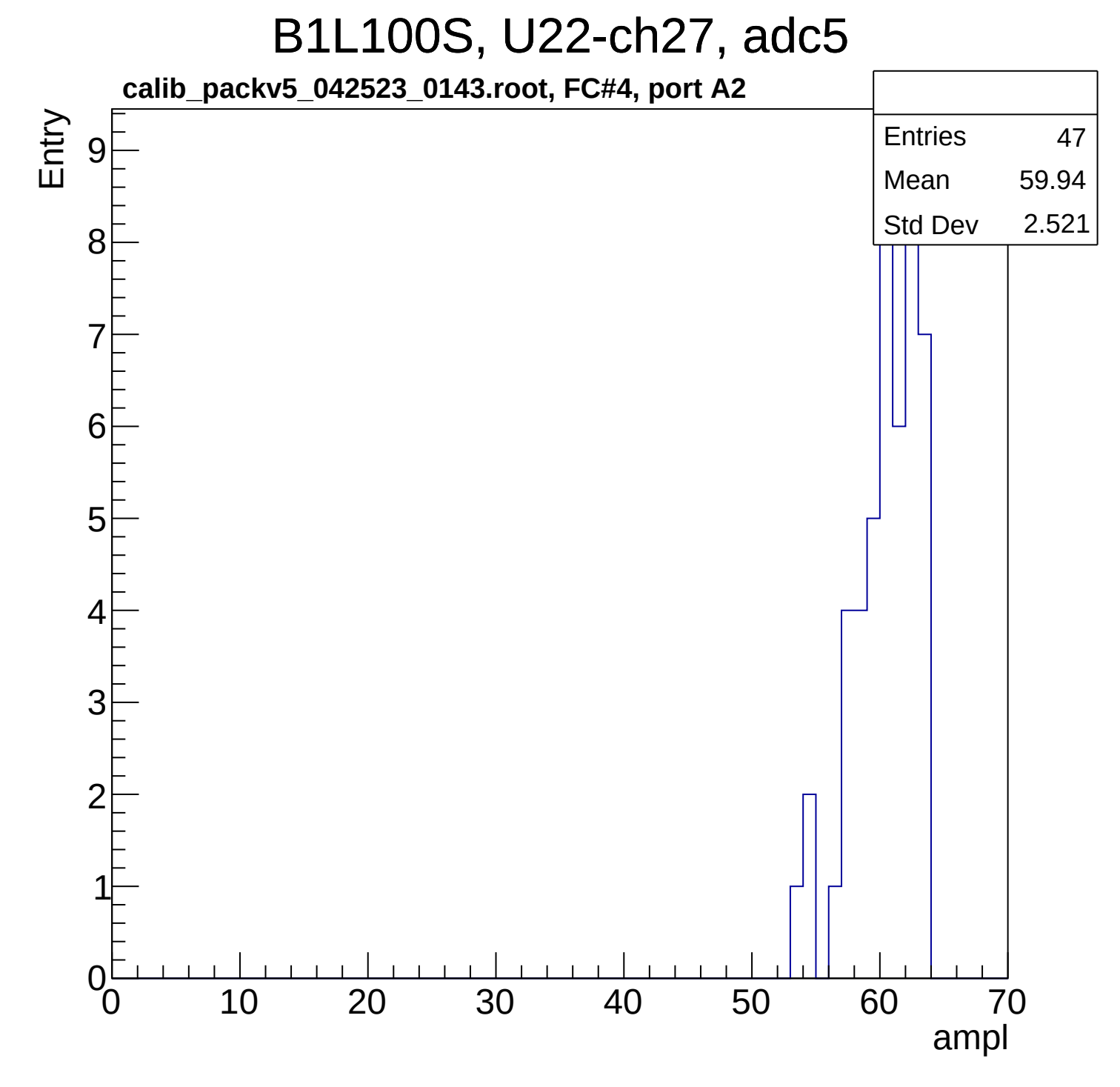
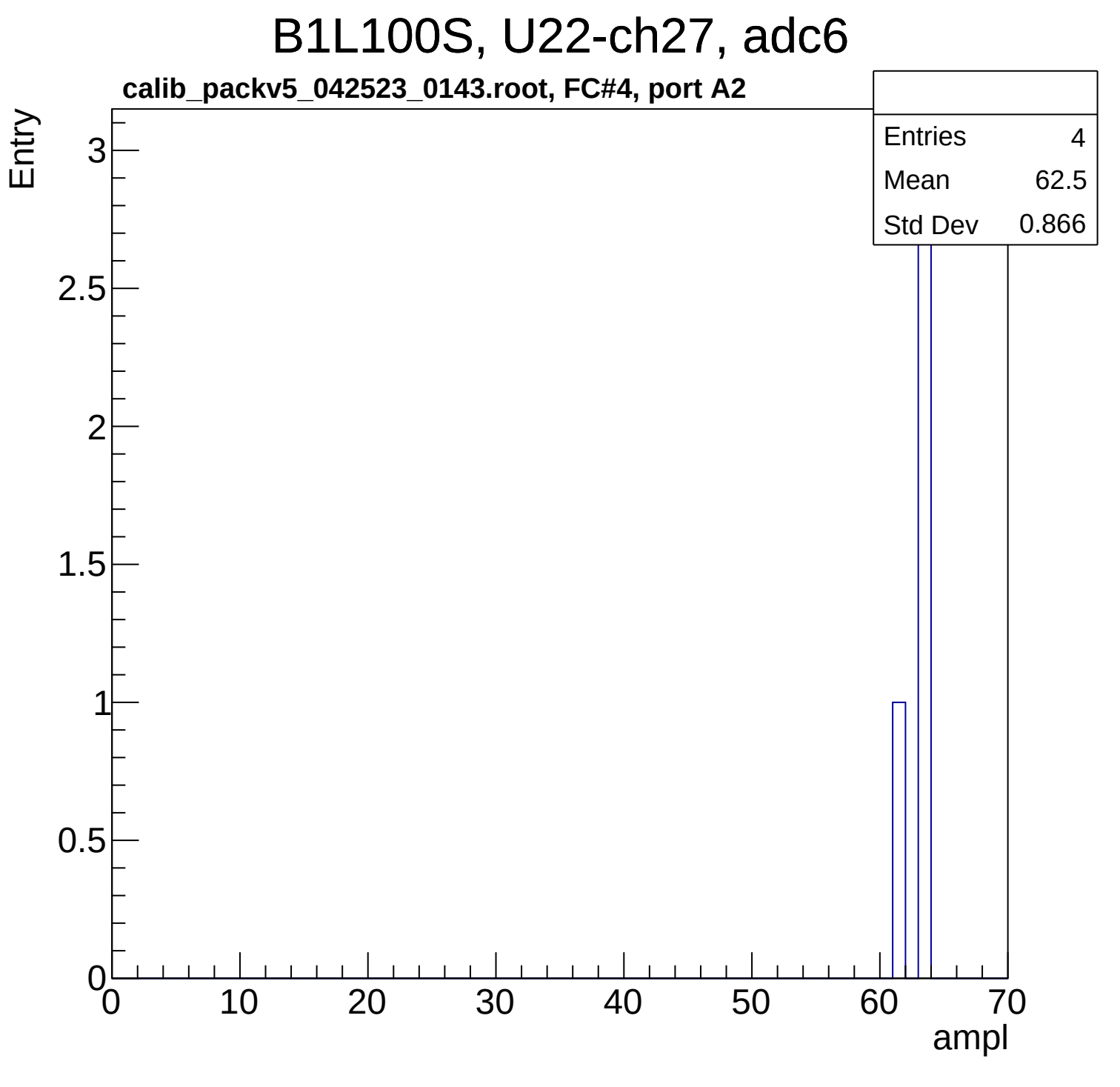
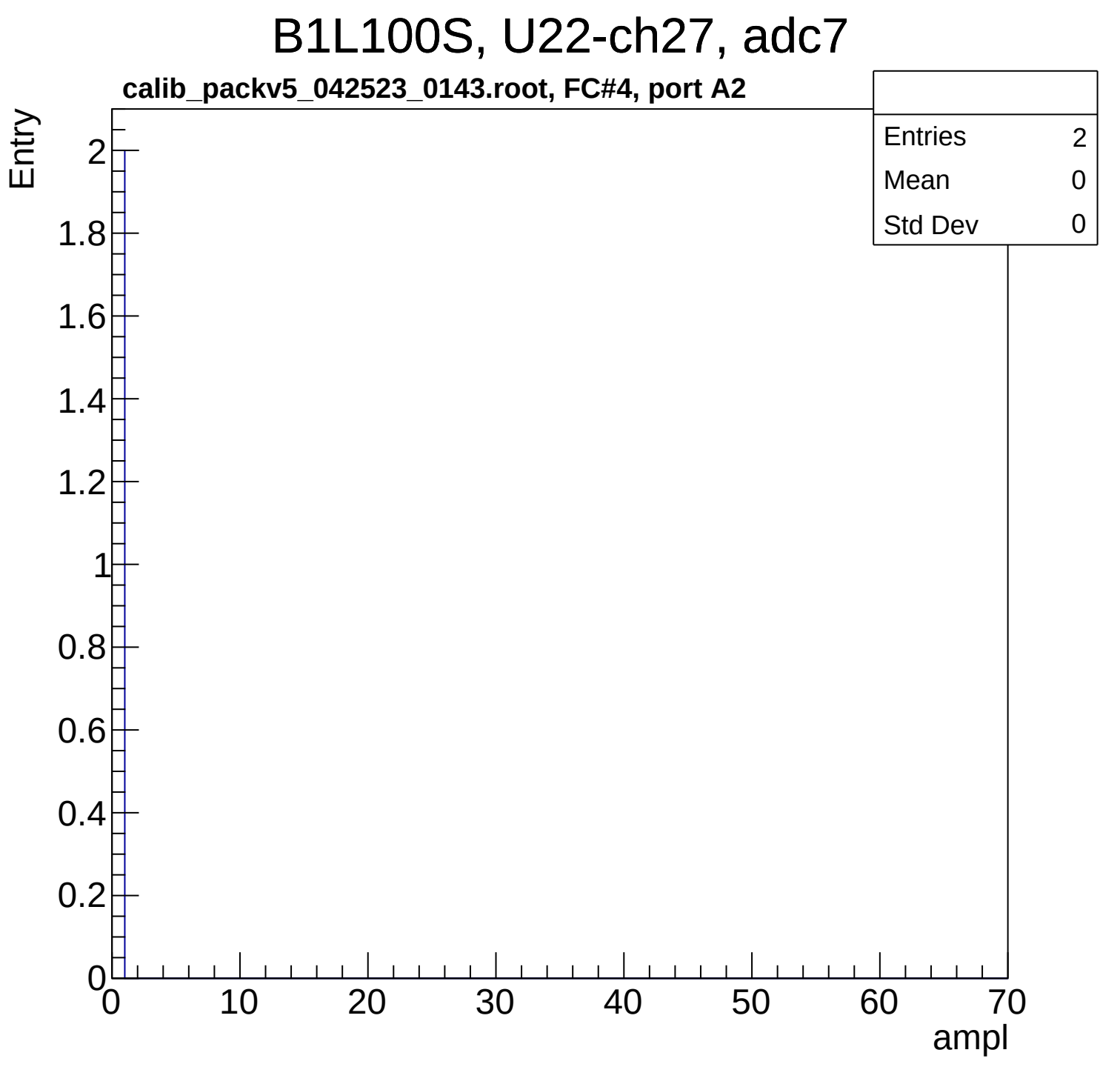
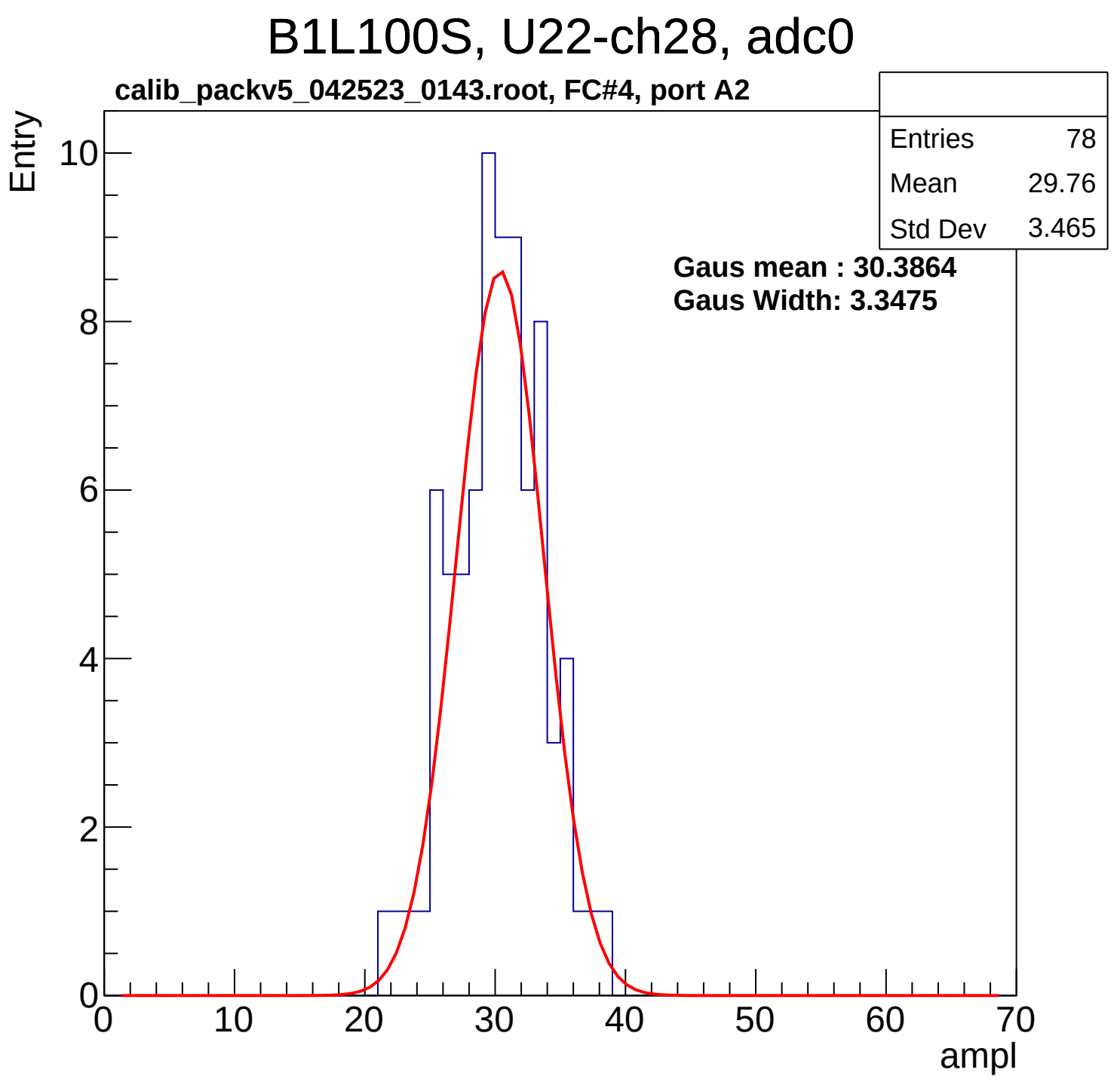
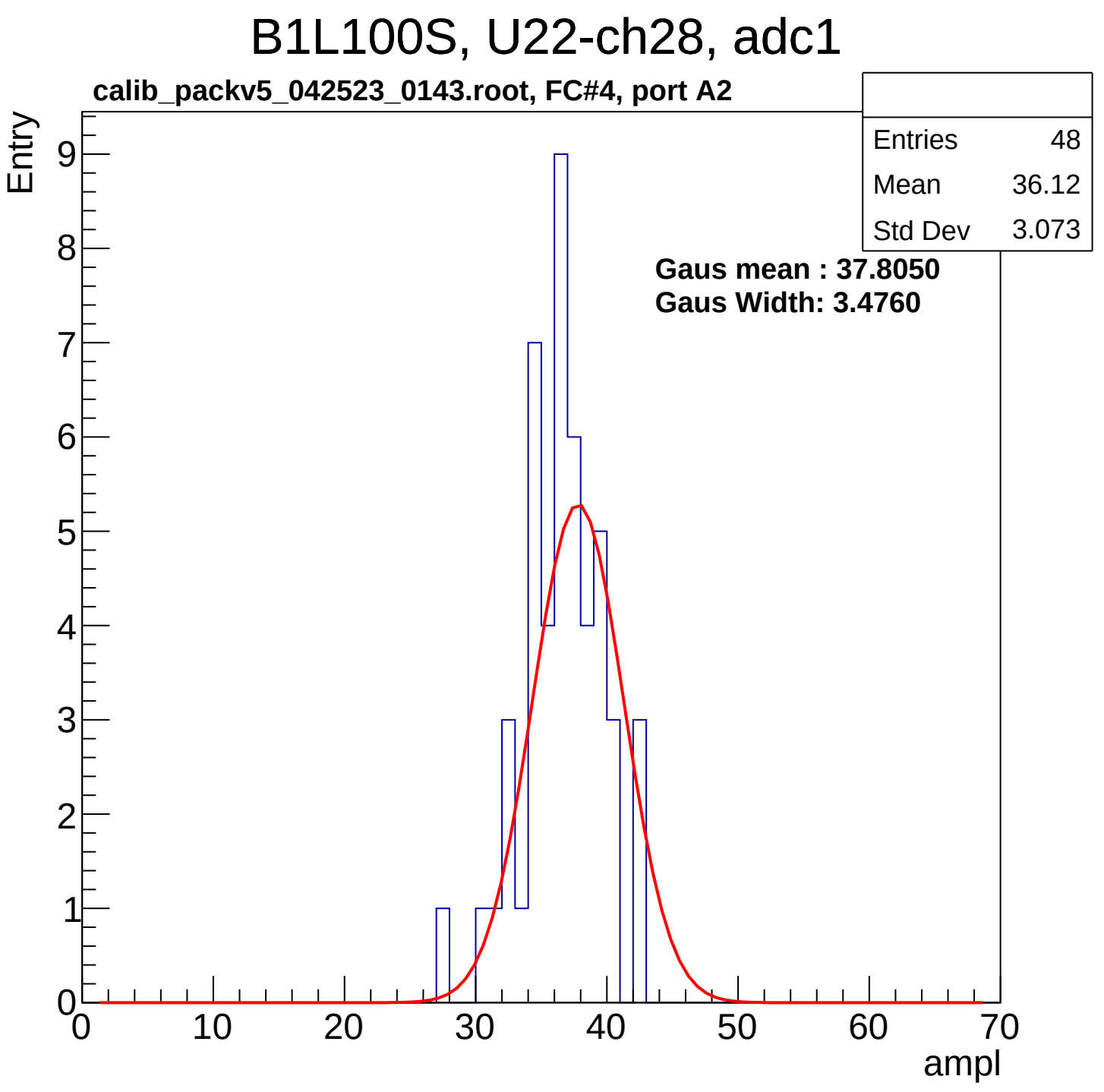
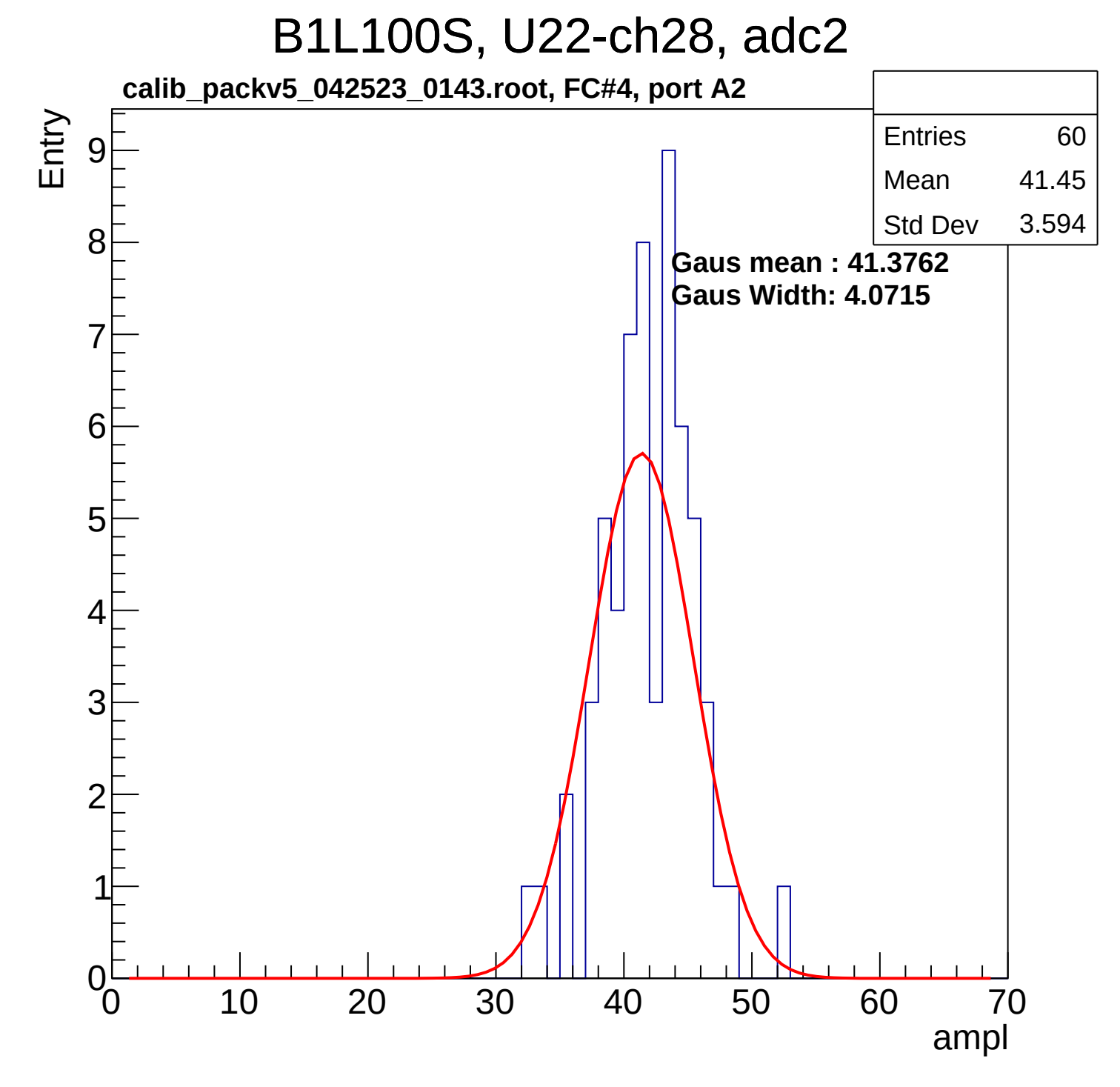

B1L100S, U22-ch27, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.94
Std Dev 2.521
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

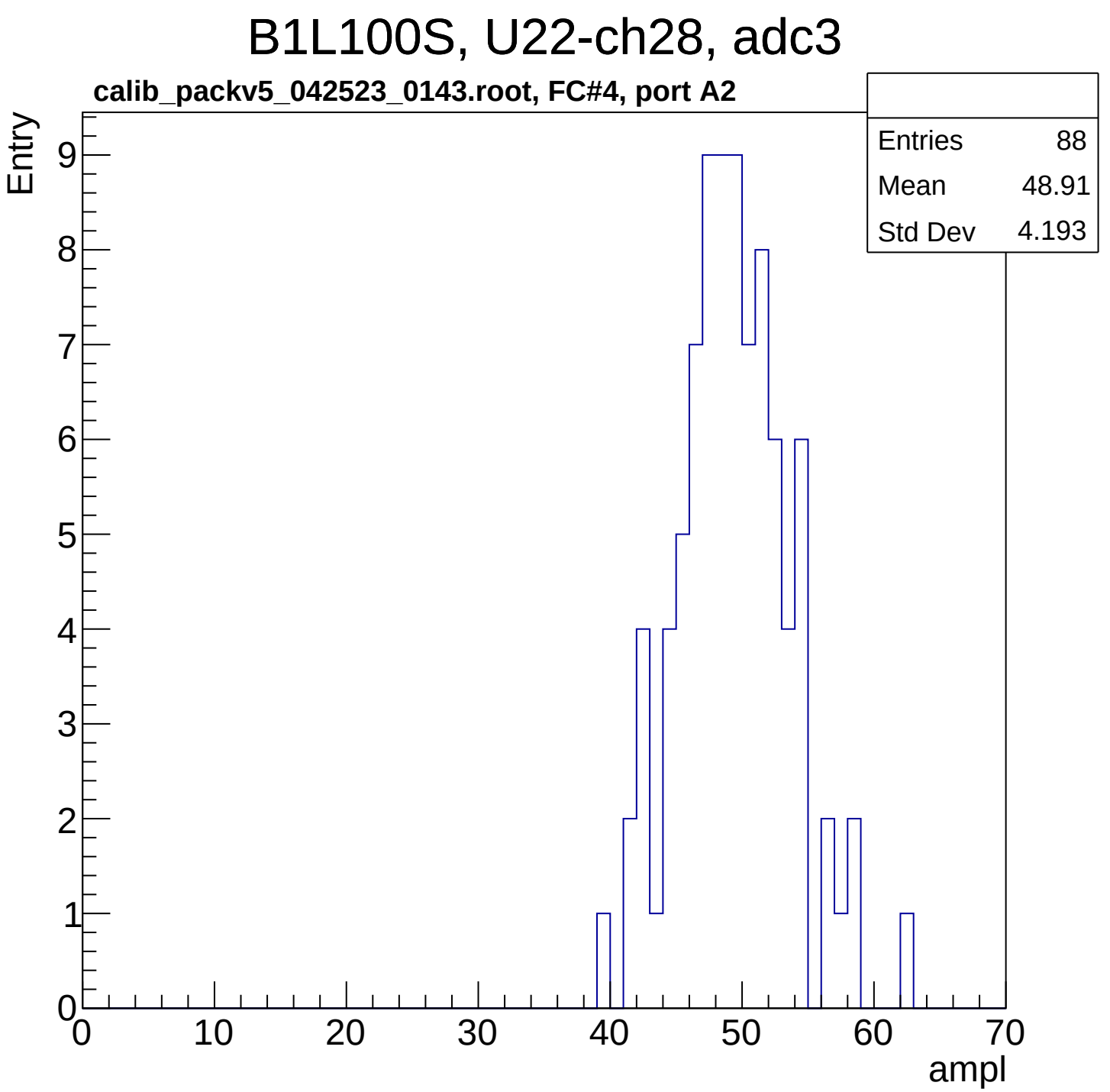

B1L100S, U22-ch27, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch27, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch28, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 29.76
Std Dev 3.465
Gaus mean : 30.3864
Gaus Width: 3.3475
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch28, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 36.12
Std Dev 3.073
Gaus mean : 37.8050
Gaus Width: 3.4760
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch28, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 41.45
Std Dev 3.594
Gaus mean : 41.3762
Gaus Width: 4.0715
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch28, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 48.91
Std Dev 4.193
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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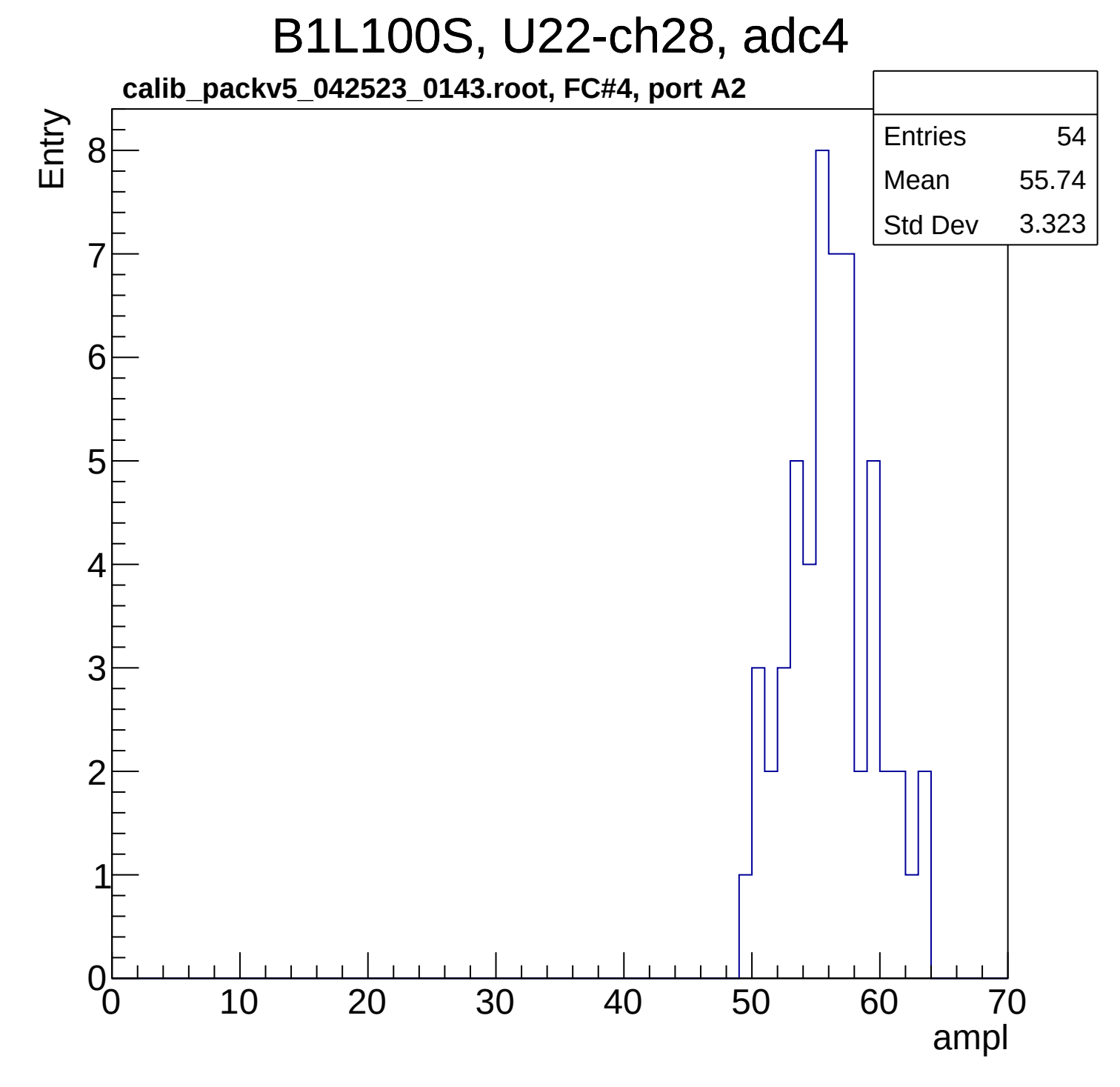
B1L100S, U22-ch28, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.74
Std Dev 3.323
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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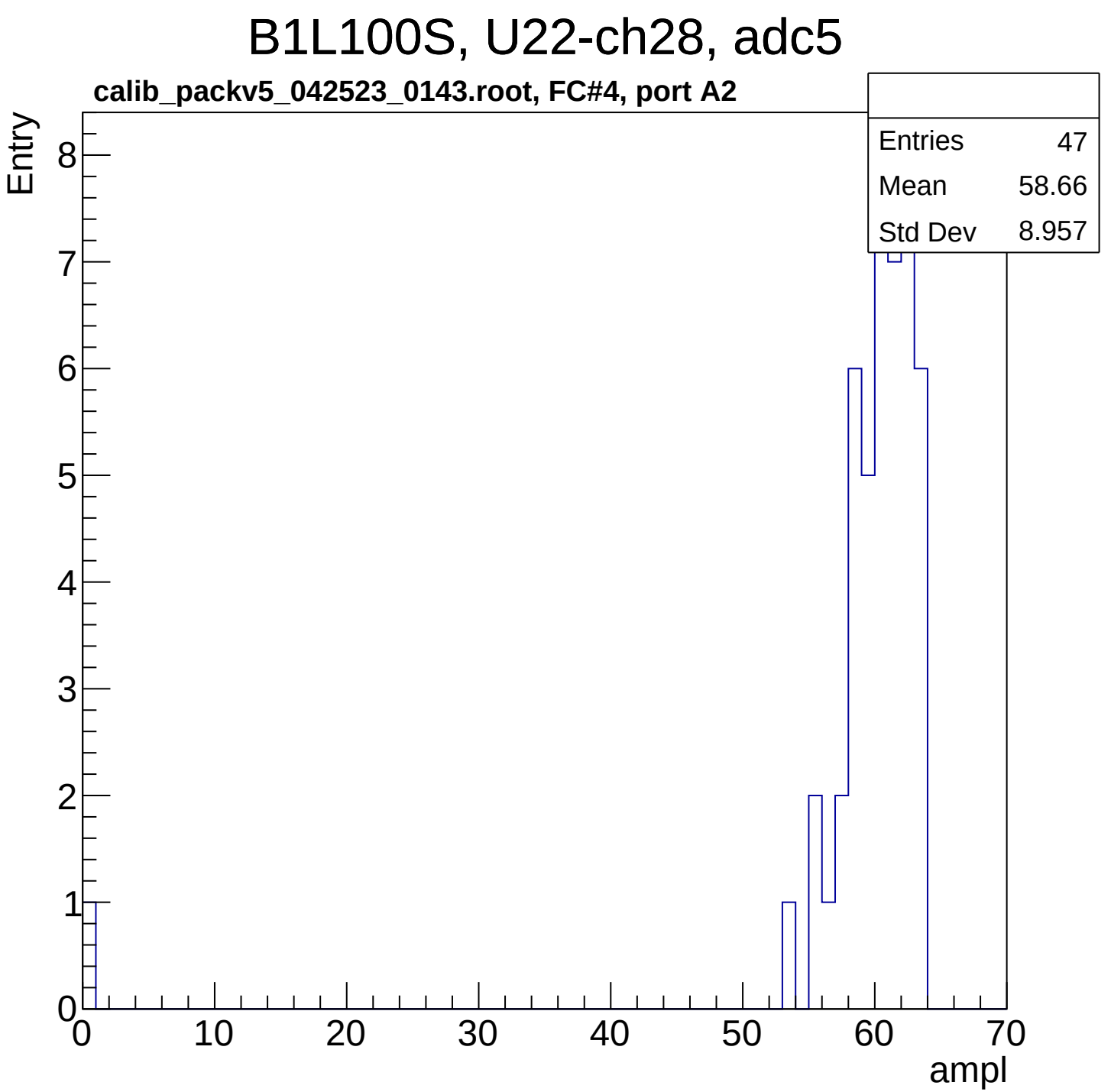
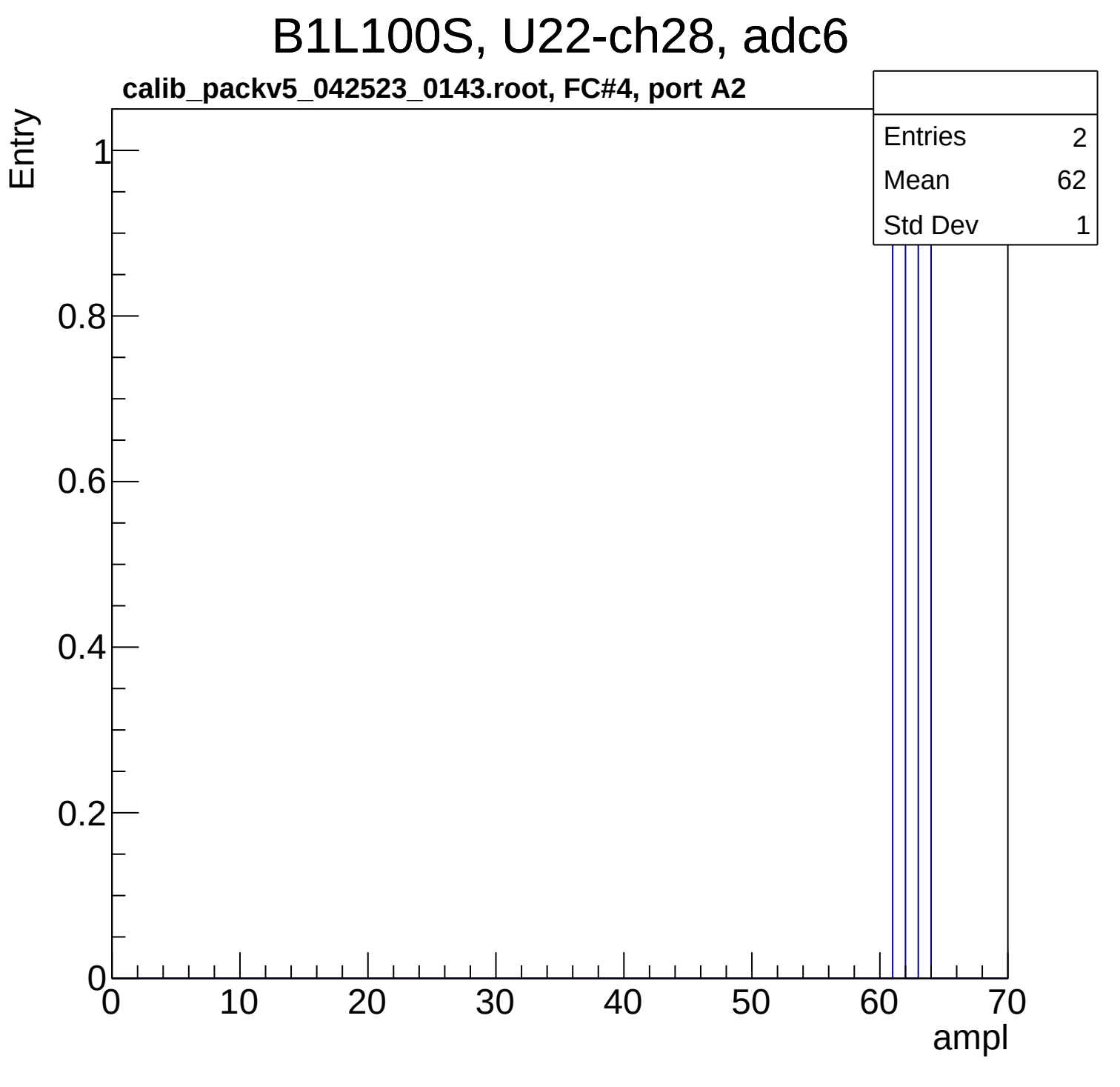
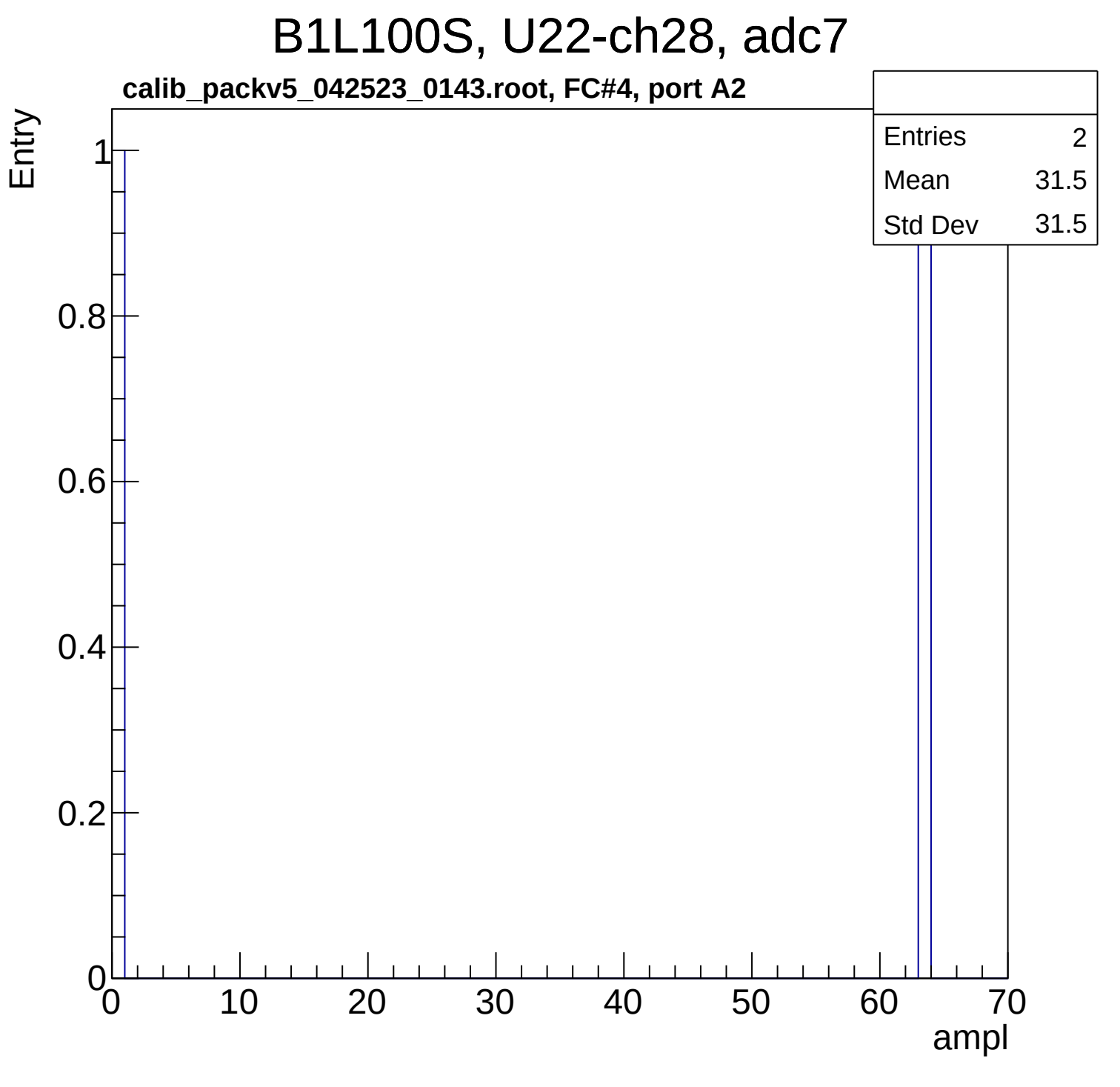
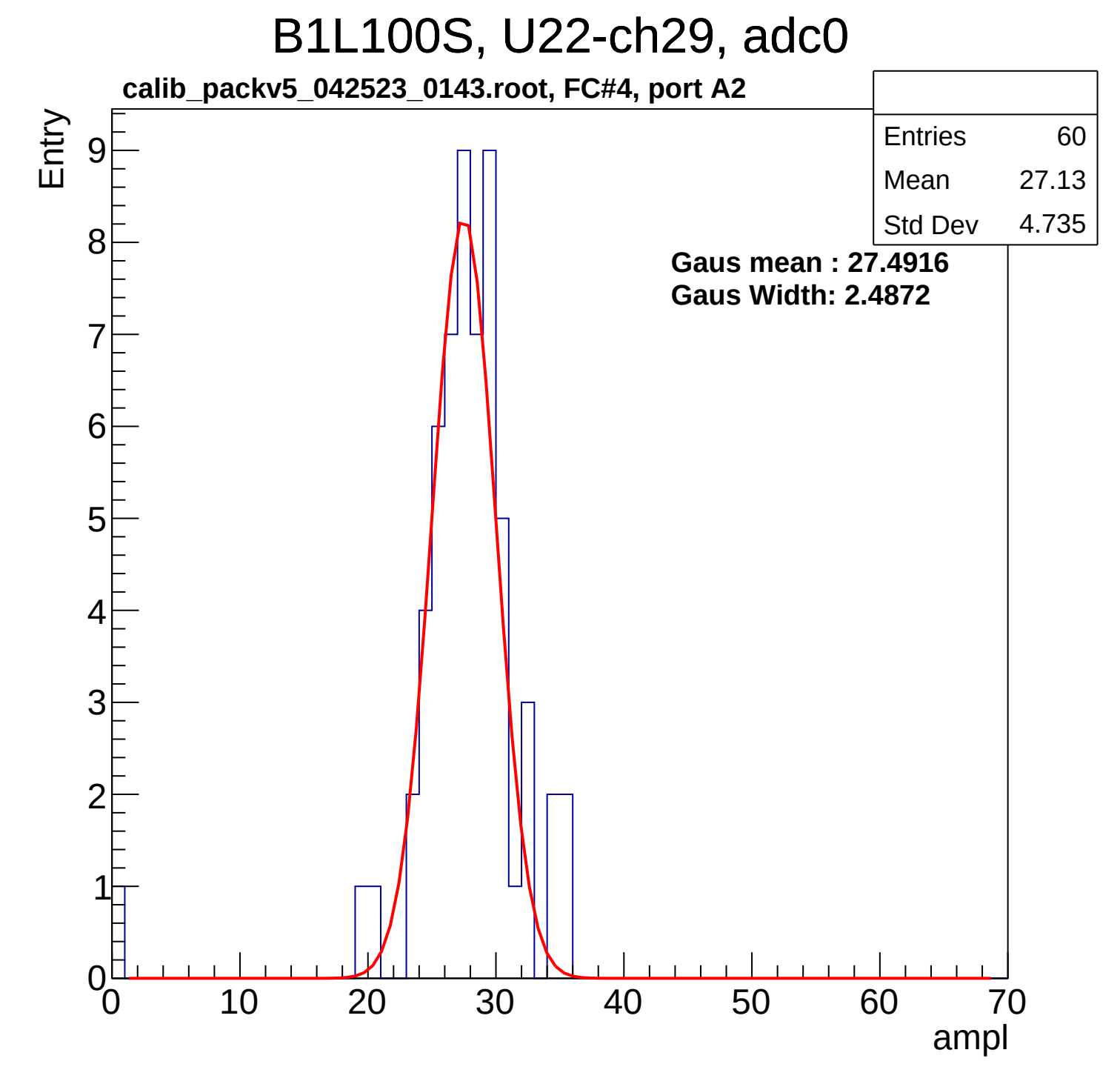
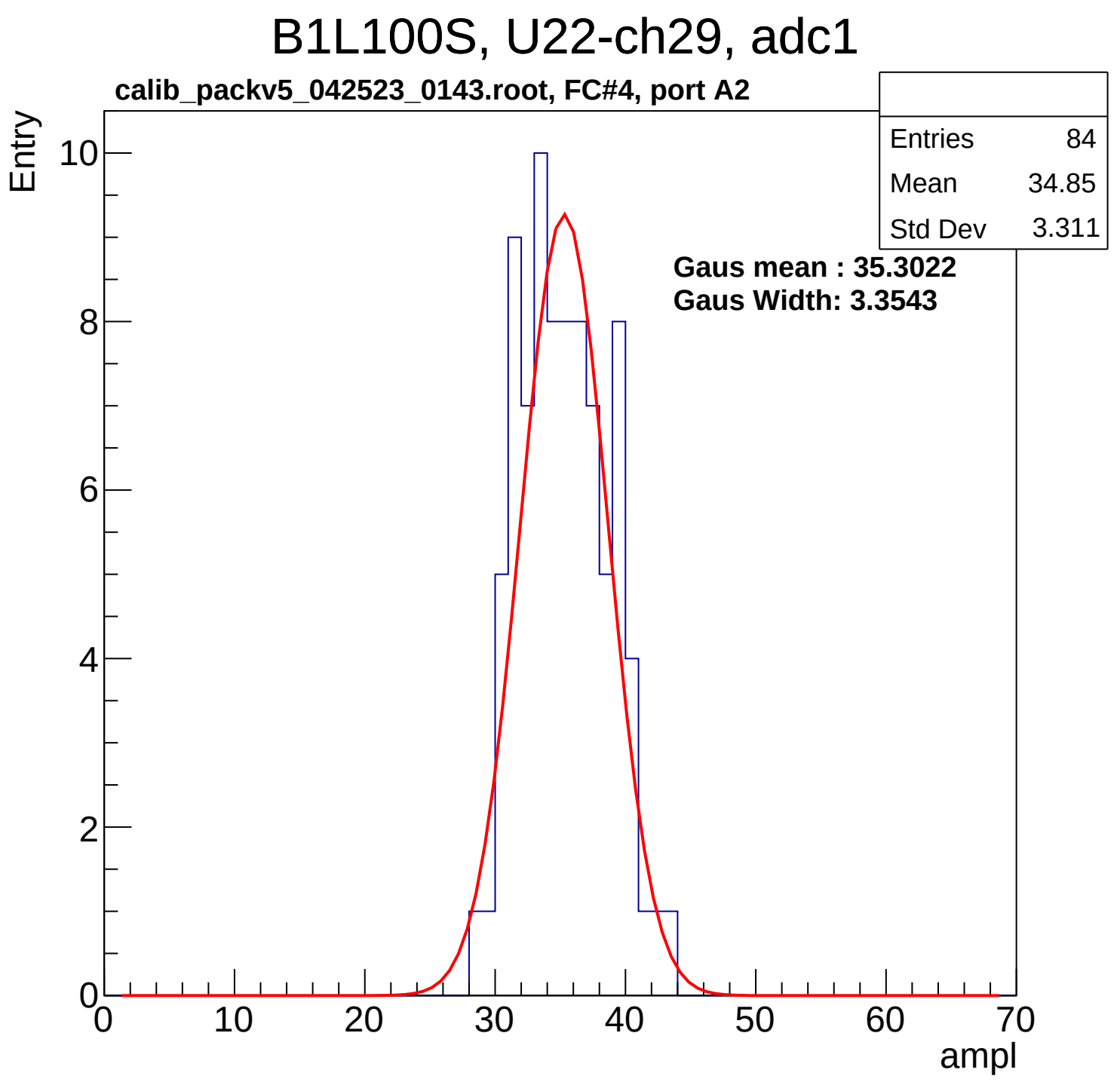
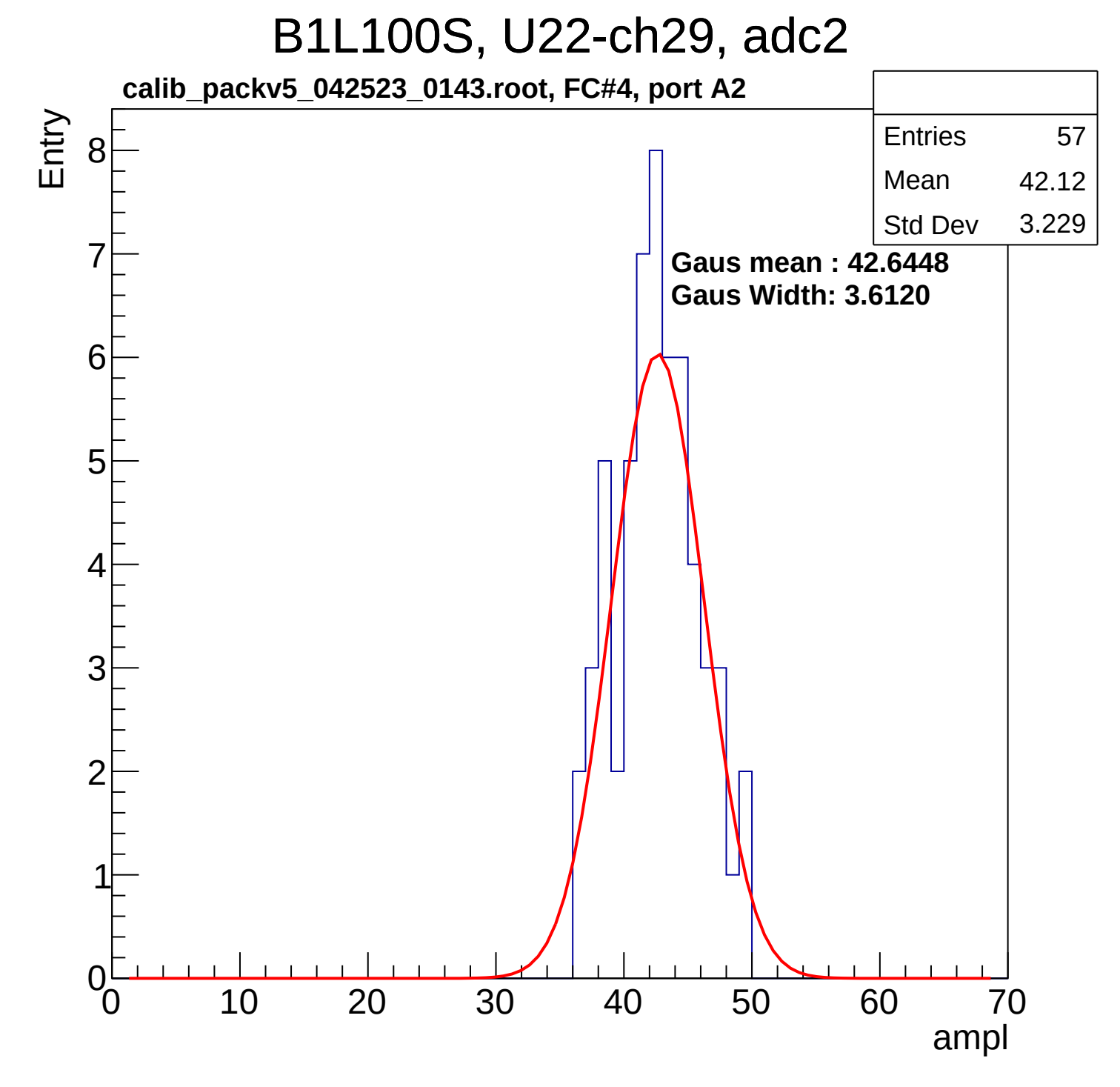

B1L100S, U22-ch28, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.66
Std Dev 8.957
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch28, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch28, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch29, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 27.13
Std Dev 4.735
Gaus mean : 27.4916
Gaus Width: 2.4872
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch29, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 34.85
Std Dev 3.311
Gaus mean : 35.3022
Gaus Width: 3.3543
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch29, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.12
Std Dev 3.229
Gaus mean : 42.6448
Gaus Width: 3.6120
Entry
ampl
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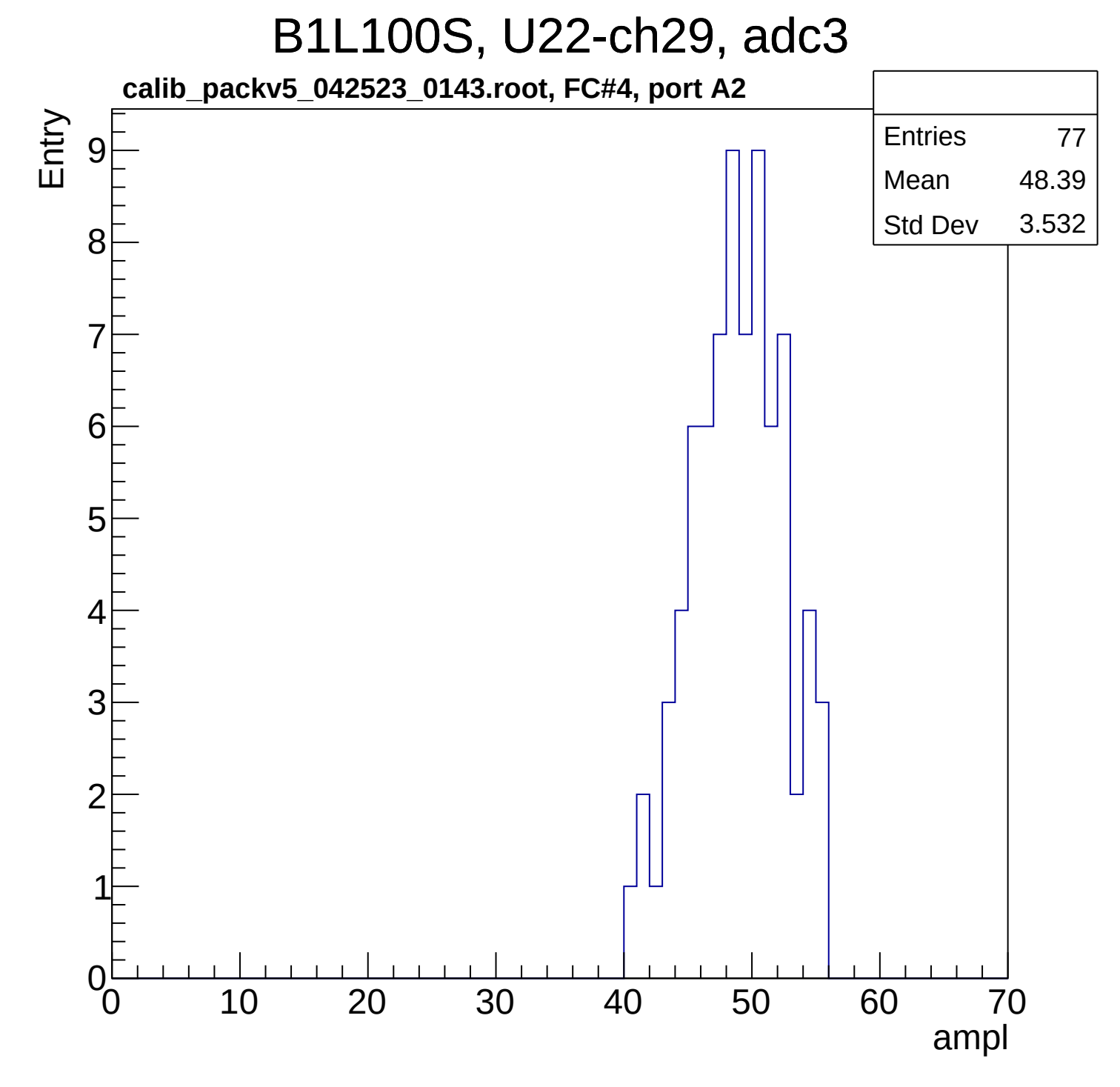
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch29, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 48.39
Std Dev 3.532
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
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60
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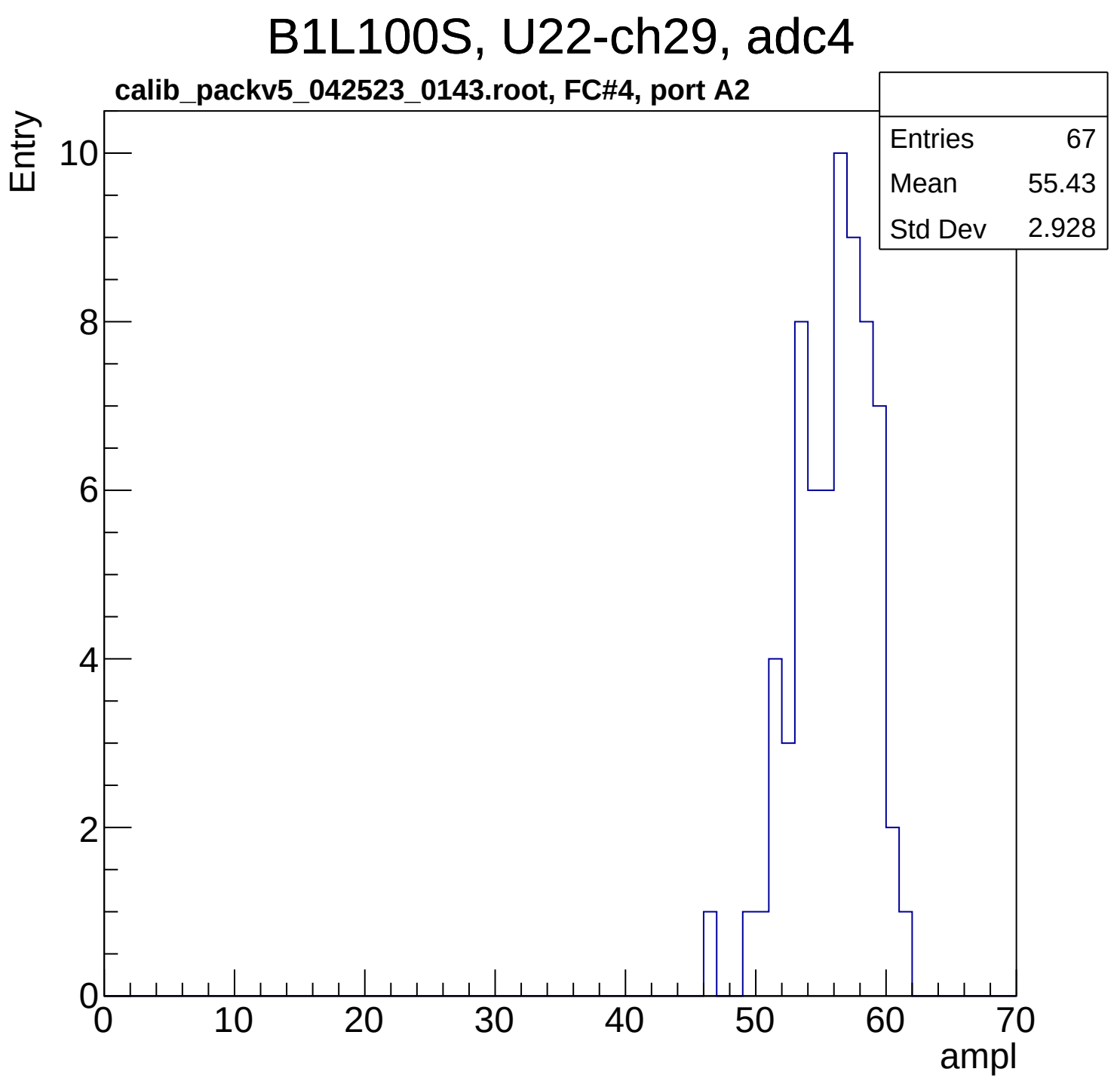
B1L100S, U22-ch29, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 55.43
Std Dev 2.928
Entry
ampl
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2
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0
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40
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60
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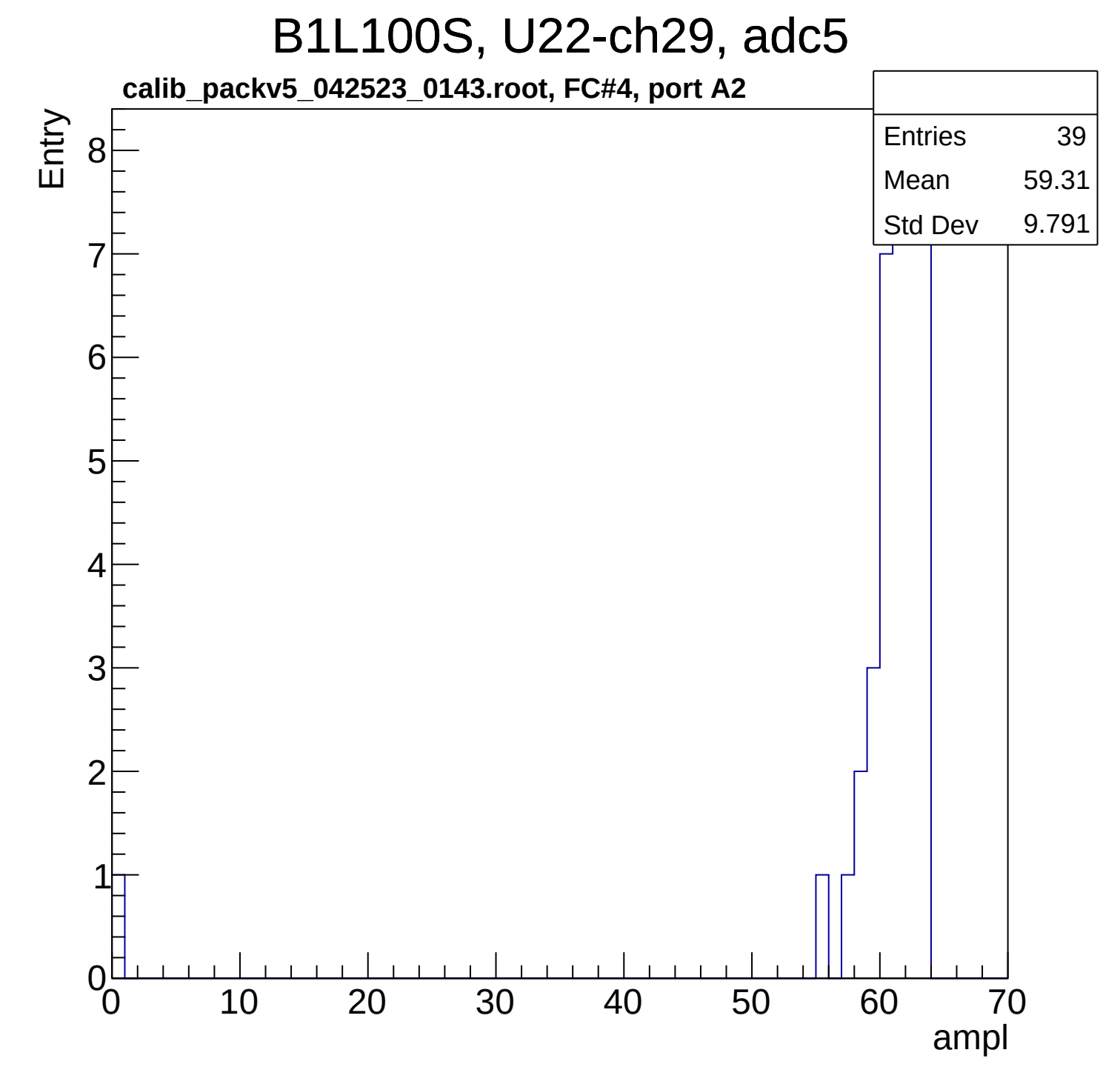
B1L100S, U22-ch29, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.31
Std Dev 9.791
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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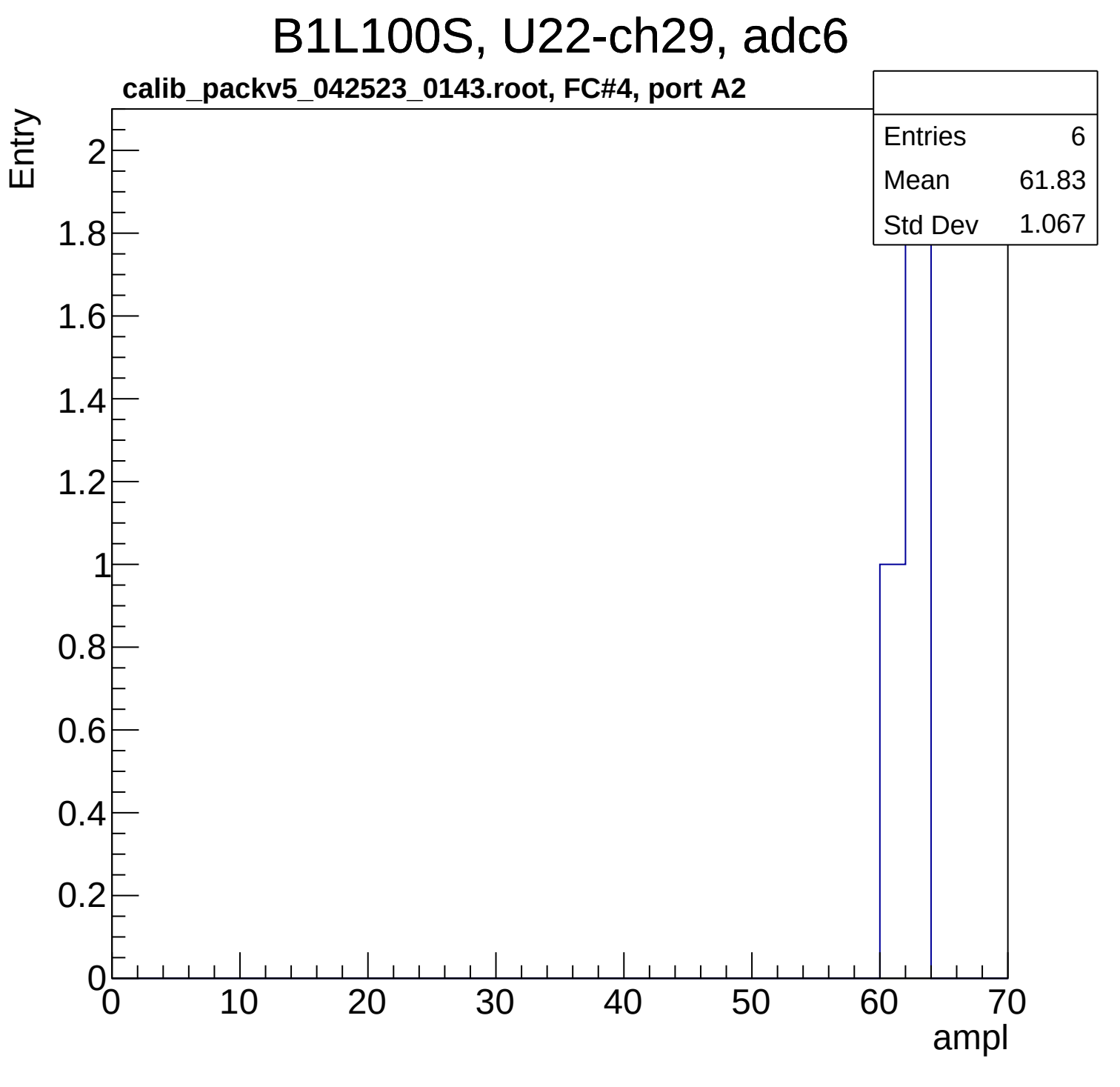
B1L100S, U22-ch29, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.83
Std Dev 1.067
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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20
30
40
50
60
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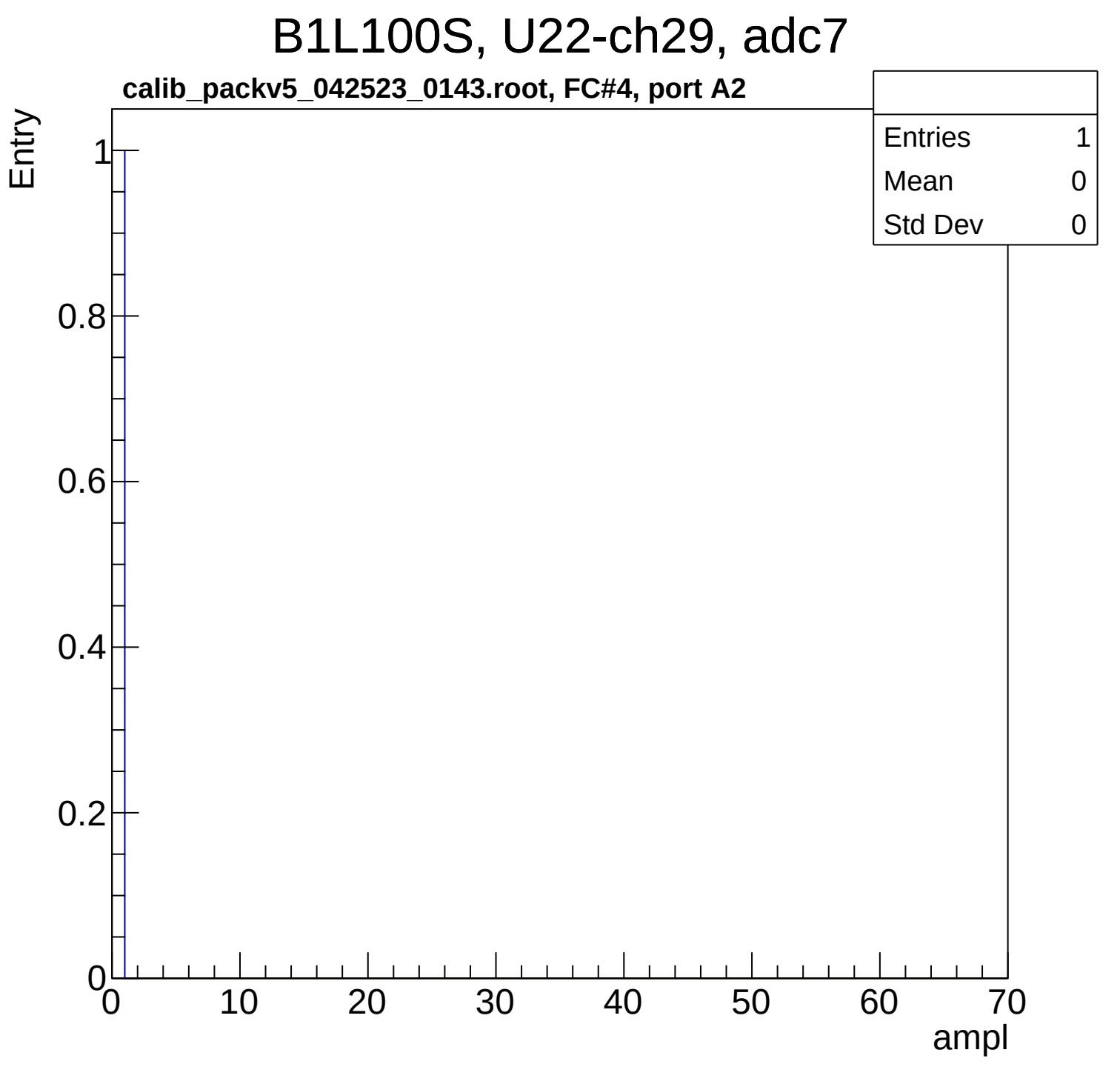
B1L100S, U22-ch29, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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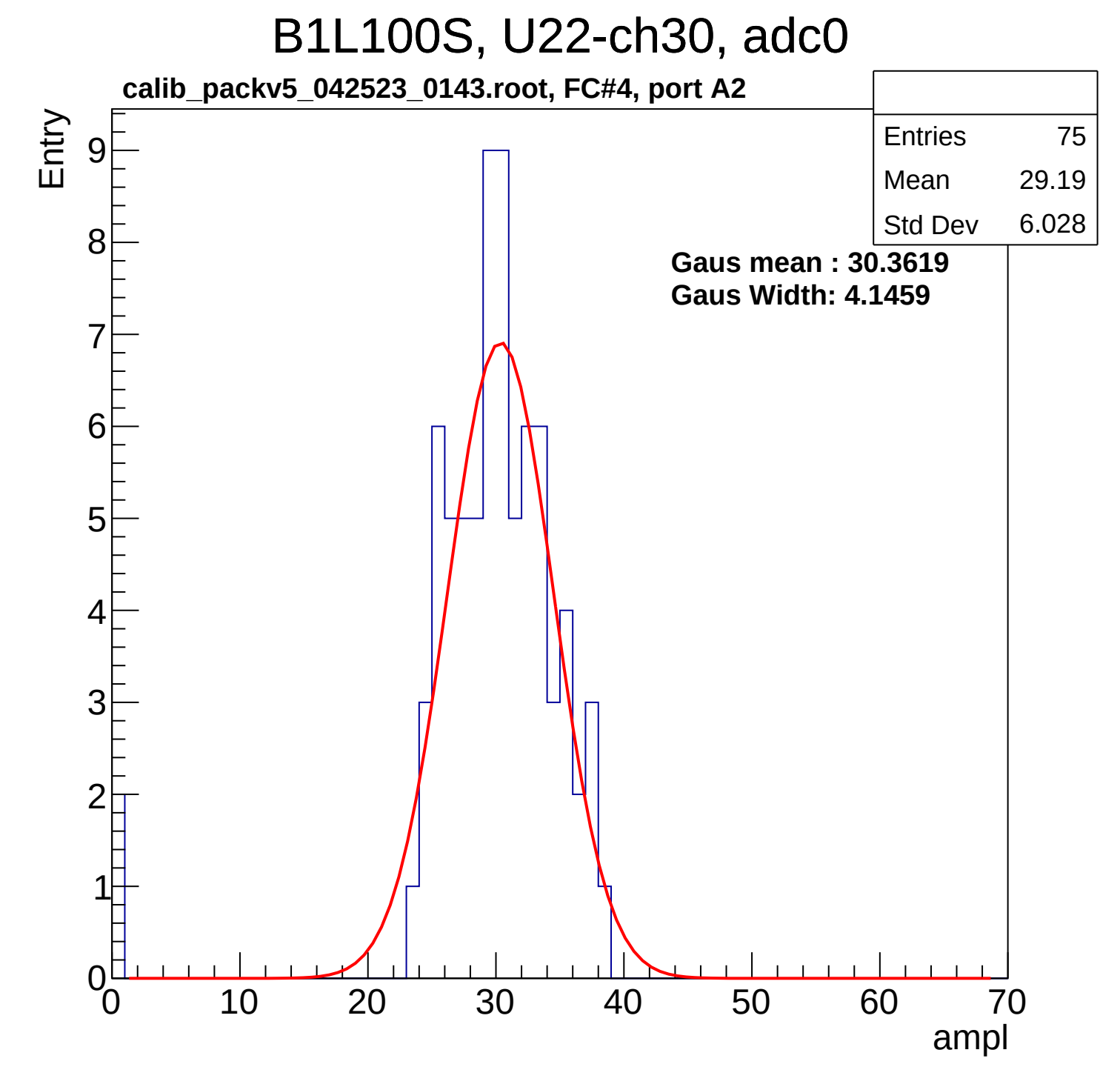
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch30, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.19
Std Dev 6.028
Gaus mean : 30.3619
Gaus Width: 4.1459
Entry
ampl
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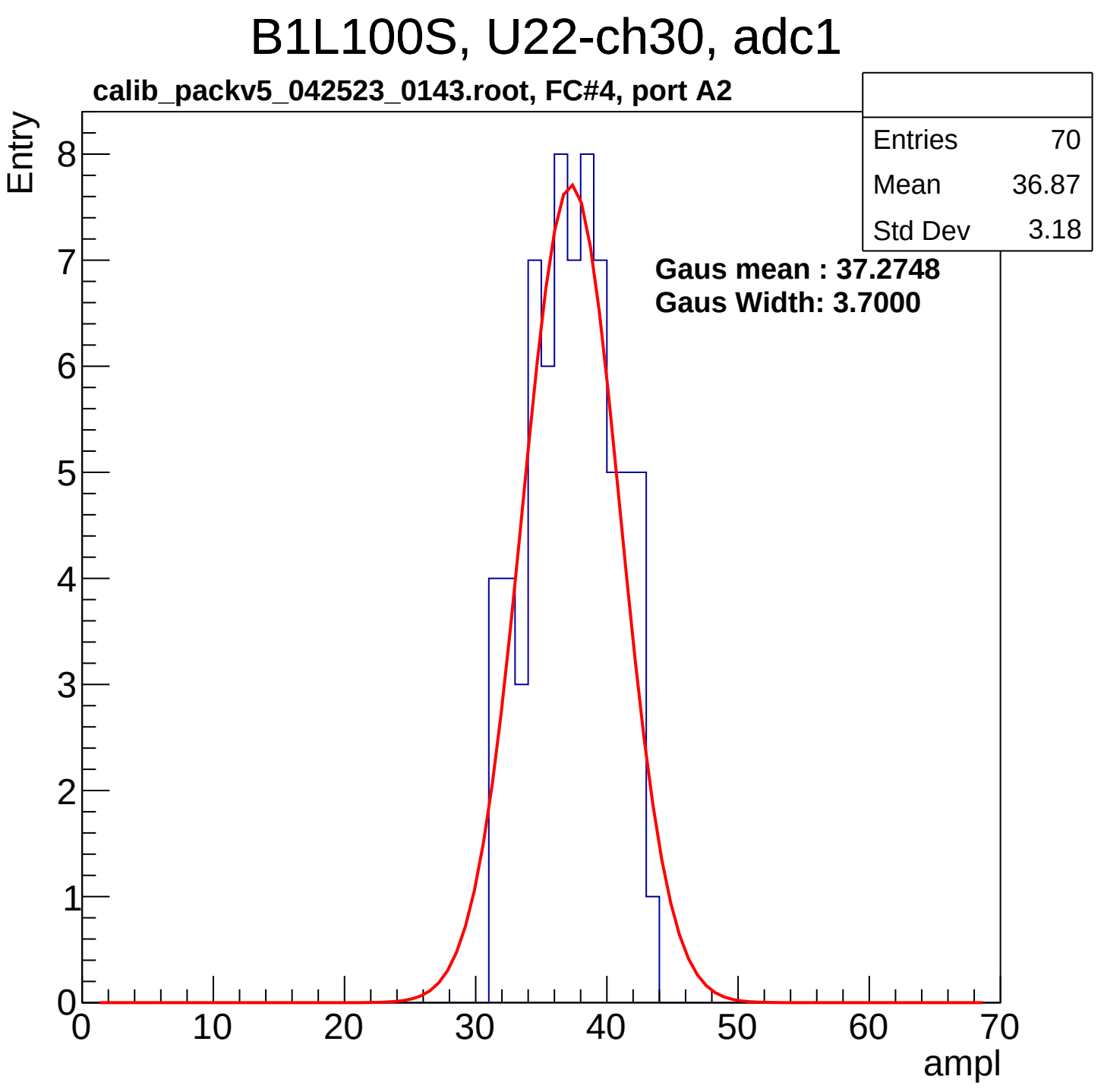
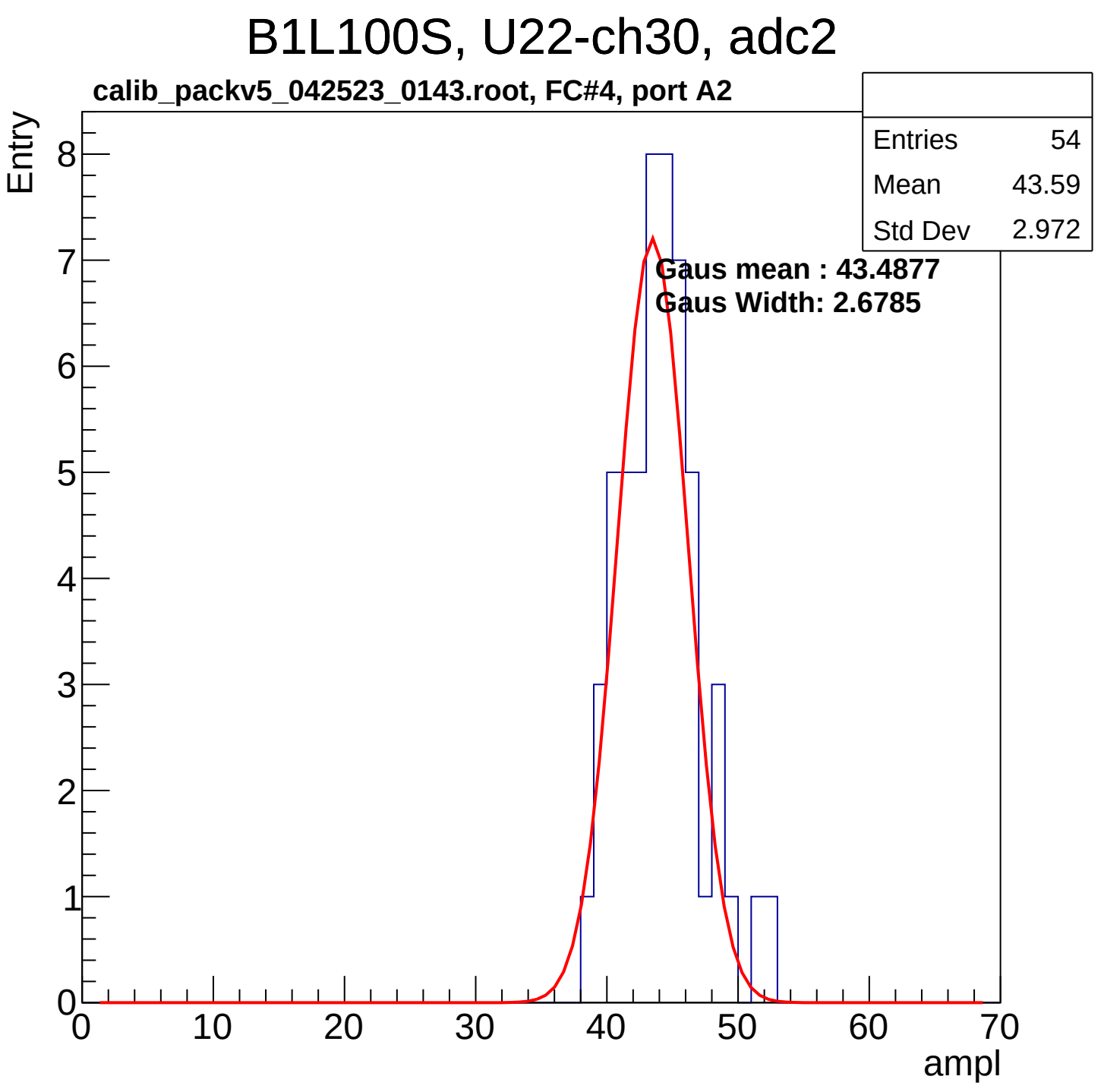
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch30, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.87
Std Dev 3.18
Gaus mean : 37.2748
Gaus Width: 3.7000
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch30, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 43.59
Std Dev 2.972
Gaus mean : 43.4877
Gaus Width: 2.6785
Entry
ampl
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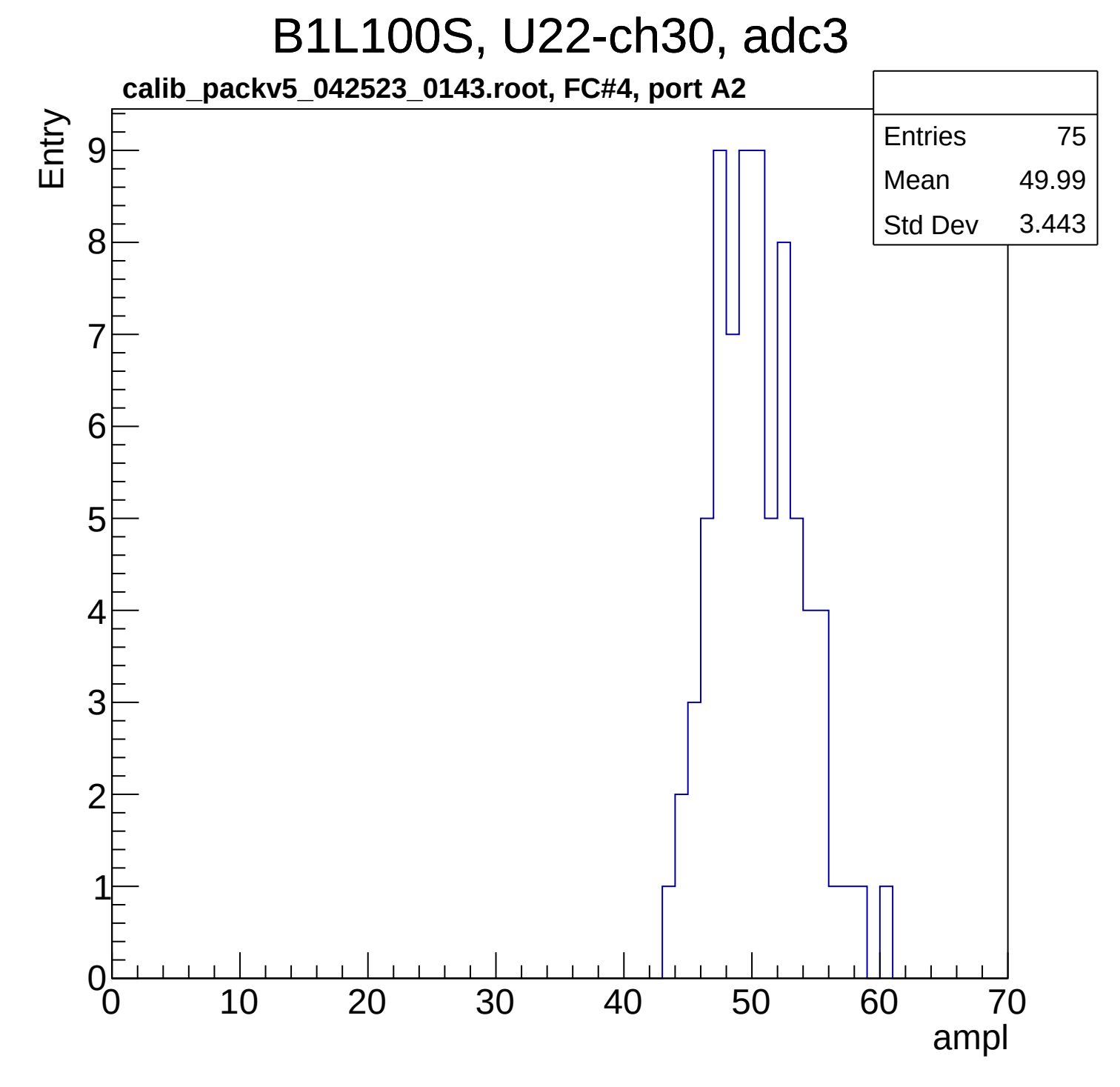
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch30, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 49.99
Std Dev 3.443
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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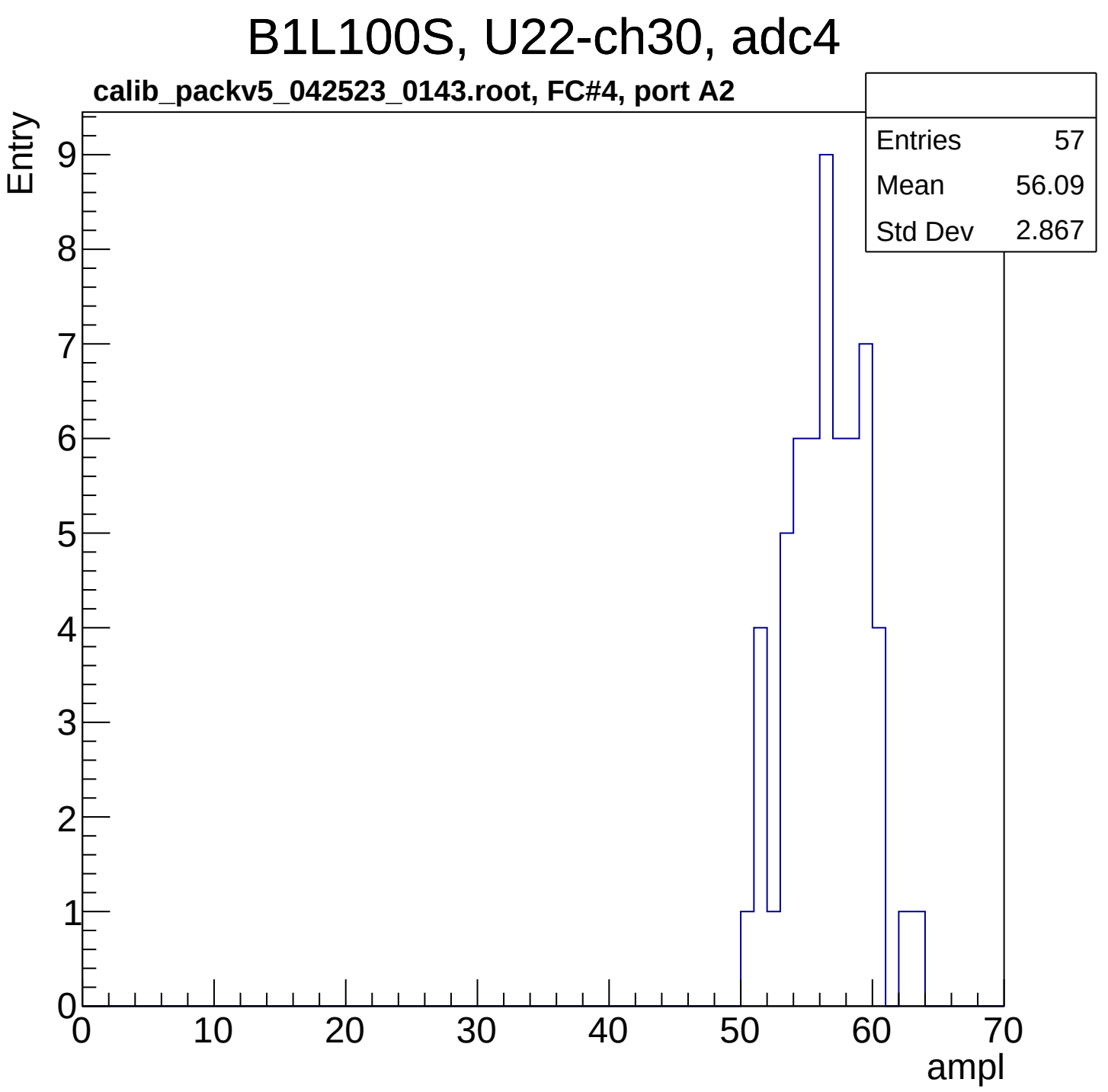
B1L100S, U22-ch30, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.09
Std Dev 2.867
Entry
ampl
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3
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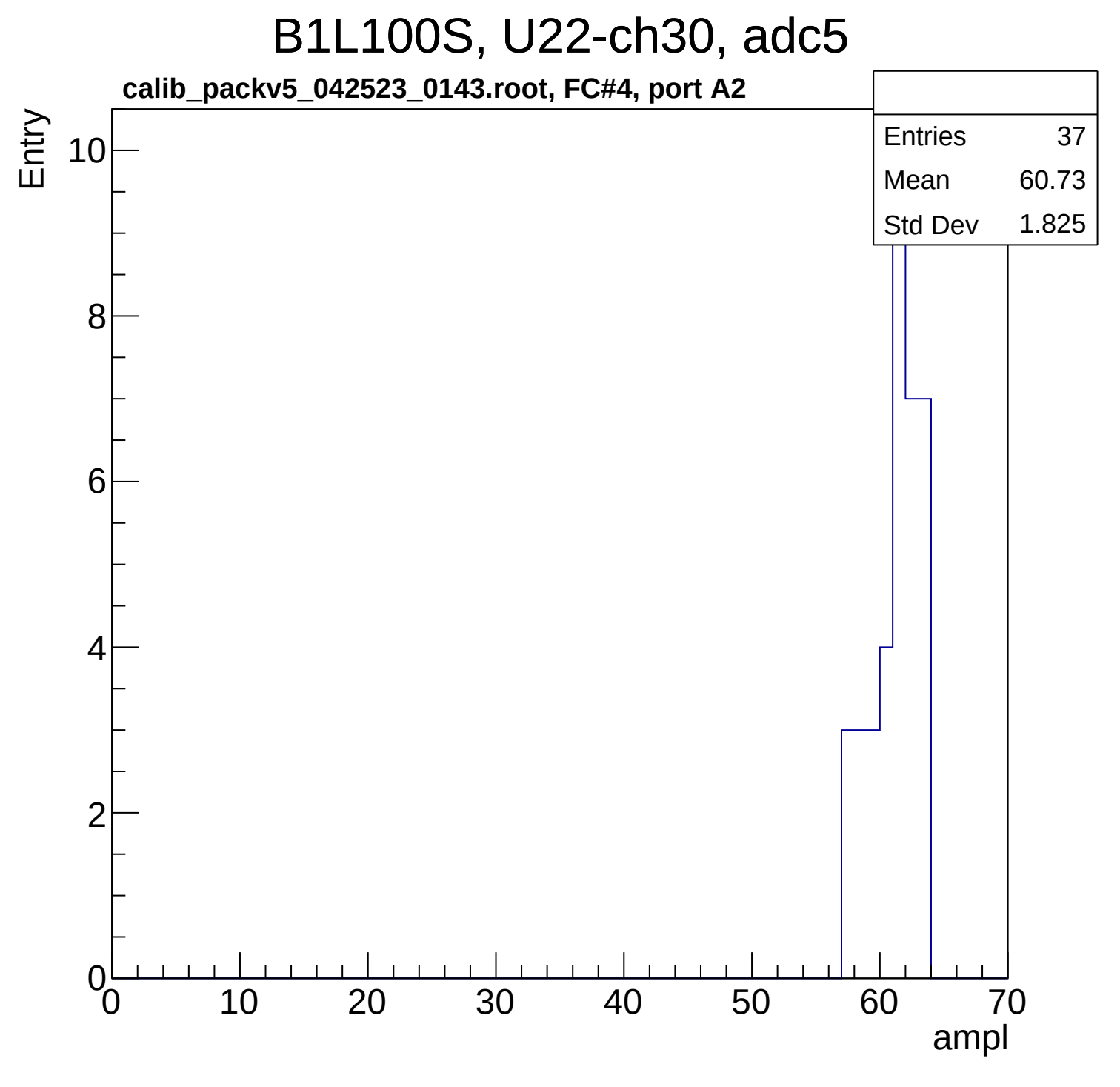
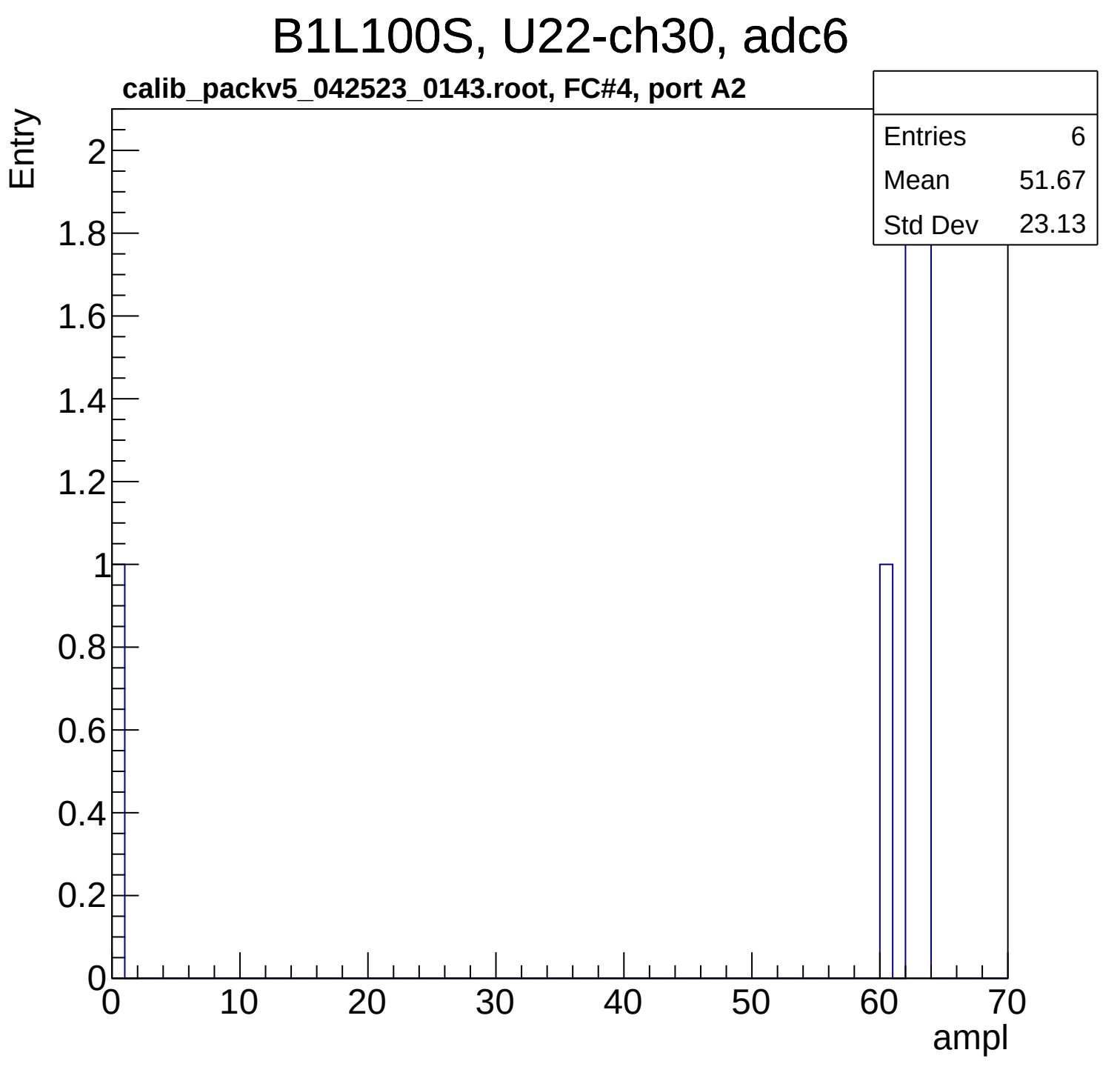

B1L100S, U22-ch30, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.73
Std Dev 1.825
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch30, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.67
Std Dev 23.13
Entry
ampl
0
0.2
0.4
0.6
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1
1.2
1.4
1.6
1.8
2
0
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40
50
60
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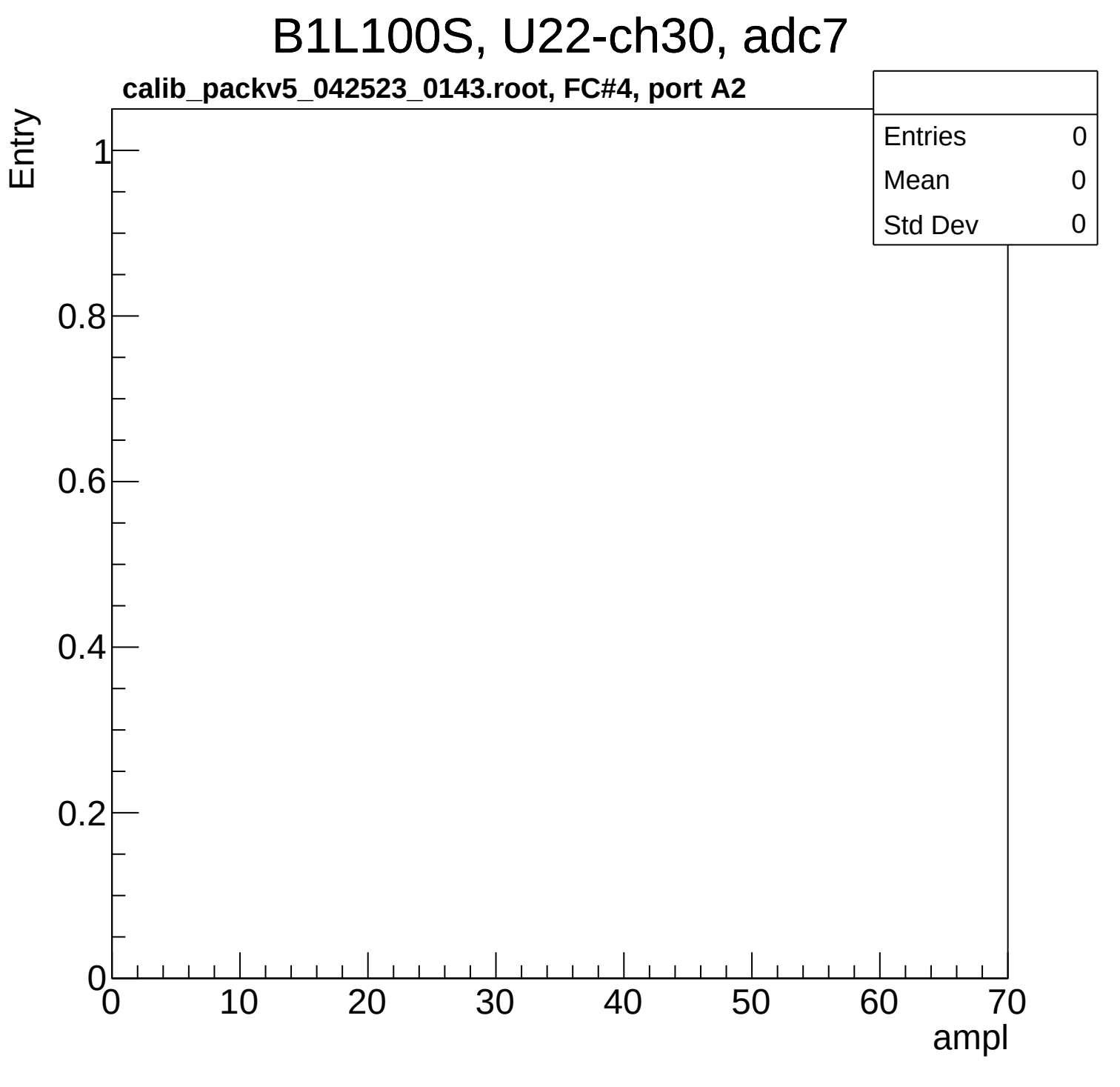
B1L100S, U22-ch30, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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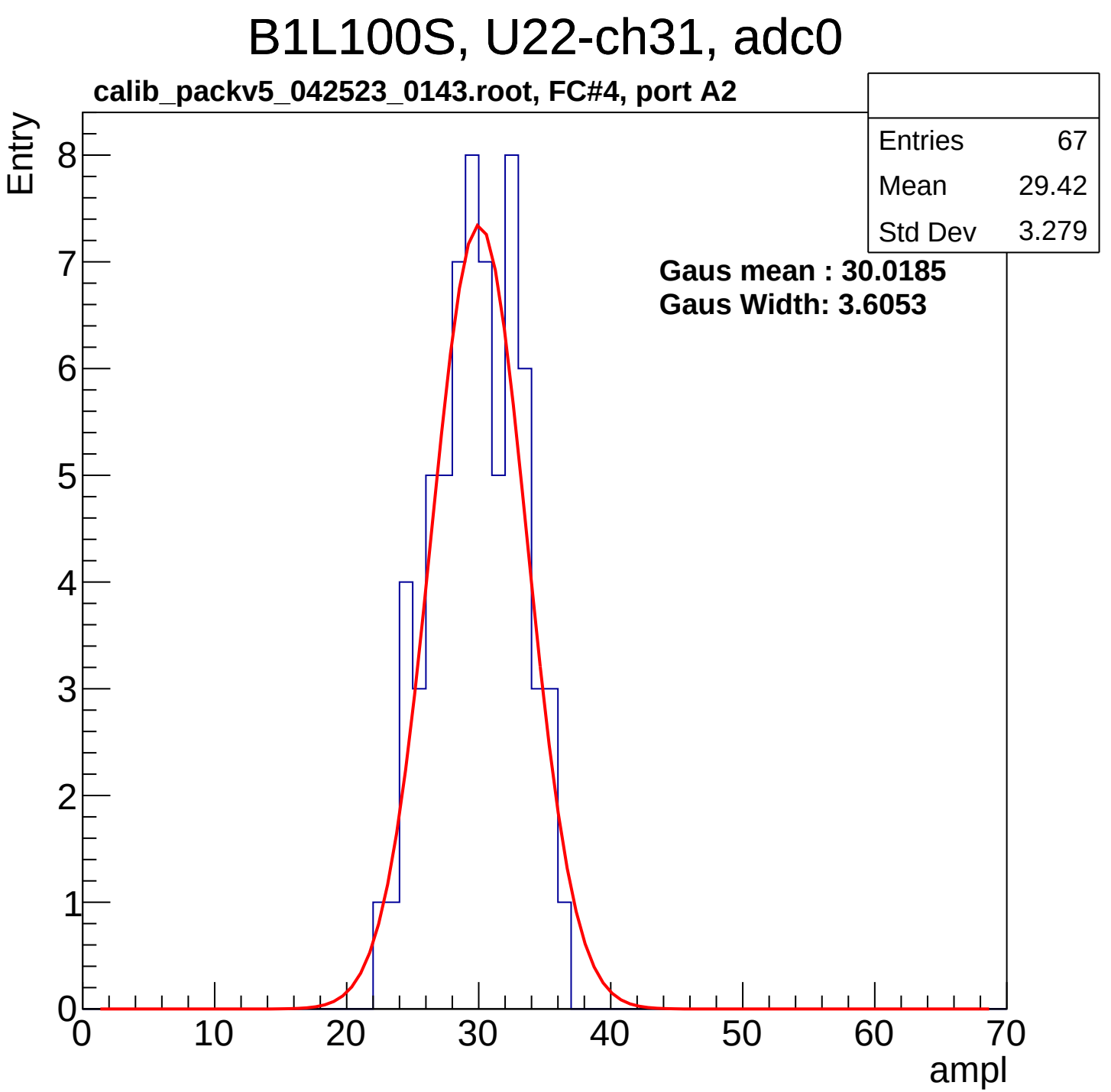
50
60
70
ampl


B1L100S, U22-ch31, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.42
Std Dev 3.279
Gaus mean : 30.0185
Gaus Width: 3.6053
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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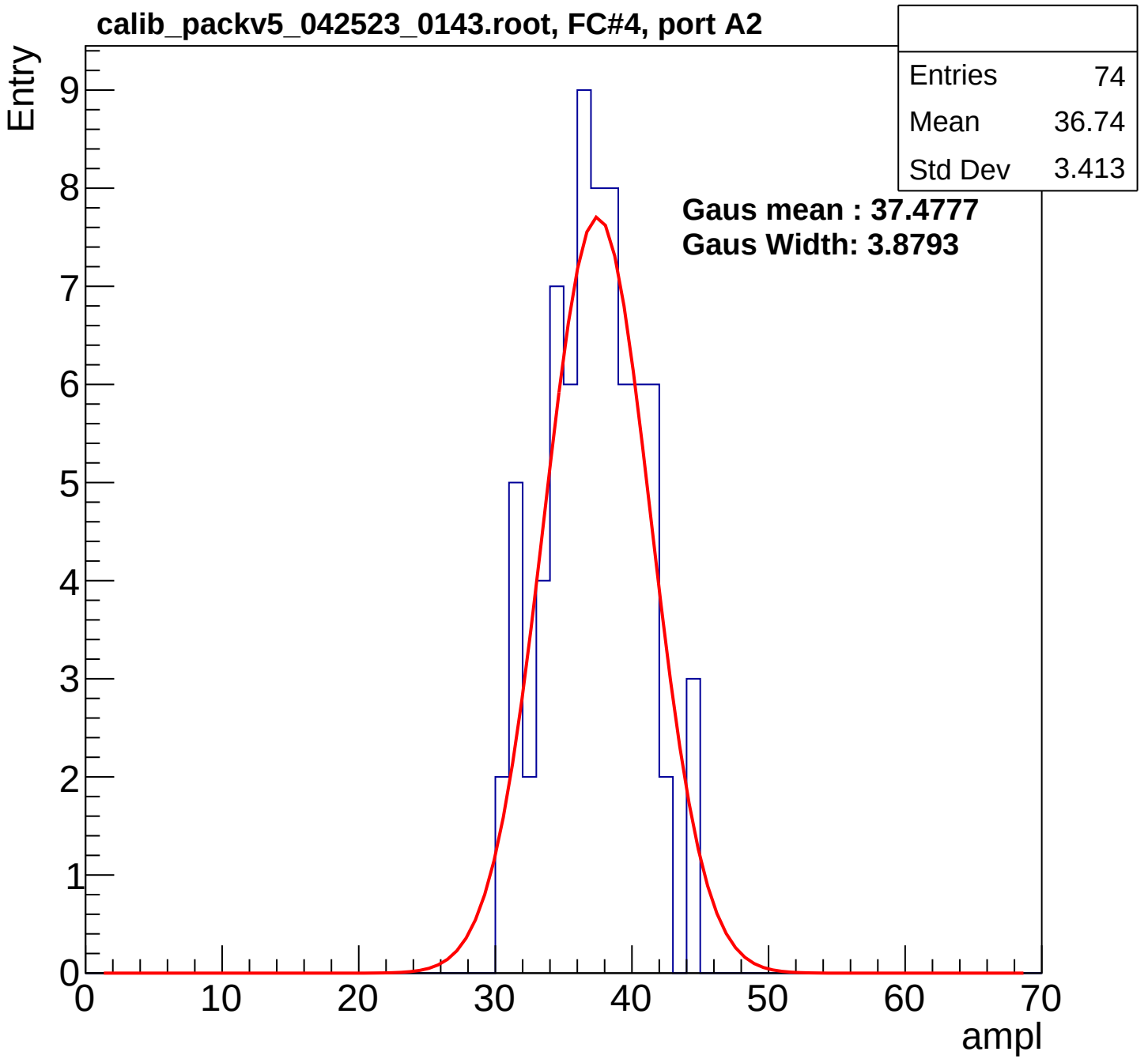
B1L100S, U22-ch31, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 36.74
Std Dev 3.413
Gaus mean : 37.4777
Gaus Width: 3.8793
Entry
ampl
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2
3
4
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6
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0
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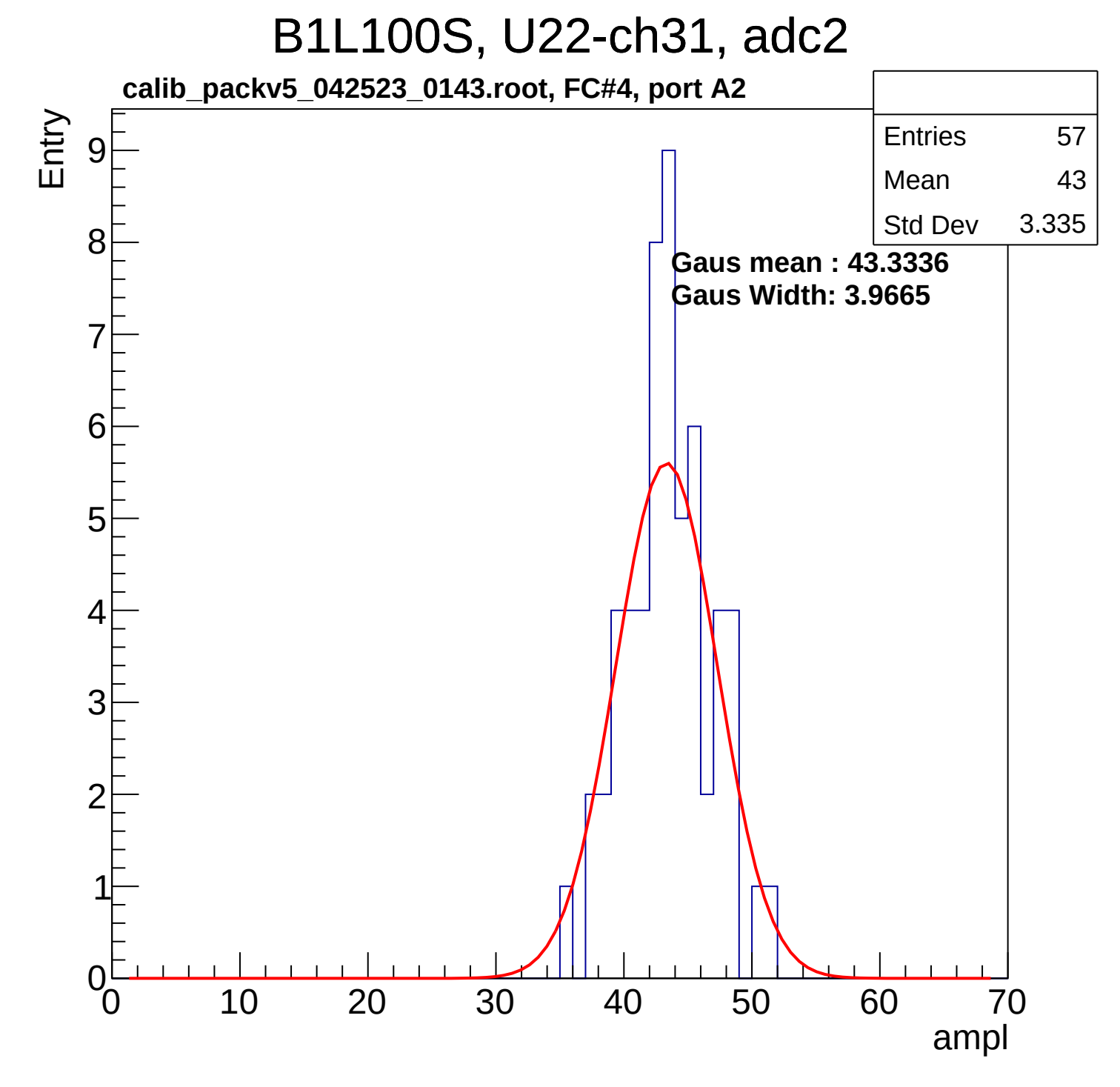
B1L100S, U22-ch31, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43
Std Dev 3.335
Gaus mean : 43.3336
Gaus Width: 3.9665
Entry
ampl
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5
6
7
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0
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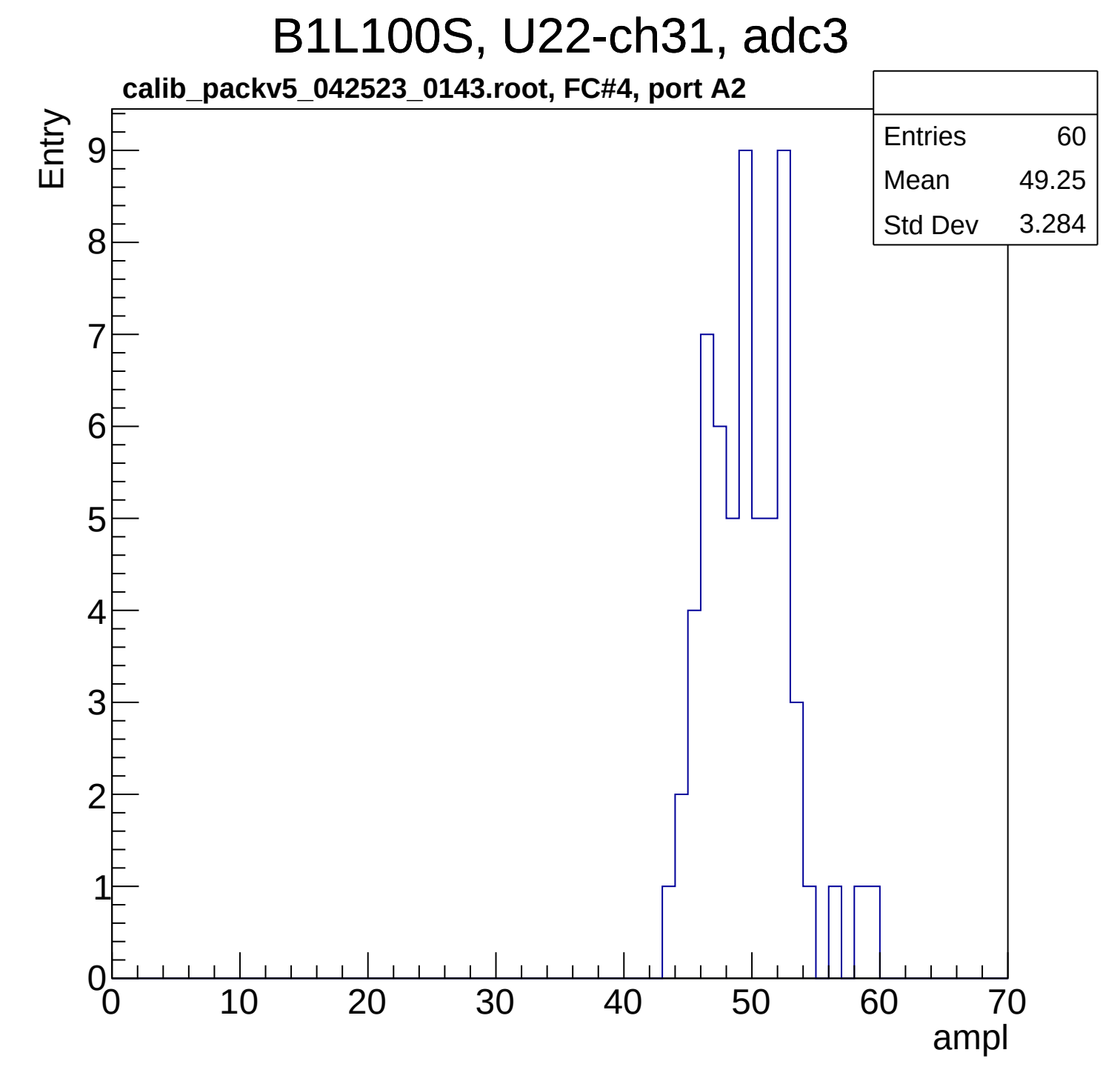
B1L100S, U22-ch31, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 49.25
Std Dev 3.284
Entry
ampl
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2
3
4
5
6
7
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0
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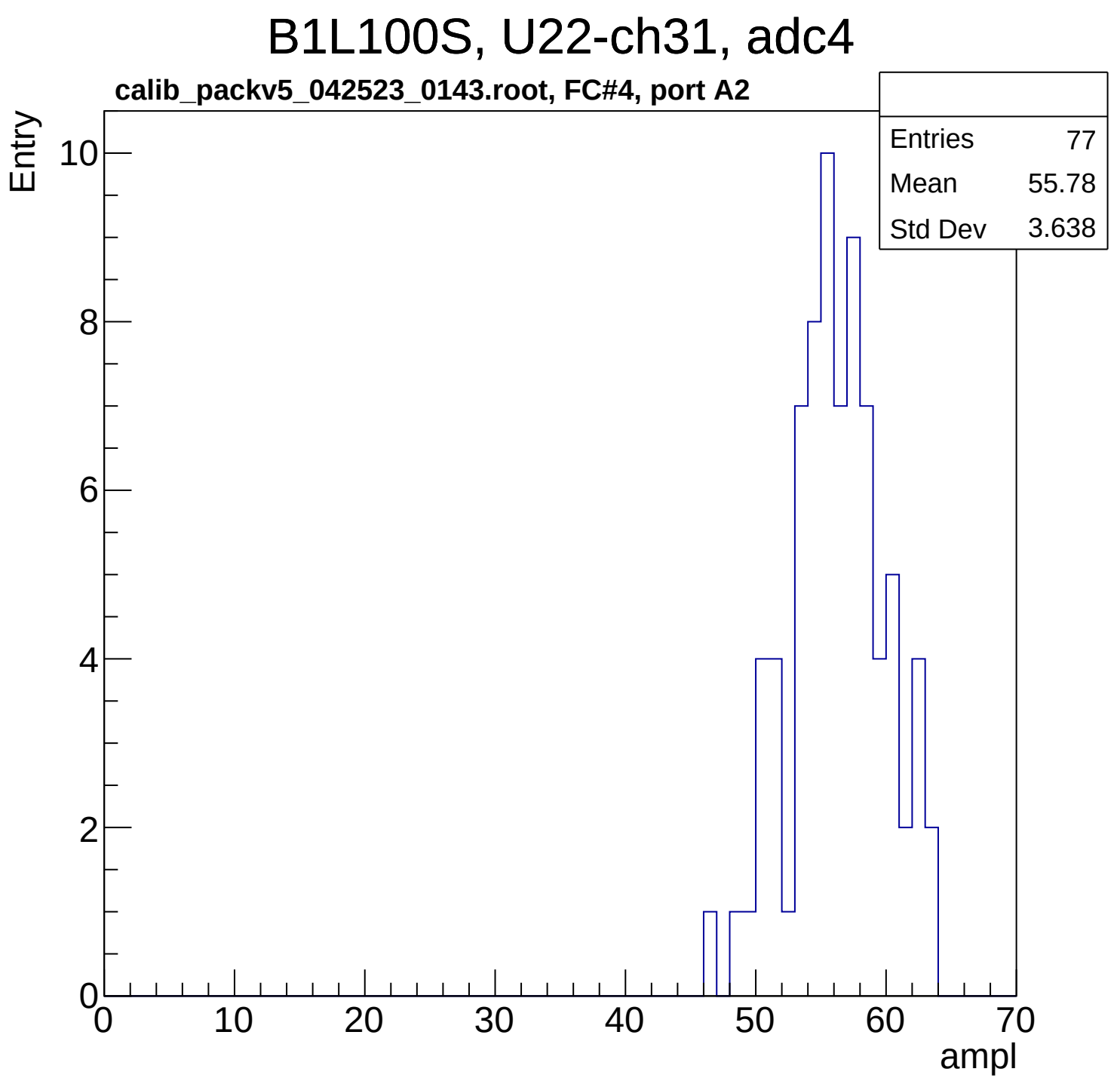
B1L100S, U22-ch31, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 55.78
Std Dev 3.638
Entry
0
2
4
6
8
10
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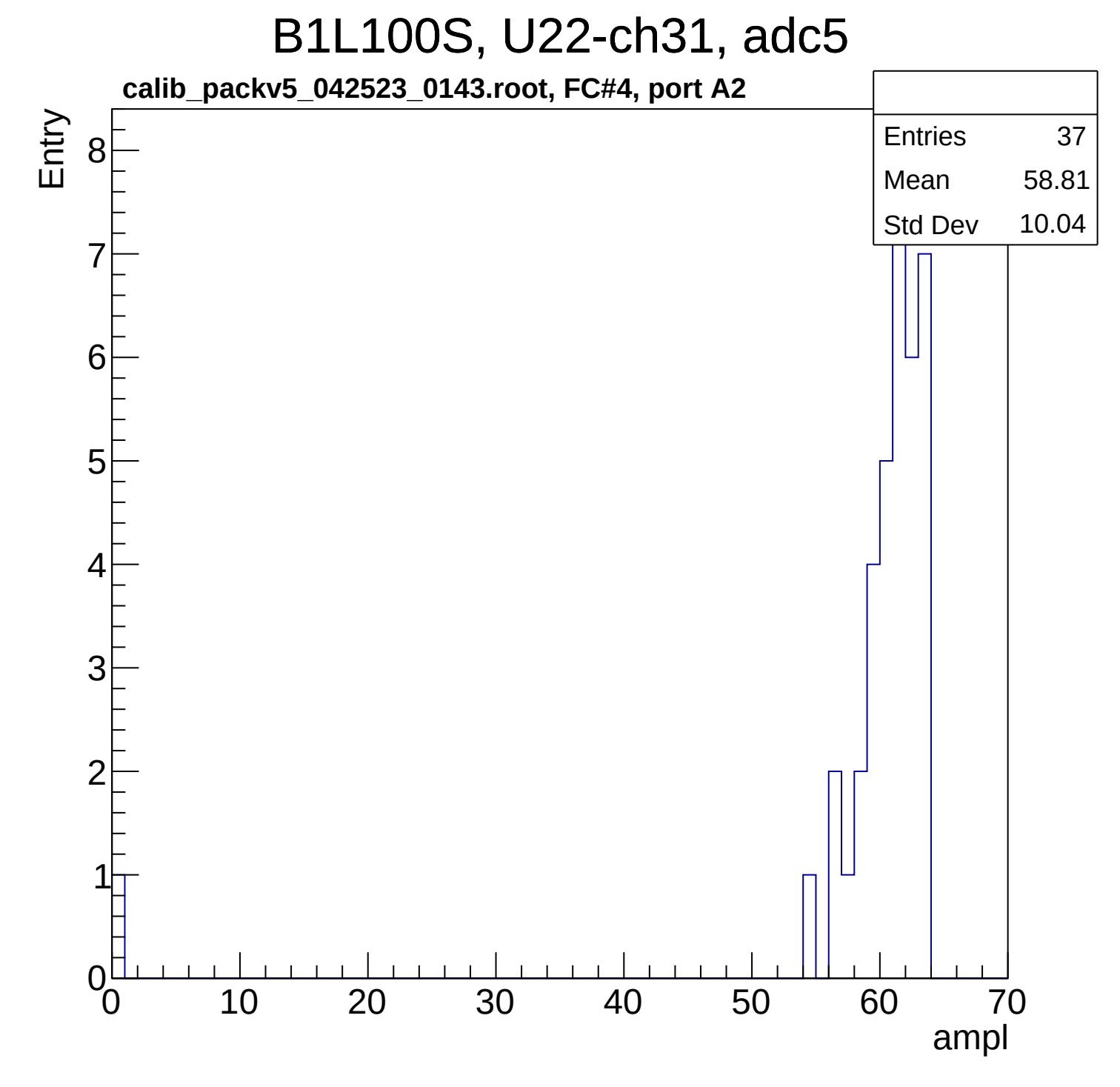
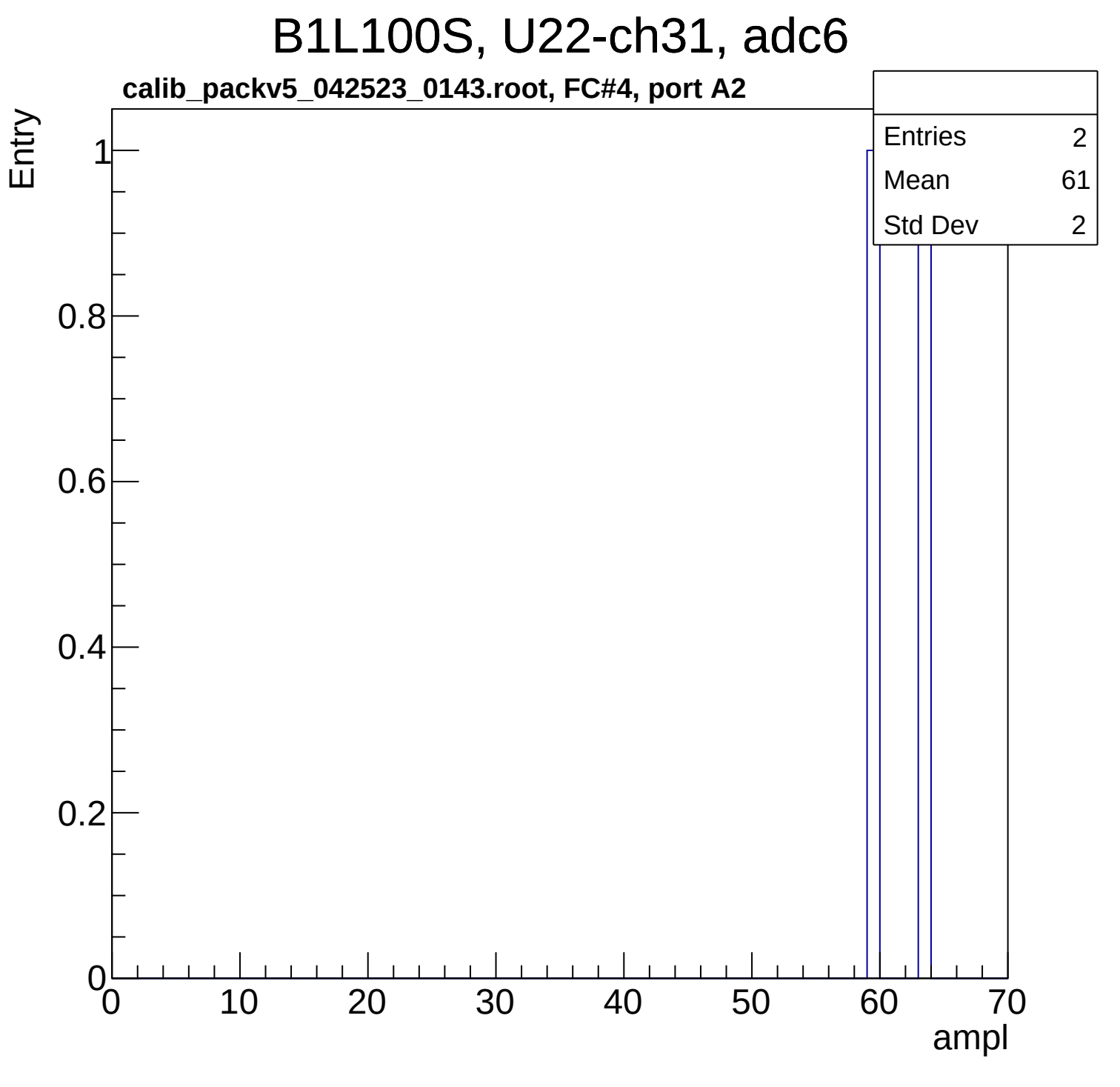
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch31, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.81
Std Dev 10.04
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch31, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61
Std Dev 2
Entry
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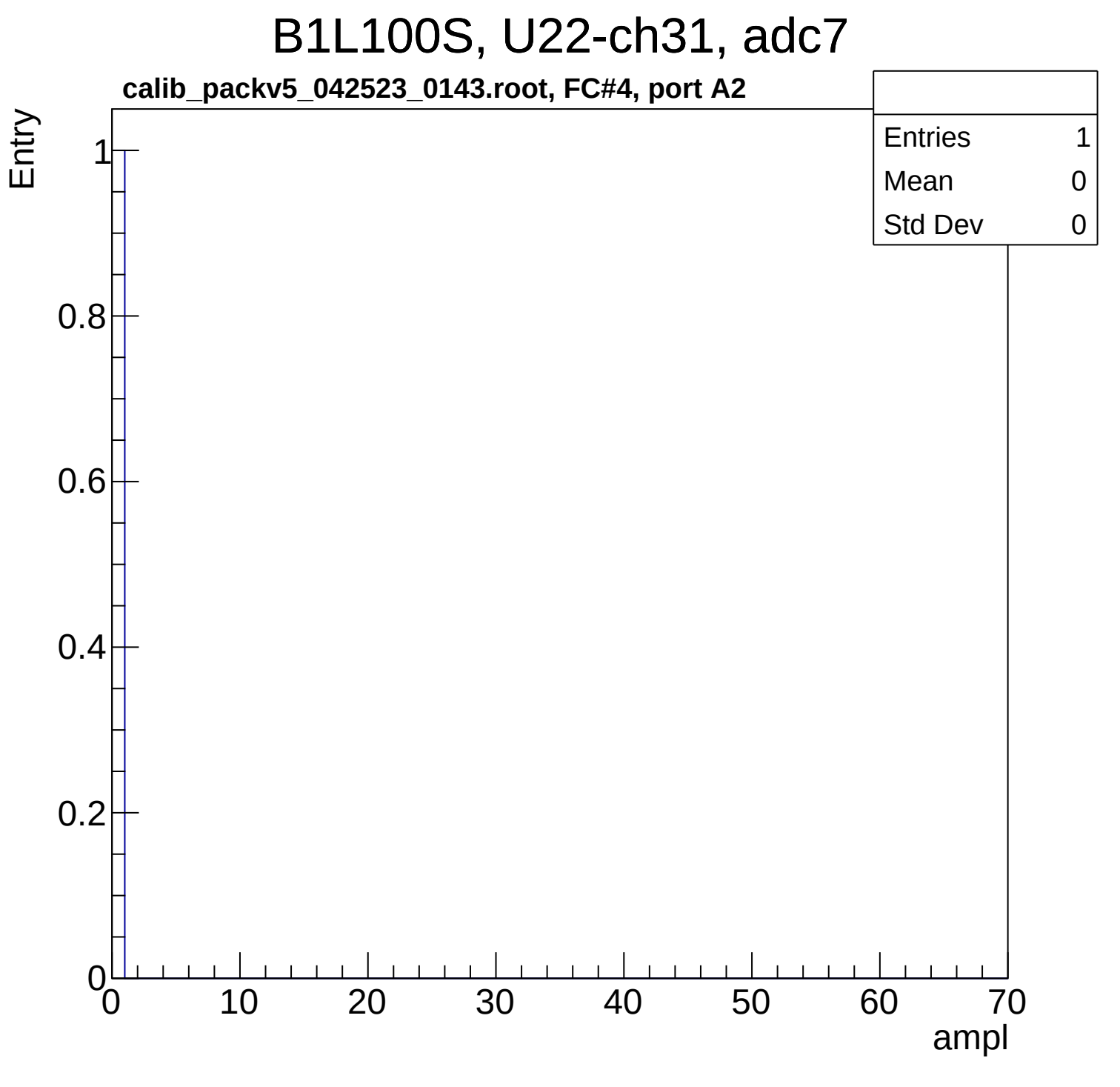
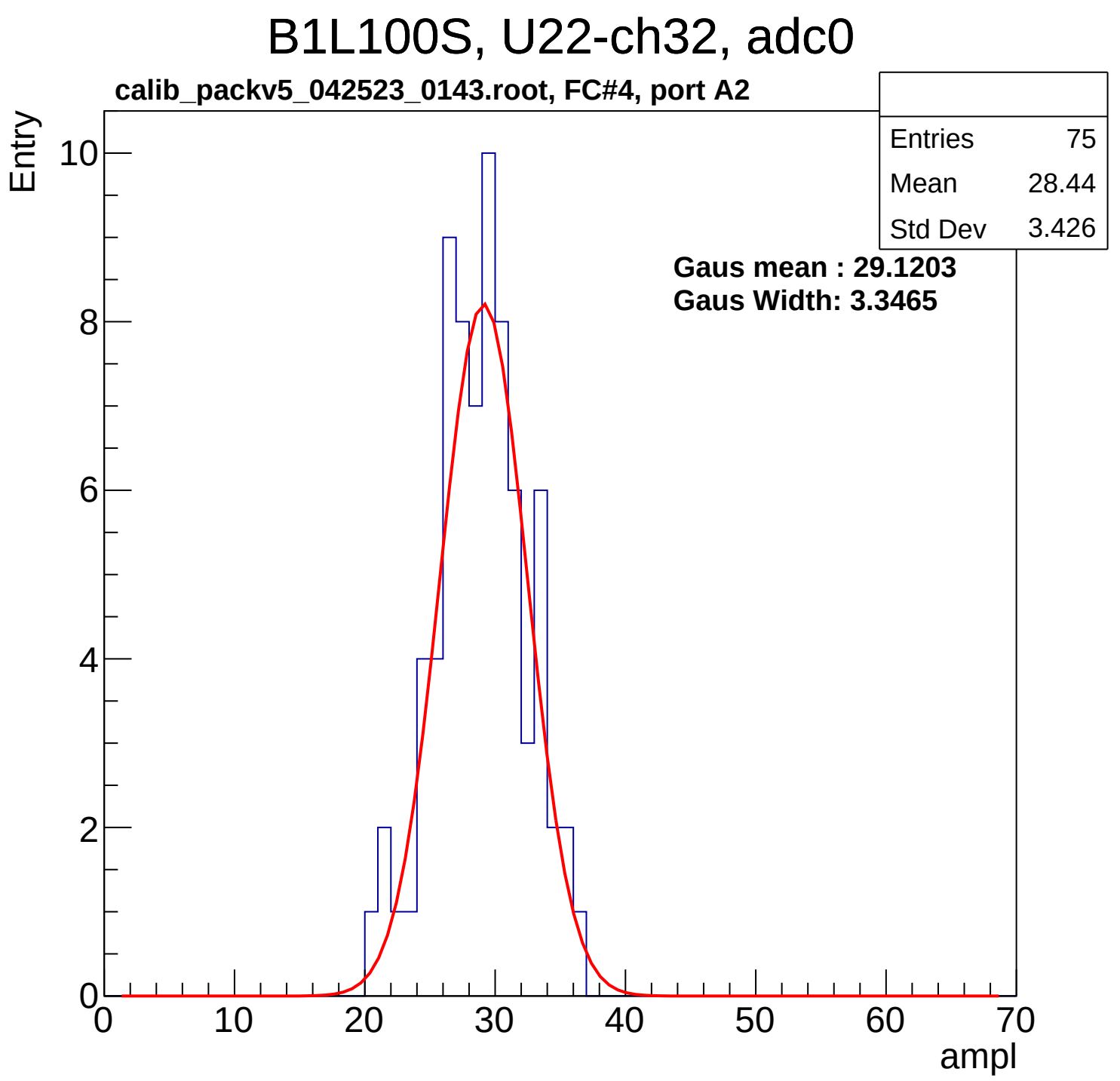
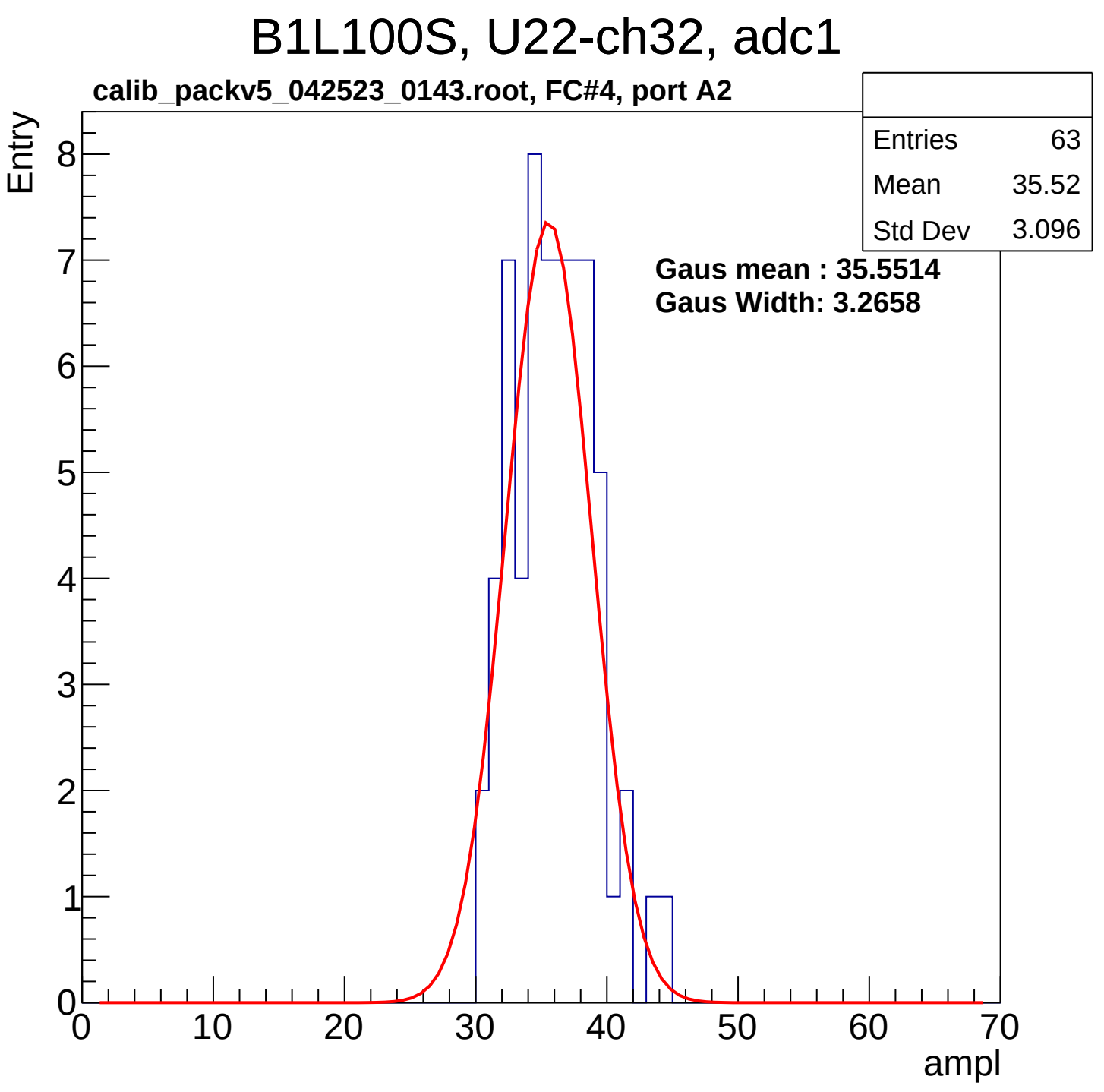
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch31, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch32, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.44
Std Dev 3.426
Gaus mean : 29.1203
Gaus Width: 3.3465
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch32, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.52
Std Dev 3.096
Gaus mean : 35.5514
Gaus Width: 3.2658
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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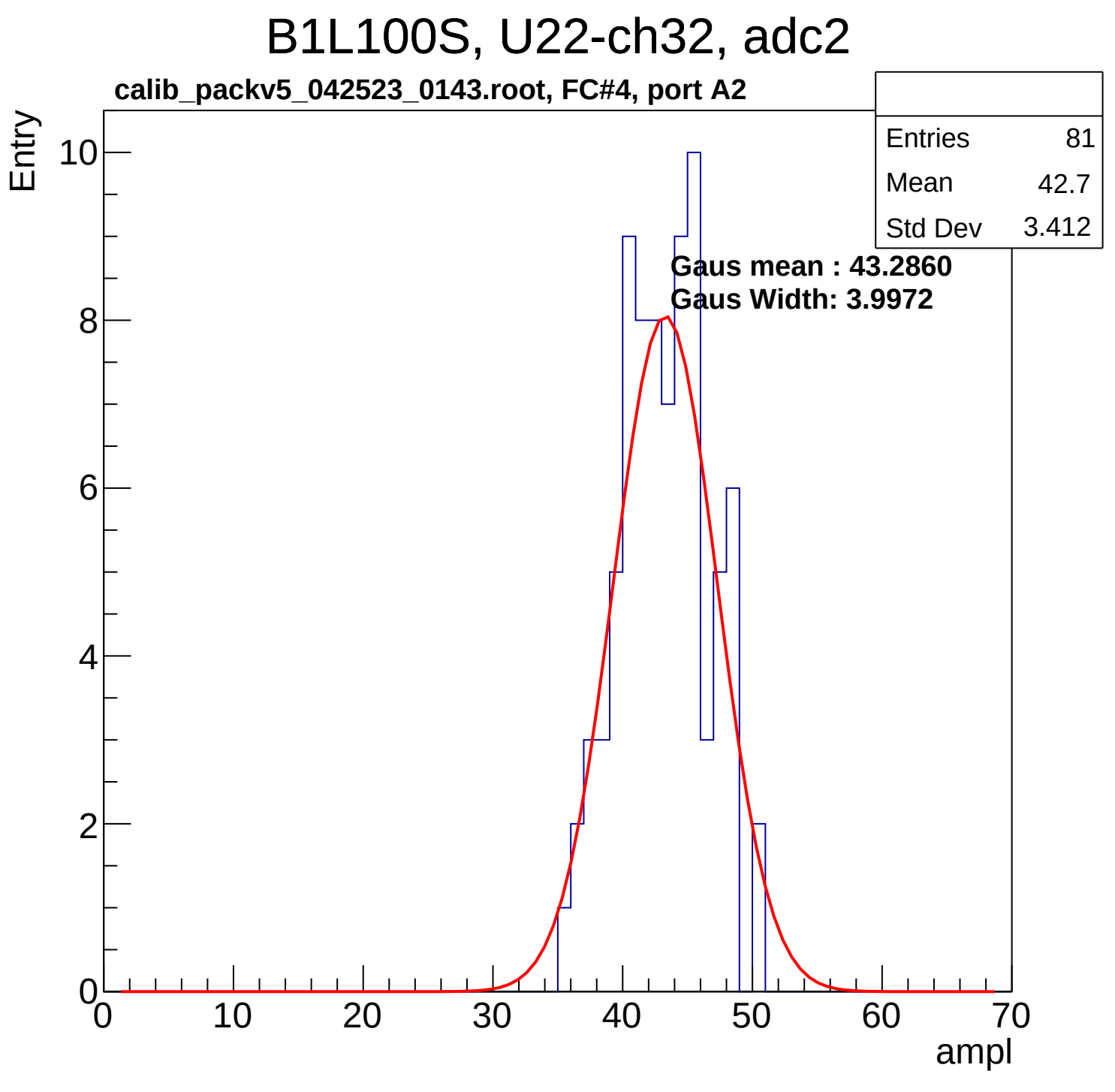
B1L100S, U22-ch32, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 42.7
Std Dev 3.412
Gaus mean : 43.2860
Gaus Width: 3.9972
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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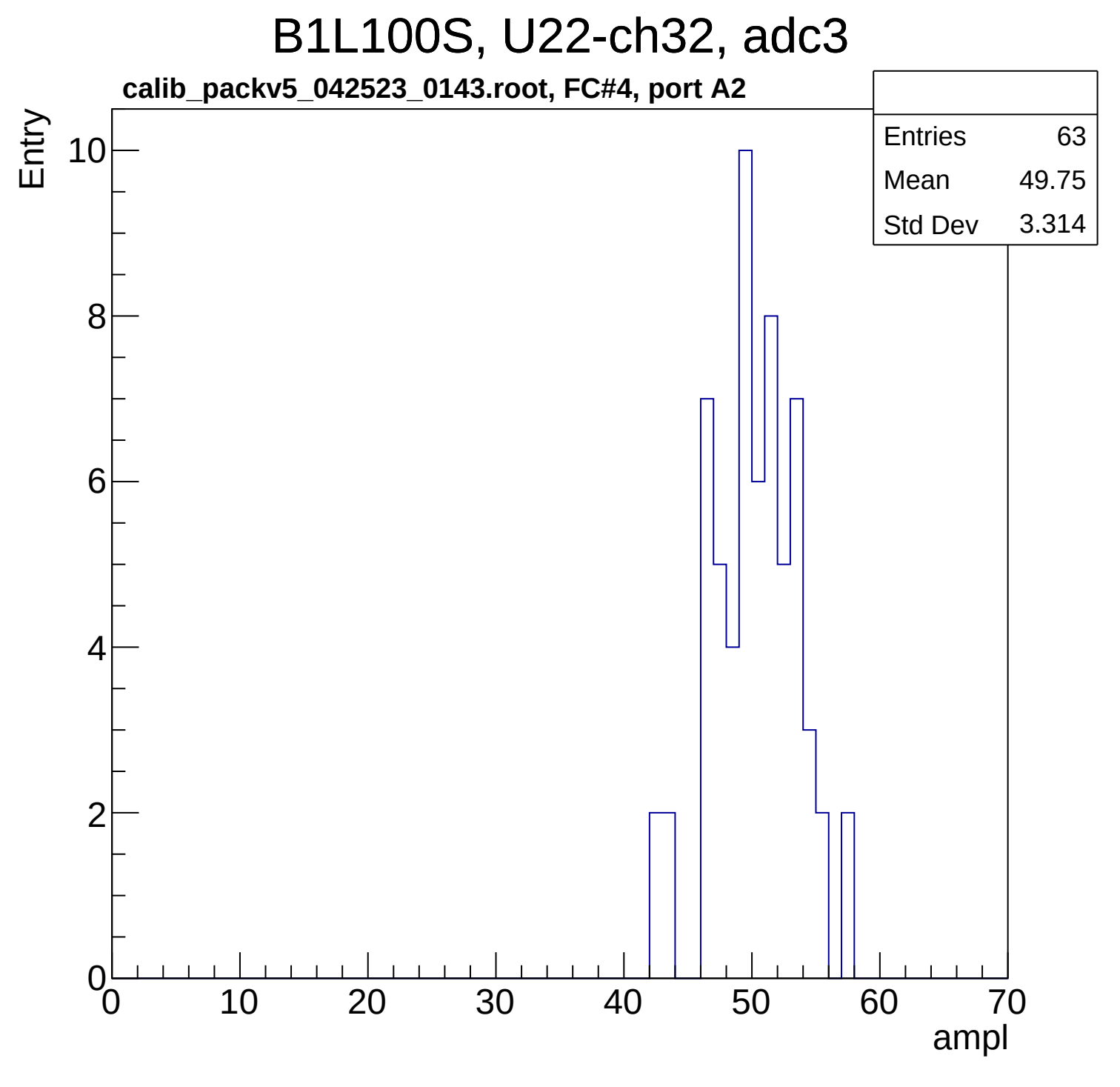
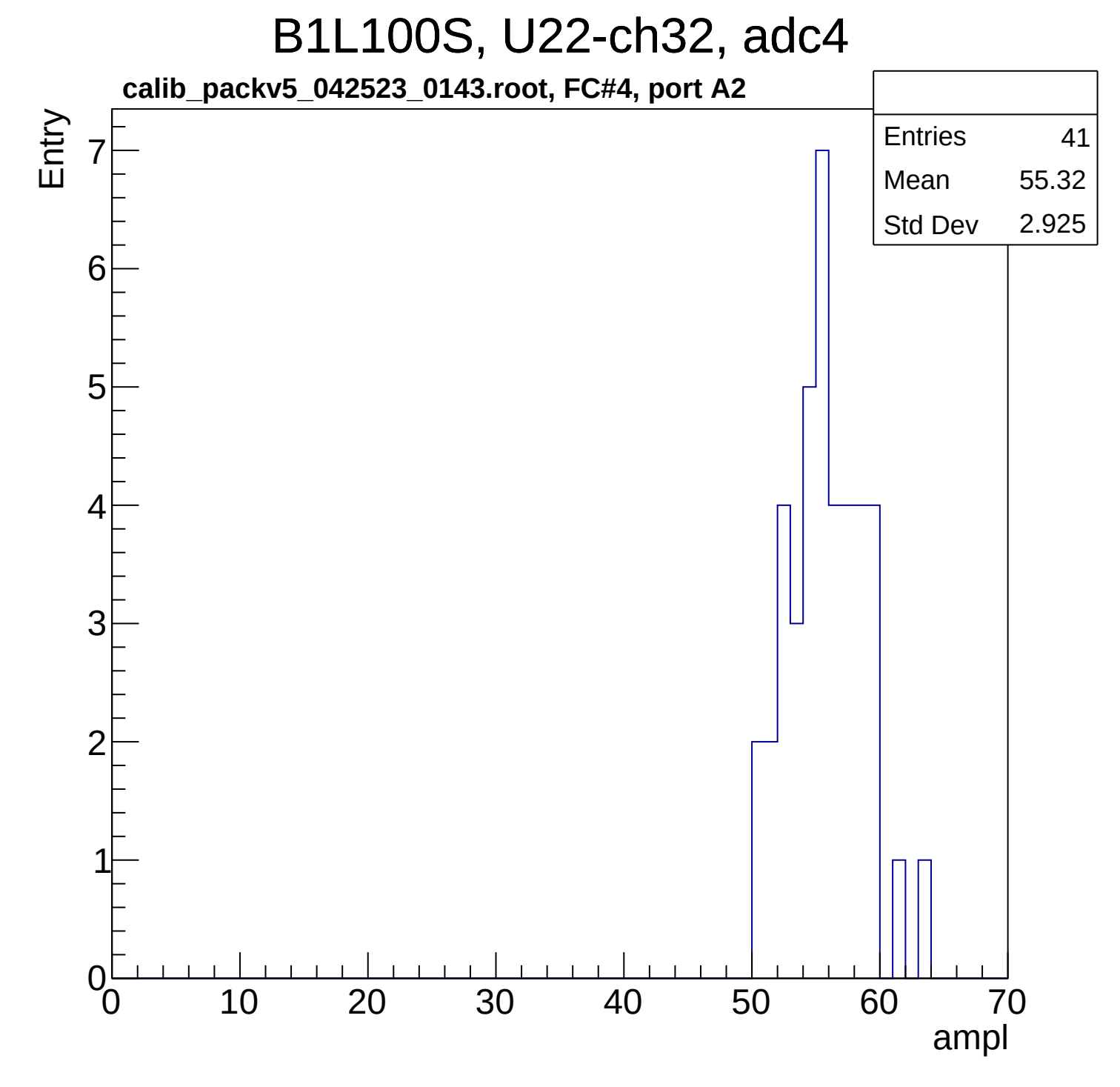
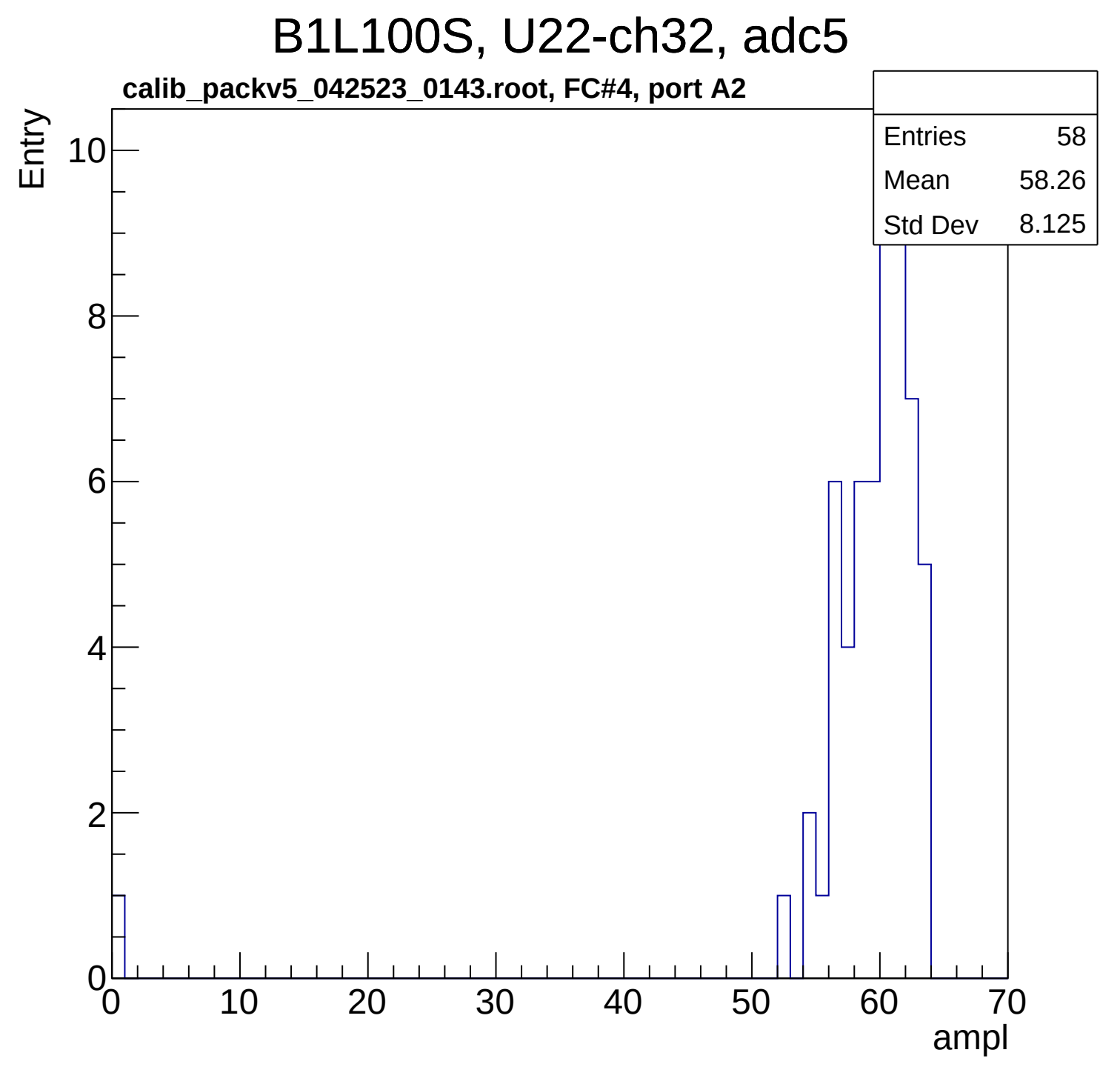
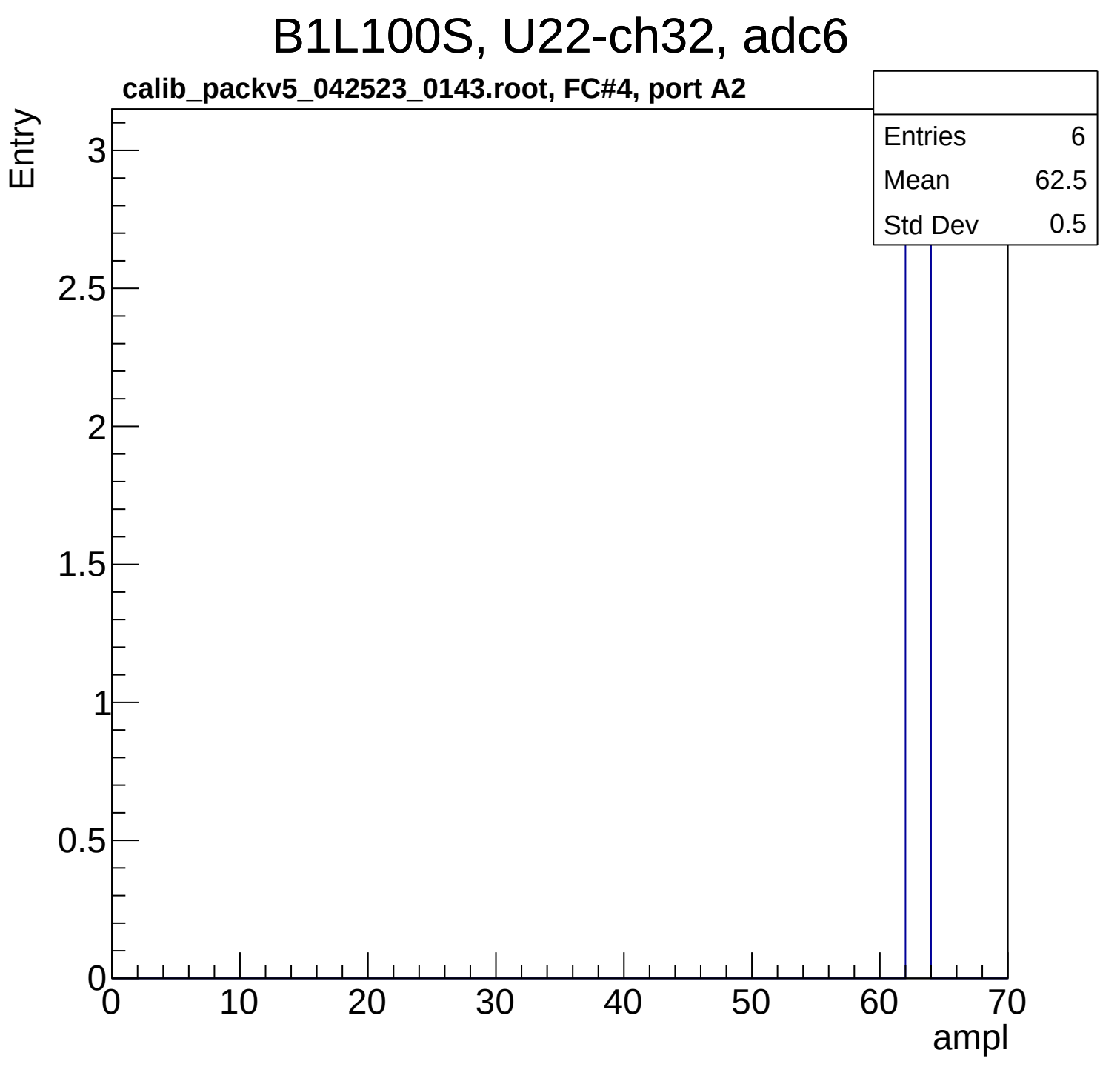
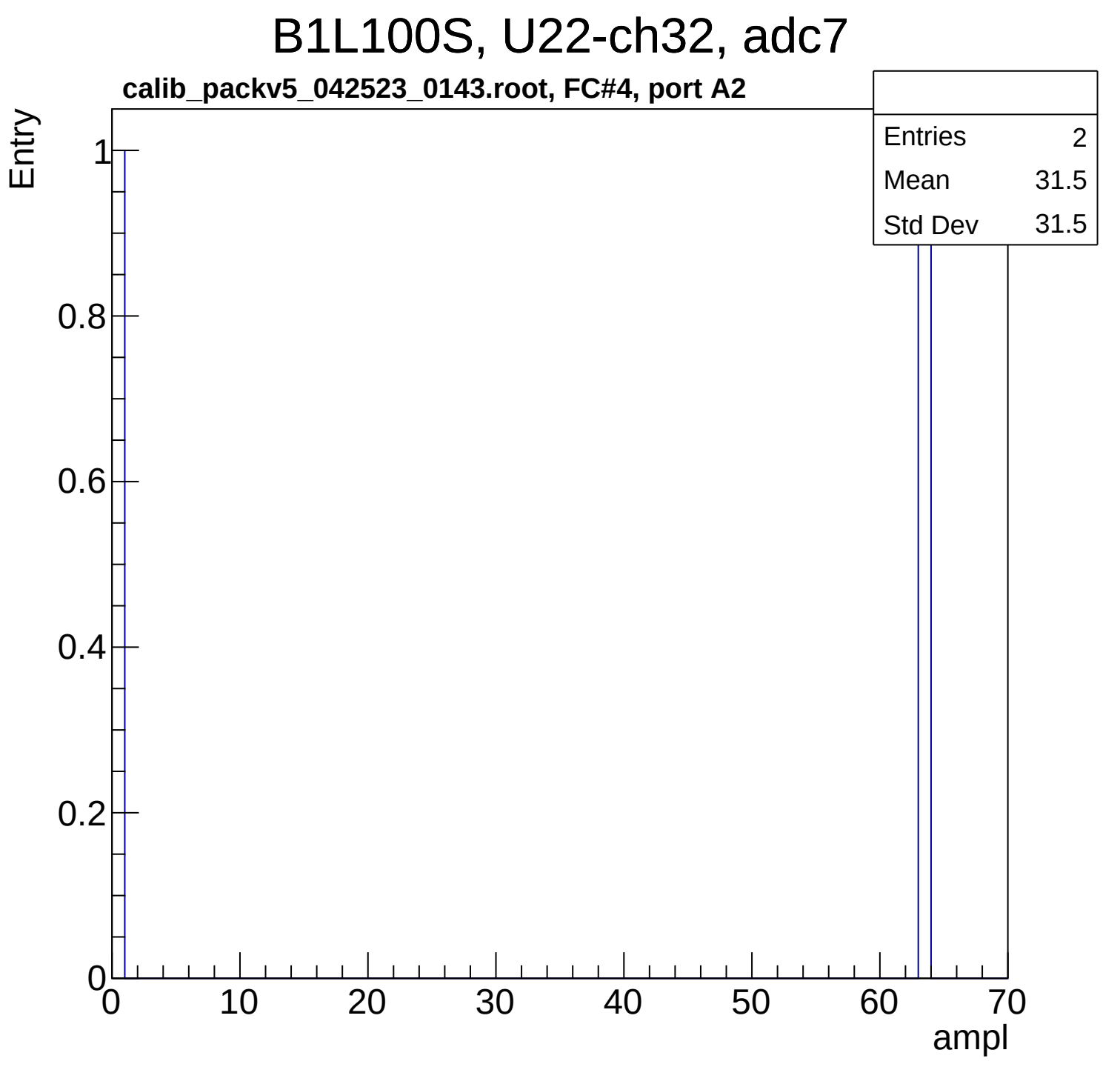

B1L100S, U22-ch32, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 49.75
Std Dev 3.314
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch32, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 55.32
Std Dev 2.925
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch32, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 58.26
Std Dev 8.125
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch32, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch32, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
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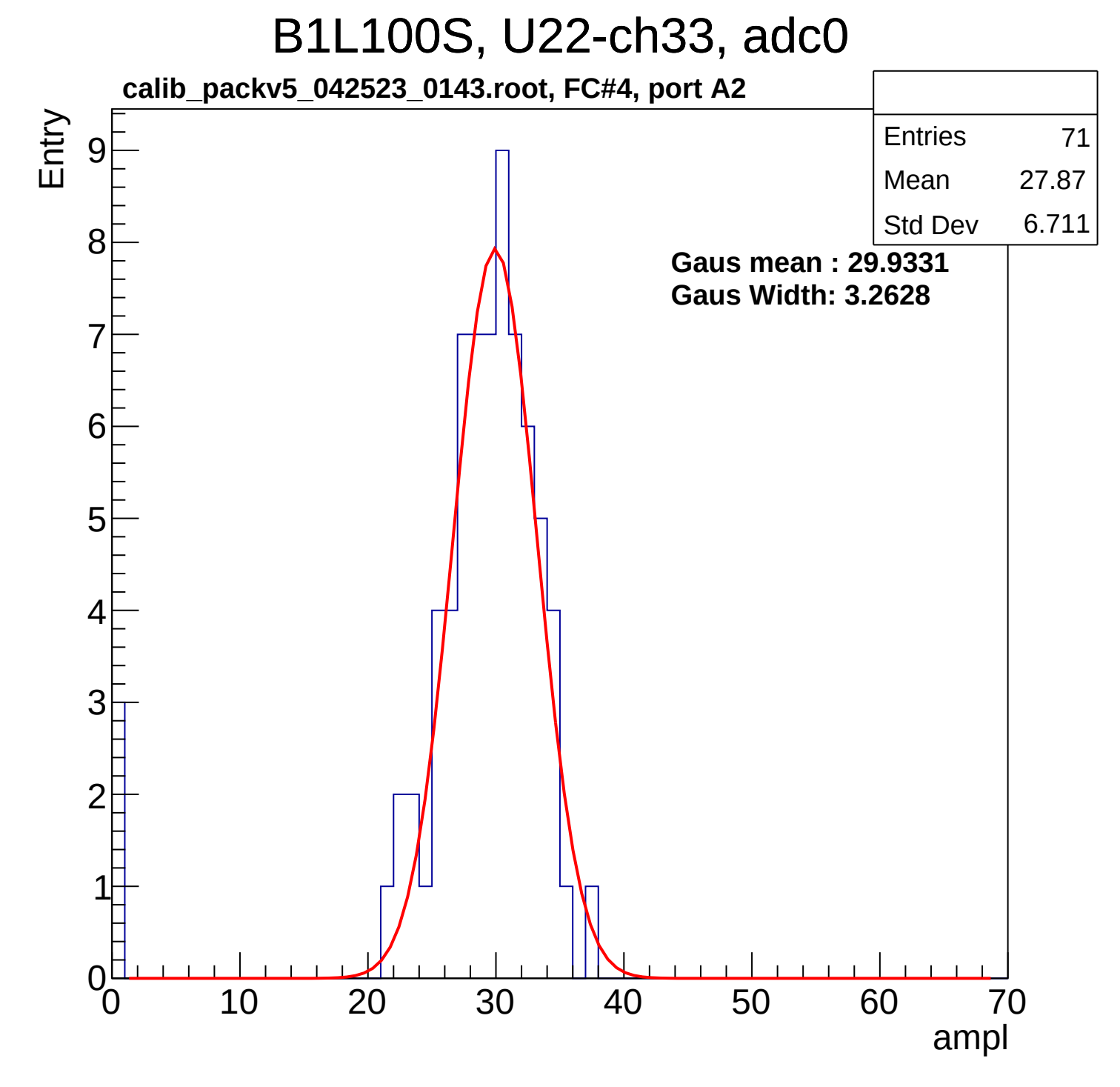
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch33, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 27.87
Std Dev 6.711
Gaus mean : 29.9331
Gaus Width: 3.2628
Entry
ampl
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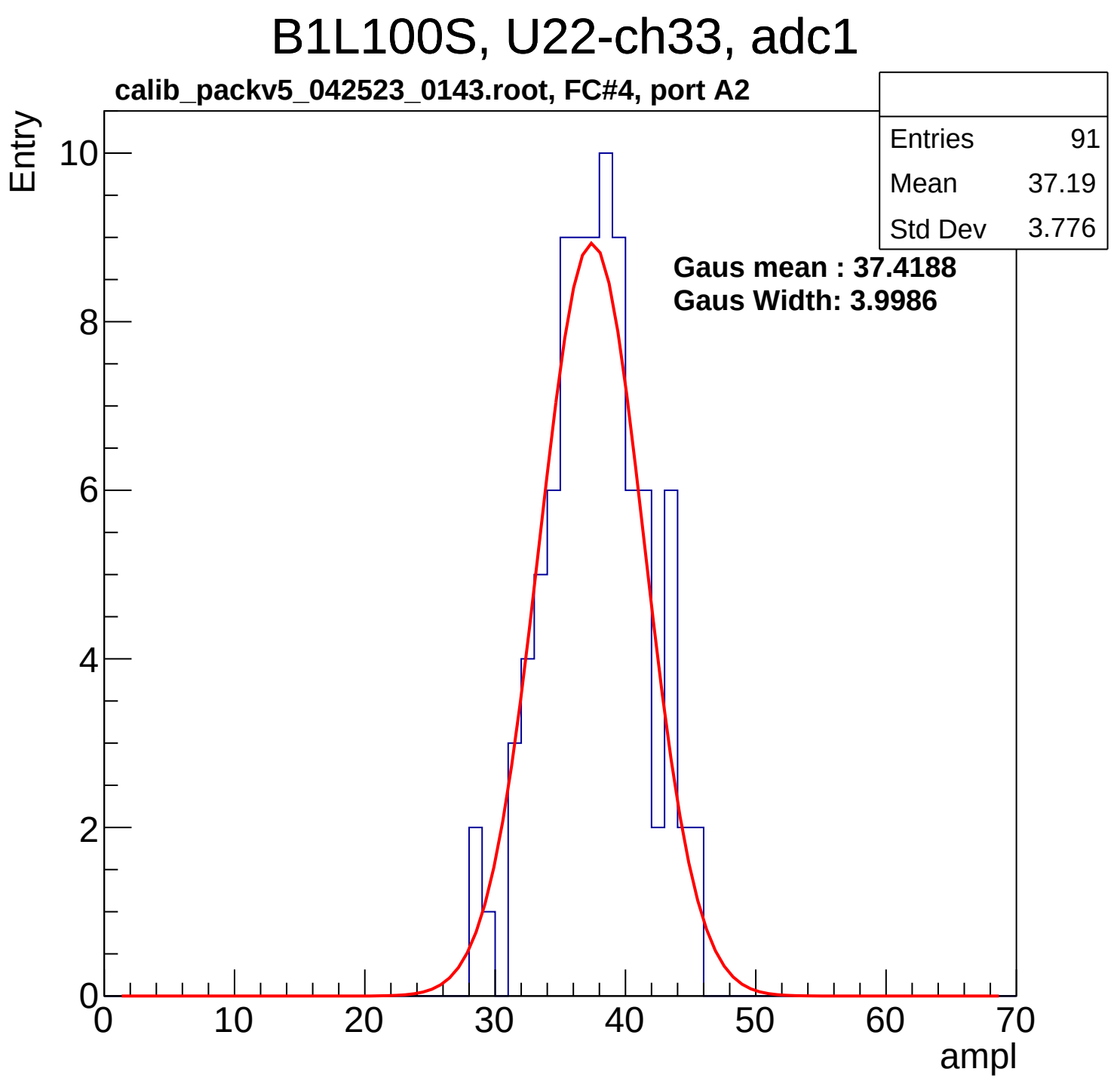
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch33, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 37.19
Std Dev 3.776
Gaus mean : 37.4188
Gaus Width: 3.9986
Entry
ampl
0
2
4
6
8
10
0
10
20
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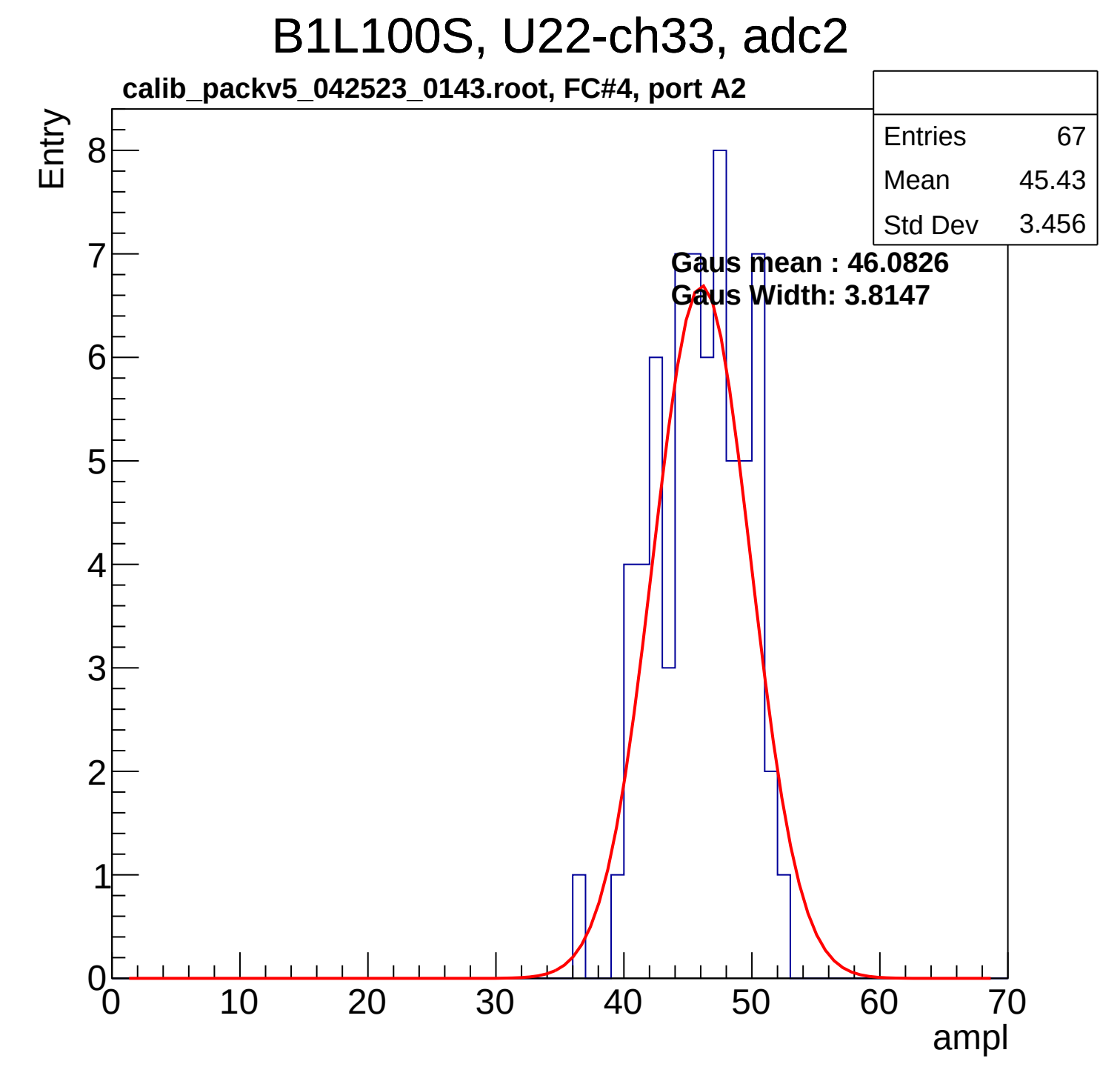
40
50
60
70


B1L100S, U22-ch33, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 45.43
Std Dev 3.456
Gaus mean : 46.0826
Gaus Width: 3.8147
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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60
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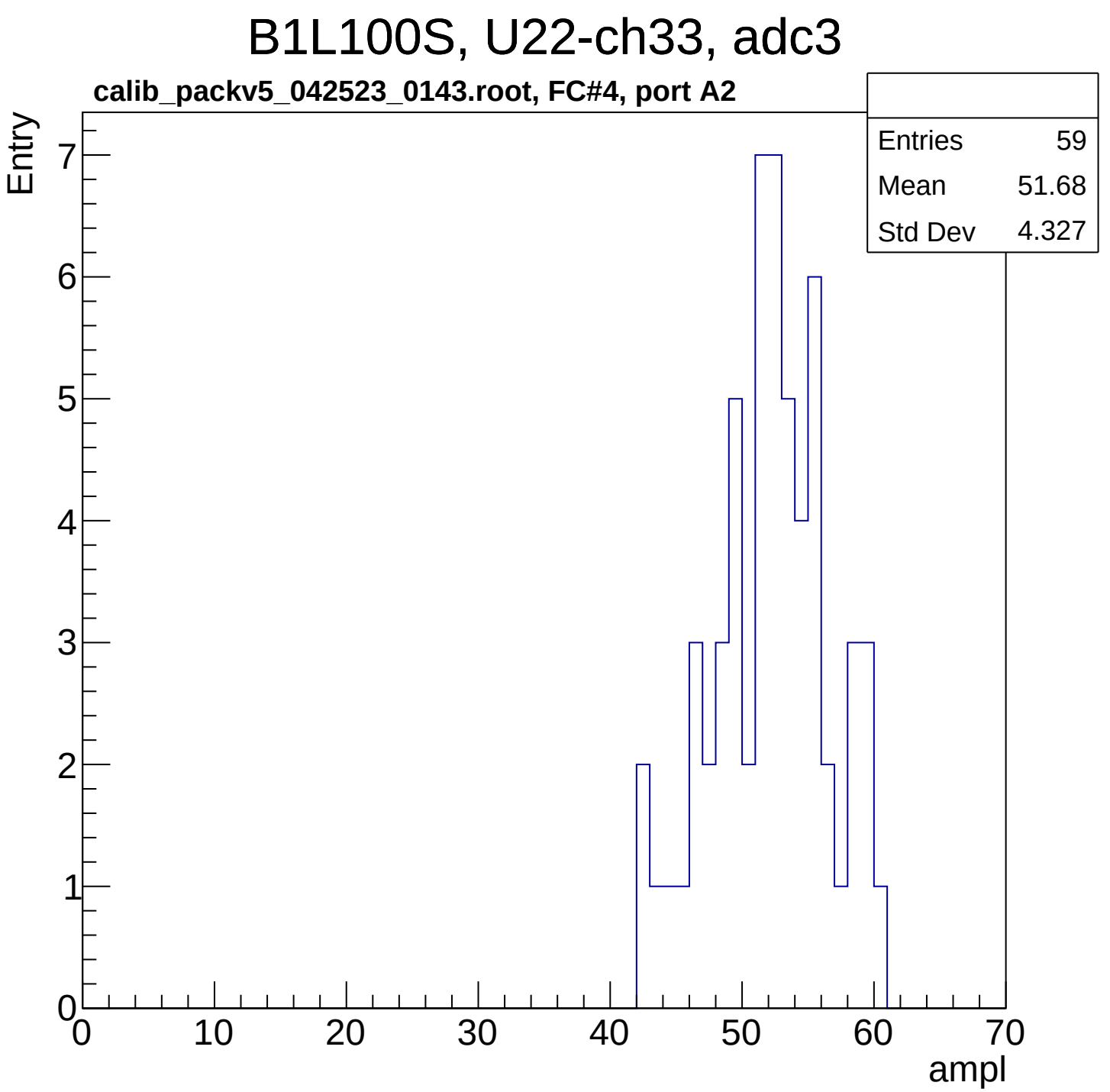
B1L100S, U22-ch33, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 51.68
Std Dev 4.327
Entry
ampl
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4
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0
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60
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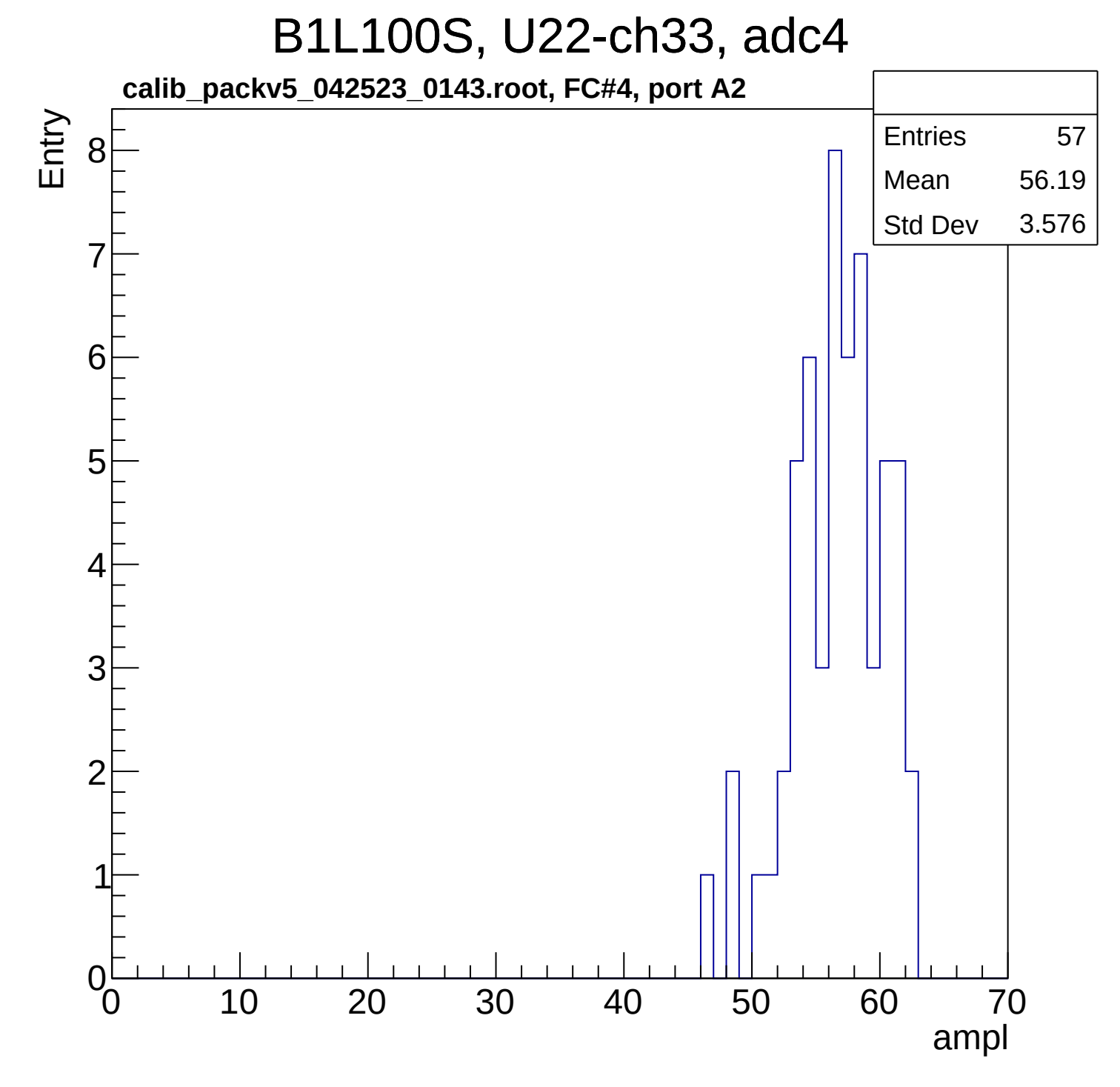
B1L100S, U22-ch33, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.19
Std Dev 3.576
Entry
ampl
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5
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0
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60
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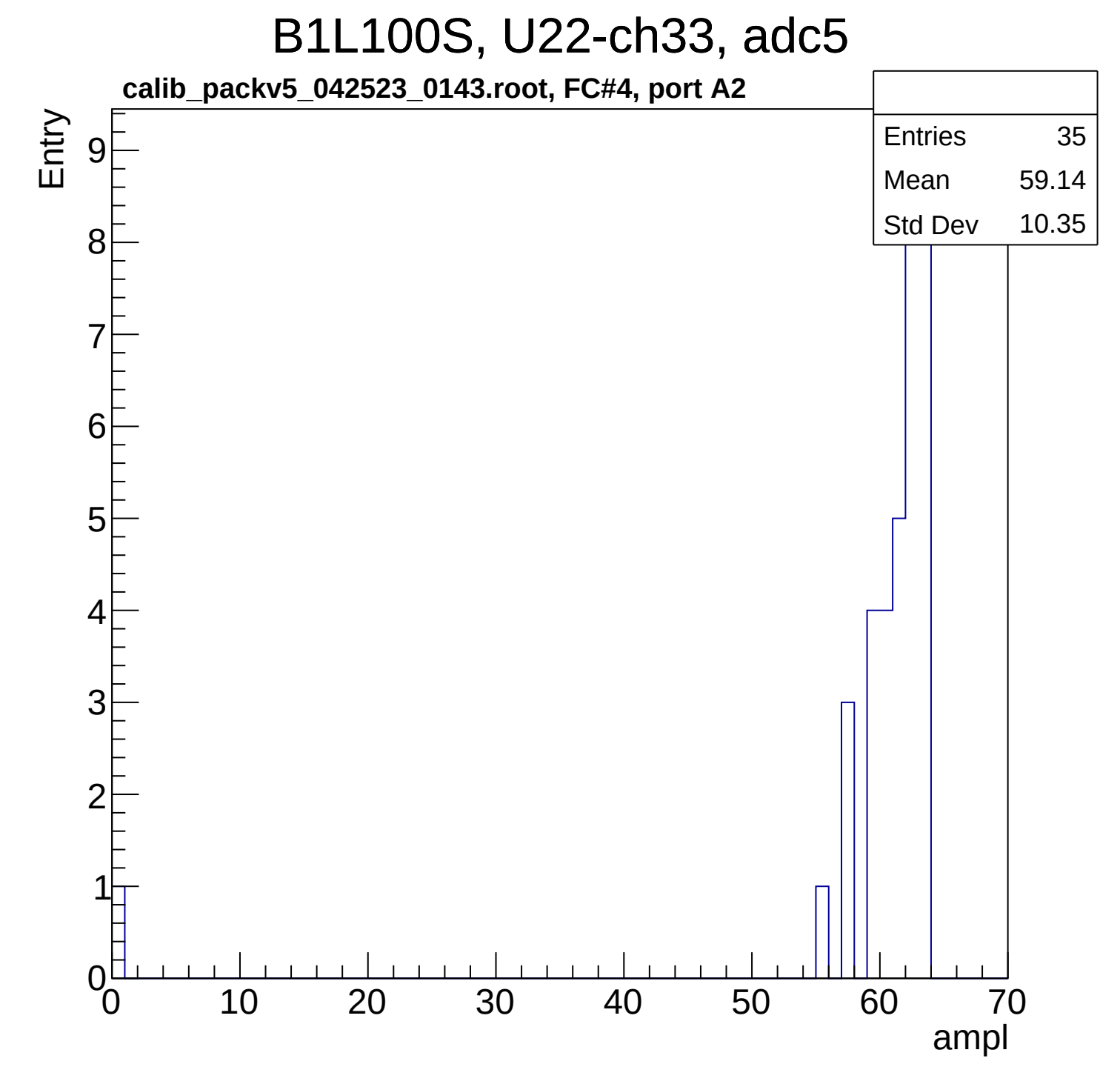
B1L100S, U22-ch33, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.14
Std Dev 10.35
Entry
ampl
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3
4
5
6
7
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0
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40
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60
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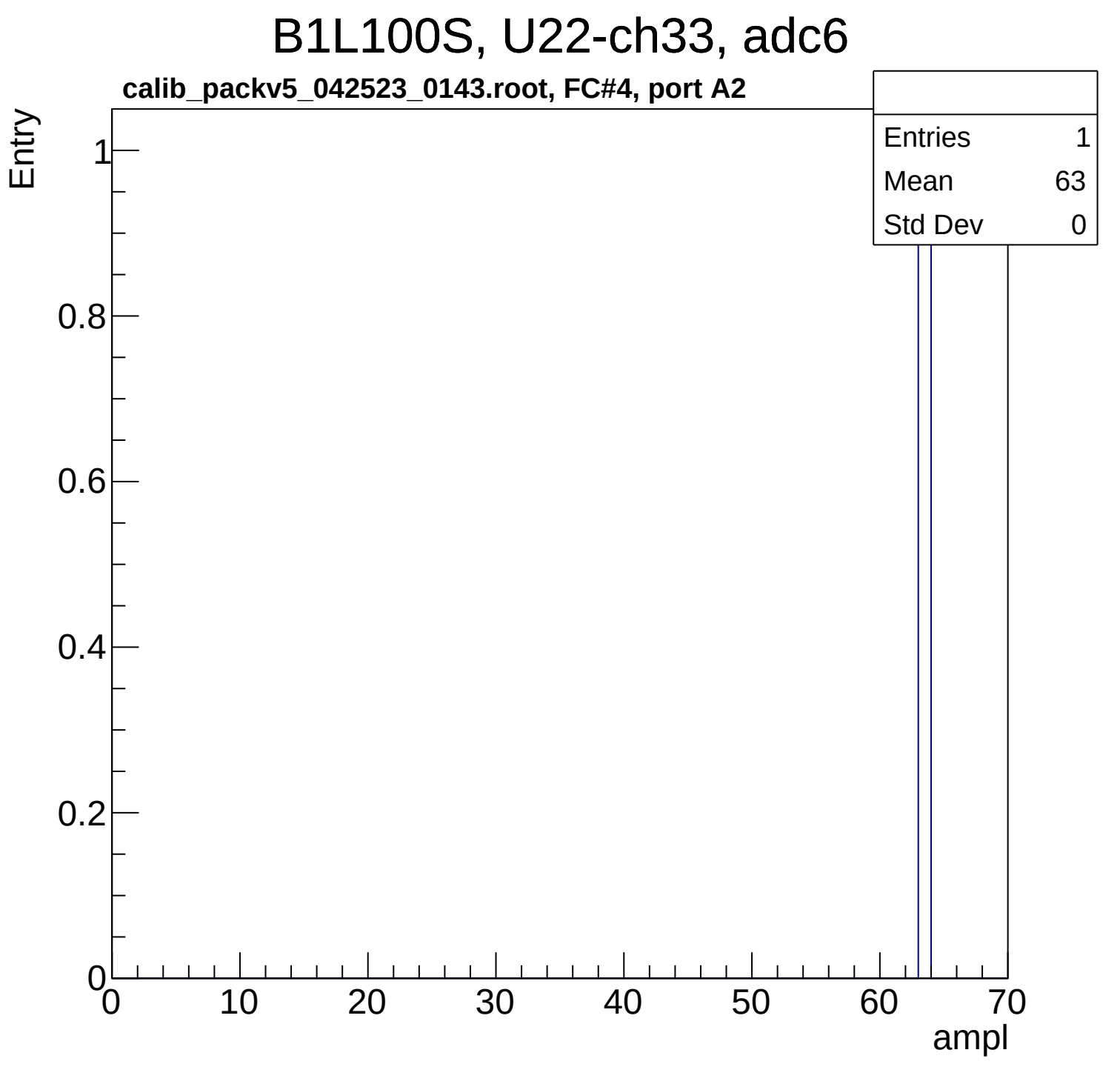
B1L100S, U22-ch33, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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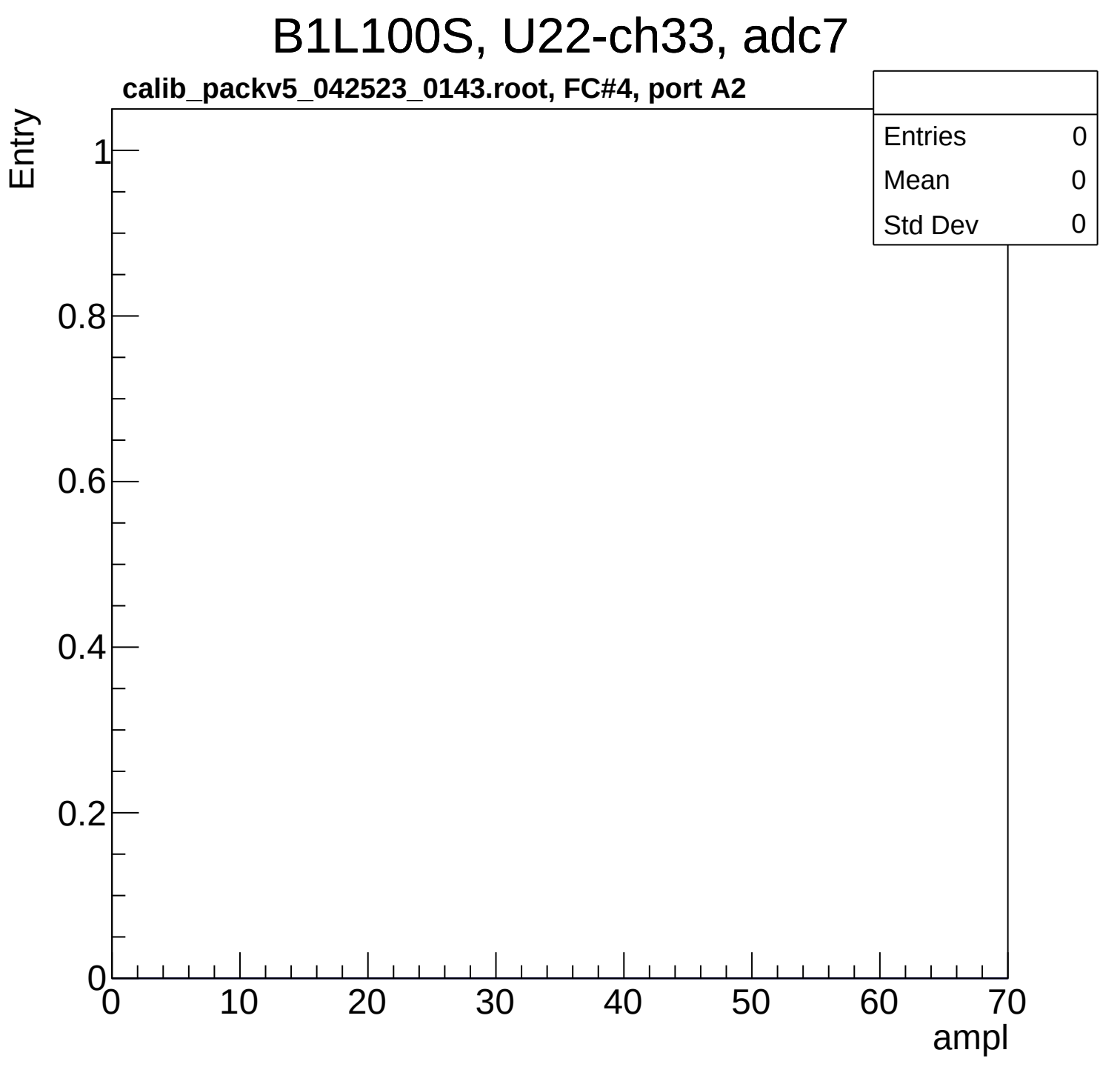
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch33, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
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10
20
30
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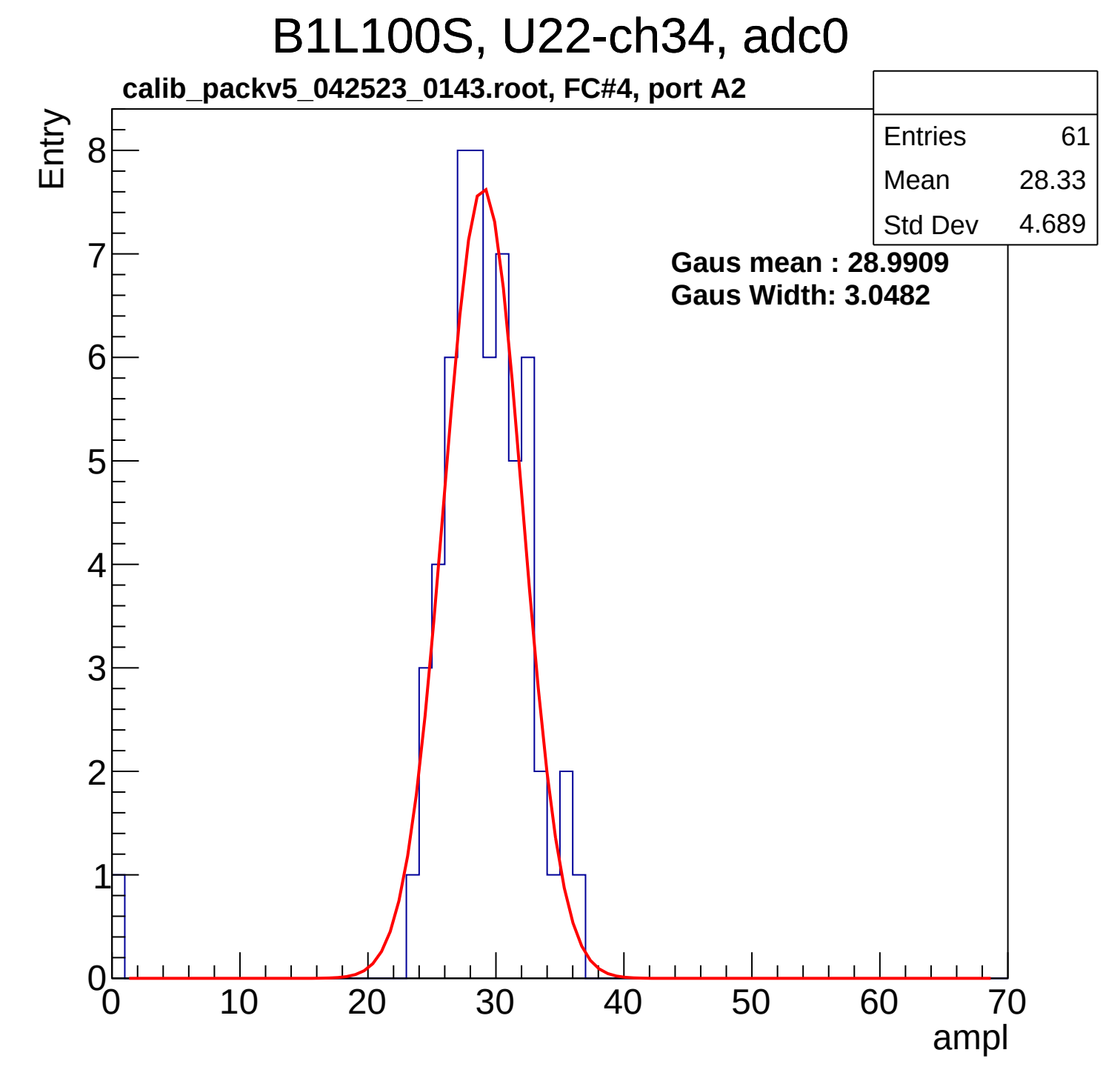
50
60
70
ampl


B1L100S, U22-ch34, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.33
Std Dev 4.689
Gaus mean : 28.9909
Gaus Width: 3.0482
Entry
ampl
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2
3
4
5
6
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0
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60
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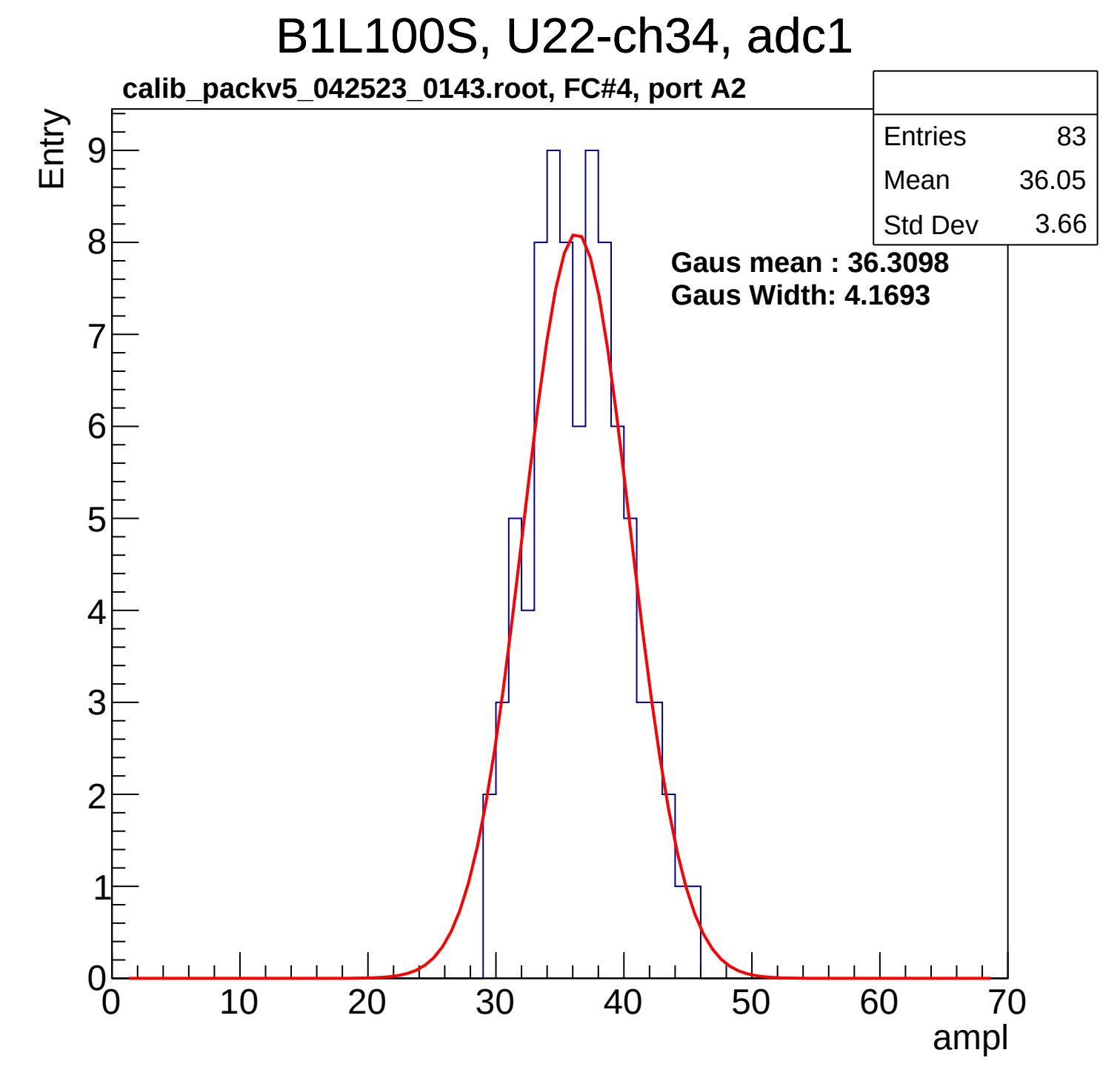
B1L100S, U22-ch34, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 36.05
Std Dev 3.66
Gaus mean : 36.3098
Gaus Width: 4.1693
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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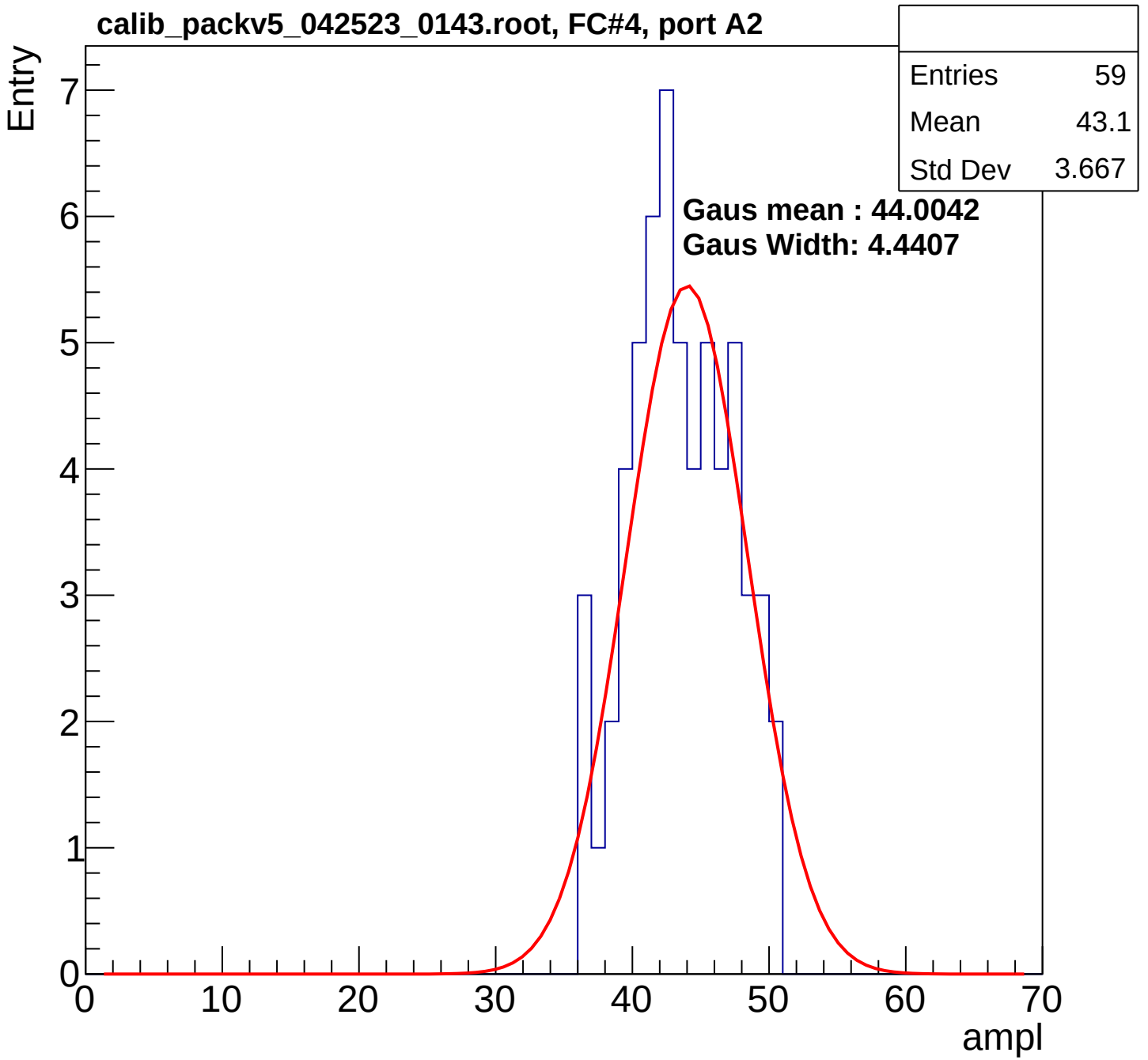
B1L100S, U22-ch34, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.1
Std Dev 3.667
Gaus mean : 44.0042
Gaus Width: 4.4407
Entry
ampl
0
1
2
3
4
5
6
7
0
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40
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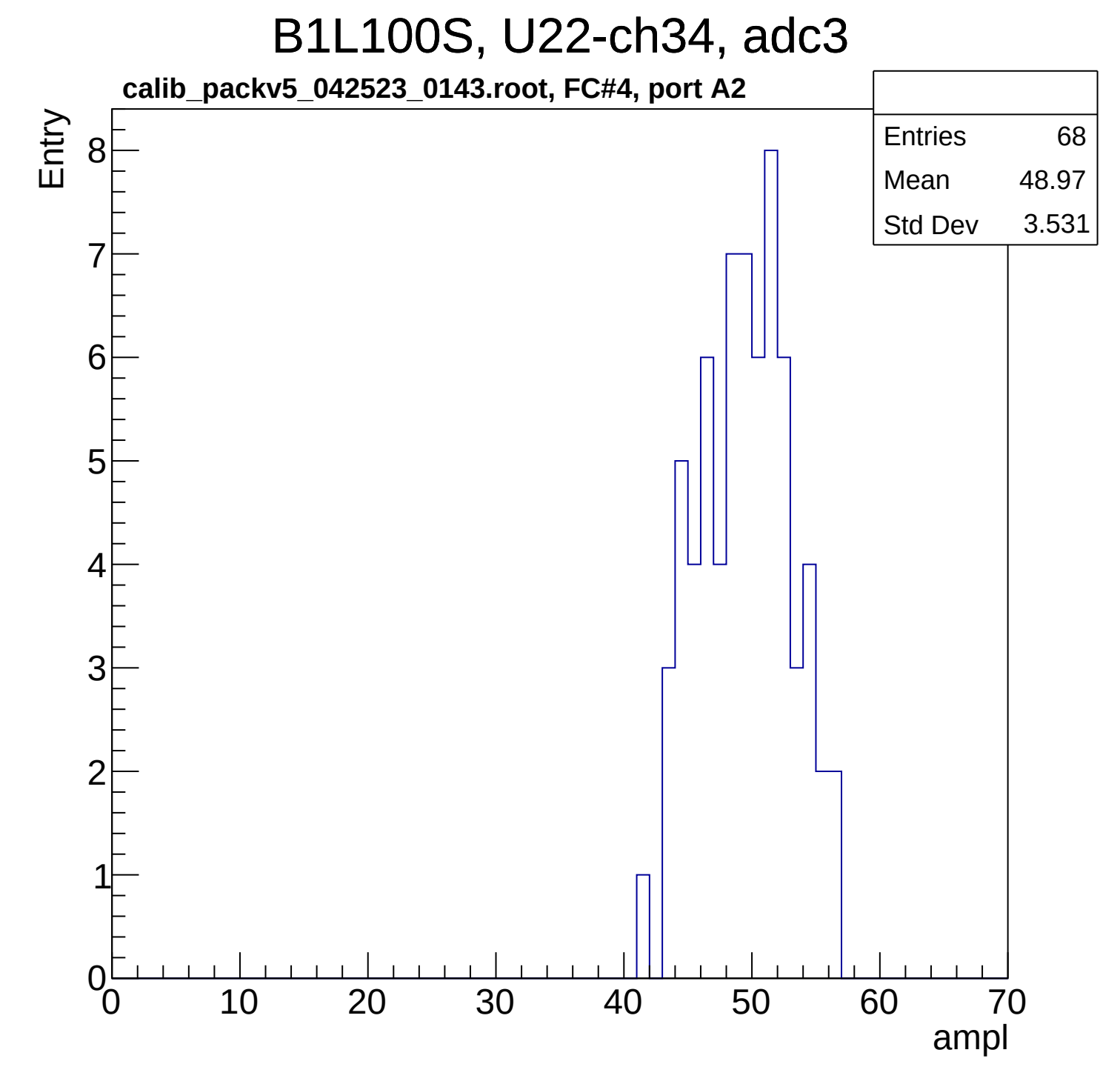
60
70


B1L100S, U22-ch34, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 48.97
Std Dev 3.531
Entry
ampl
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3
4
5
6
7
8
0
10
20
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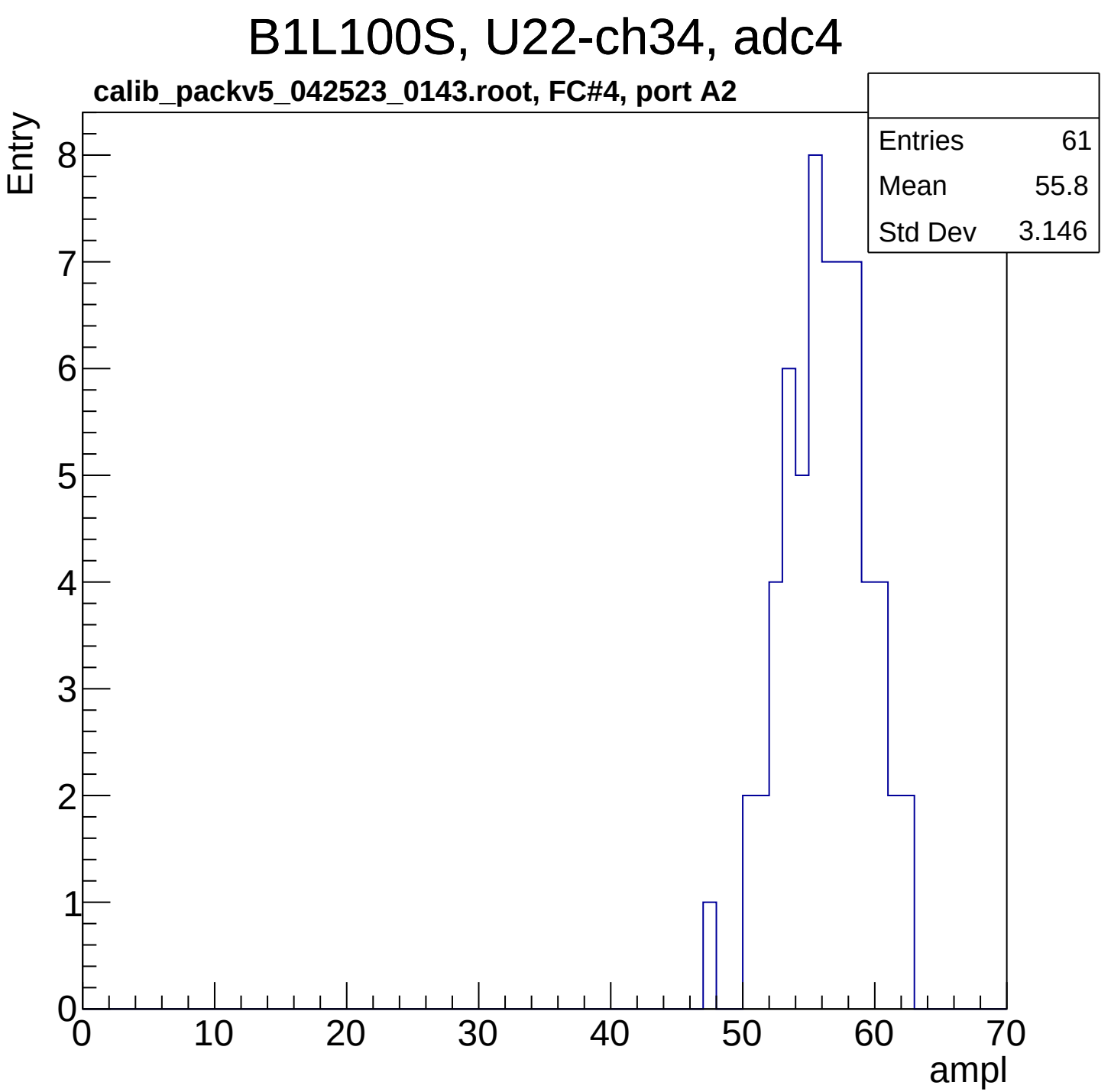
40
50
60
70


B1L100S, U22-ch34, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.8
Std Dev 3.146
Entry
ampl
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3
4
5
6
7
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0
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60
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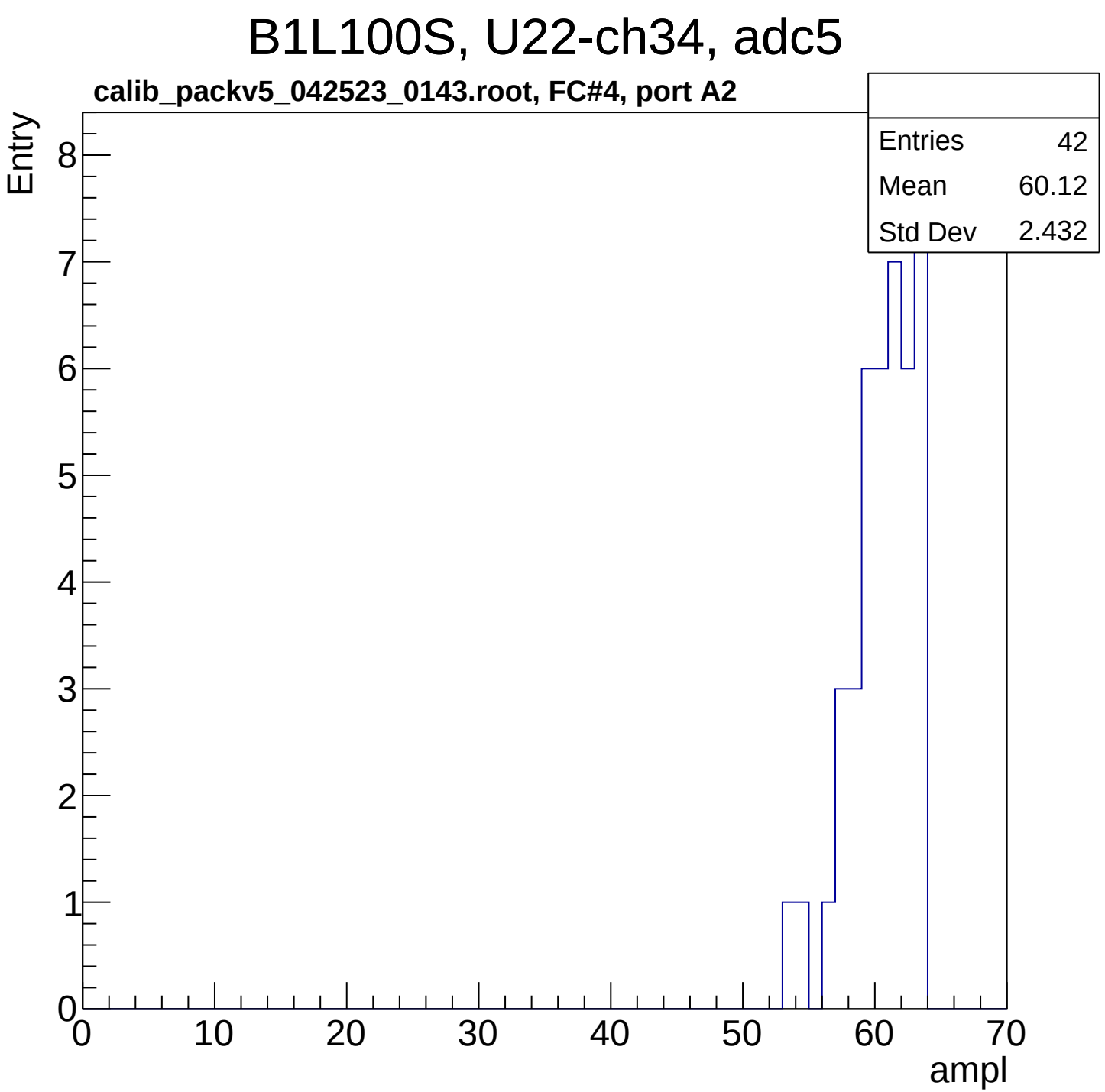
B1L100S, U22-ch34, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 60.12
Std Dev 2.432
Entry
ampl
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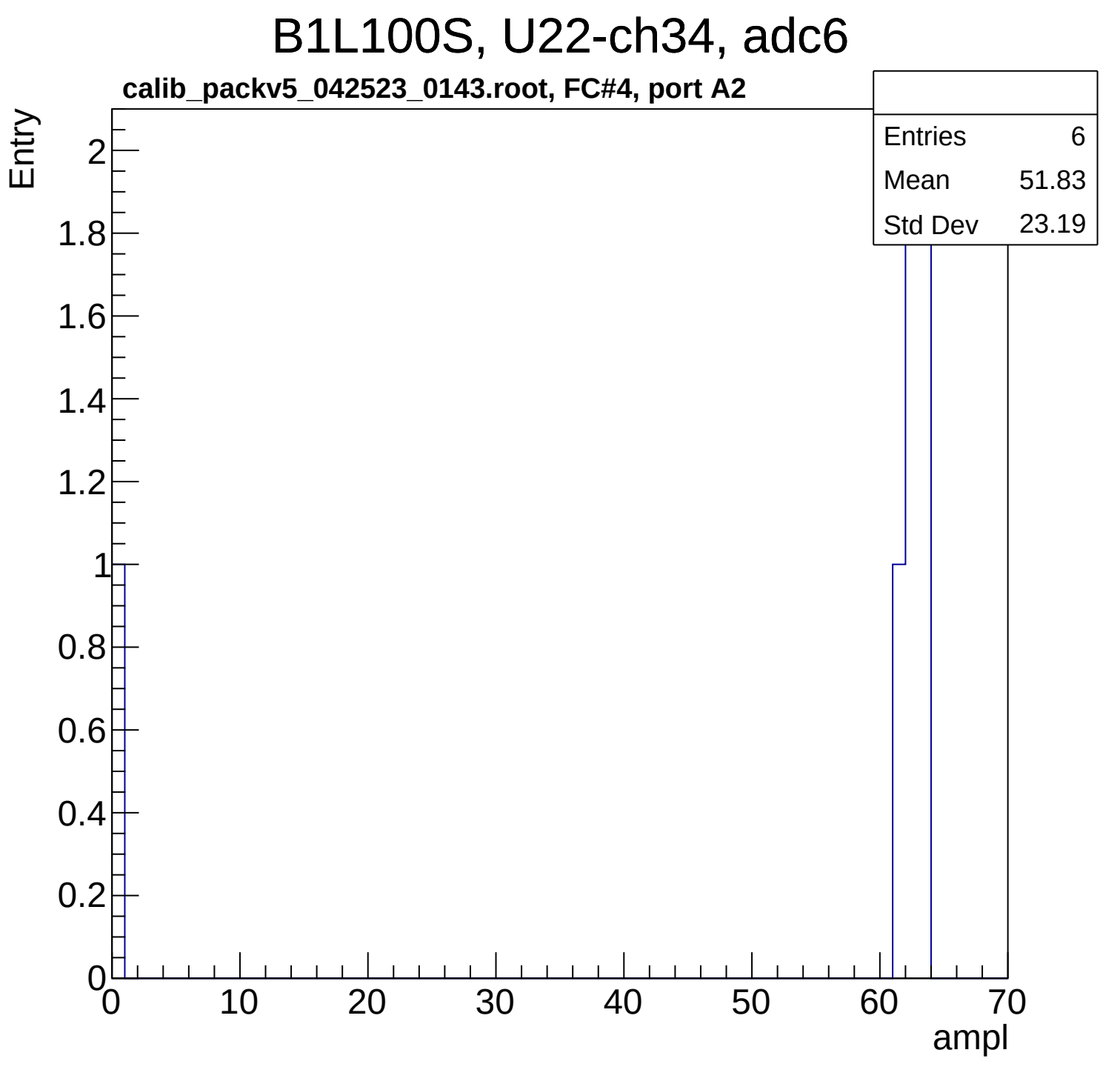
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch34, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 51.83
Std Dev 23.19
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
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60
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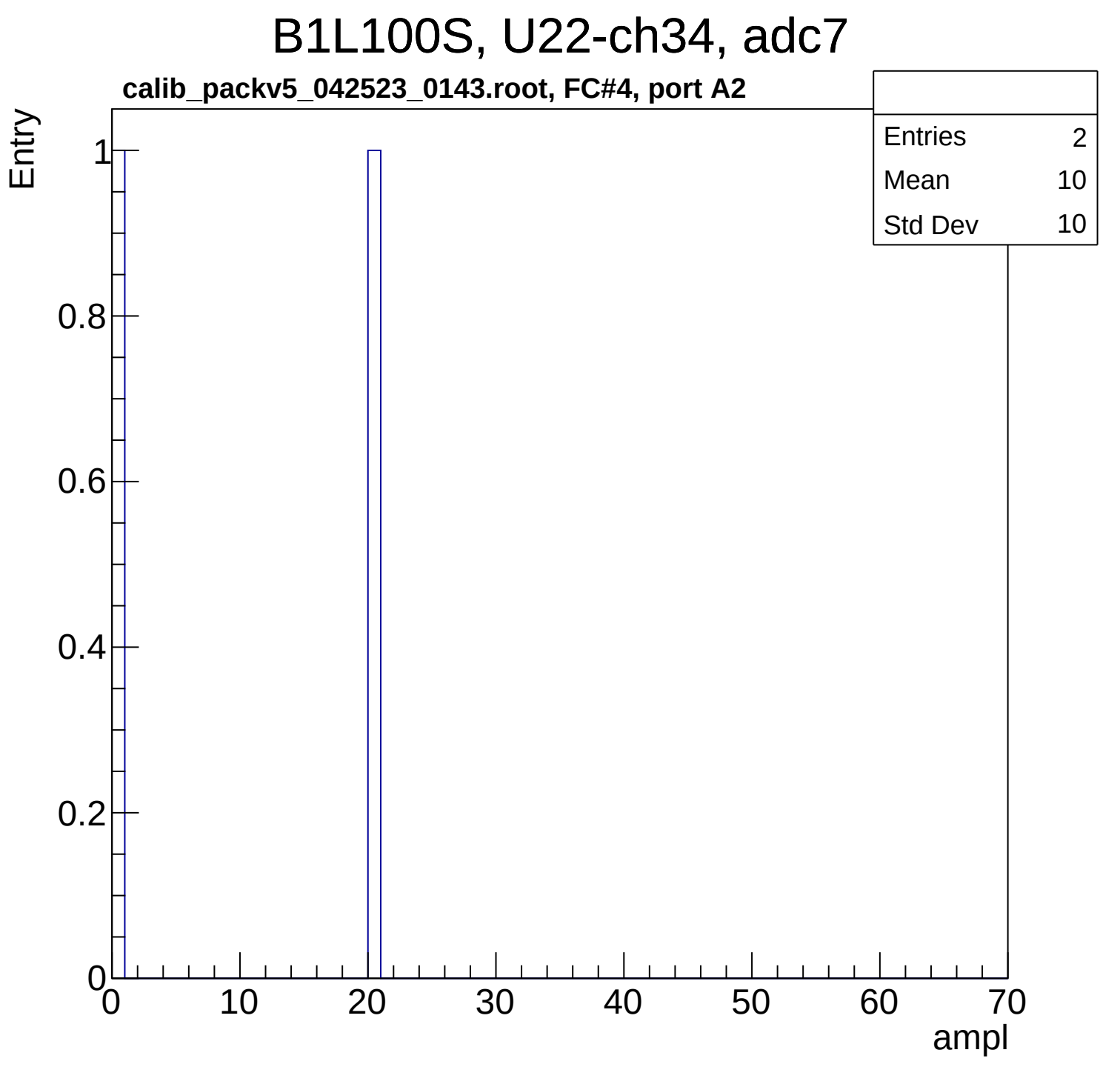
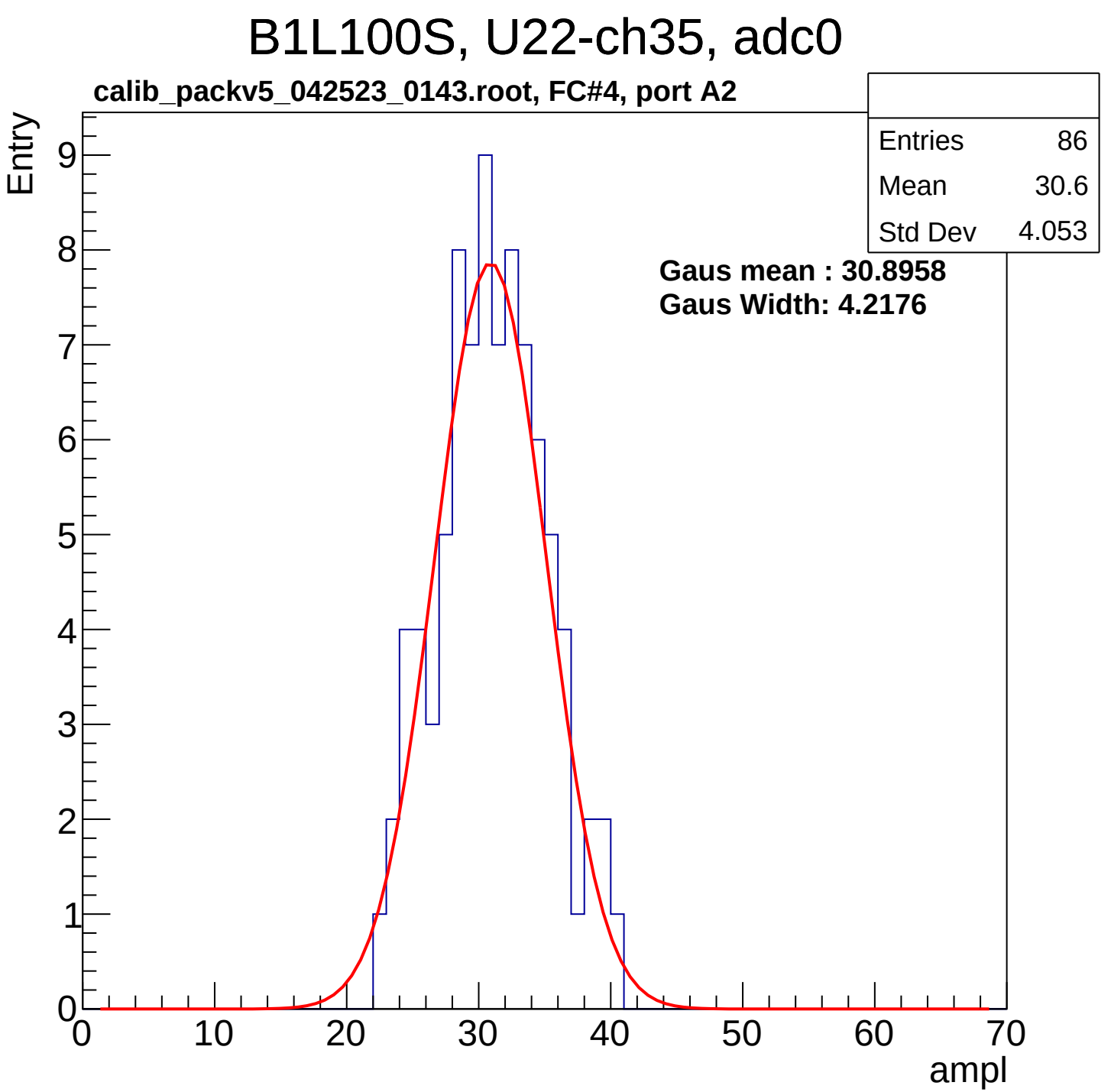

B1L100S, U22-ch34, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch35, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 30.6
Std Dev 4.053
Gaus mean : 30.8958
Gaus Width: 4.2176
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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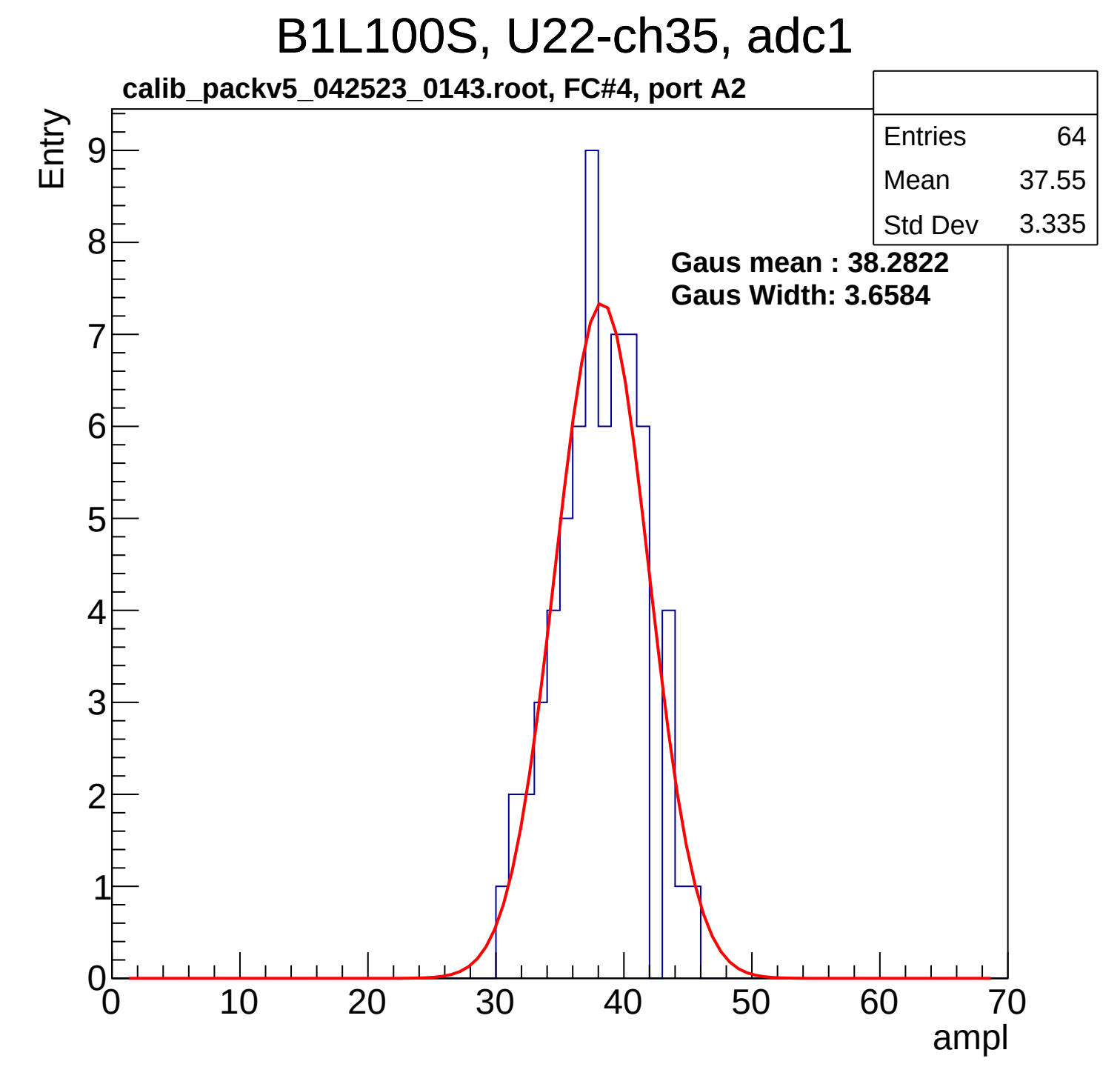
B1L100S, U22-ch35, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.55
Std Dev 3.335
Gaus mean : 38.2822
Gaus Width: 3.6584
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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60
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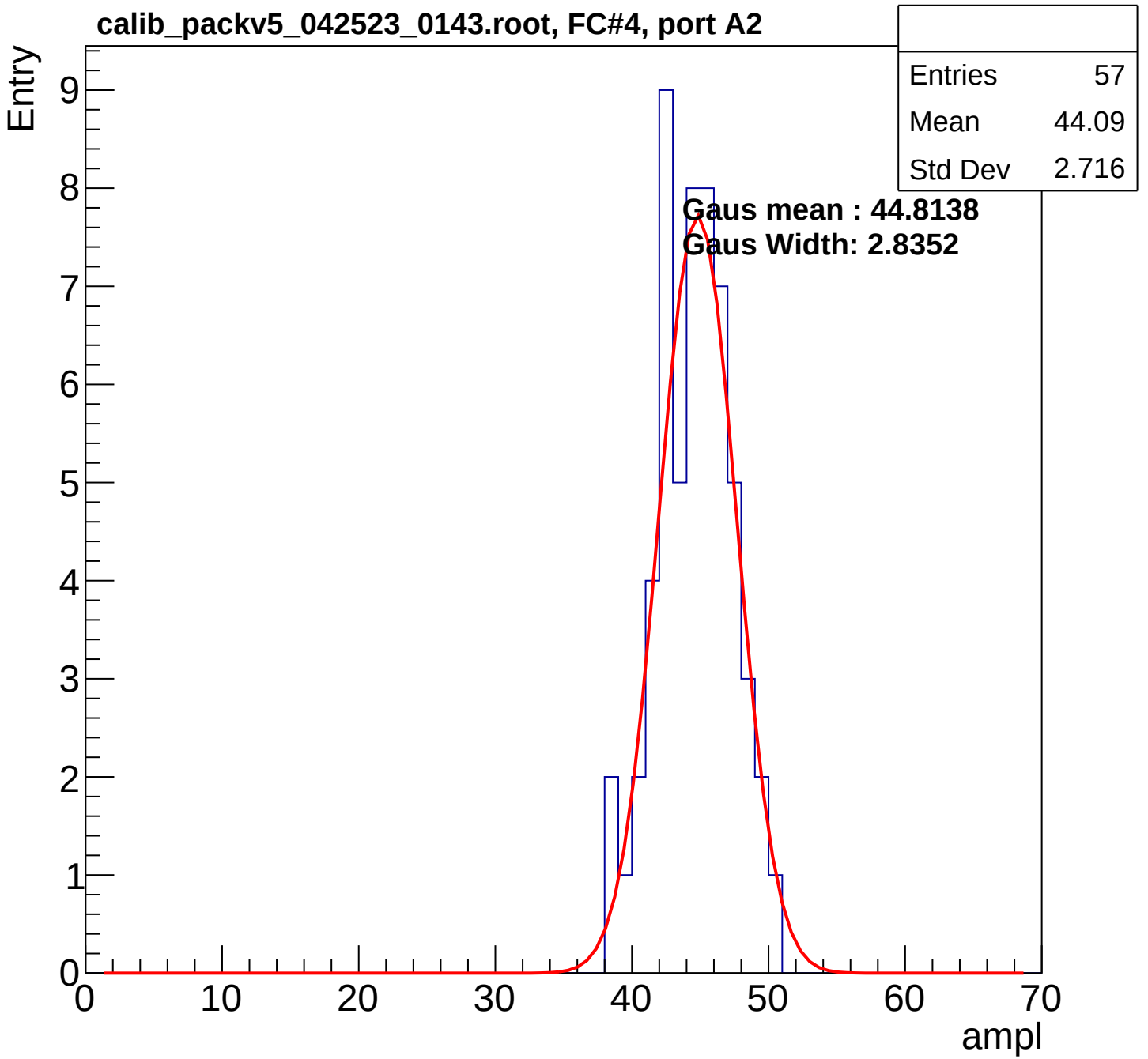
B1L100S, U22-ch35, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 44.09
Std Dev 2.716
Gaus mean : 44.8138
Gaus Width: 2.8352
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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60
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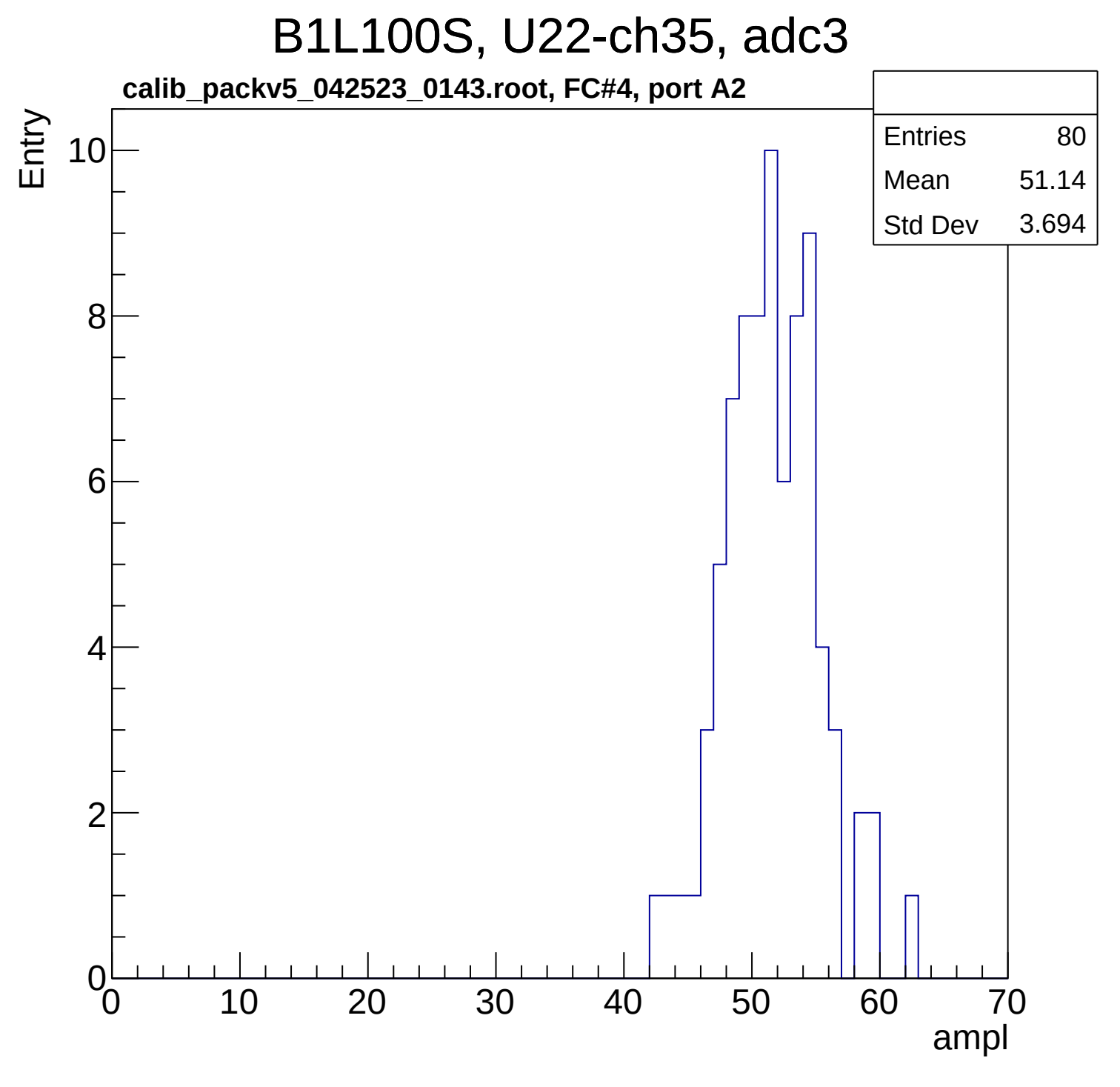
B1L100S, U22-ch35, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 51.14
Std Dev 3.694
Entry
ampl
0
2
4
6
8
10
0
10
20
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40
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60
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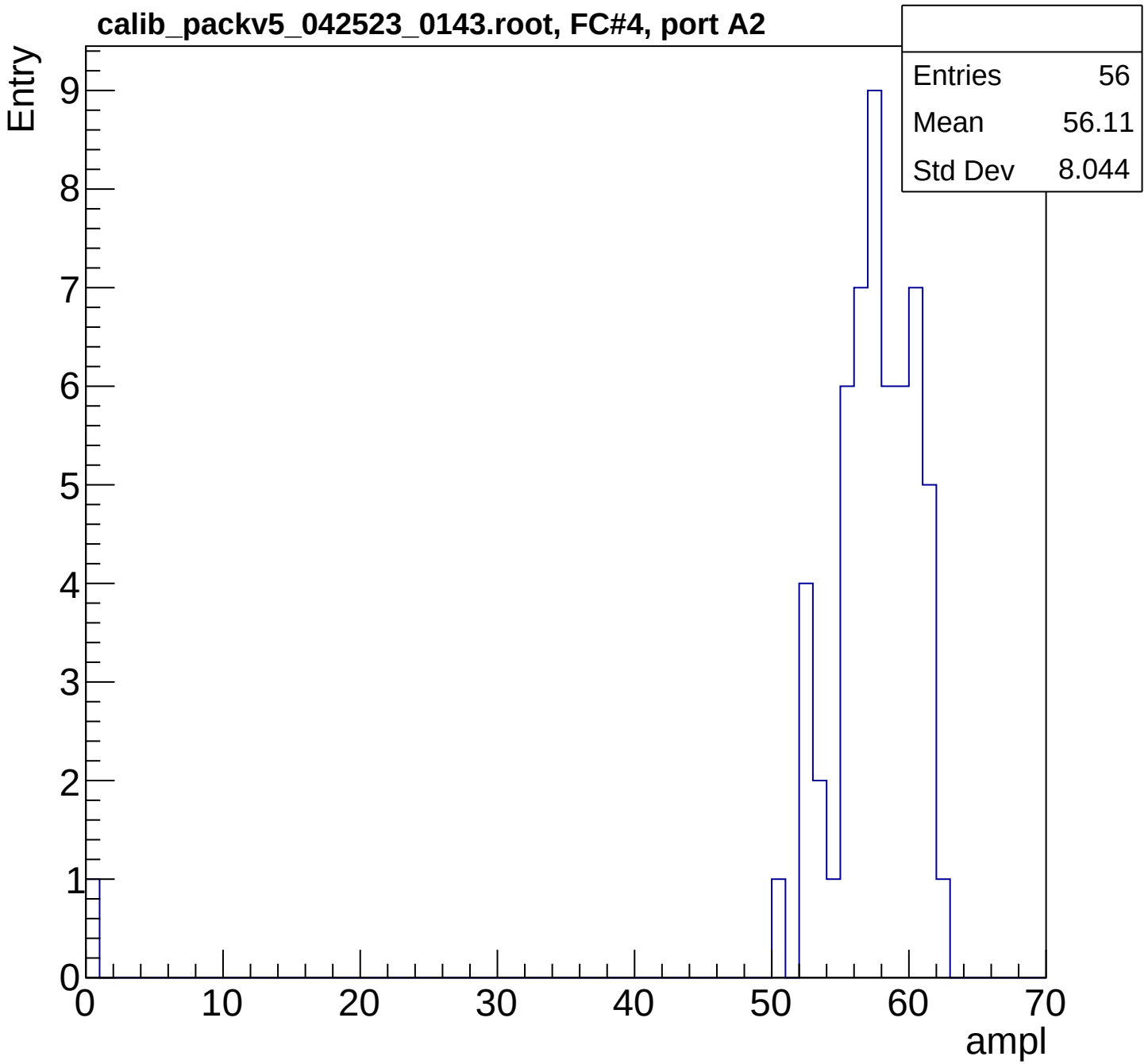
B1L100S, U22-ch35, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 56.11
Std Dev 8.044
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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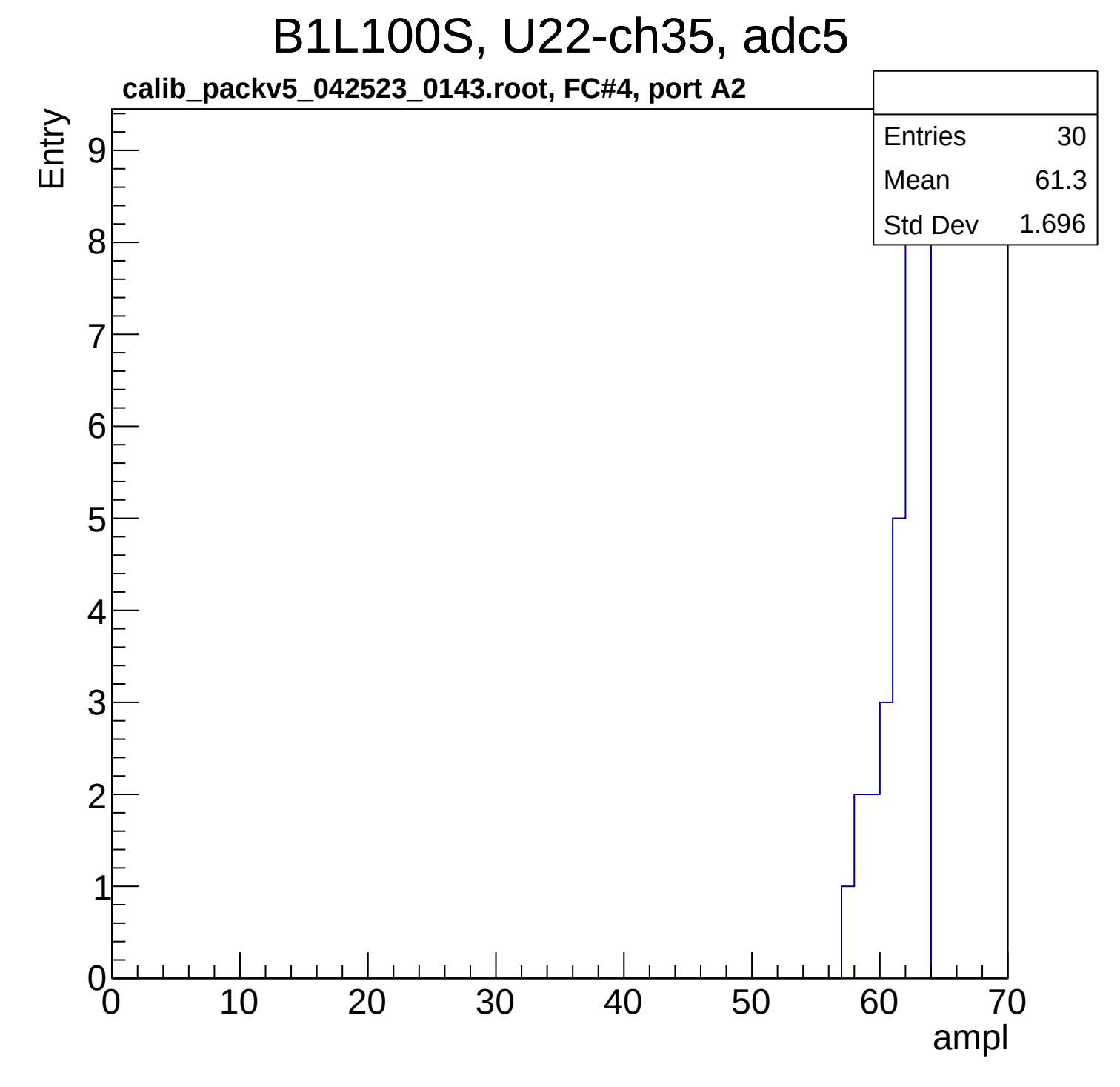
B1L100S, U22-ch35, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 61.3
Std Dev 1.696
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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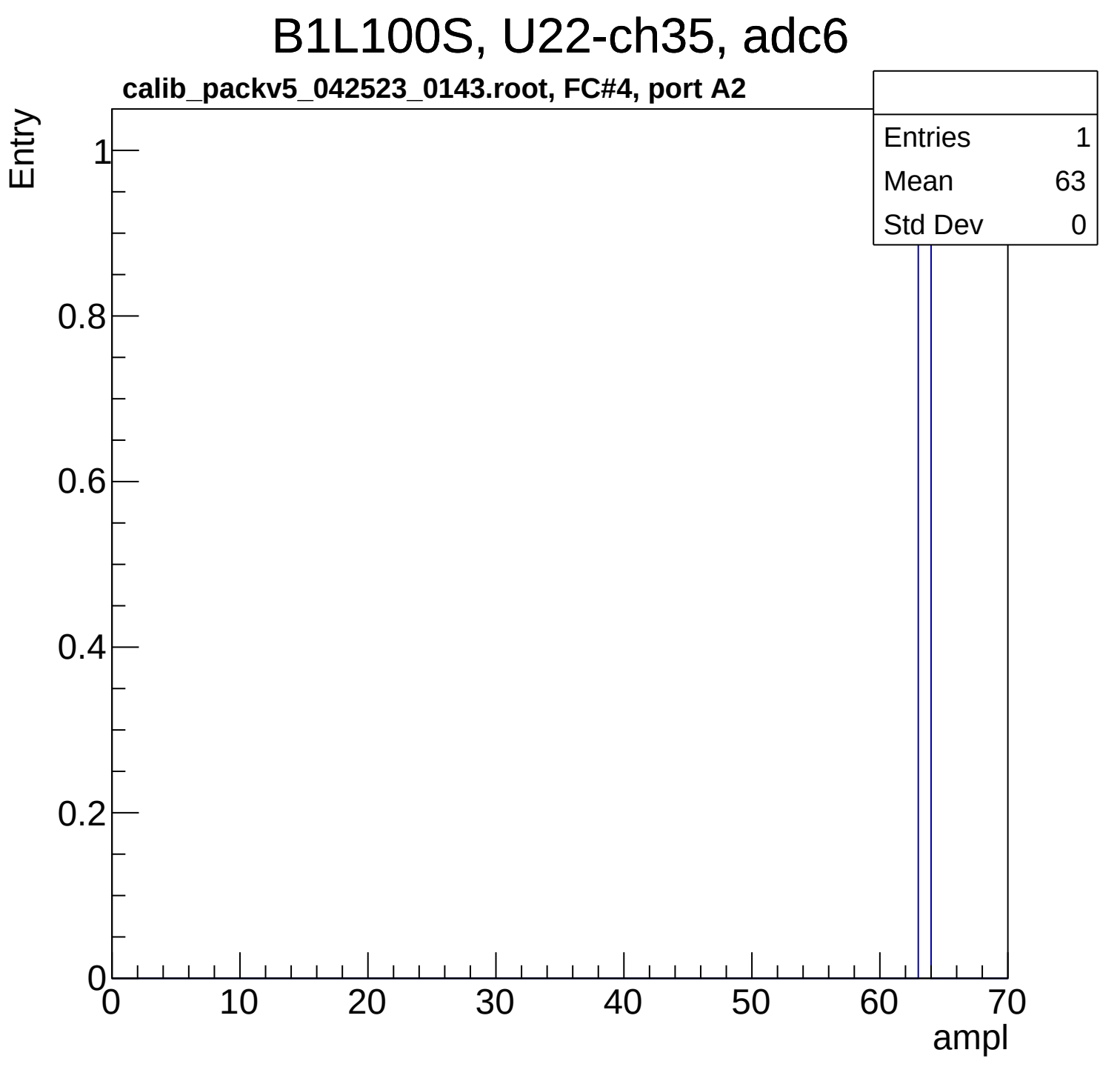
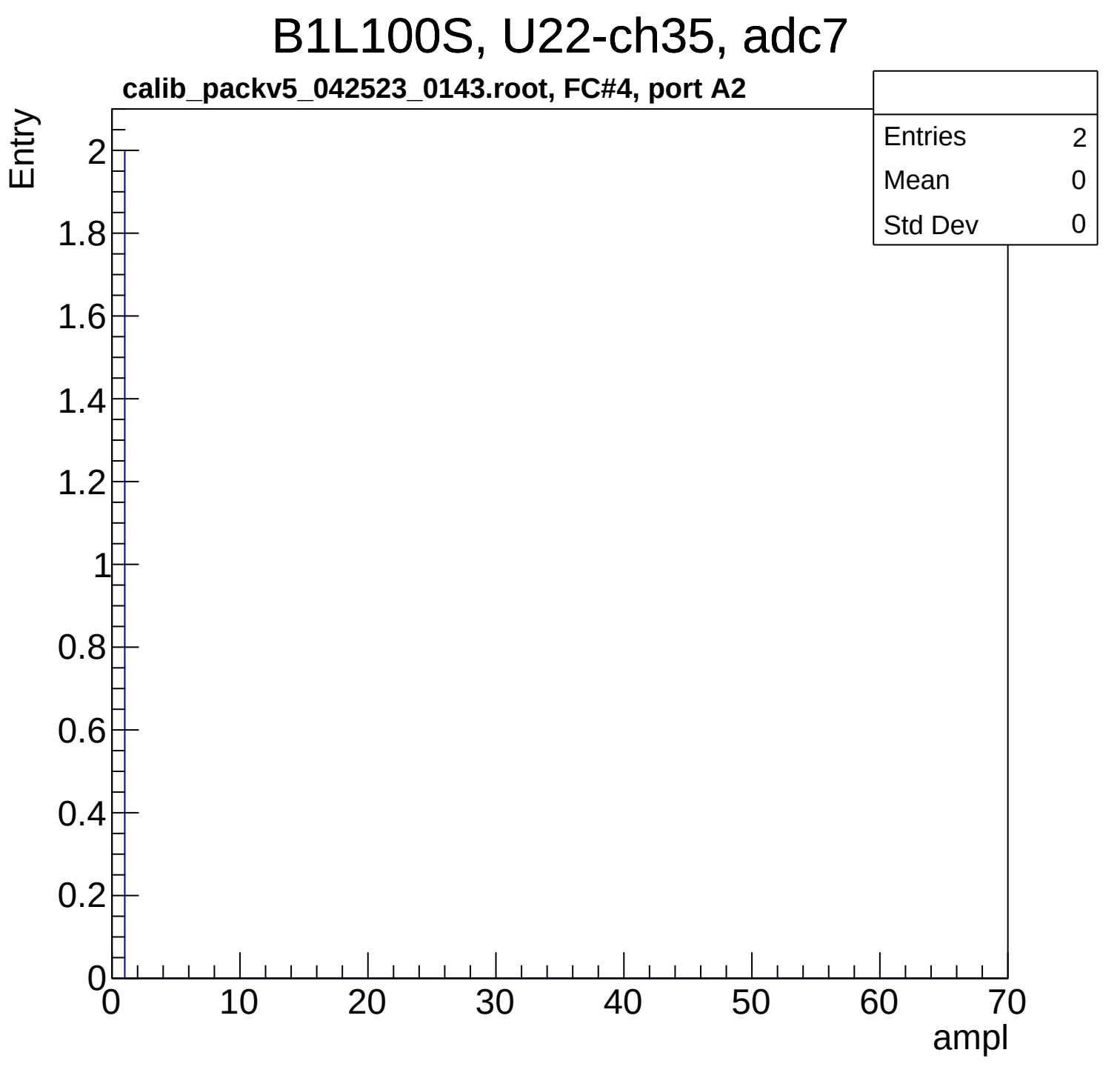
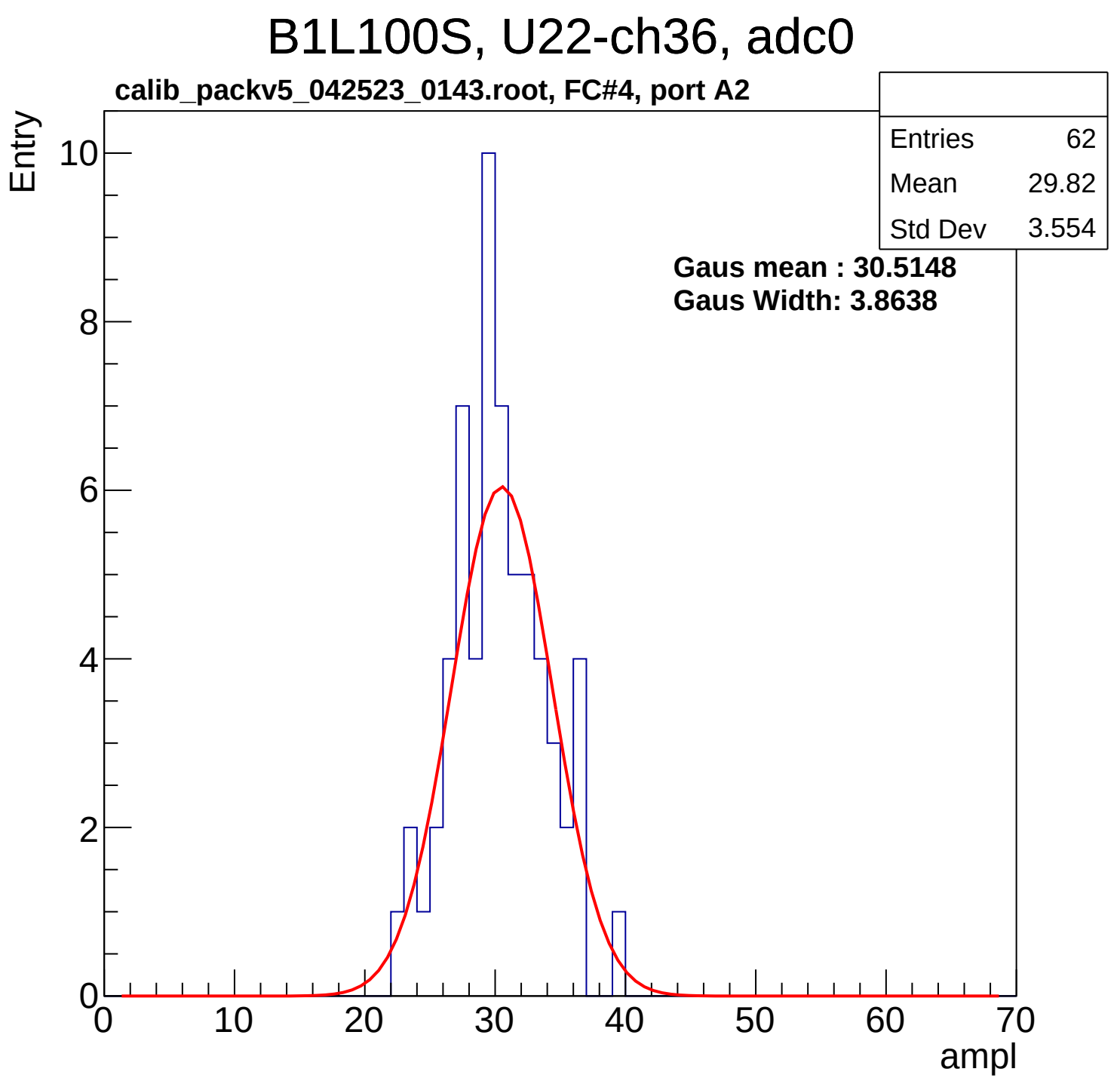
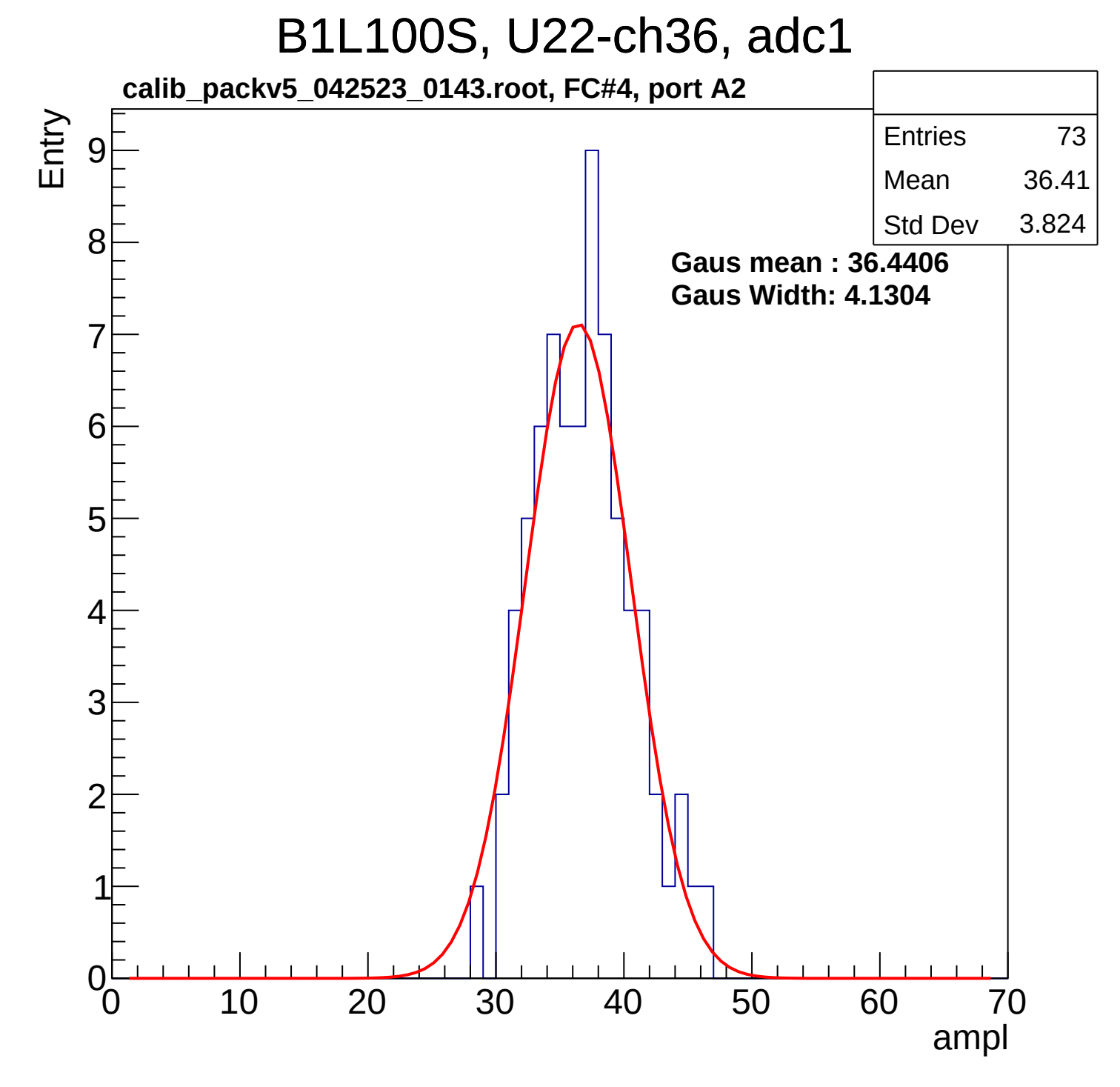

B1L100S, U22-ch35, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch35, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch36, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 29.82
Std Dev 3.554
Gaus mean : 30.5148
Gaus Width: 3.8638
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch36, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.41
Std Dev 3.824
Gaus mean : 36.4406
Gaus Width: 4.1304
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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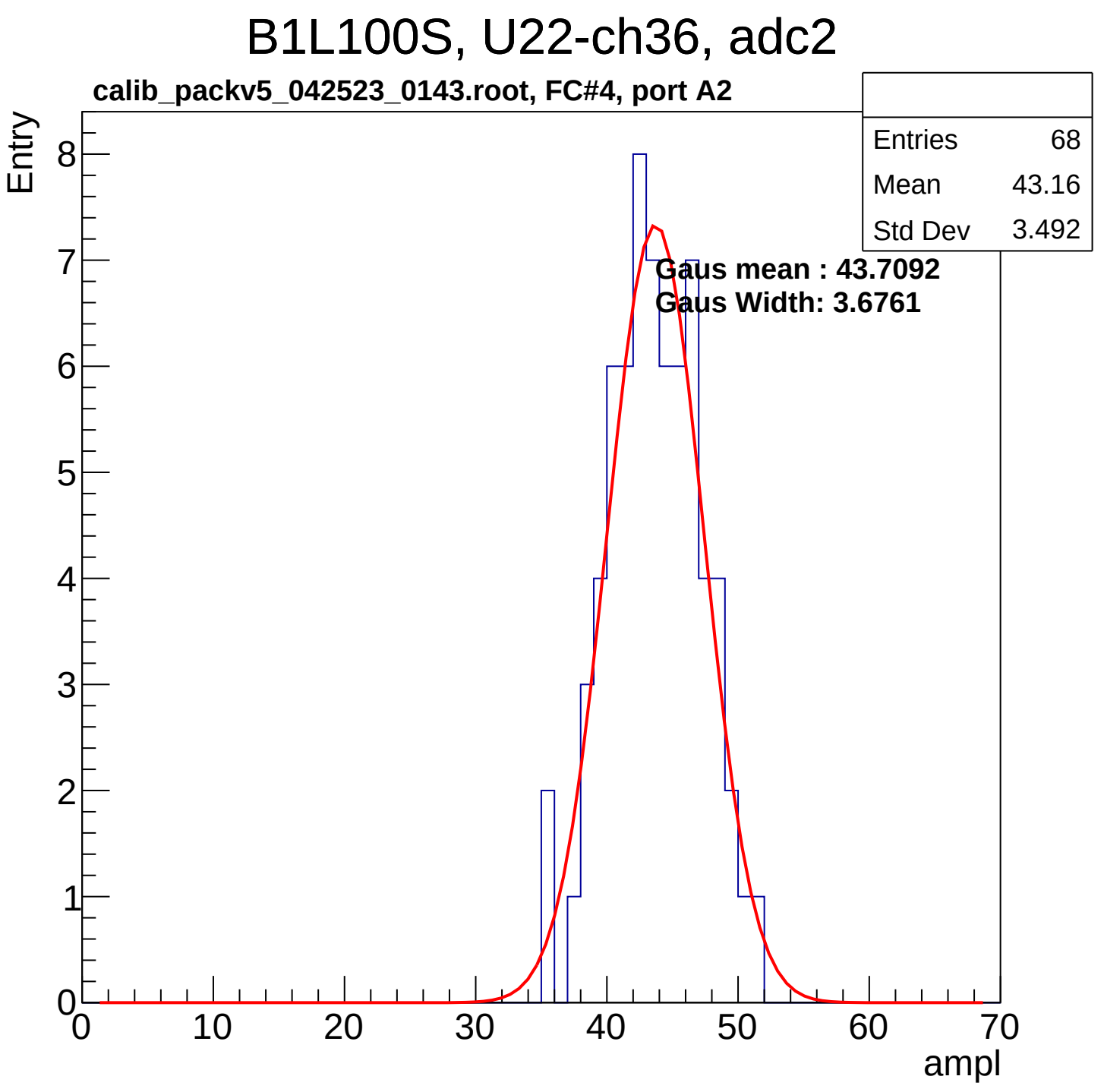
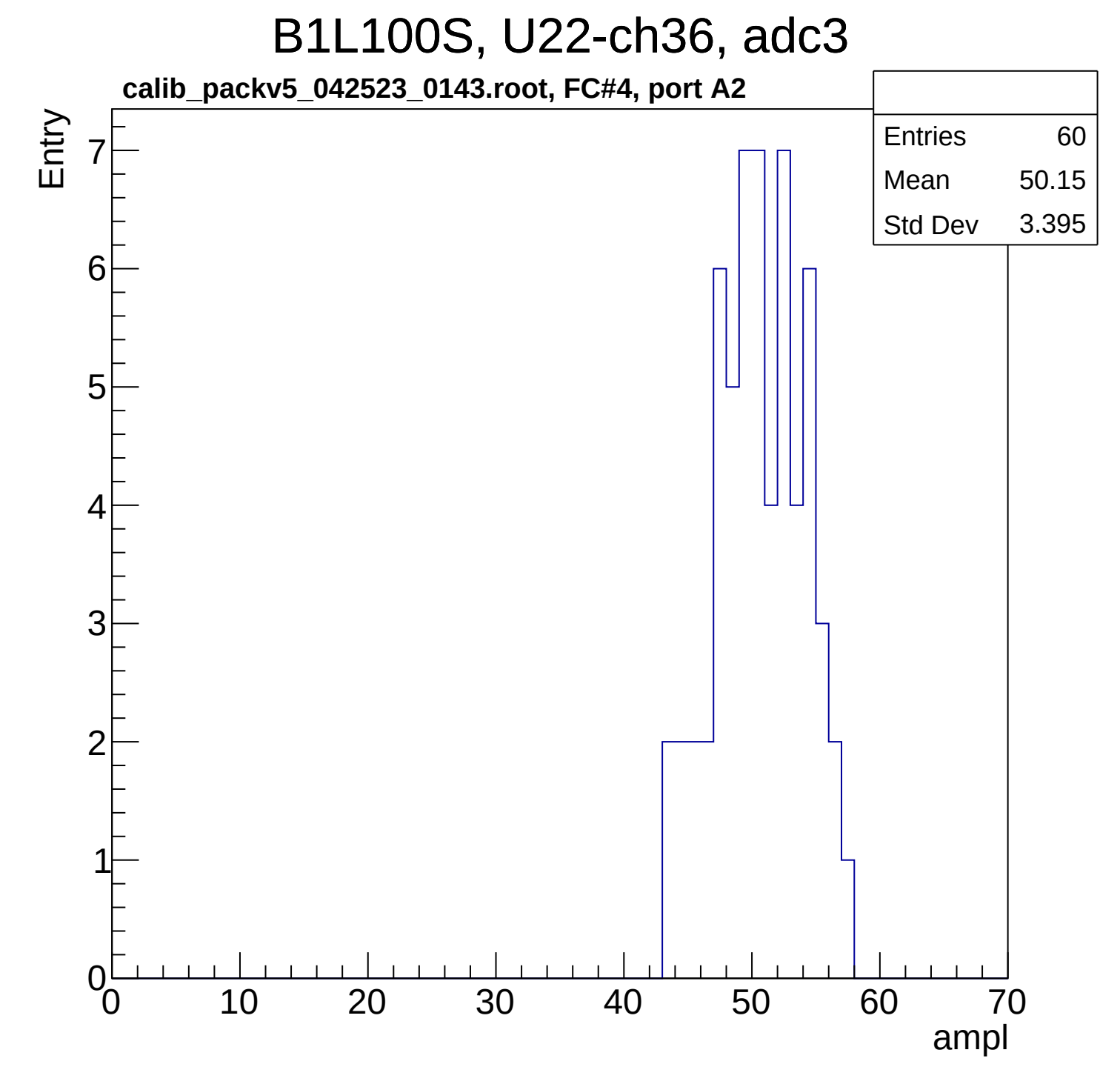
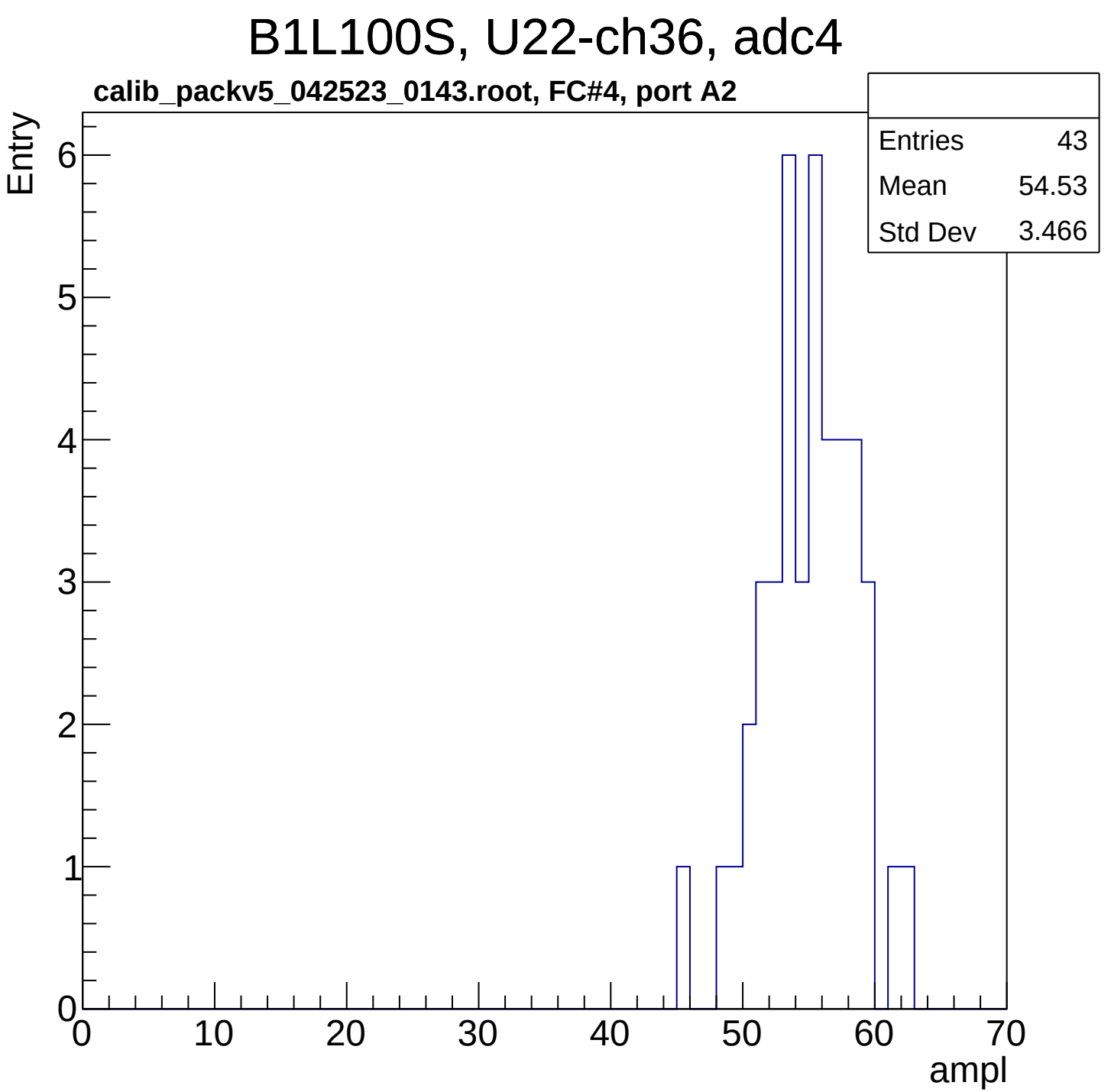
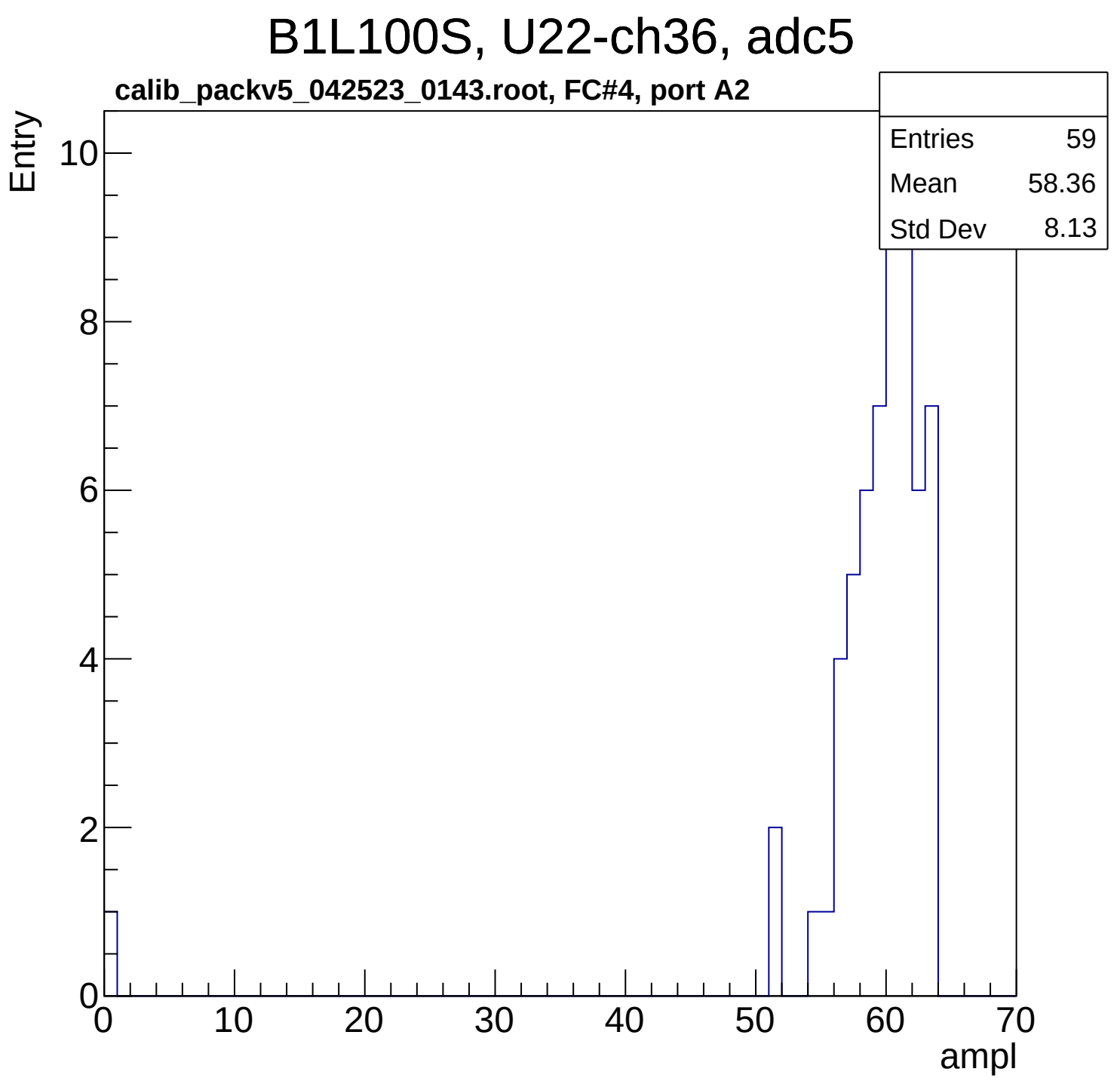
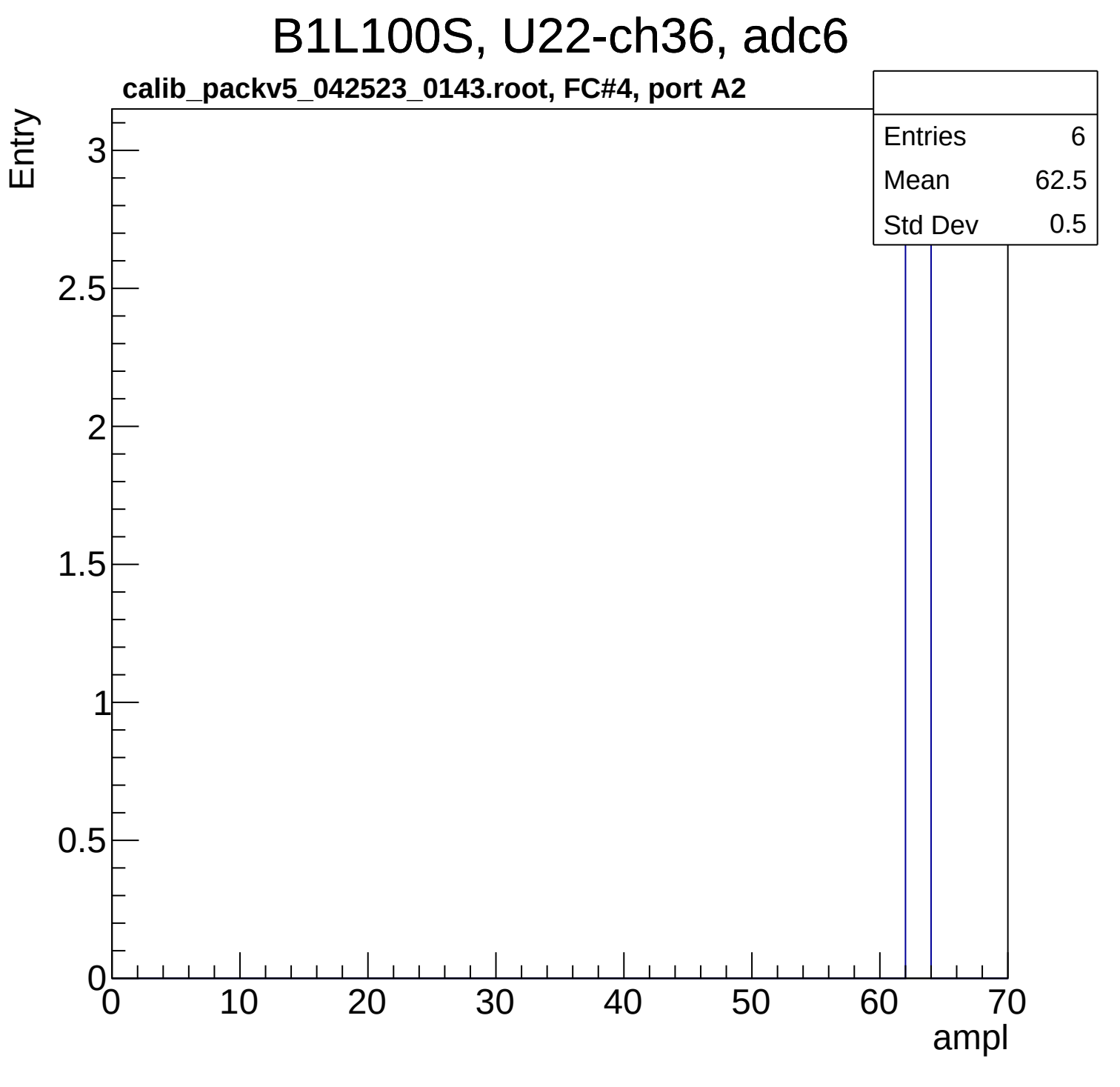
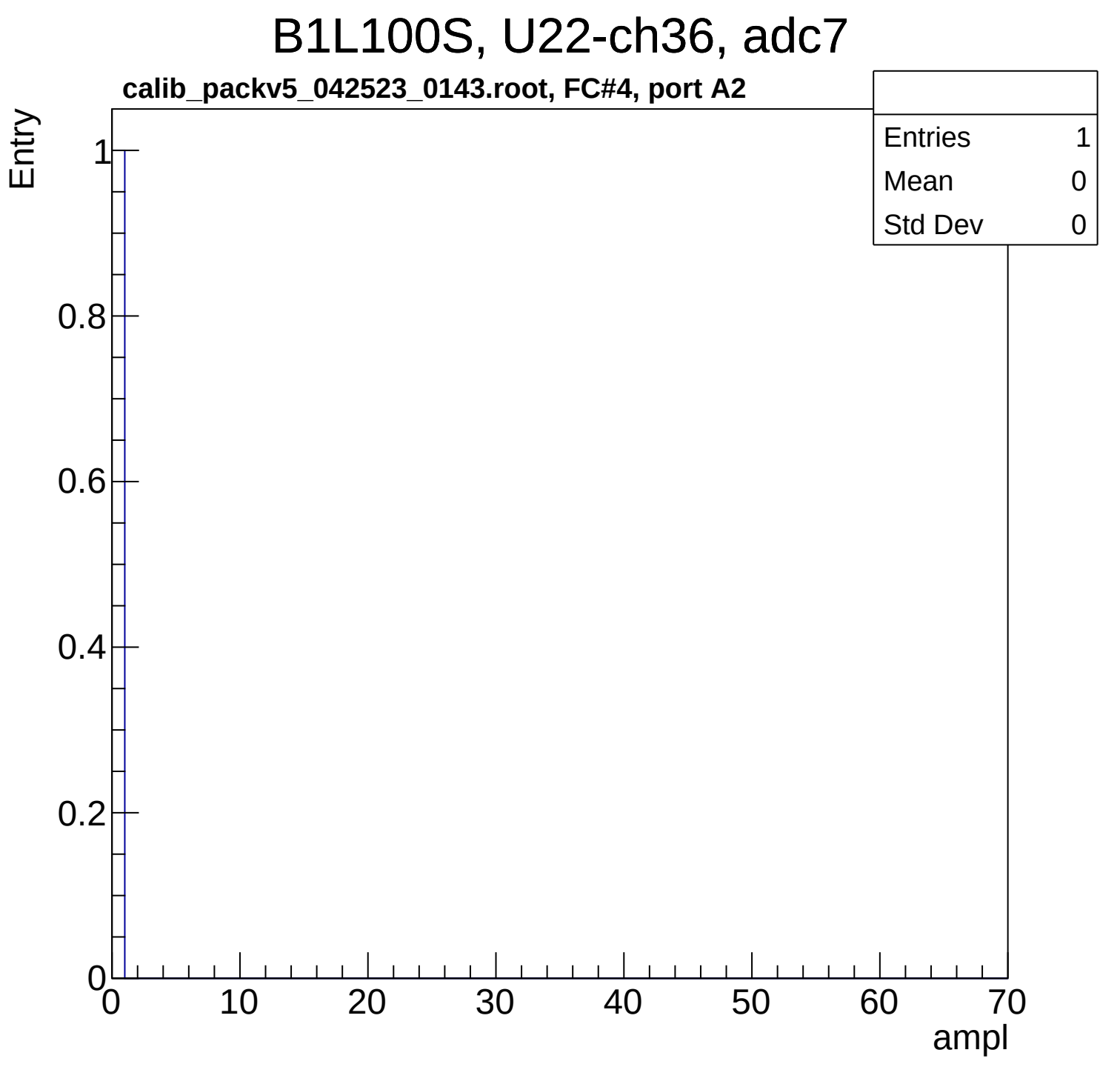

B1L100S, U22-ch36, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.16
Std Dev 3.492
Gaus mean : 43.7092
Gaus Width: 3.6761
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

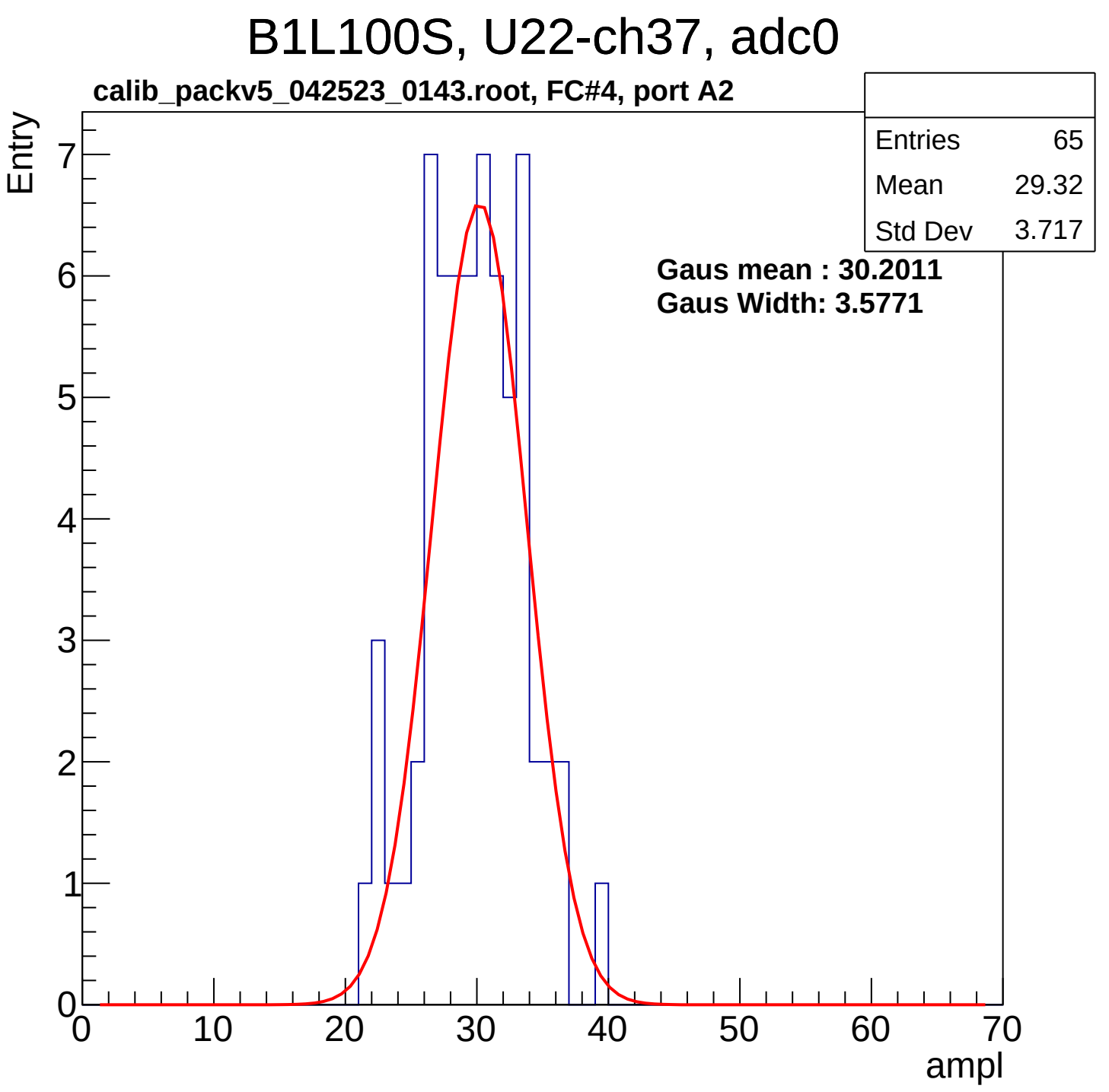

B1L100S, U22-ch36, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 50.15
Std Dev 3.395
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

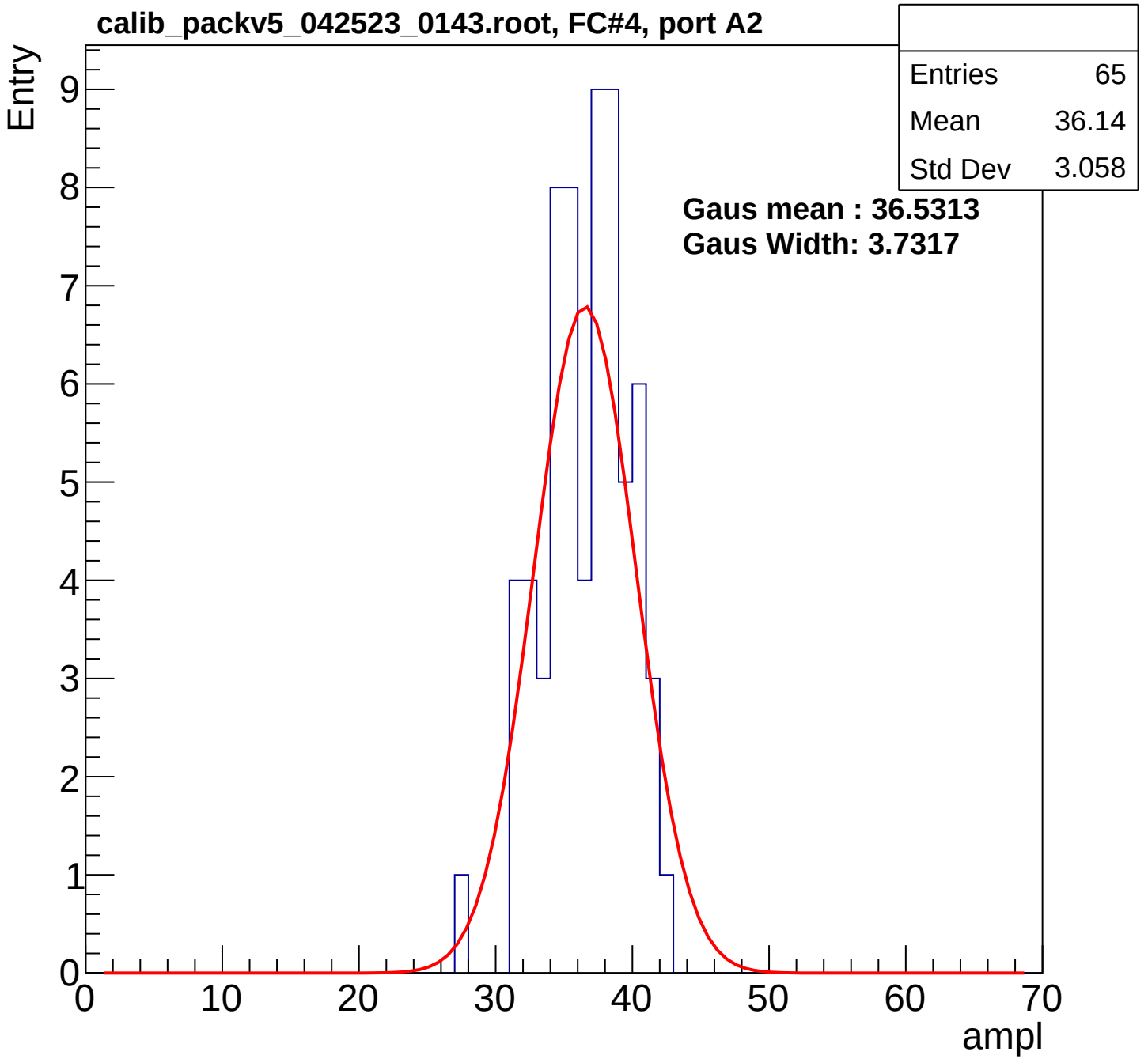

B1L100S, U22-ch36, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 54.53
Std Dev 3.466
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch36, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.36
Std Dev 8.13
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch36, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.5
Std Dev 0.5
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch36, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch37, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.32
Std Dev 3.717
Gaus mean : 30.2011
Gaus Width: 3.5771
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch37, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.14
Std Dev 3.058
Gaus mean : 36.5313
Gaus Width: 3.7317
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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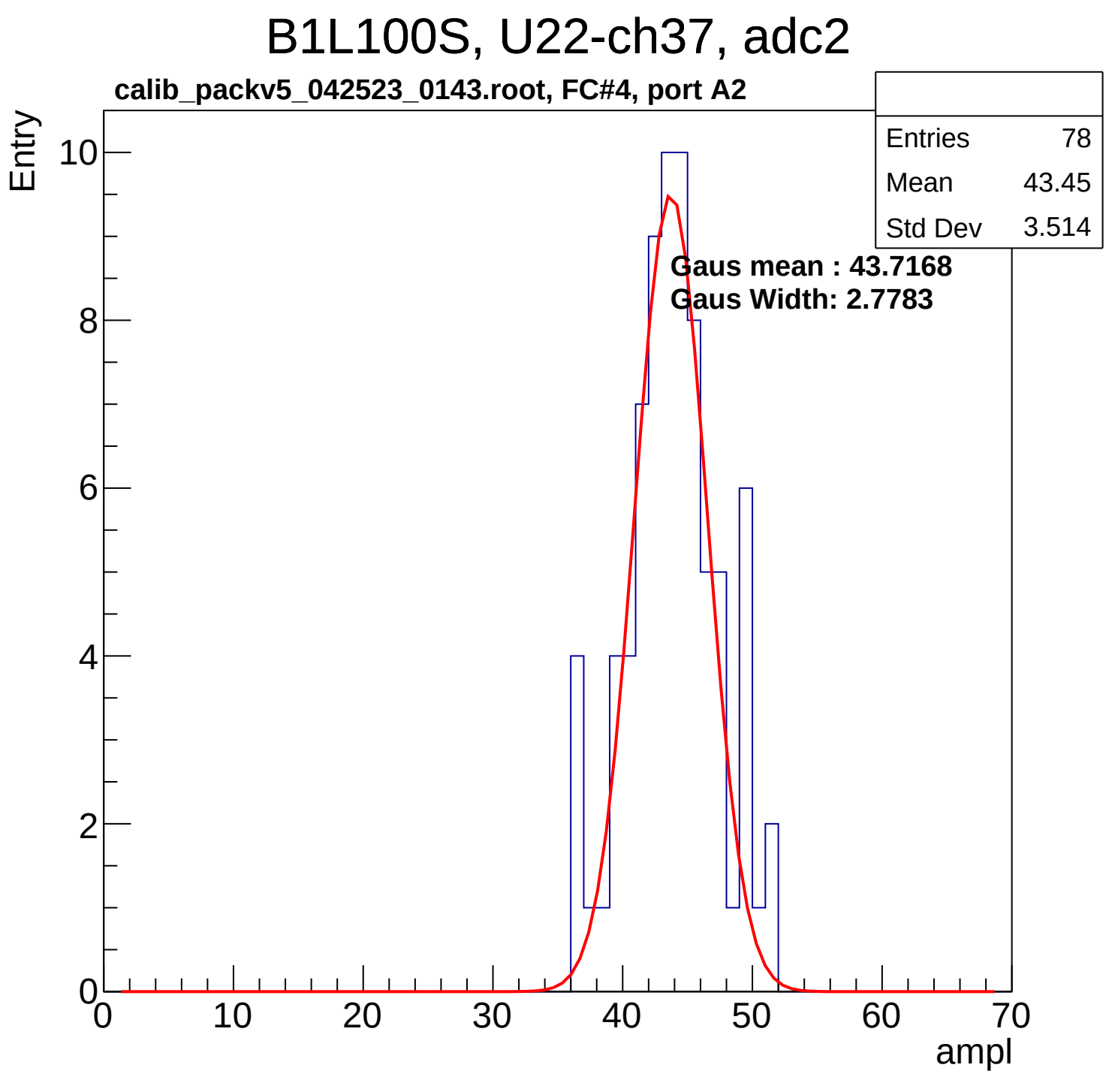
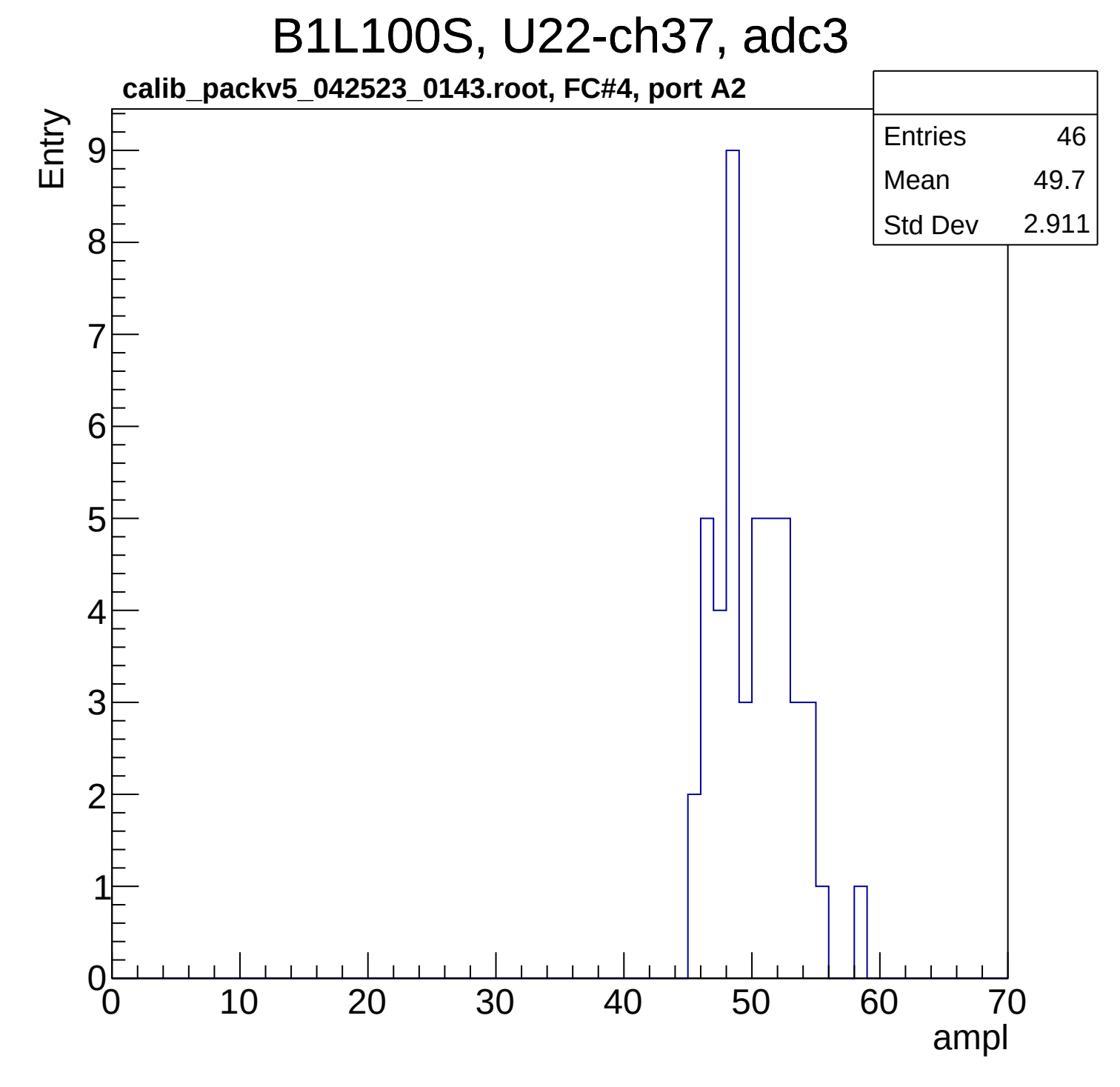
60
70


B1L100S, U22-ch37, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.45
Std Dev 3.514
Gaus mean : 43.7168
Gaus Width: 2.7783
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch37, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 49.7
Std Dev 2.911
Entry
ampl
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2
3
4
5
6
7
8
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0
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40
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60
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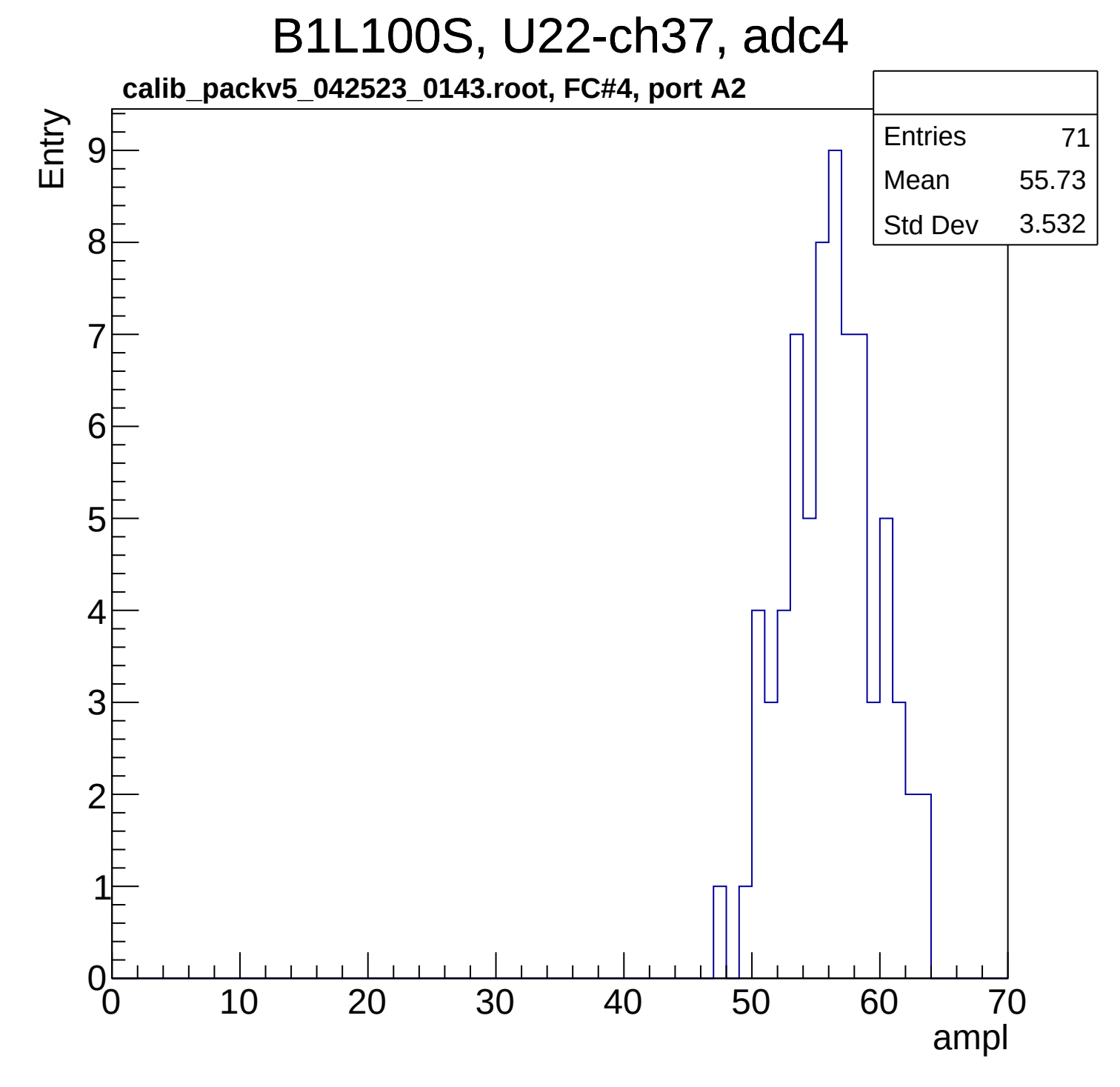
B1L100S, U22-ch37, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 55.73
Std Dev 3.532
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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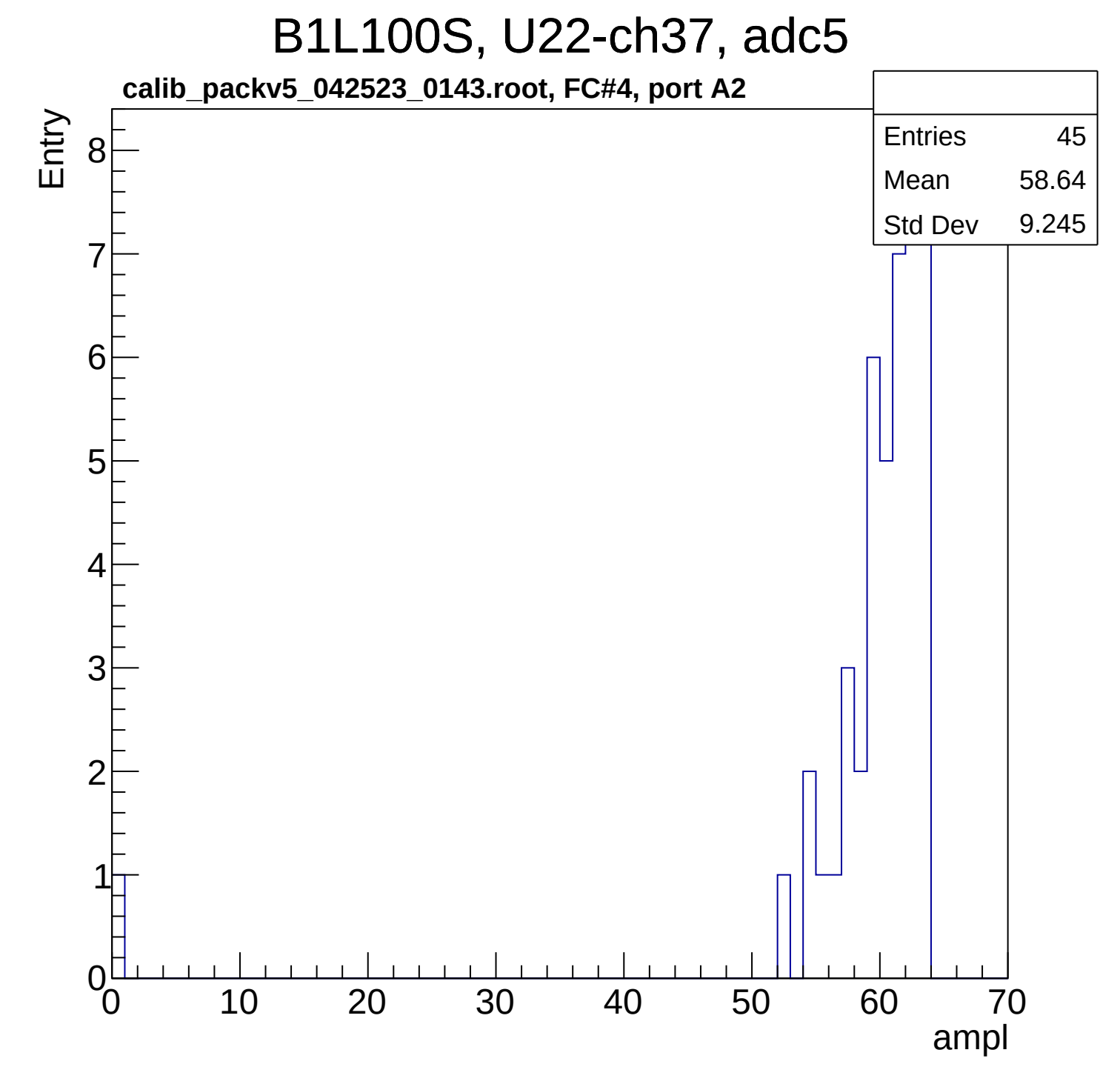
B1L100S, U22-ch37, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.64
Std Dev 9.245
Entry
ampl
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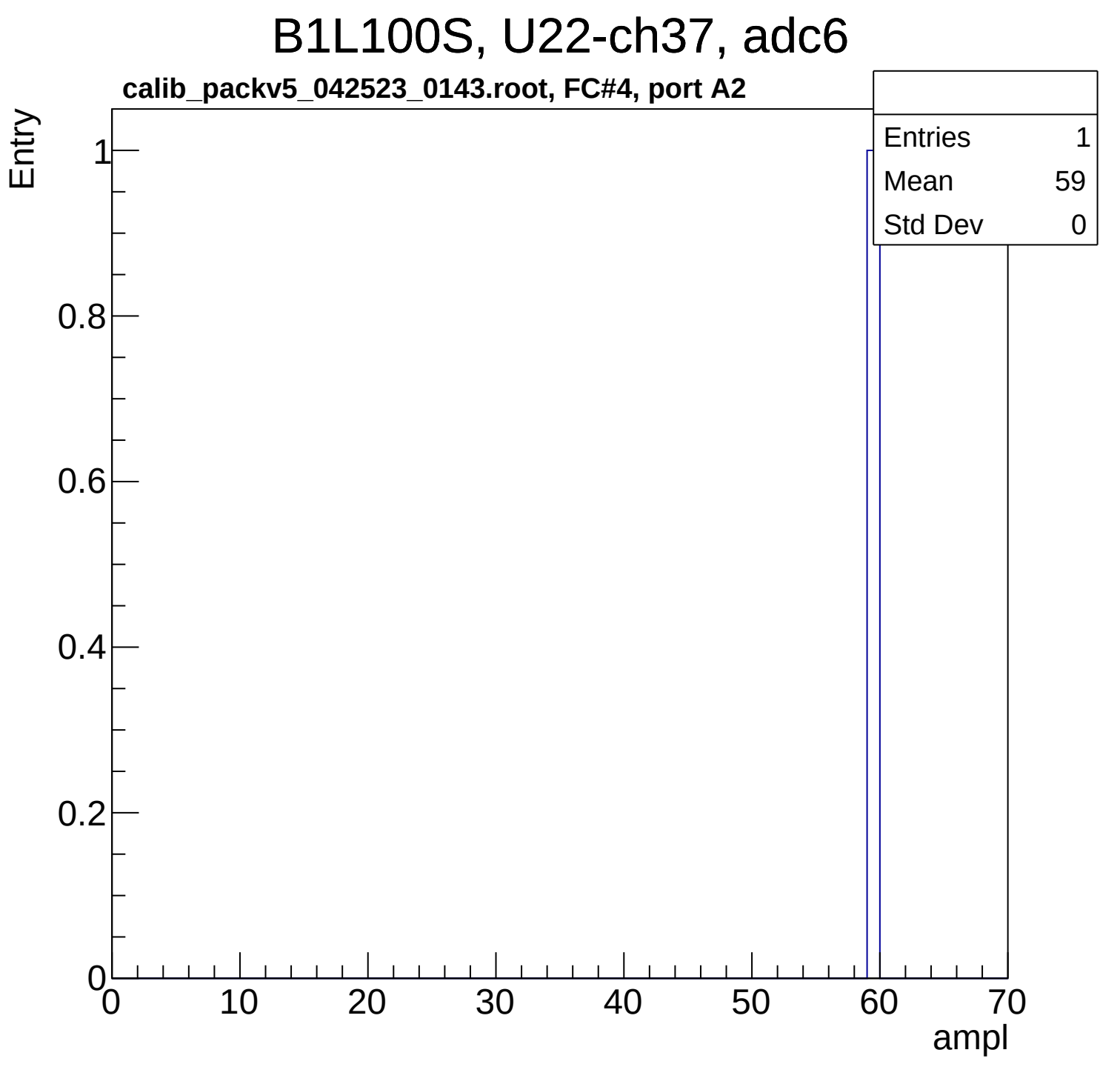
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch37, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 59
Std Dev 0
Entry
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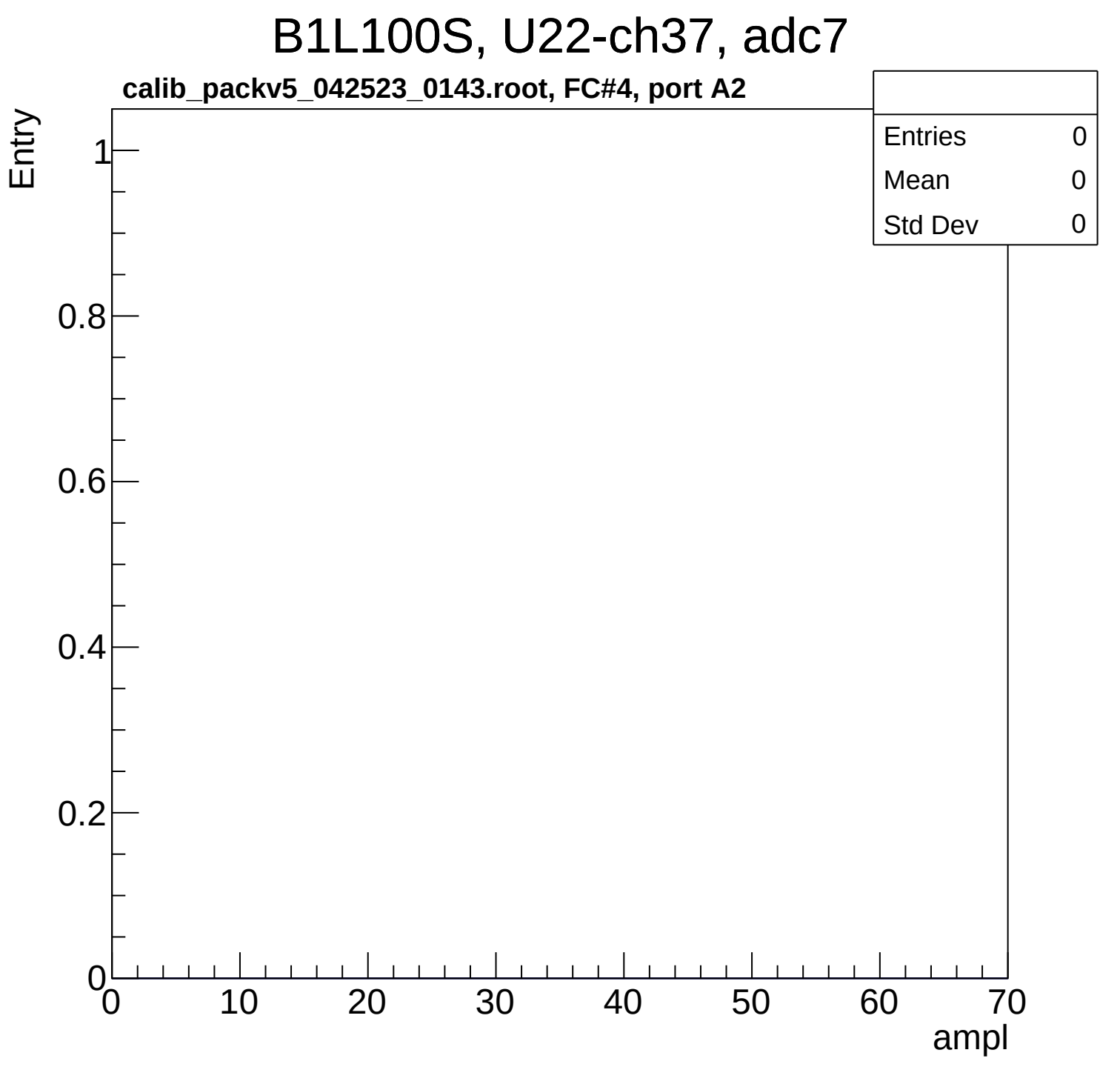
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch37, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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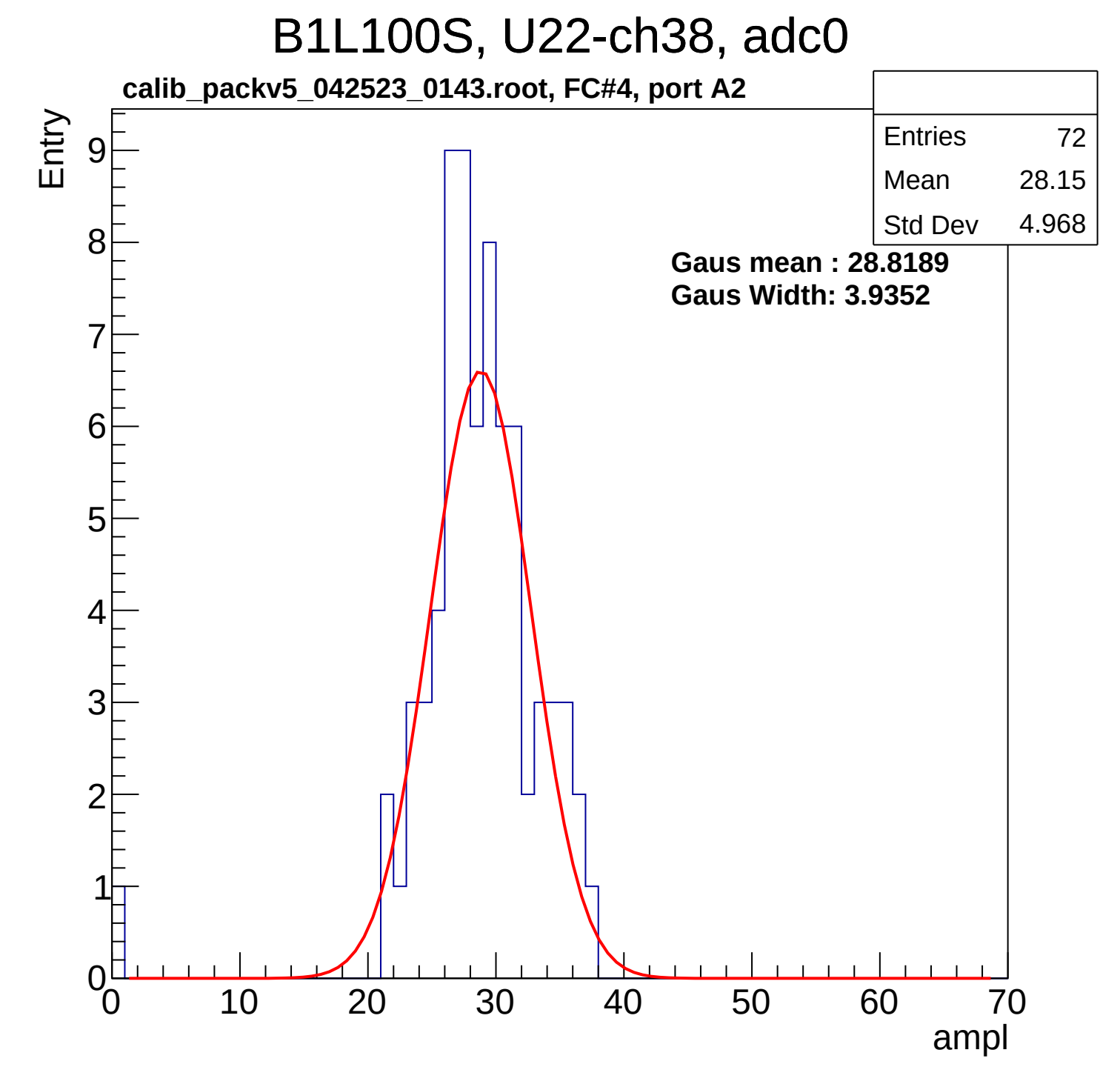
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch38, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.15
Std Dev 4.968
Gaus mean : 28.8189
Gaus Width: 3.9352
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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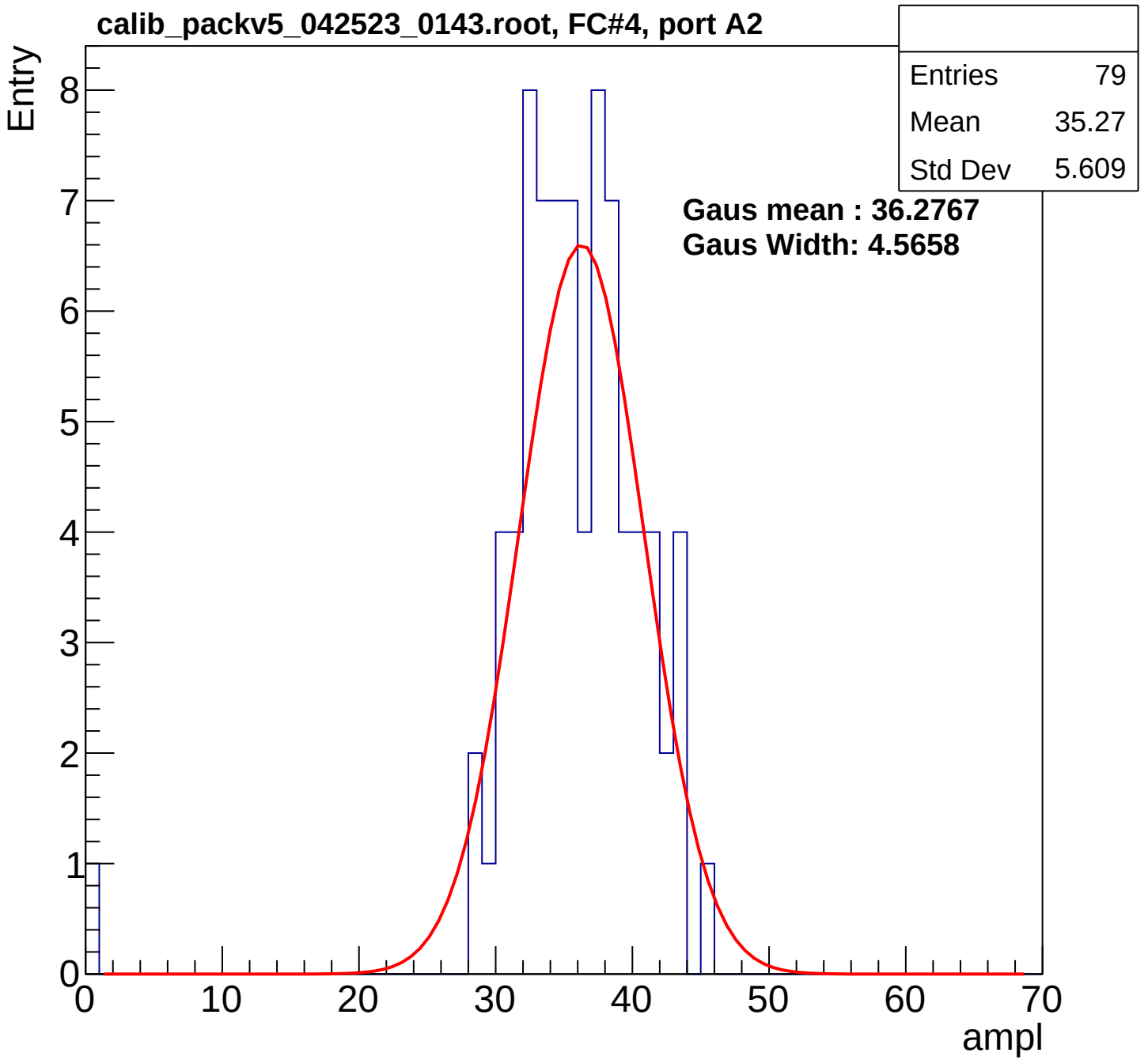
B1L100S, U22-ch38, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.27
Std Dev 5.609
Gaus mean : 36.2767
Gaus Width: 4.5658
Entry
ampl
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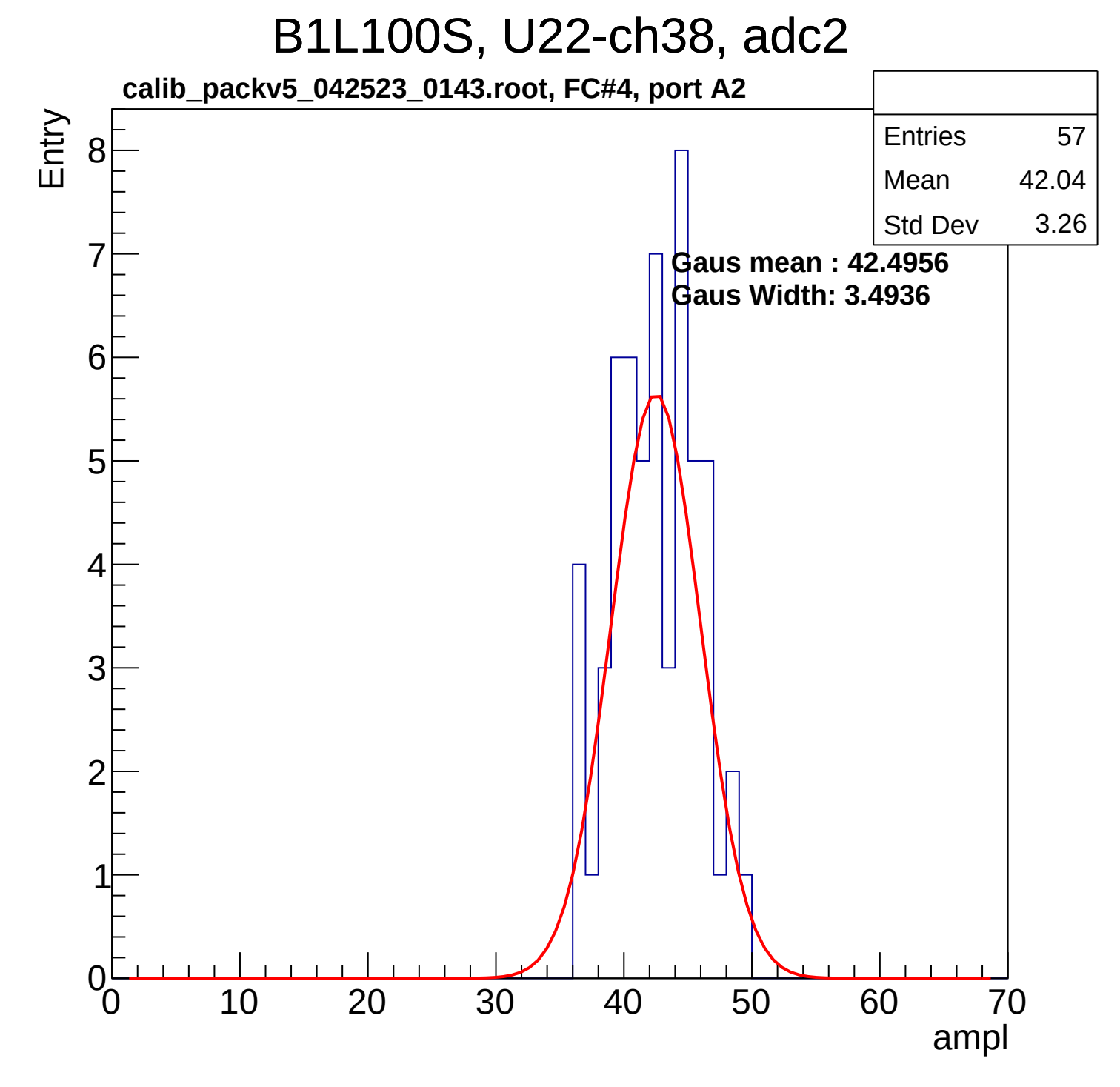
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch38, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 42.04
Std Dev 3.26
Gaus mean : 42.4956
Gaus Width: 3.4936
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
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40
50
60
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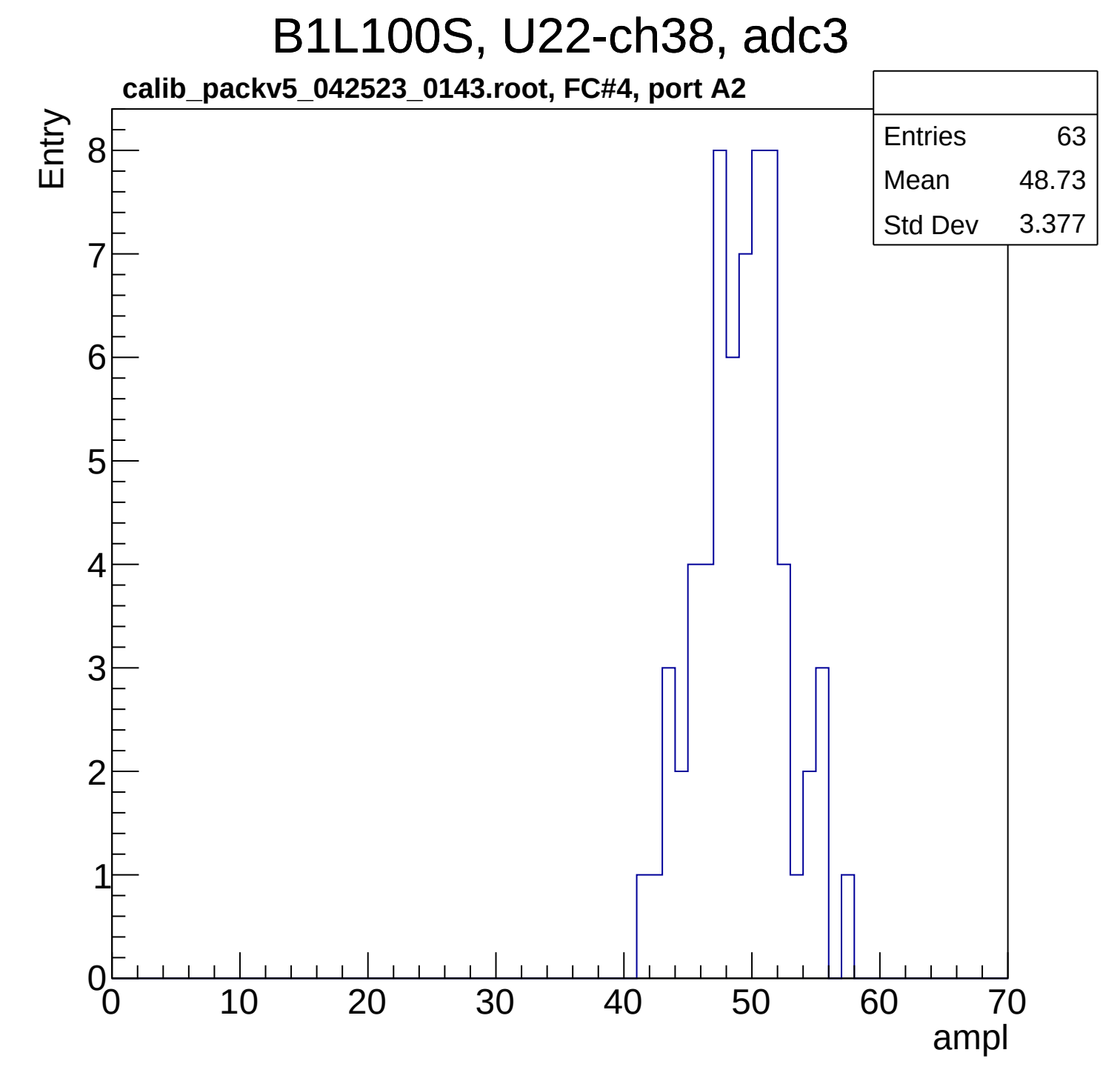
B1L100S, U22-ch38, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.73
Std Dev 3.377
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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60
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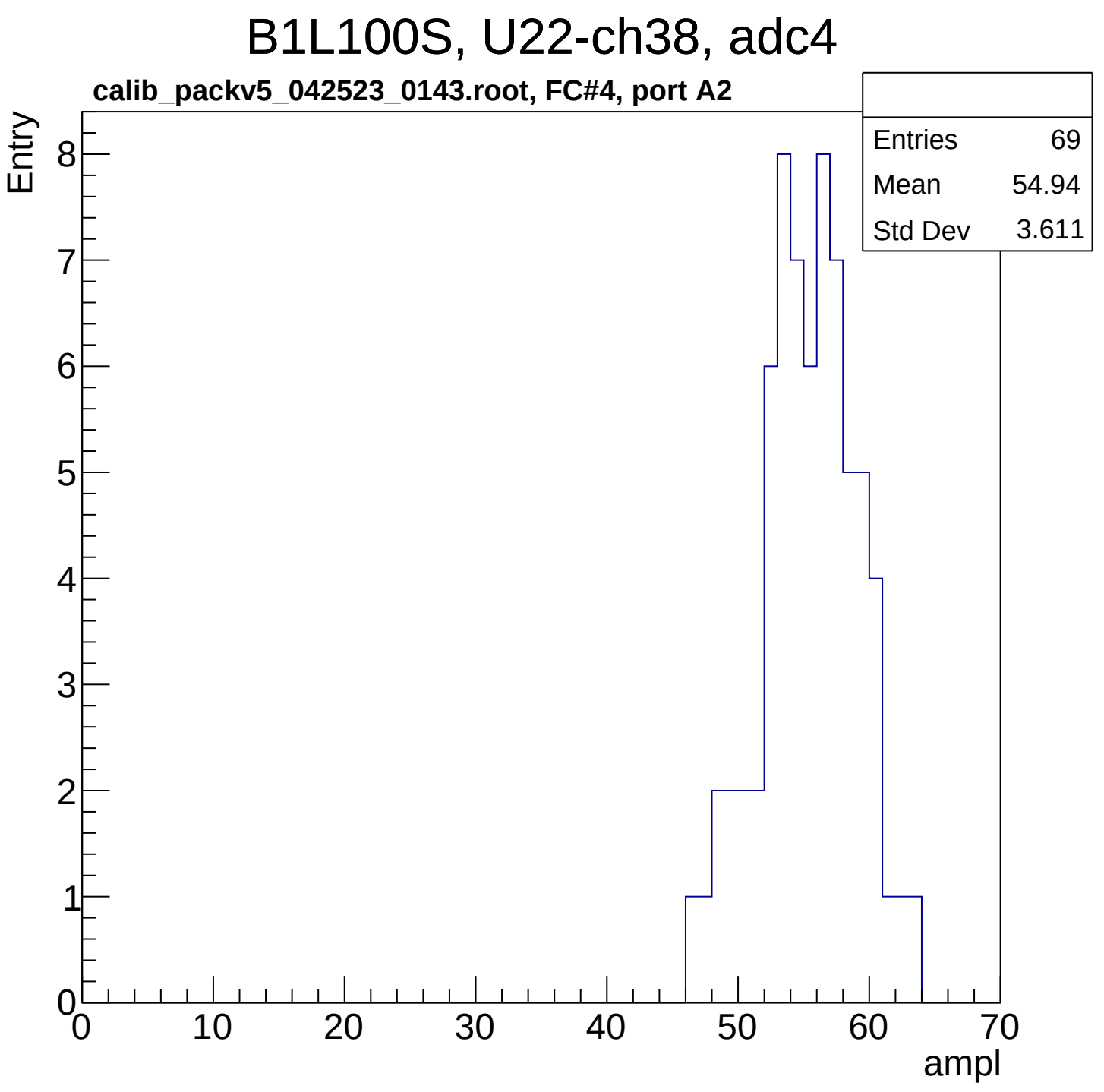
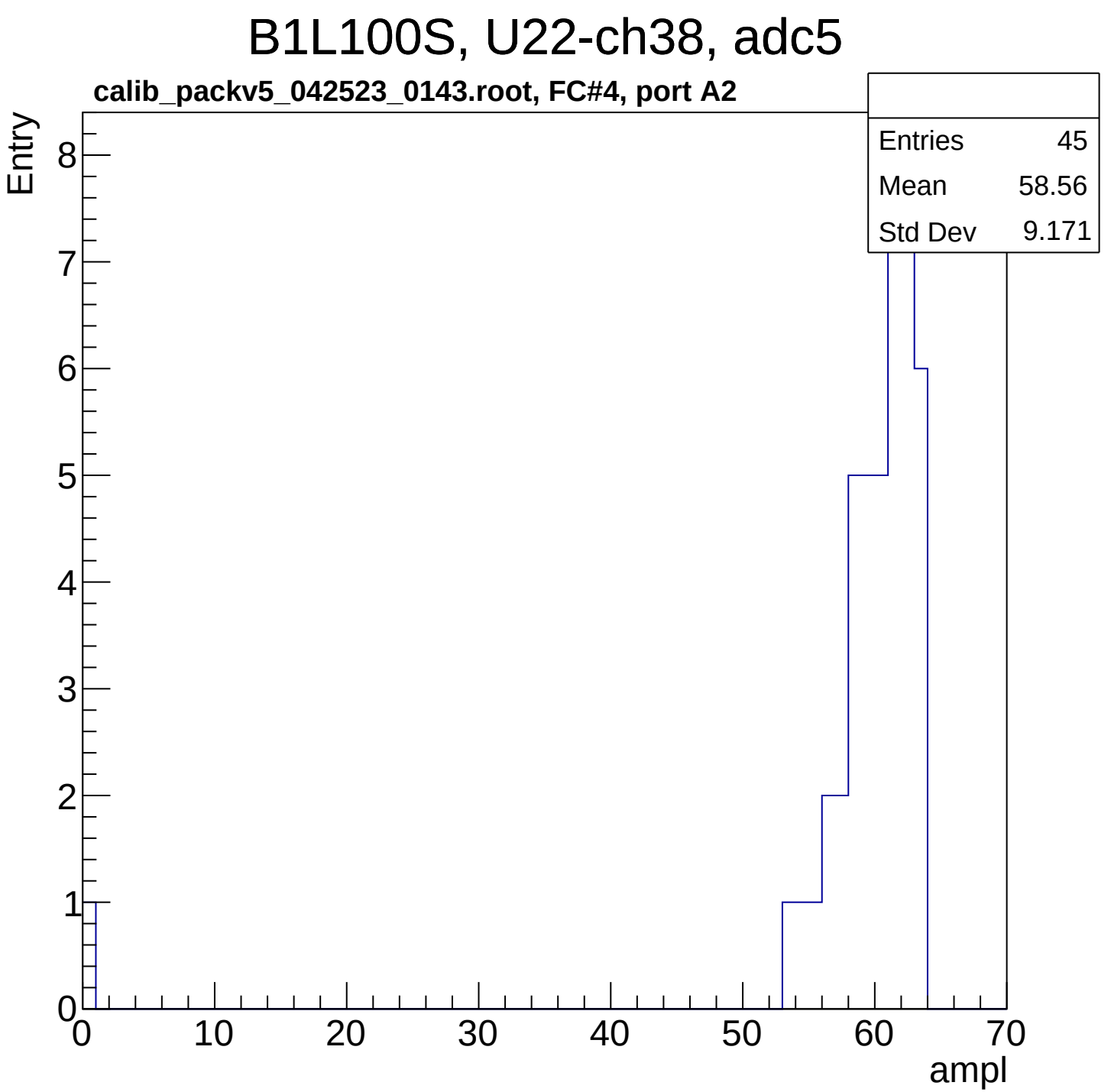

B1L100S, U22-ch38, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 54.94
Std Dev 3.611
Entry
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch38, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 58.56
Std Dev 9.171
Entry
ampl
0
1
2
3
4
5
6
7
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0
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40
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60
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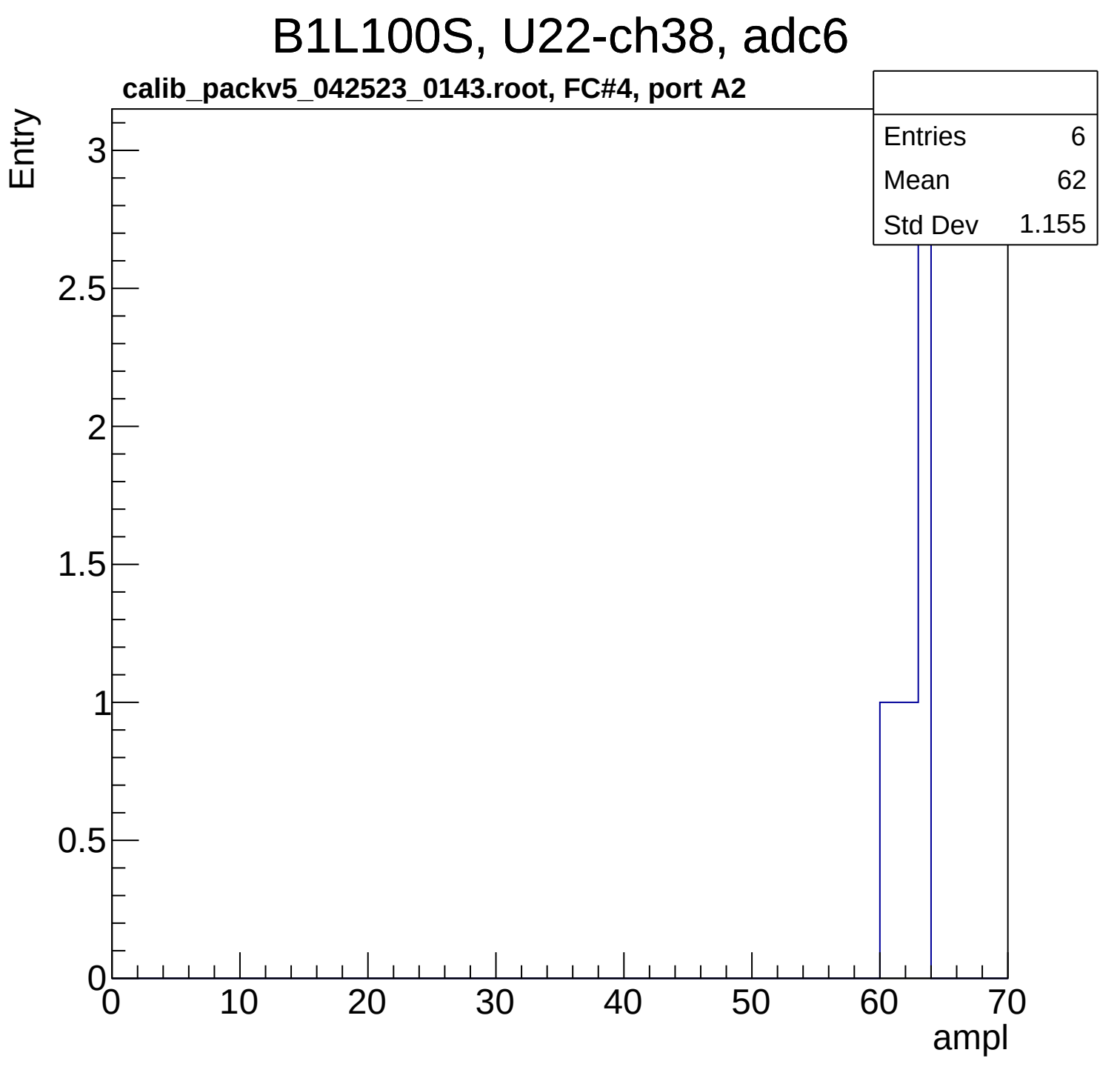
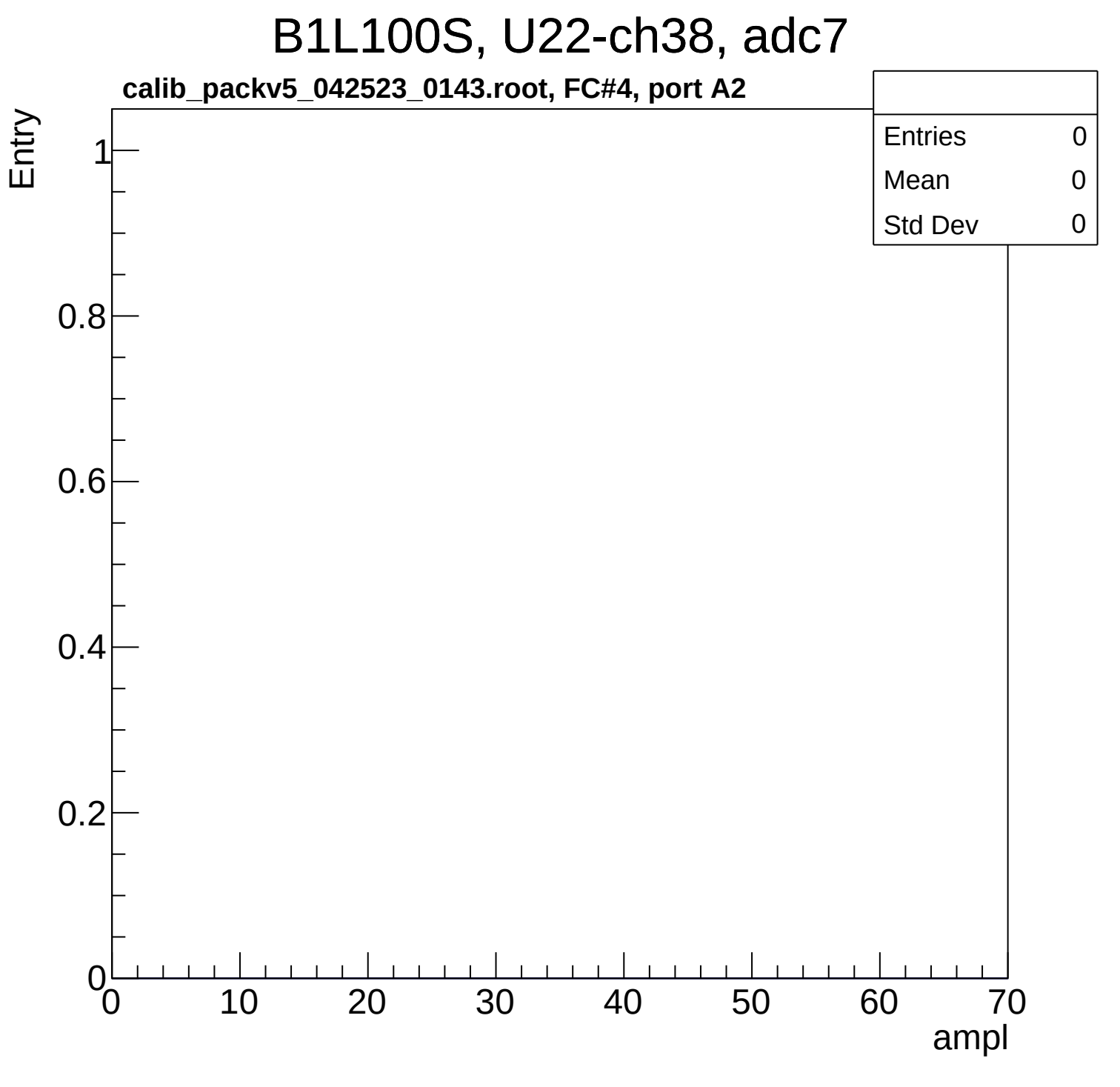
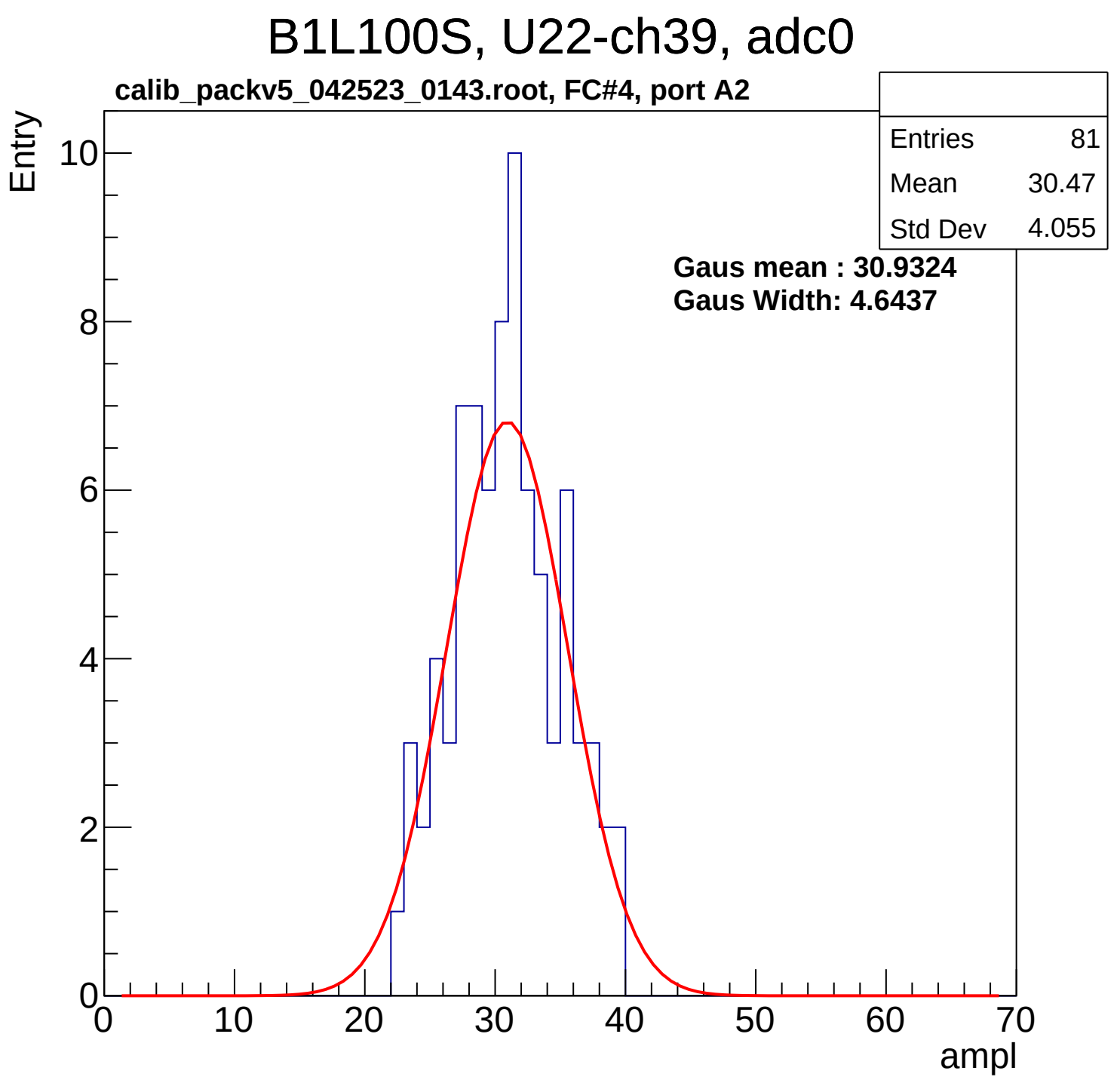
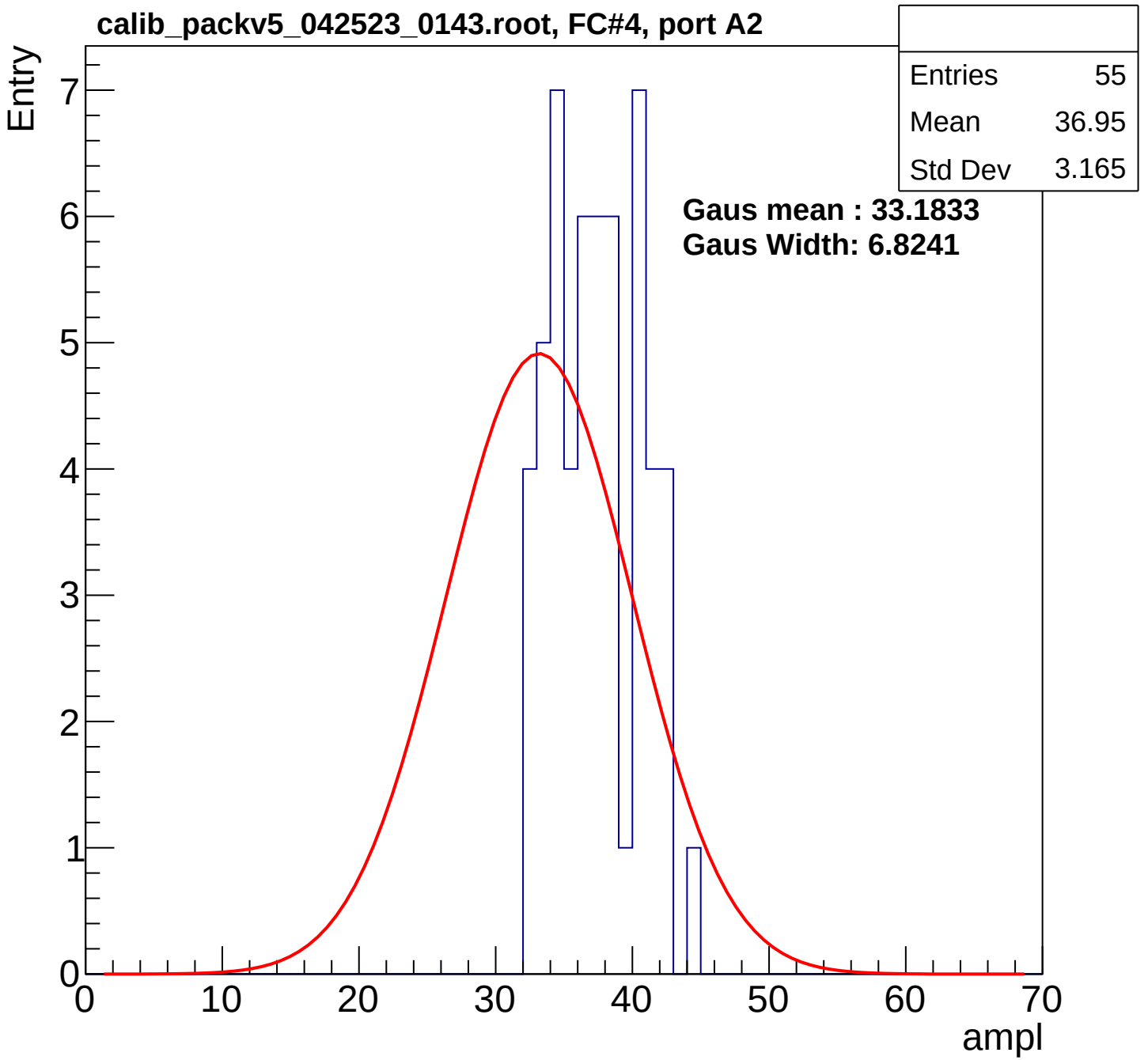

B1L100S, U22-ch38, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62
Std Dev 1.155
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch38, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch39, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 30.47
Std Dev 4.055
Gaus mean : 30.9324
Gaus Width: 4.6437
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 36.95
Std Dev 3.165
Gaus mean : 33.1833
Gaus Width: 6.8241
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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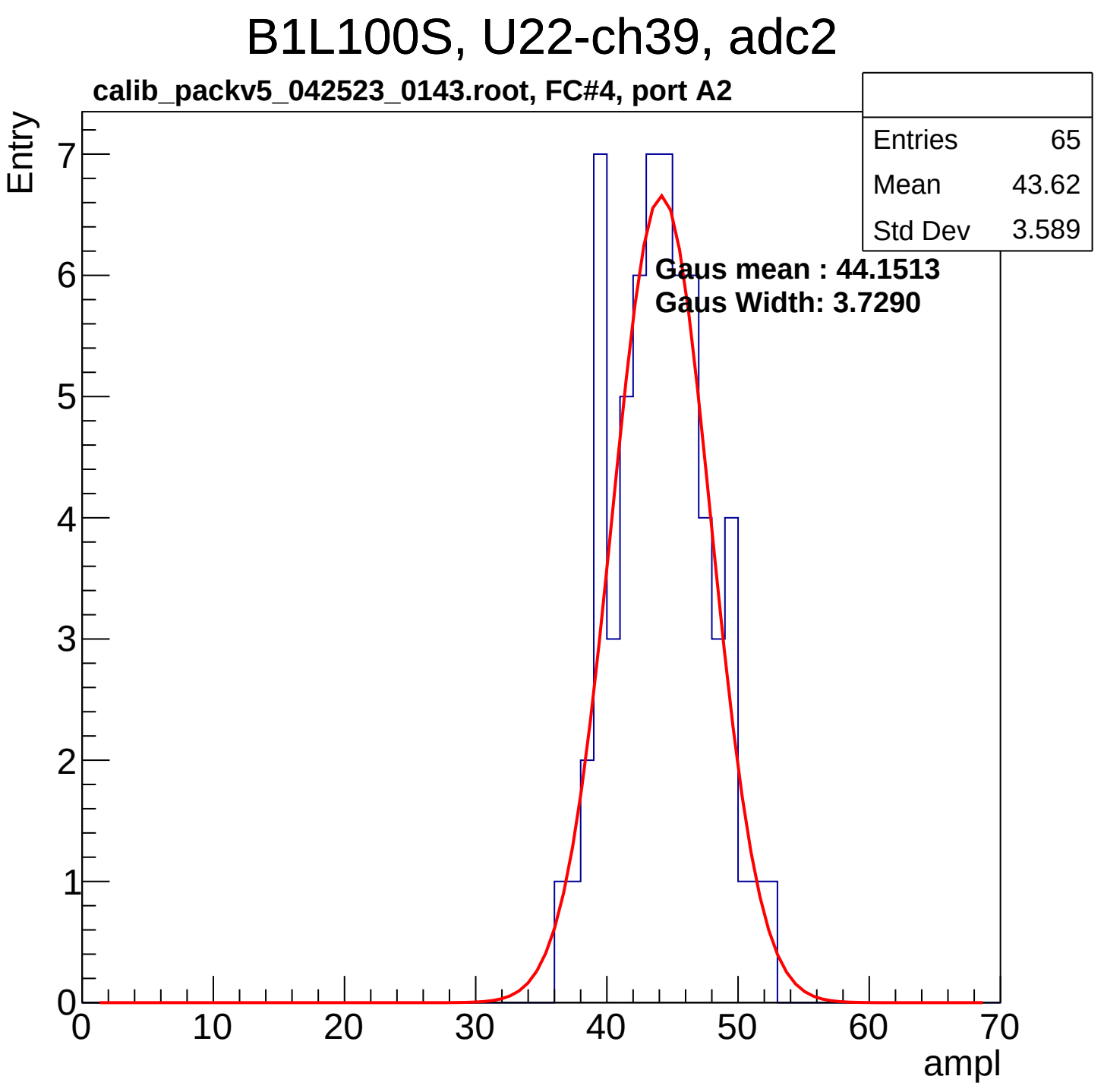
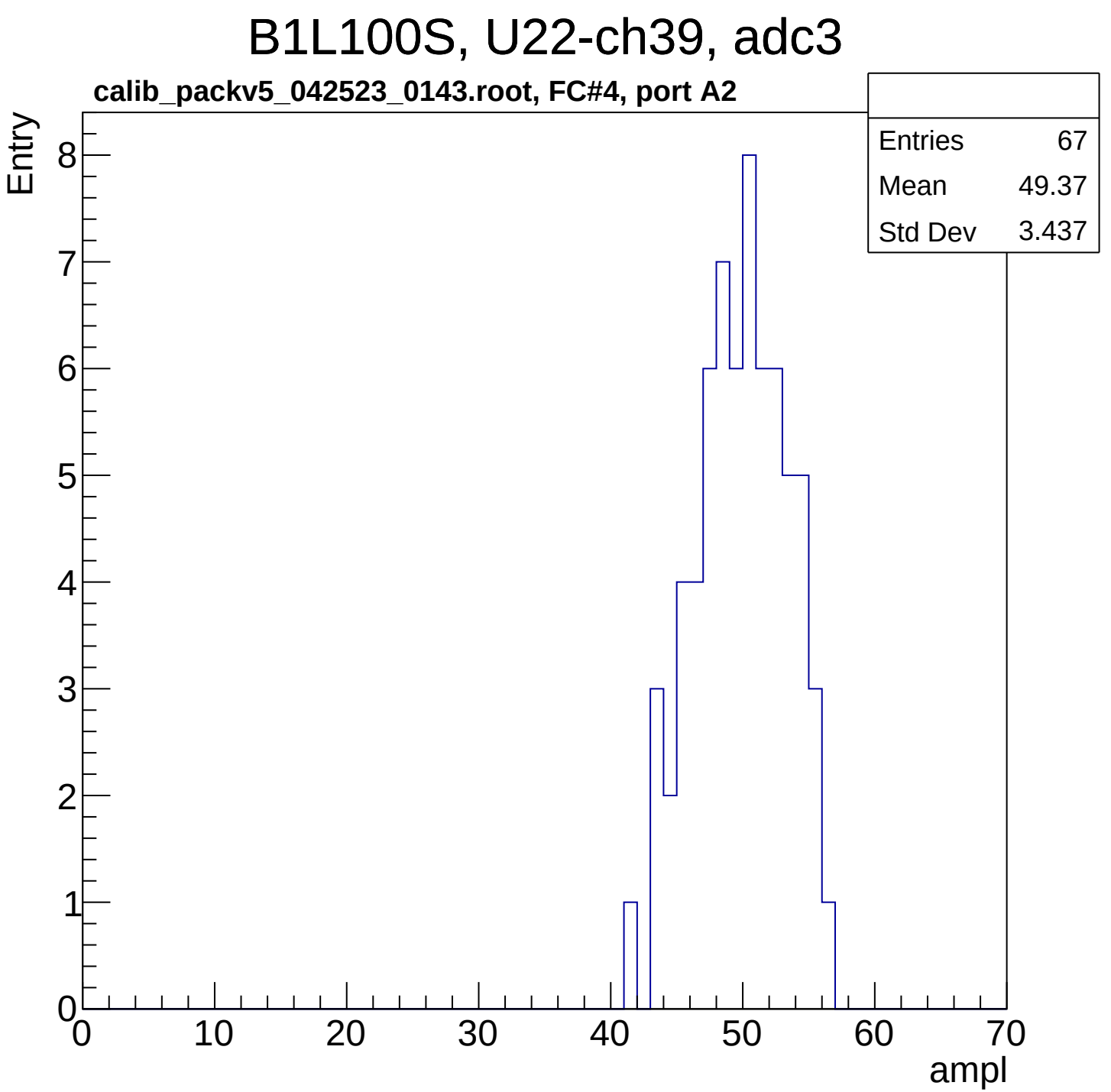

B1L100S, U22-ch39, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.62
Std Dev 3.589
Gaus mean : 44.1513
Gaus Width: 3.7290
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.37
Std Dev 3.437
Entry
ampl
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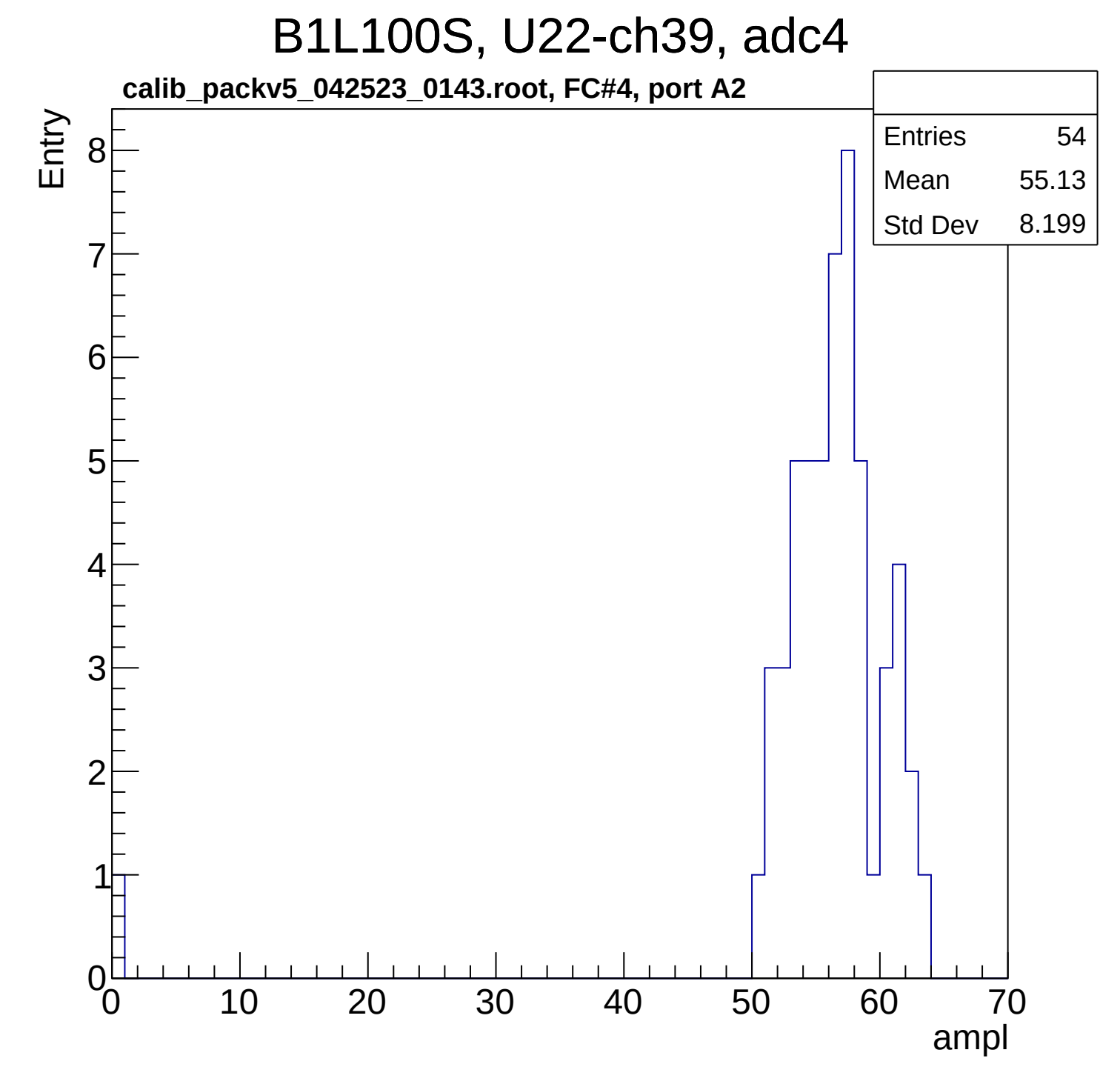
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.13
Std Dev 8.199
Entry
ampl
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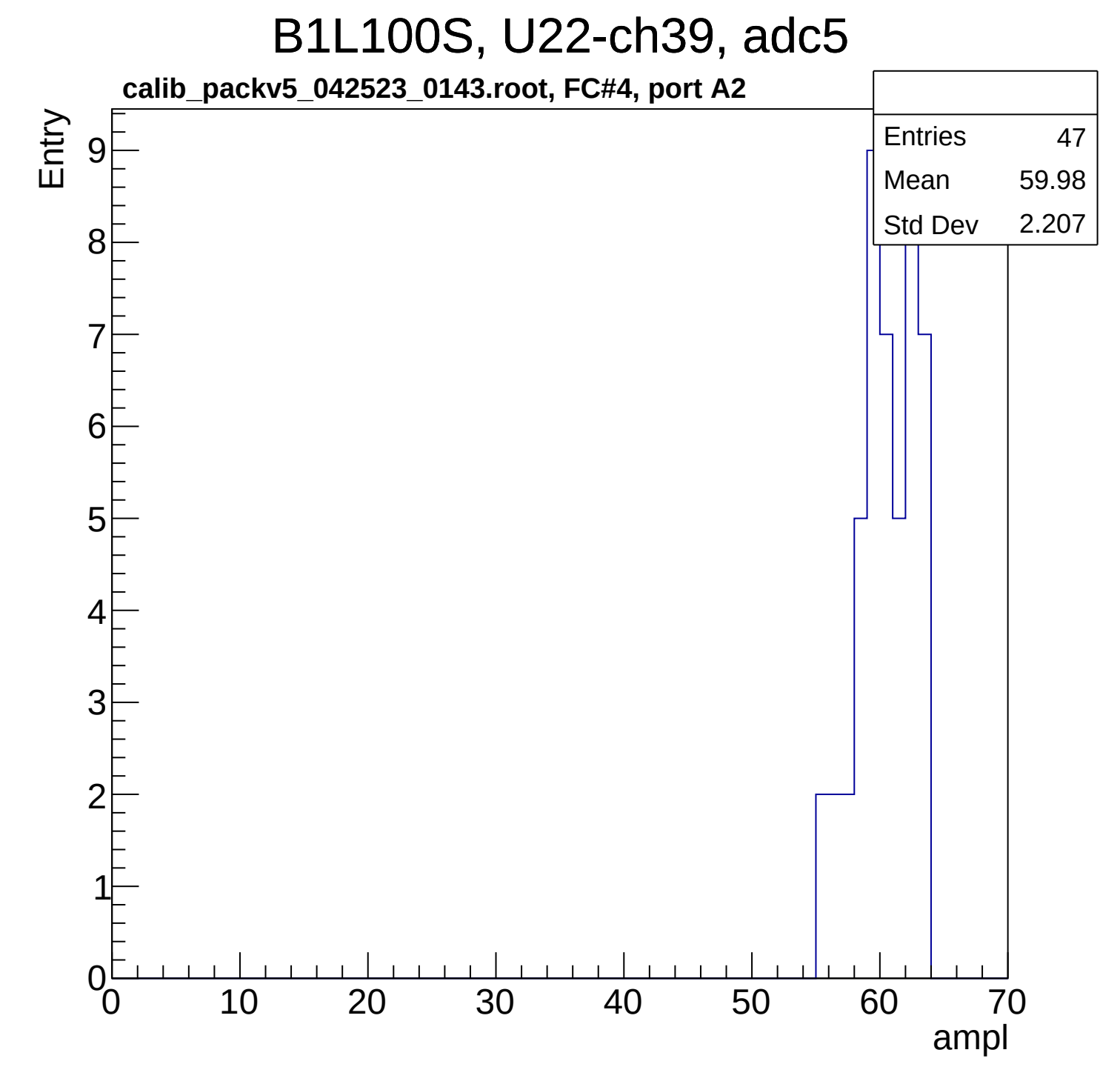
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.98
Std Dev 2.207
Entry
ampl
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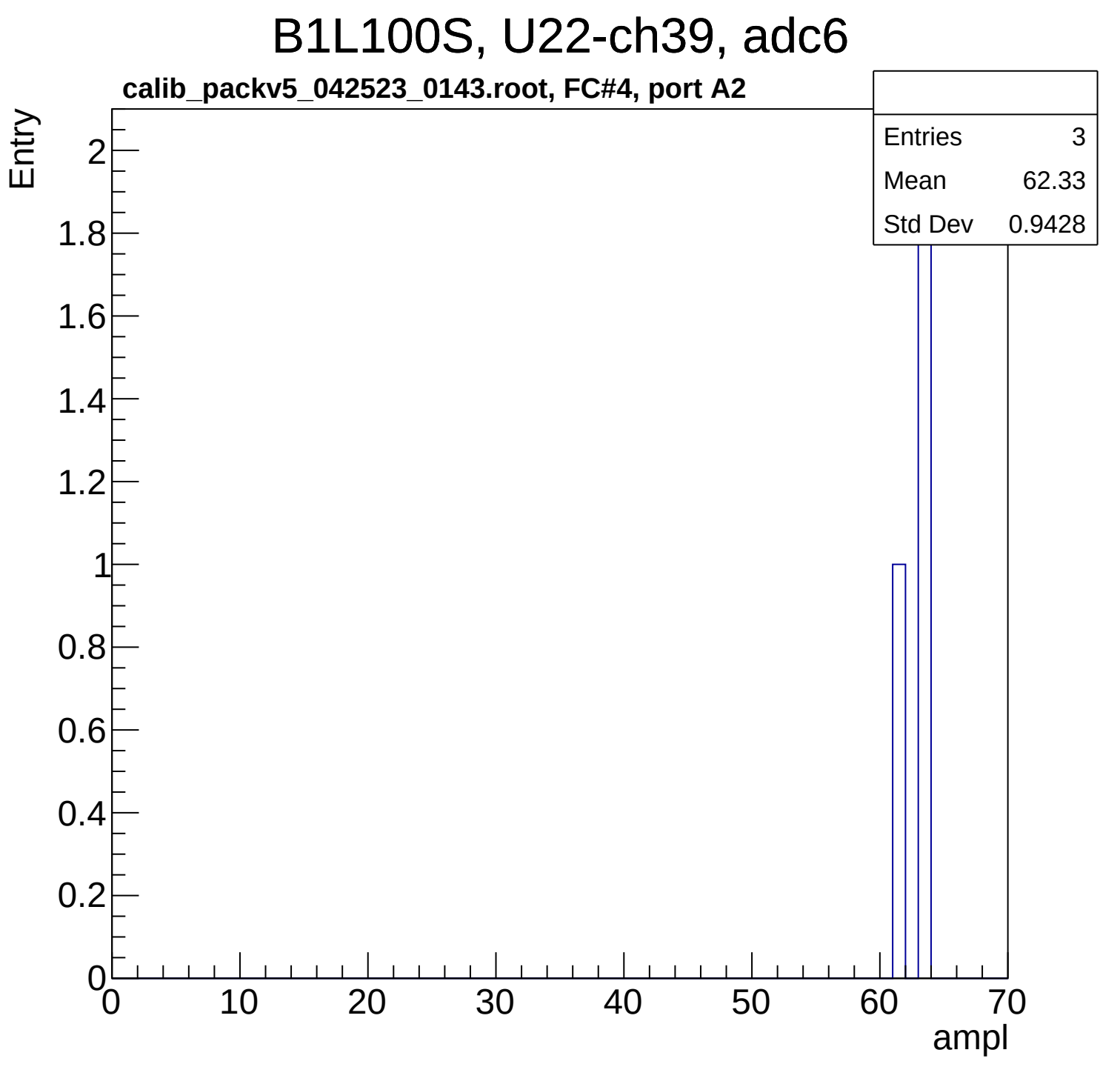
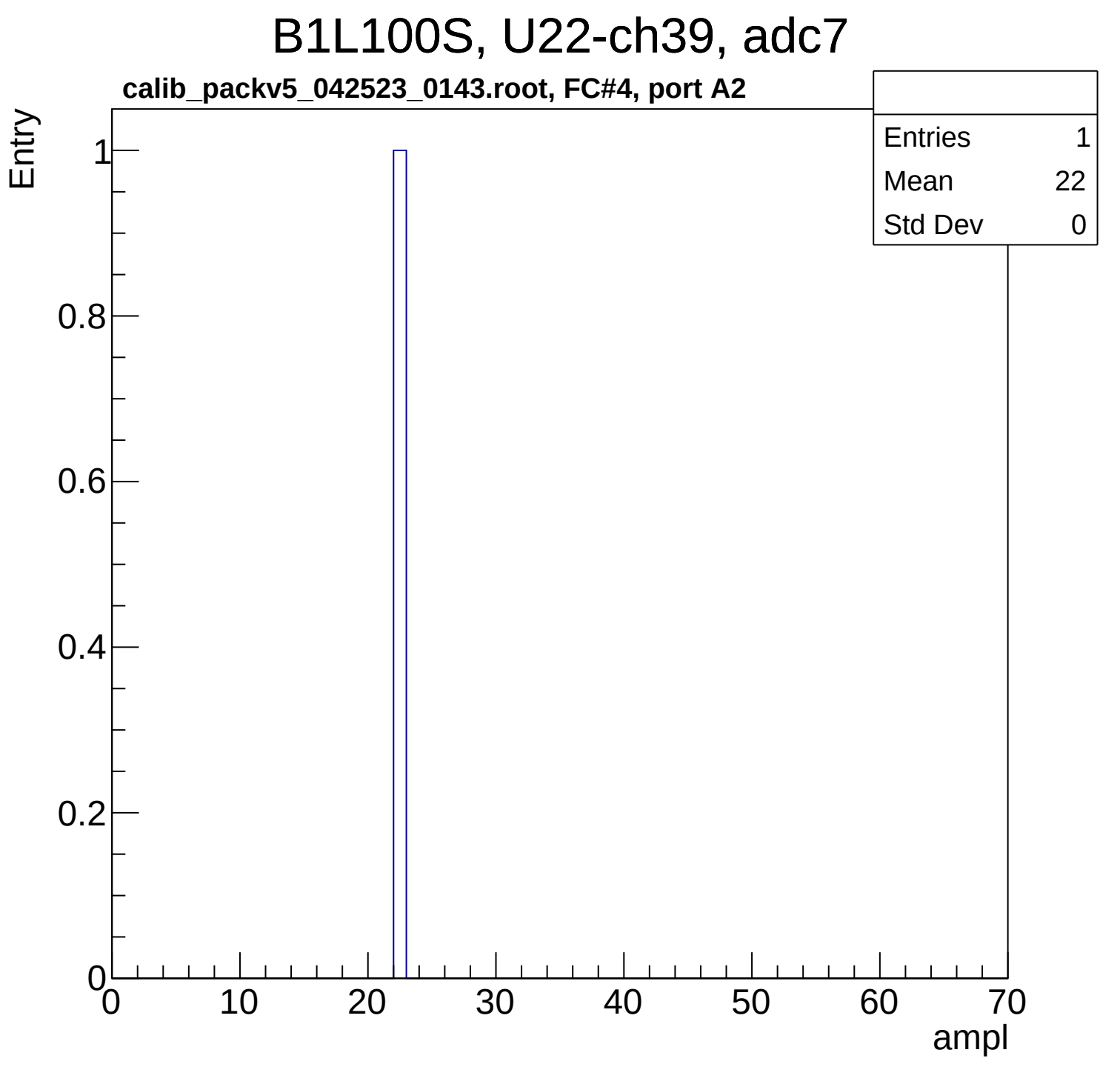
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch39, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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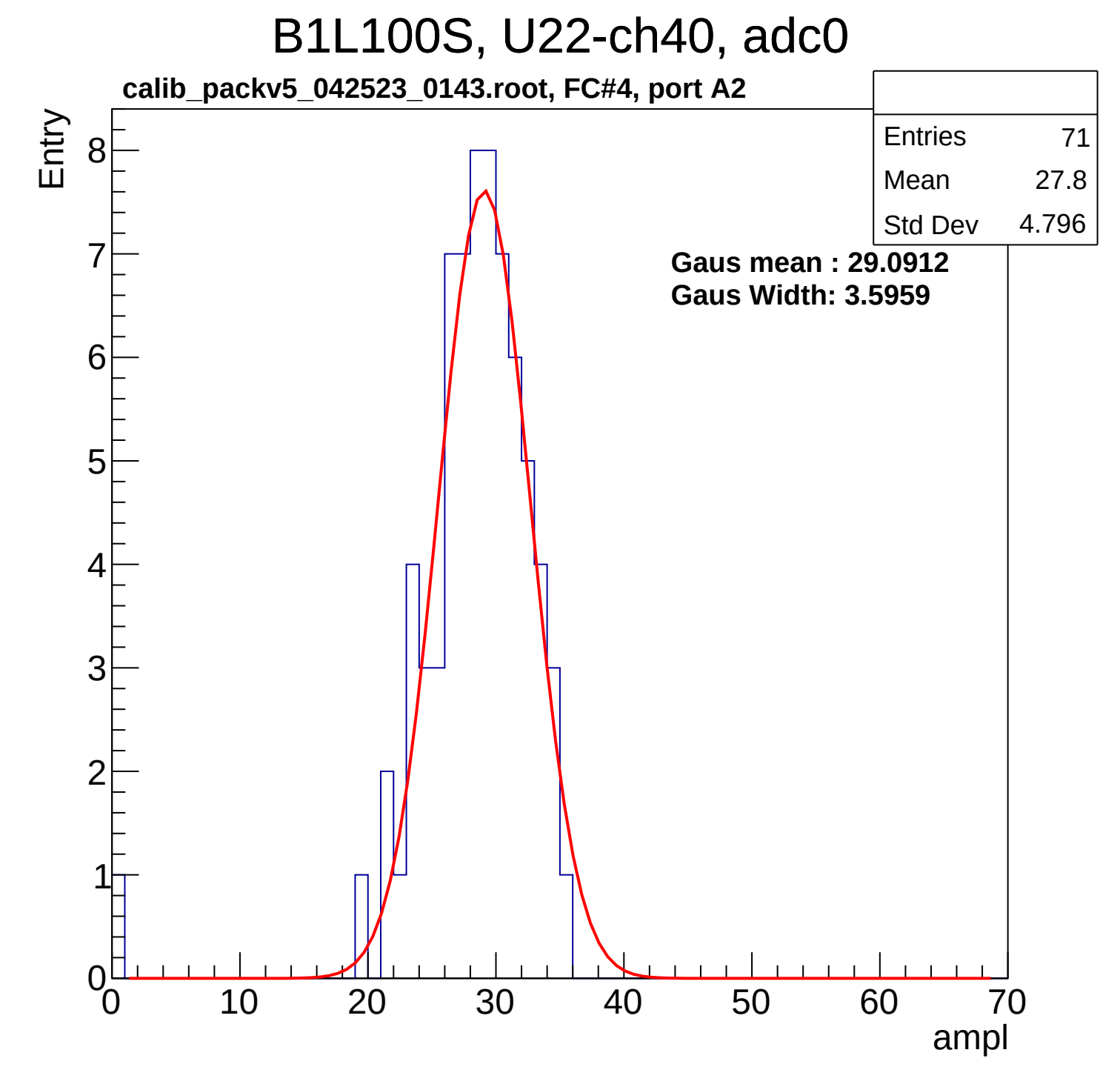
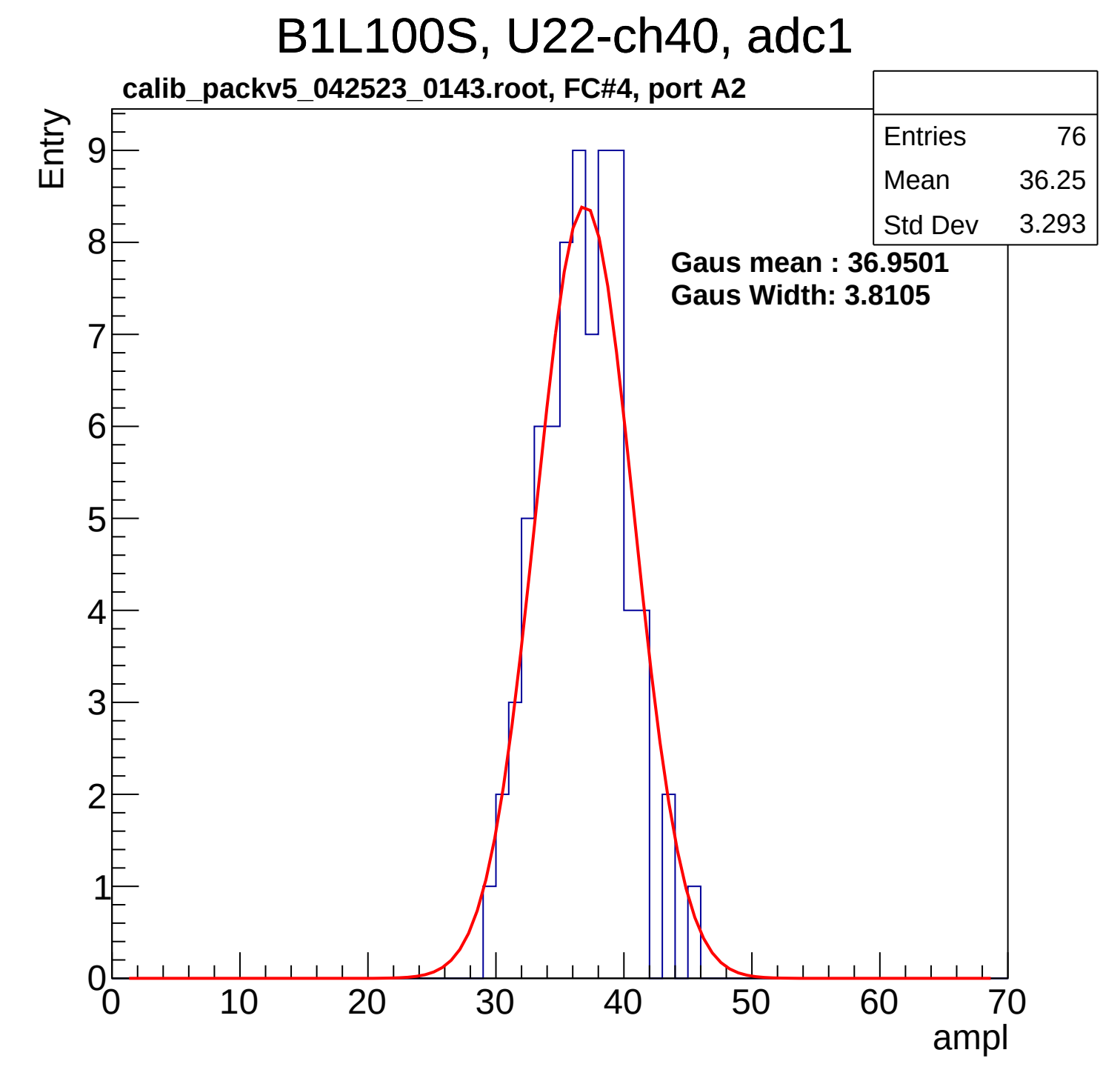
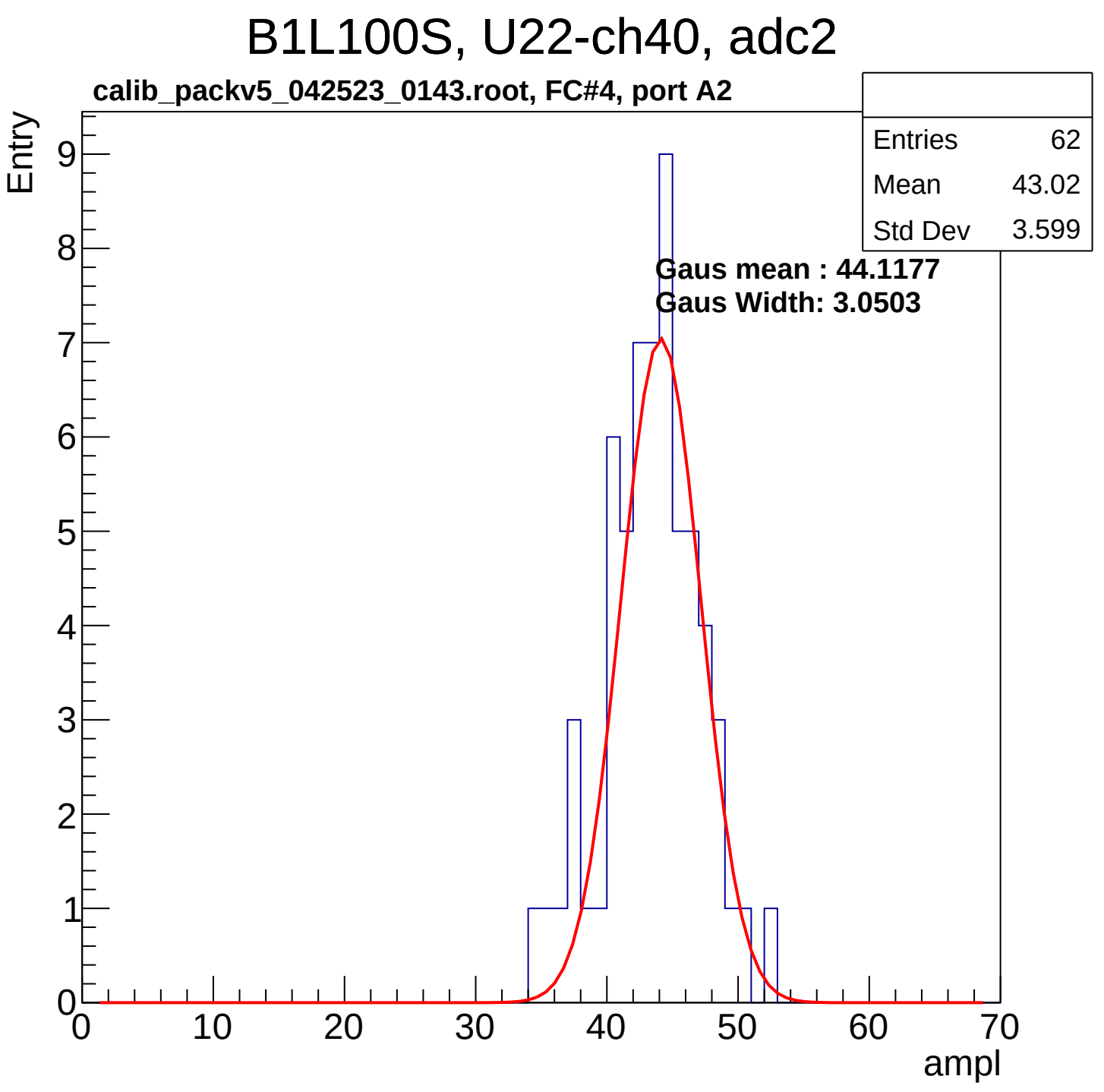
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch40, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 27.8
Std Dev 4.796
Gaus mean : 29.0912
Gaus Width: 3.5959
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch40, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.25
Std Dev 3.293
Gaus mean : 36.9501
Gaus Width: 3.8105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch40, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.02
Std Dev 3.599
Gaus mean : 44.1177
Gaus Width: 3.0503
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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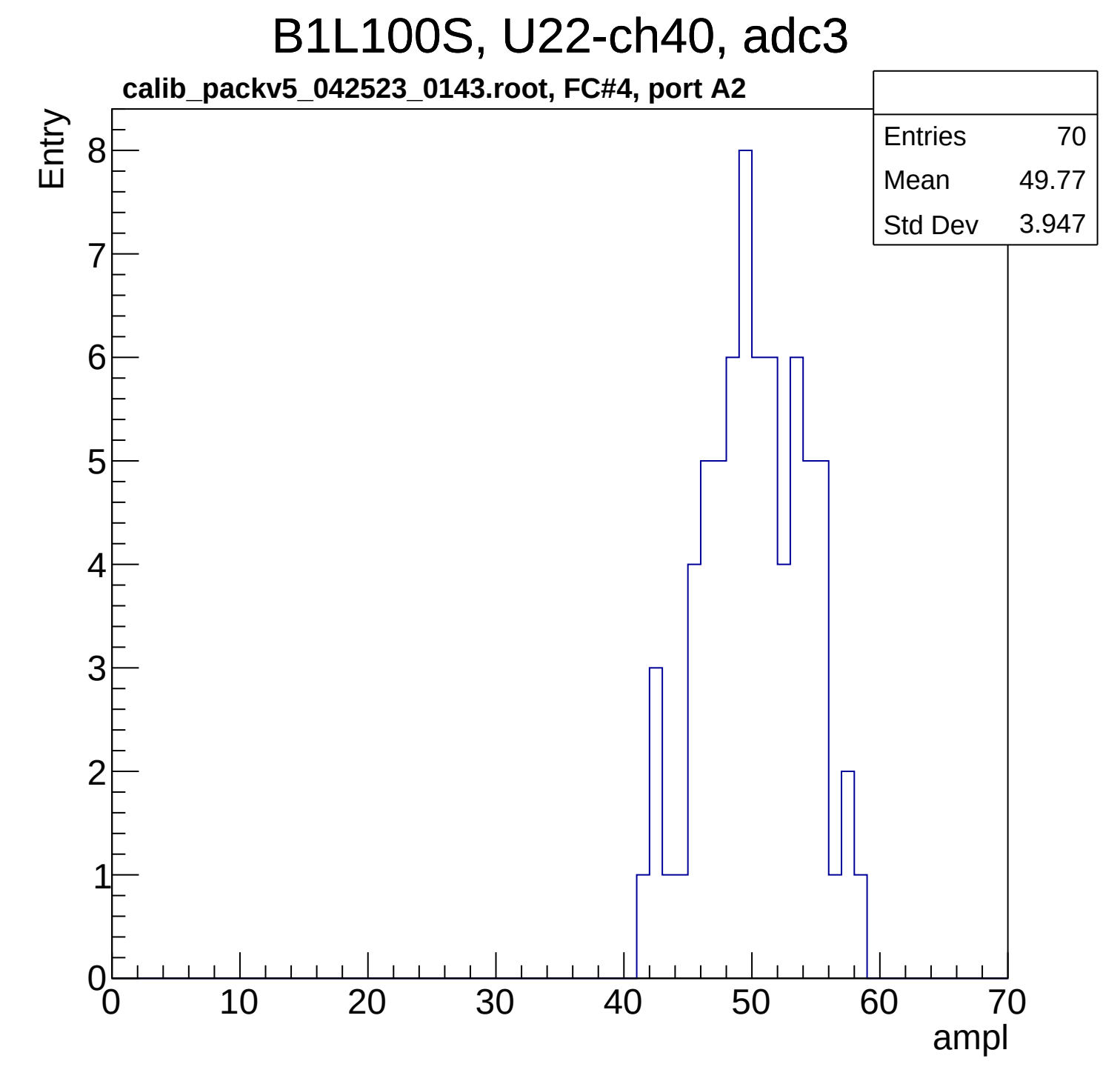
B1L100S, U22-ch40, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 49.77
Std Dev 3.947
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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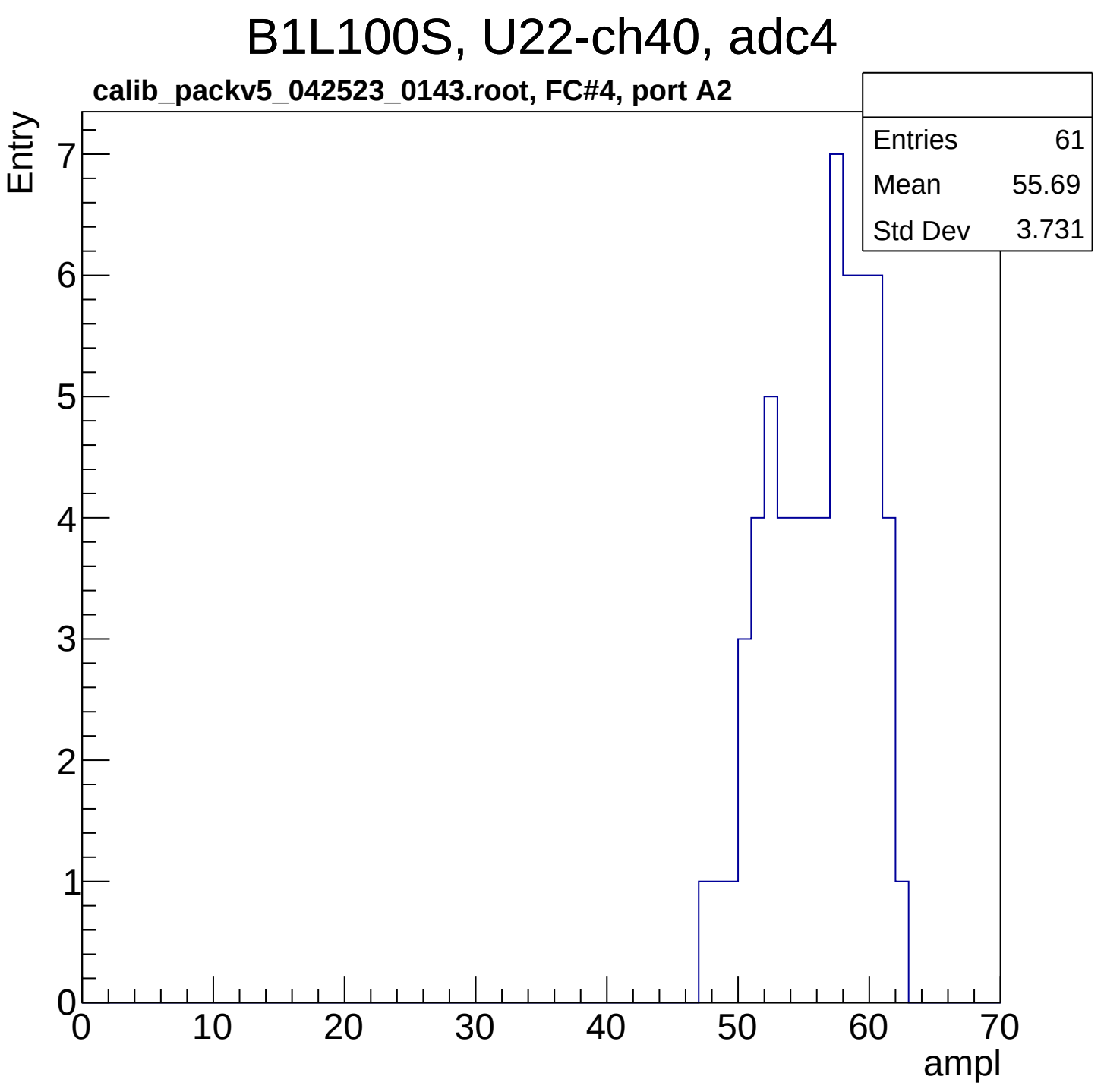
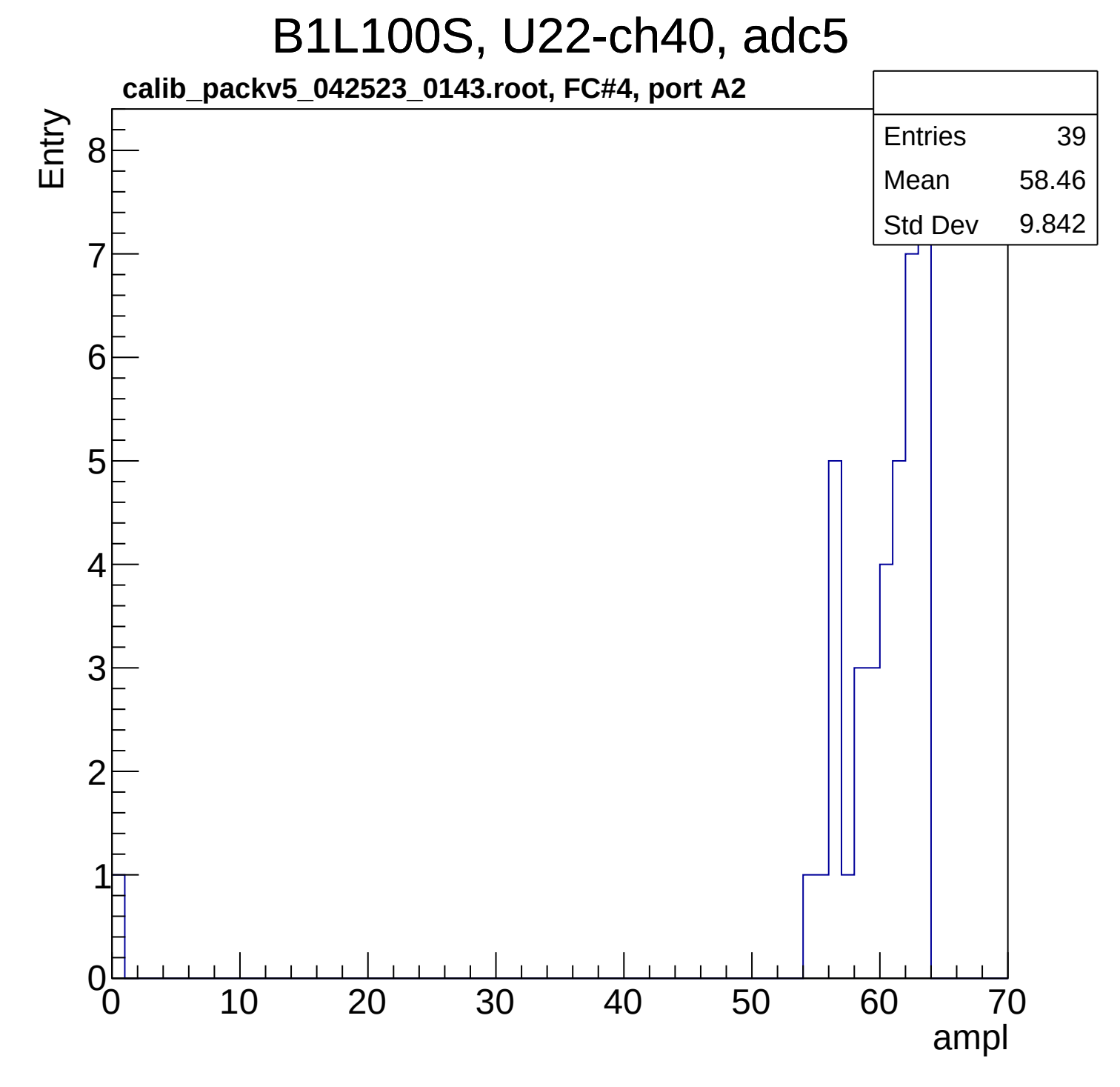
40
50
60
70


B1L100S, U22-ch40, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 55.69
Std Dev 3.731
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch40, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.46
Std Dev 9.842
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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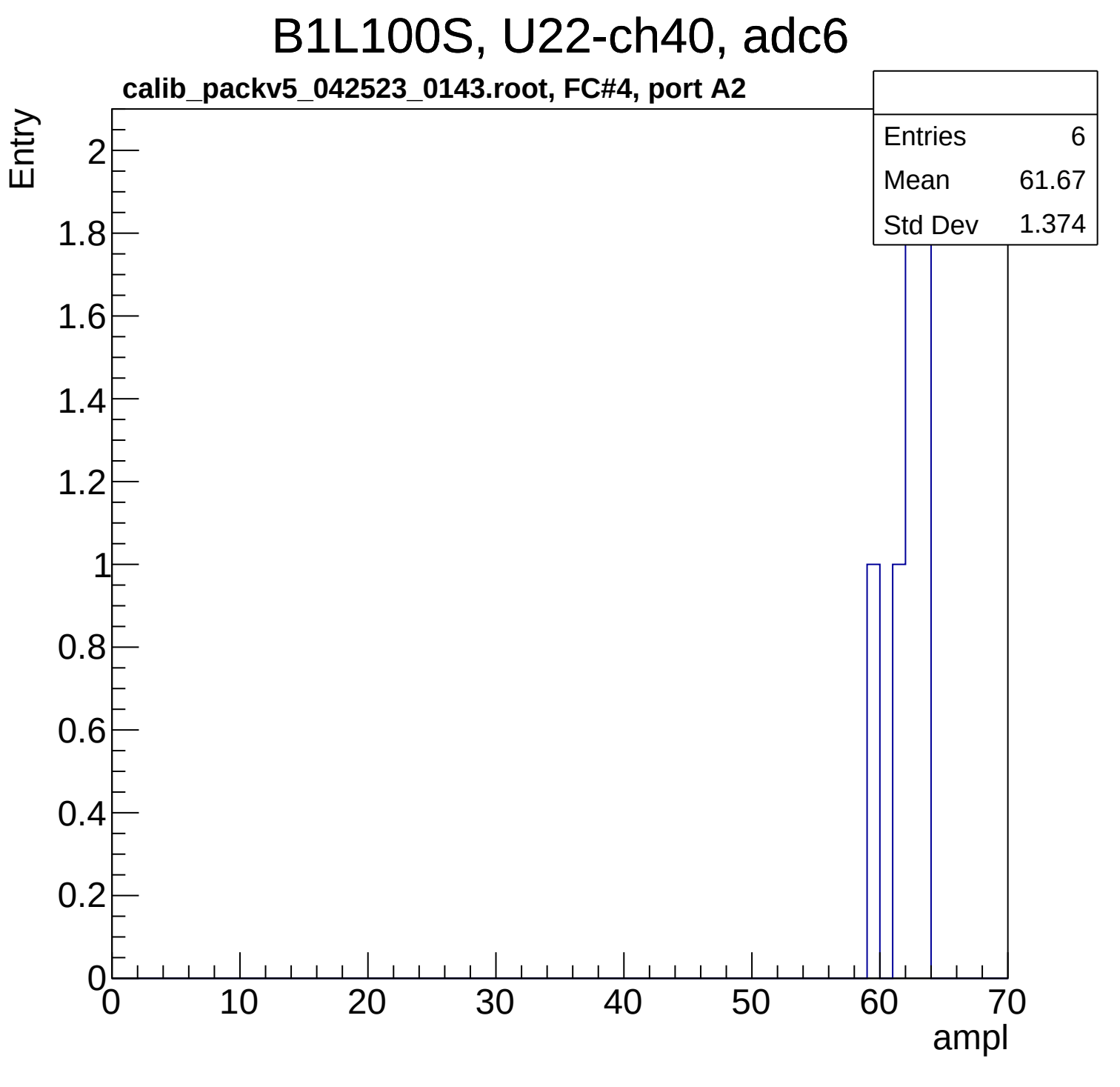
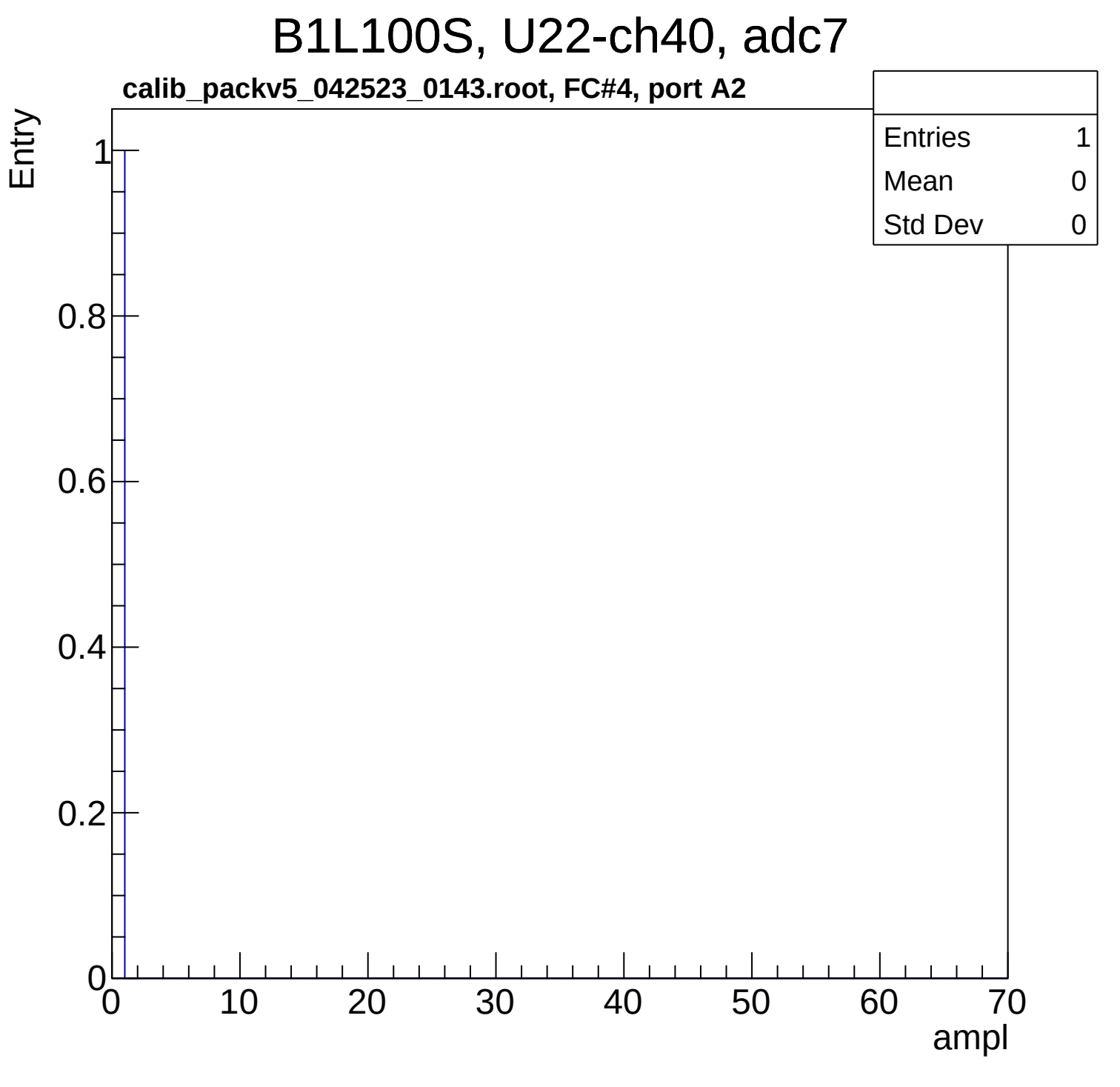
60
70


B1L100S, U22-ch40, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.67
Std Dev 1.374
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch40, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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30
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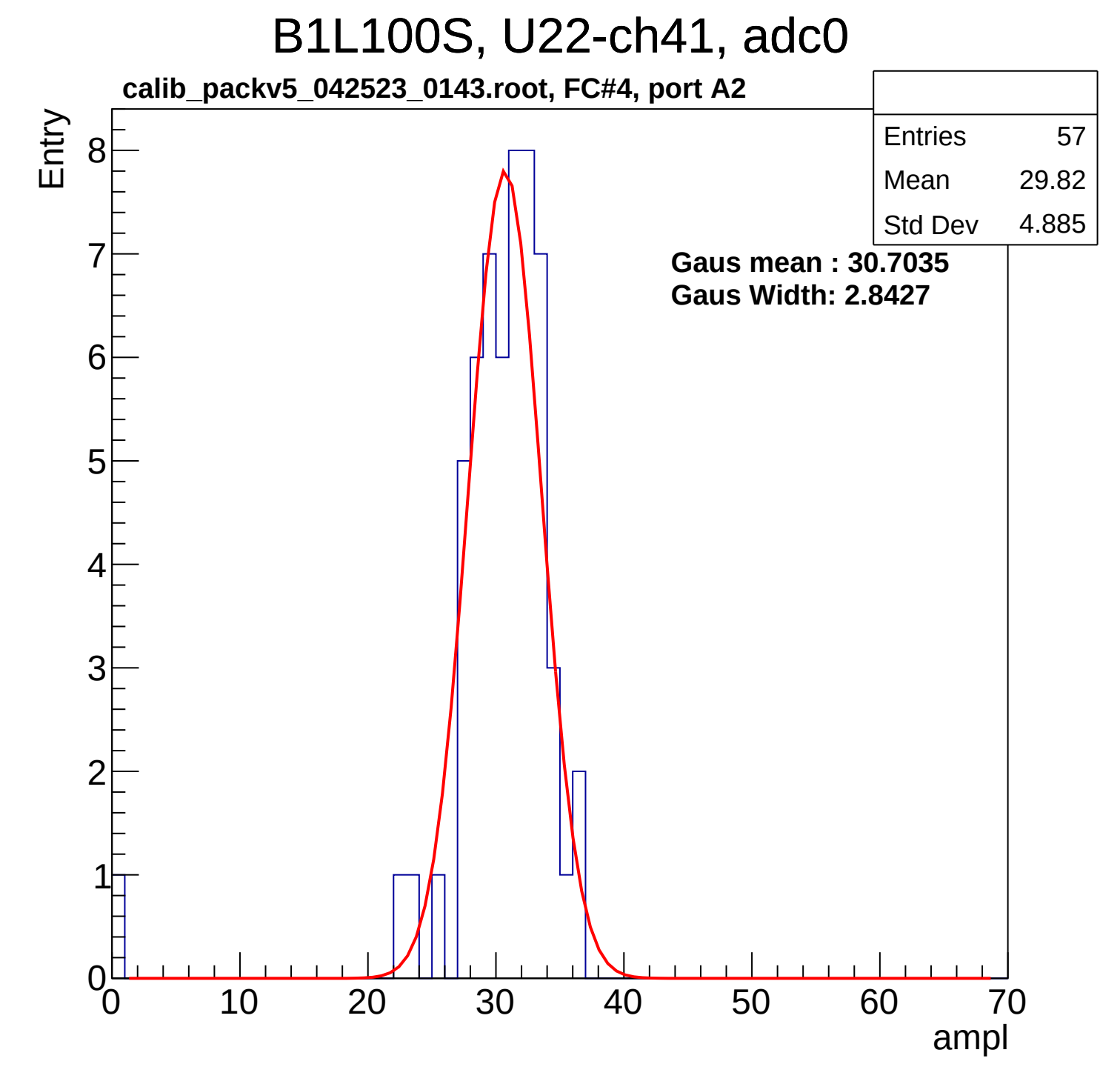
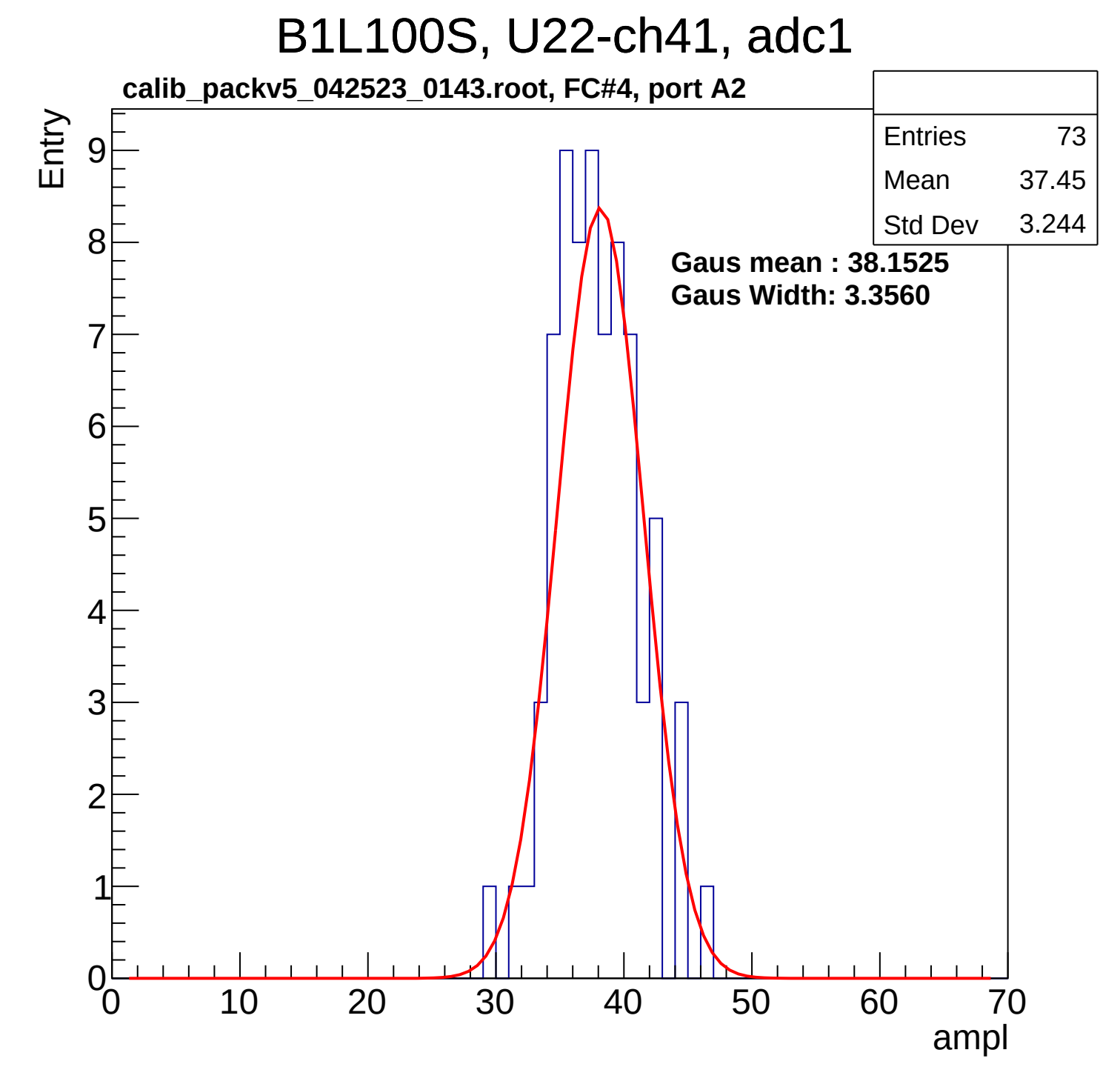
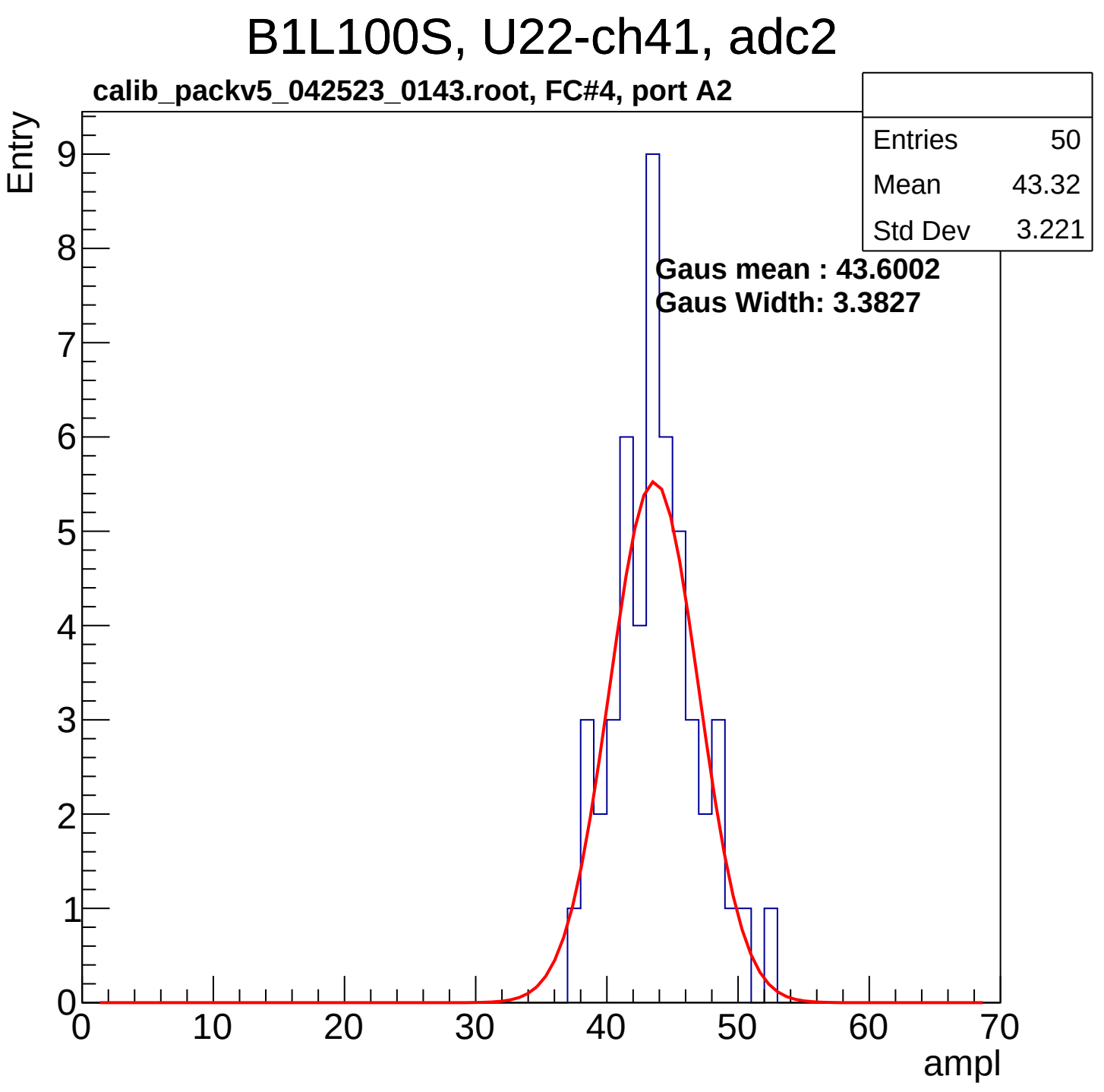
50
60
70
ampl


B1L100S, U22-ch41, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.82
Std Dev 4.885
Gaus mean : 30.7035
Gaus Width: 2.8427
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch41, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 37.45
Std Dev 3.244
Gaus mean : 38.1525
Gaus Width: 3.3560
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch41, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 43.32
Std Dev 3.221
Gaus mean : 43.6002
Gaus Width: 3.3827
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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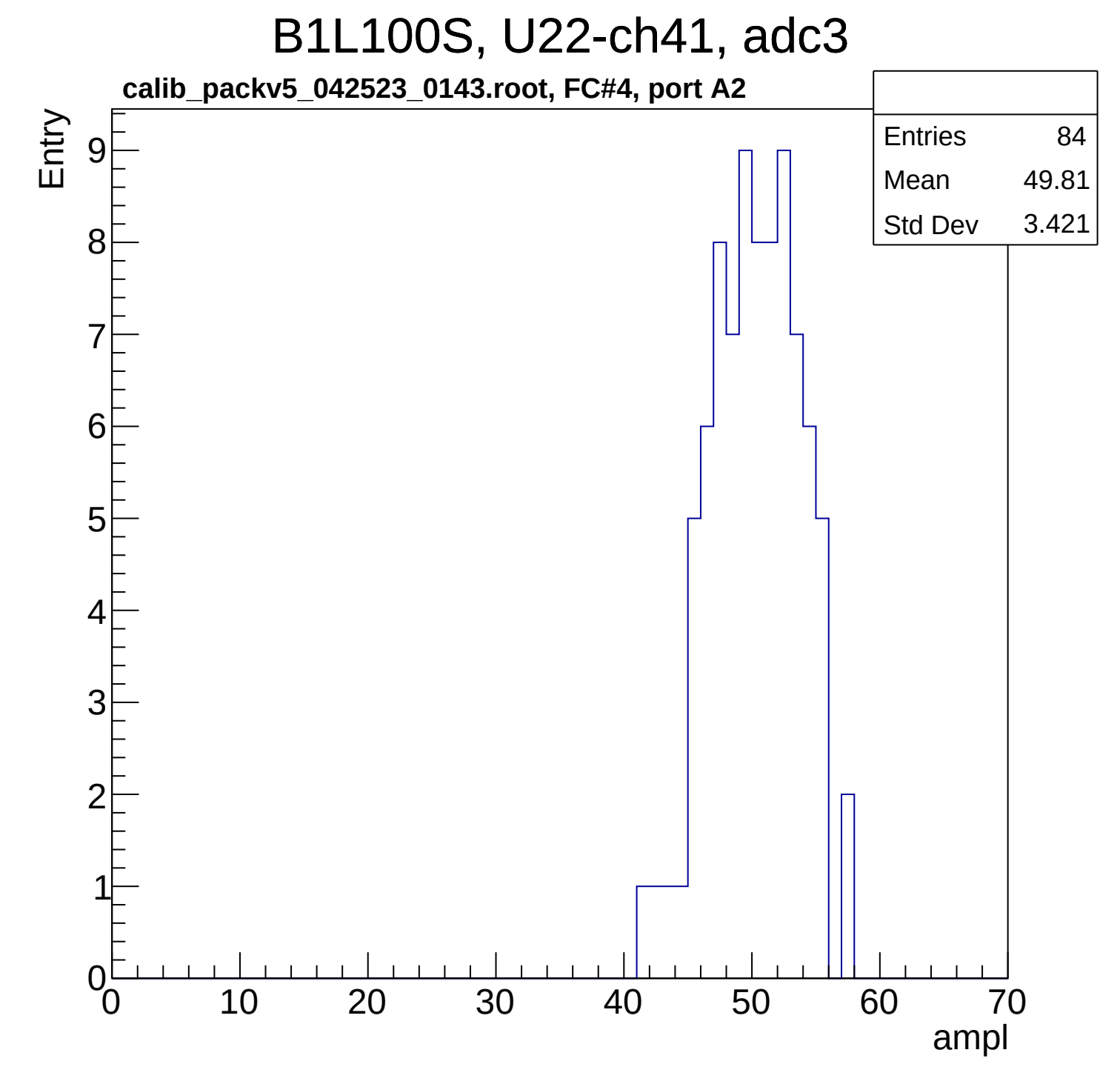
B1L100S, U22-ch41, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 49.81
Std Dev 3.421
Entry
ampl
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2
3
4
5
6
7
8
9
0
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60
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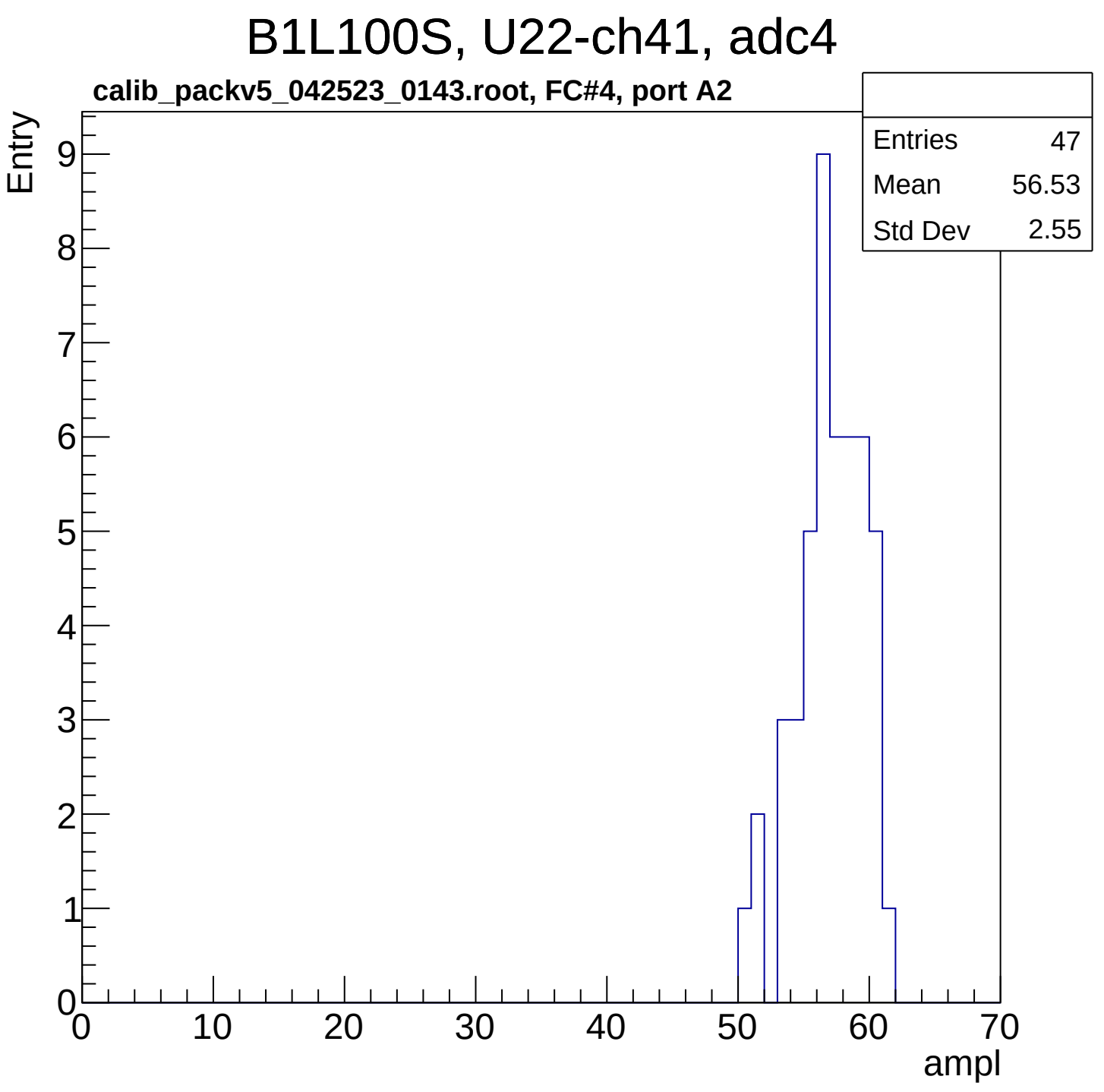
B1L100S, U22-ch41, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 56.53
Std Dev 2.55
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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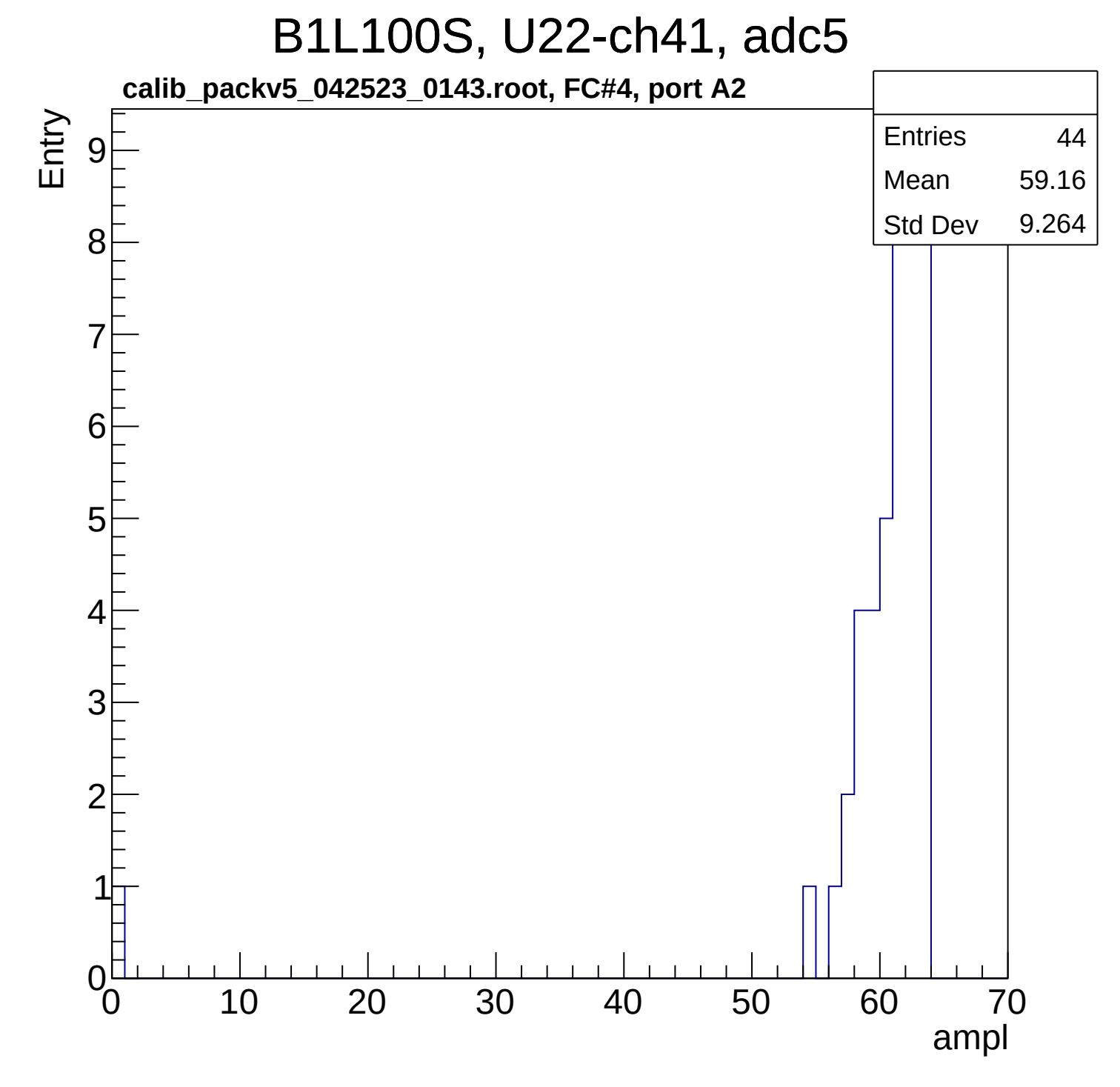
B1L100S, U22-ch41, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 59.16
Std Dev 9.264
Entry
ampl
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3
4
5
6
7
8
9
0
10
20
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40
50
60
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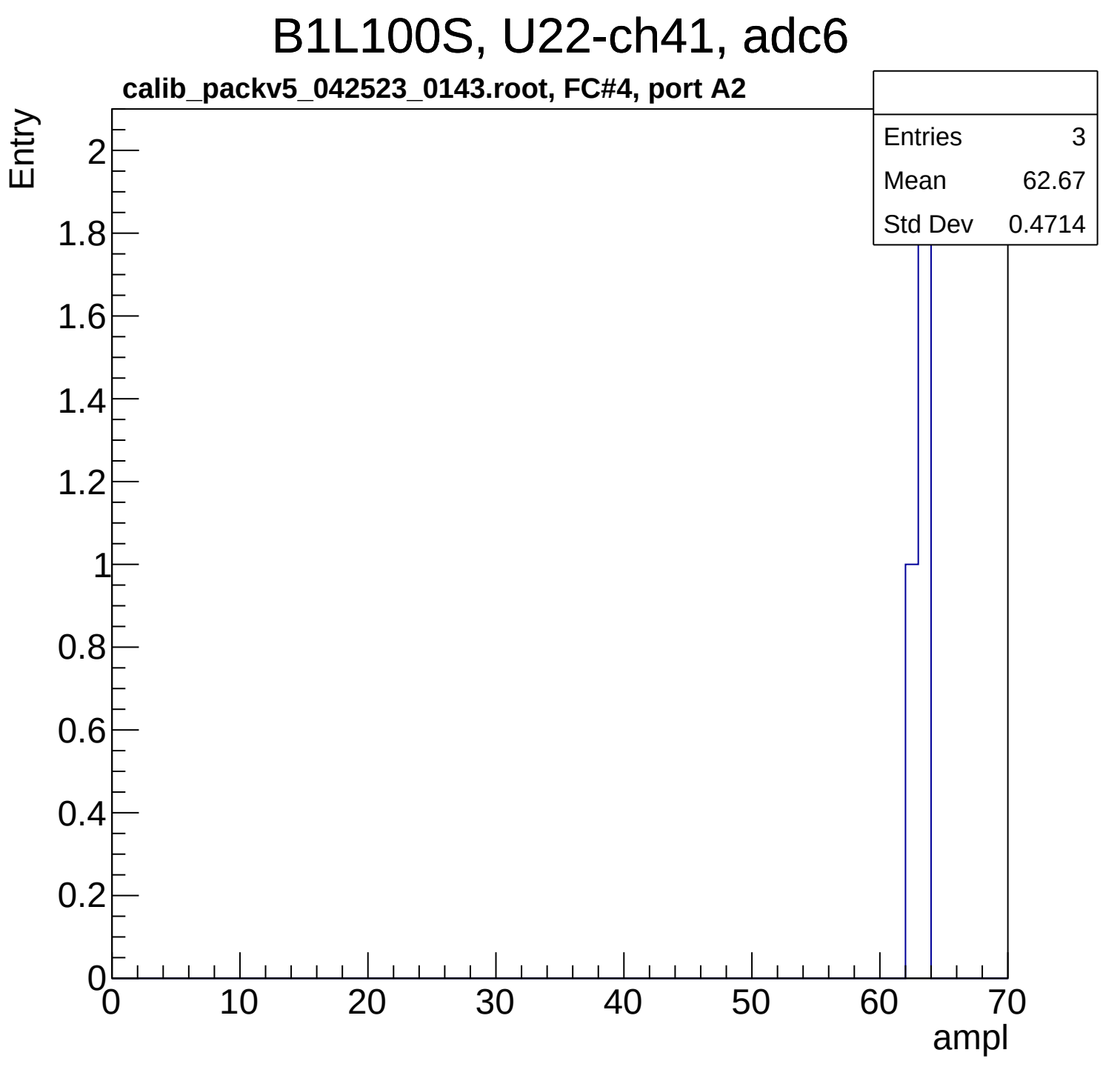
B1L100S, U22-ch41, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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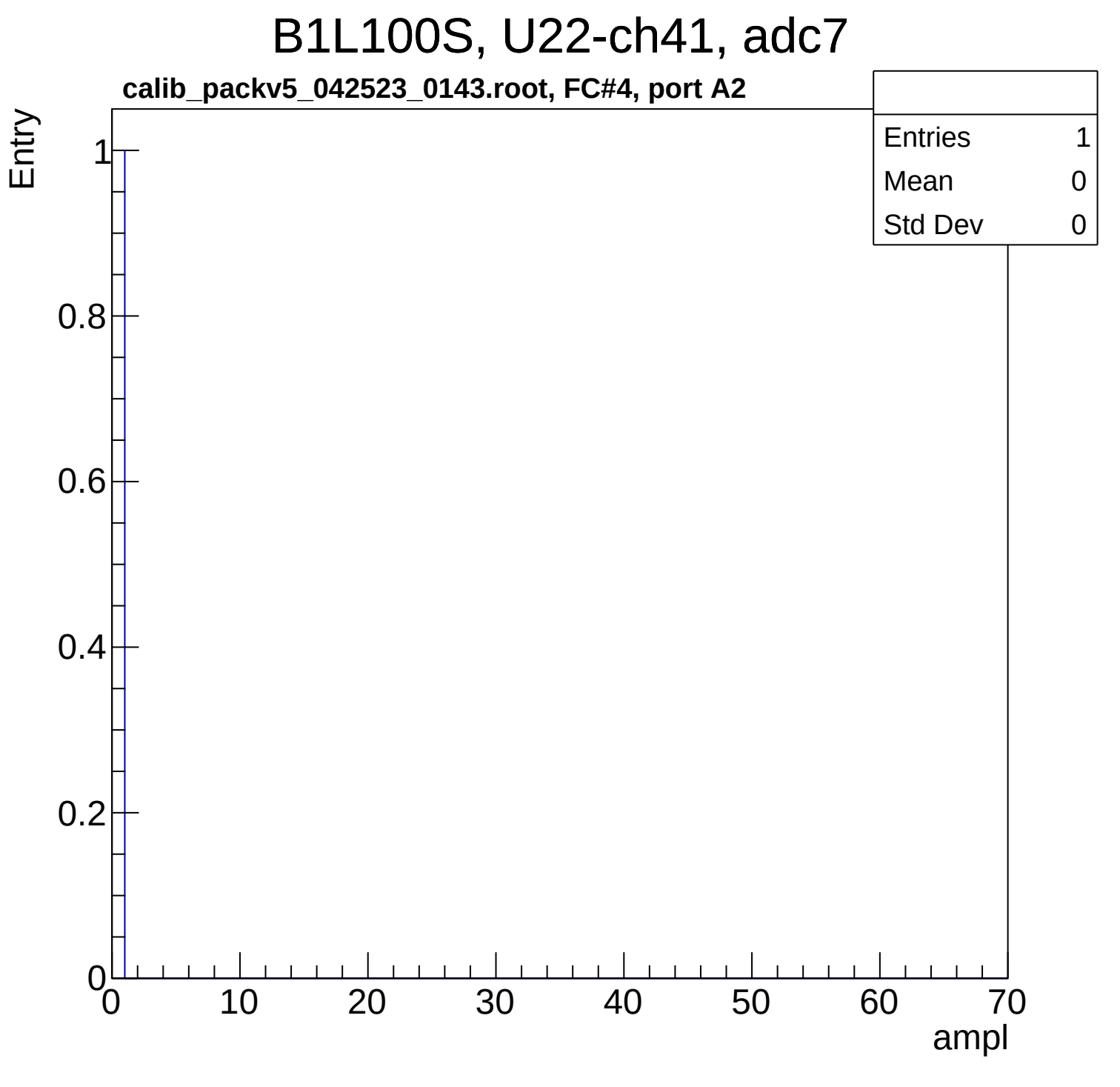
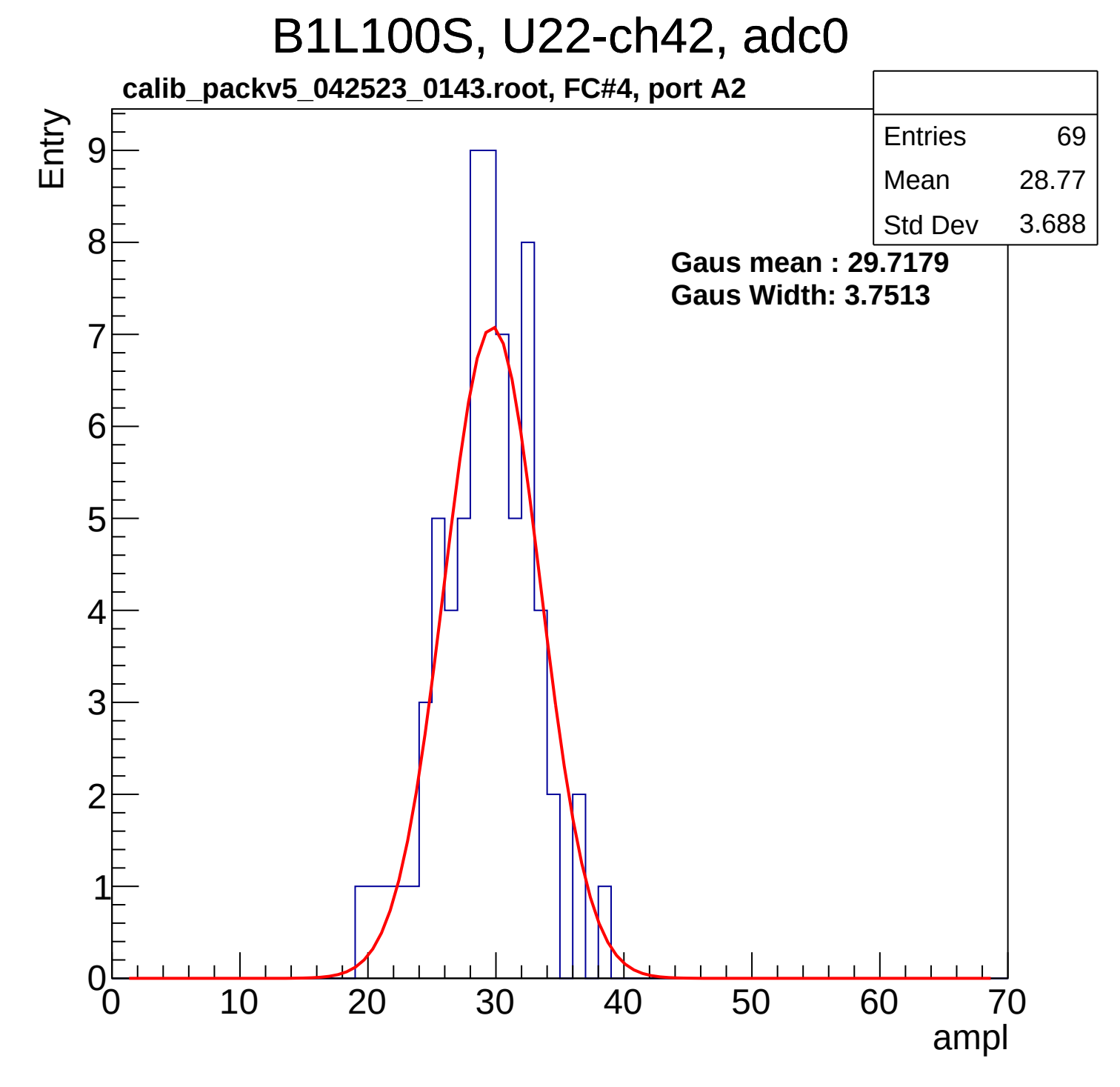
20
30
40
50
60
70


B1L100S, U22-ch41, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch42, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 28.77
Std Dev 3.688
Gaus mean : 29.7179
Gaus Width: 3.7513
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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60
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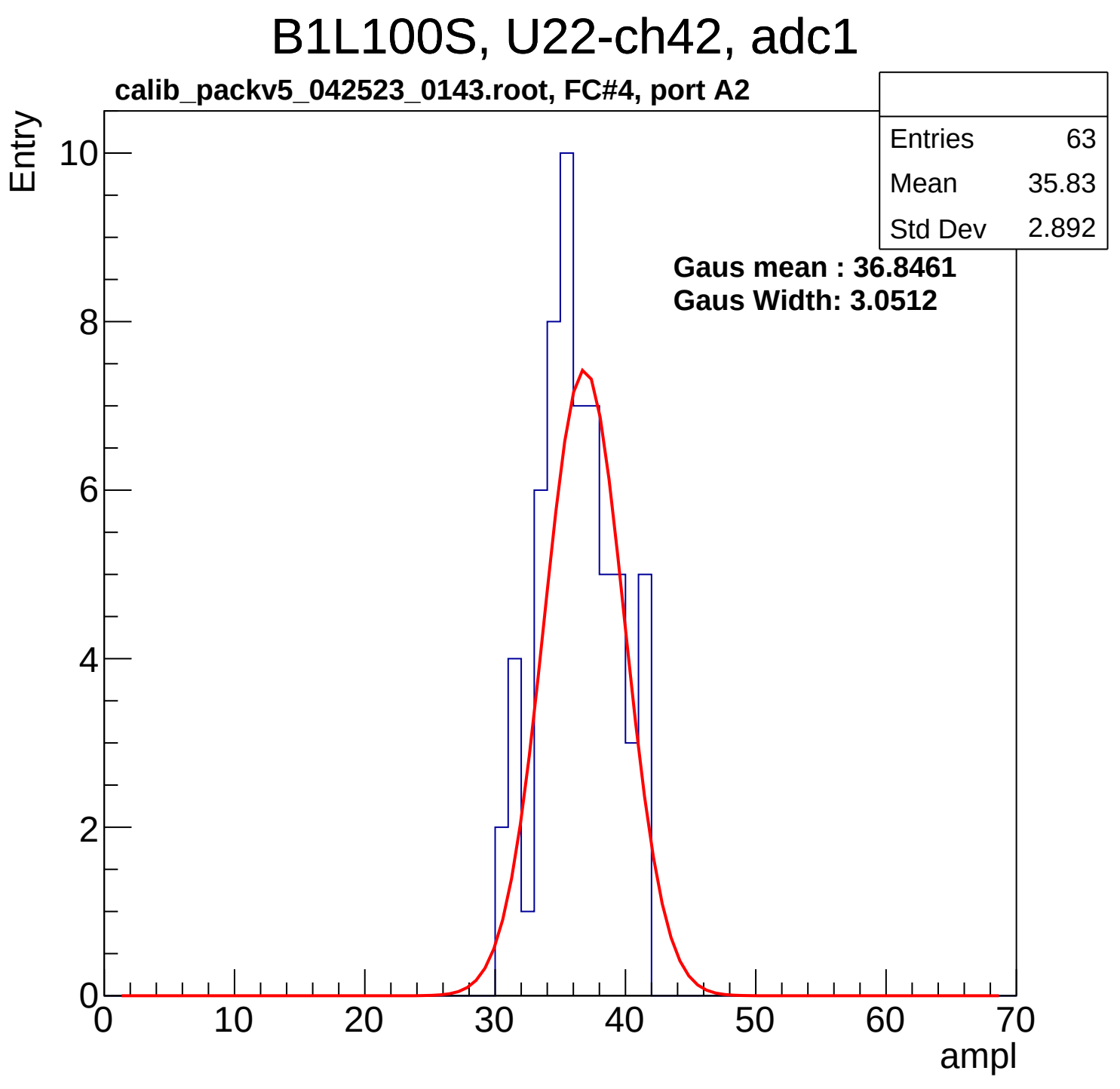
B1L100S, U22-ch42, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 35.83
Std Dev 2.892
Gaus mean : 36.8461
Gaus Width: 3.0512
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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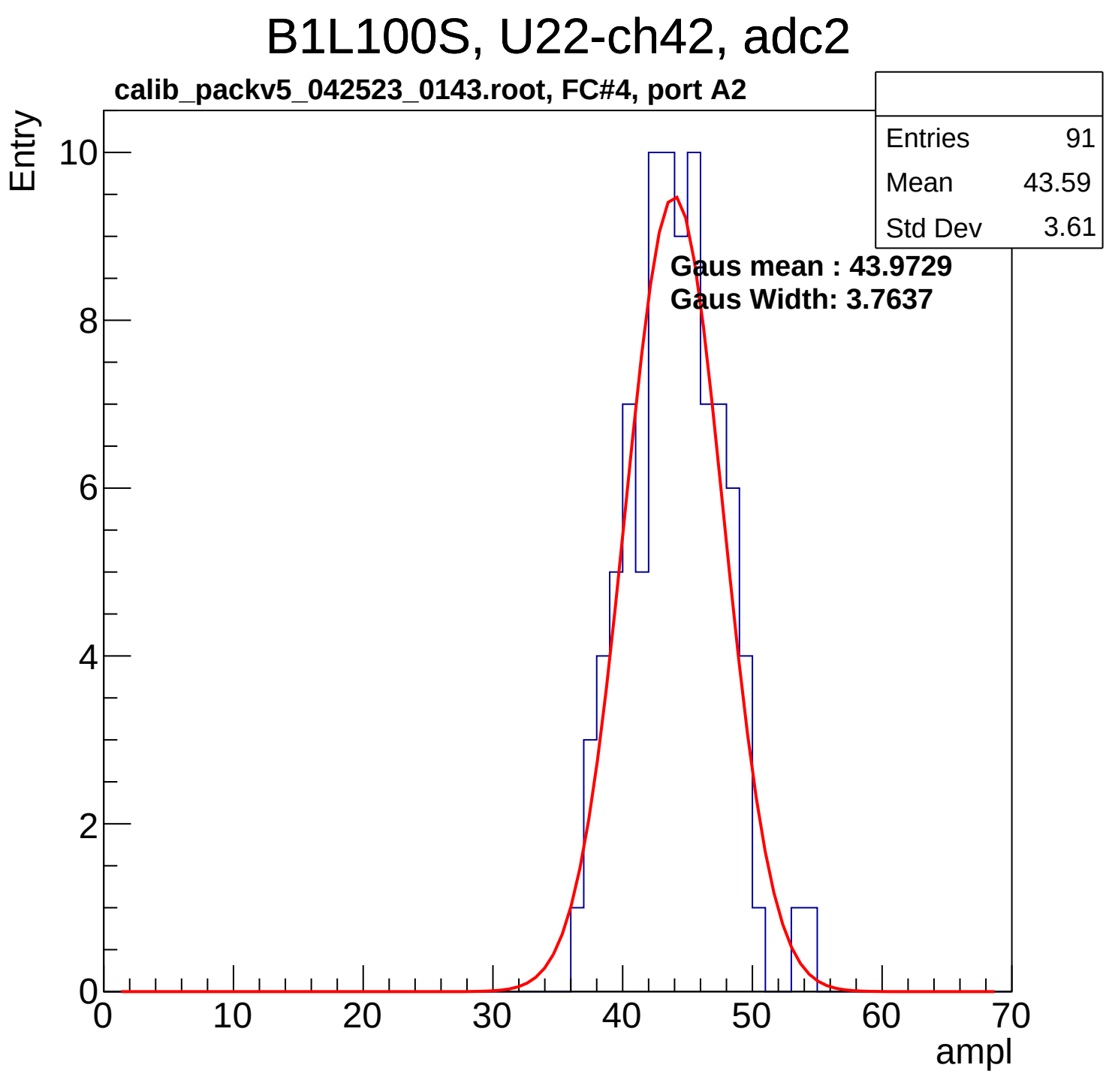
B1L100S, U22-ch42, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 91
Mean 43.59
Std Dev 3.61
Gaus mean : 43.9729
Gaus Width: 3.7637
Entry
ampl
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2
4
6
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0
10
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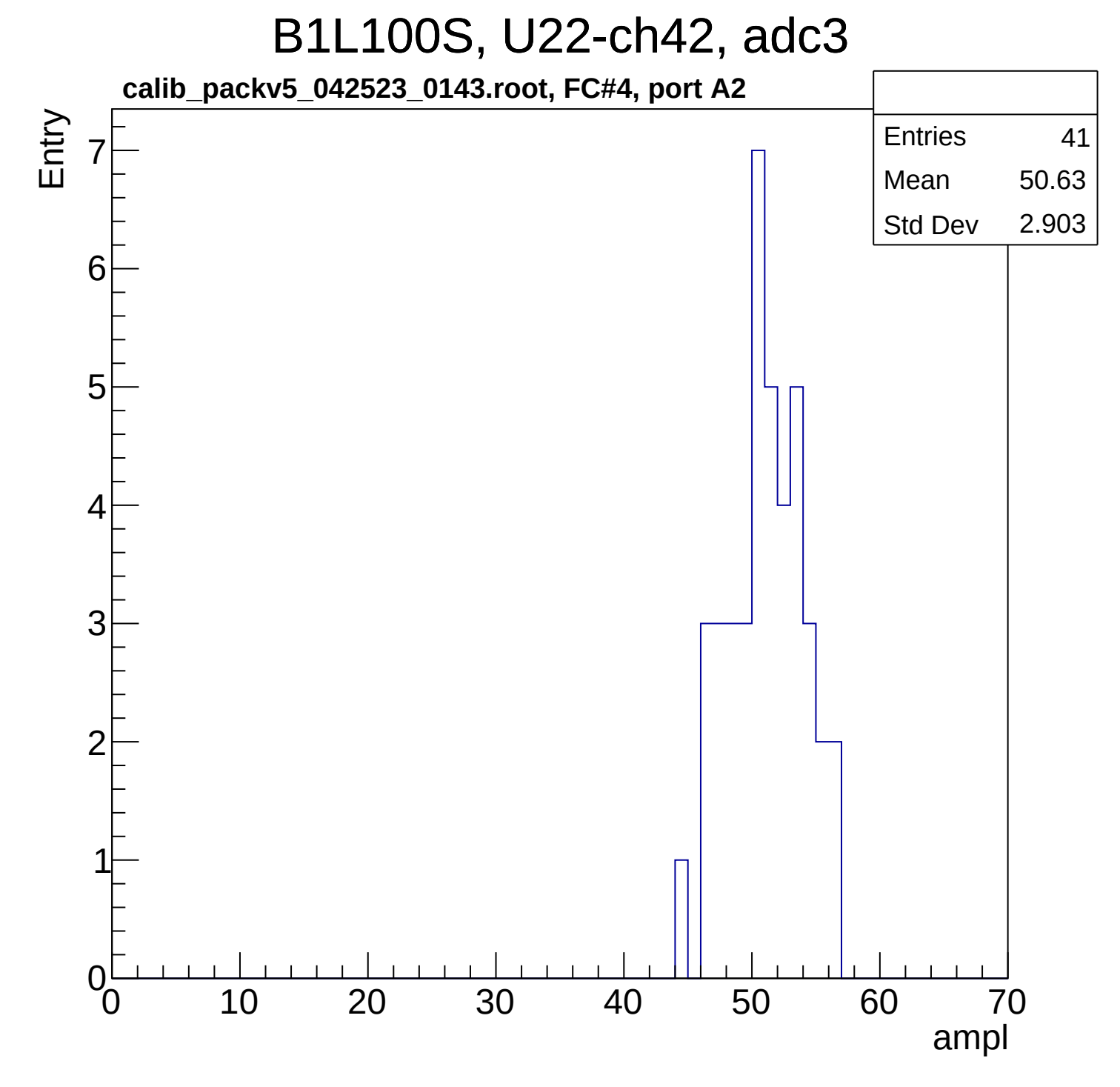
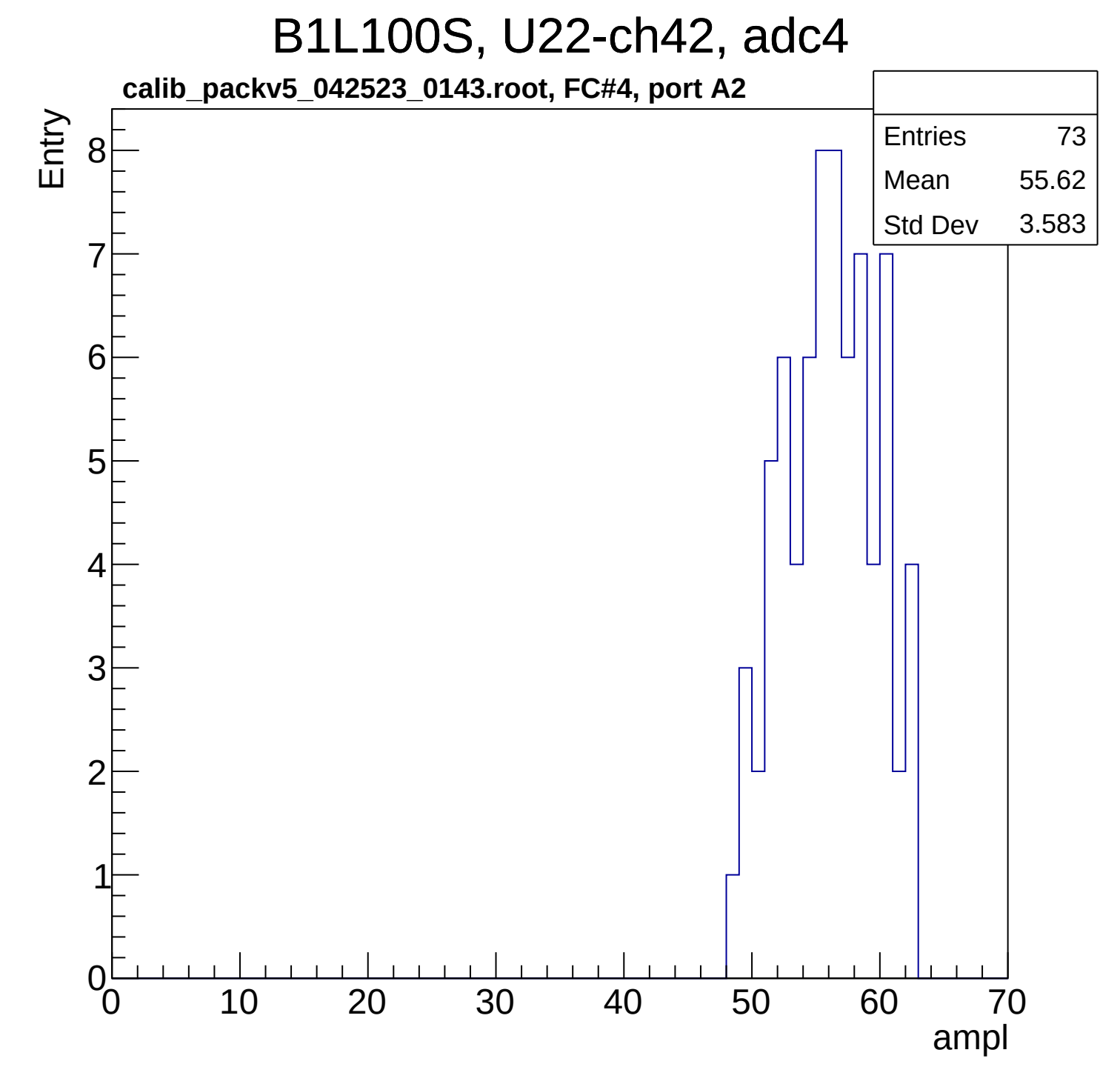
30
40
50
60
70


B1L100S, U22-ch42, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 50.63
Std Dev 2.903
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch42, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 55.62
Std Dev 3.583
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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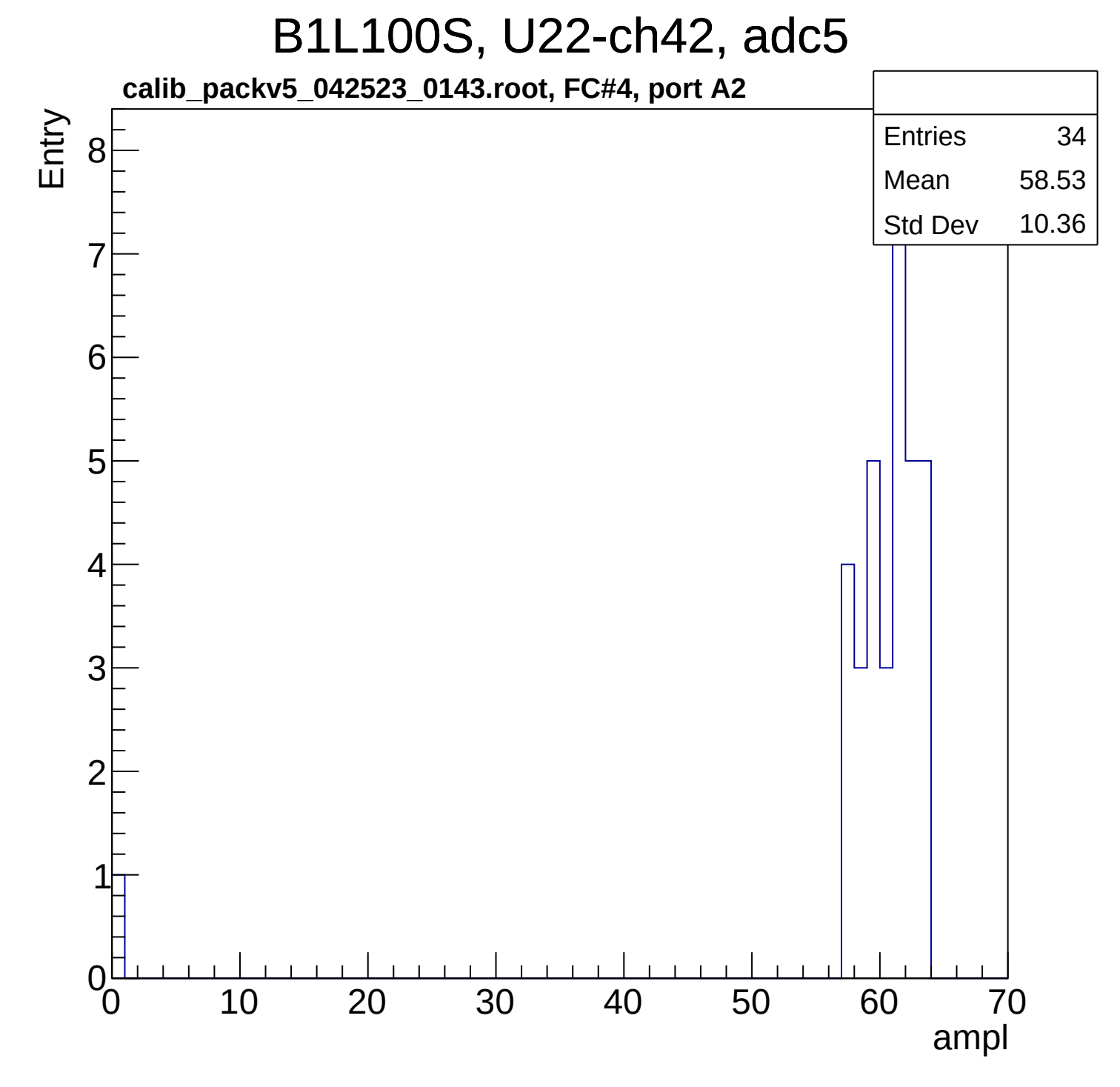
B1L100S, U22-ch42, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 58.53
Std Dev 10.36
Entry
ampl
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2
3
4
5
6
7
8
0
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20
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40
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60
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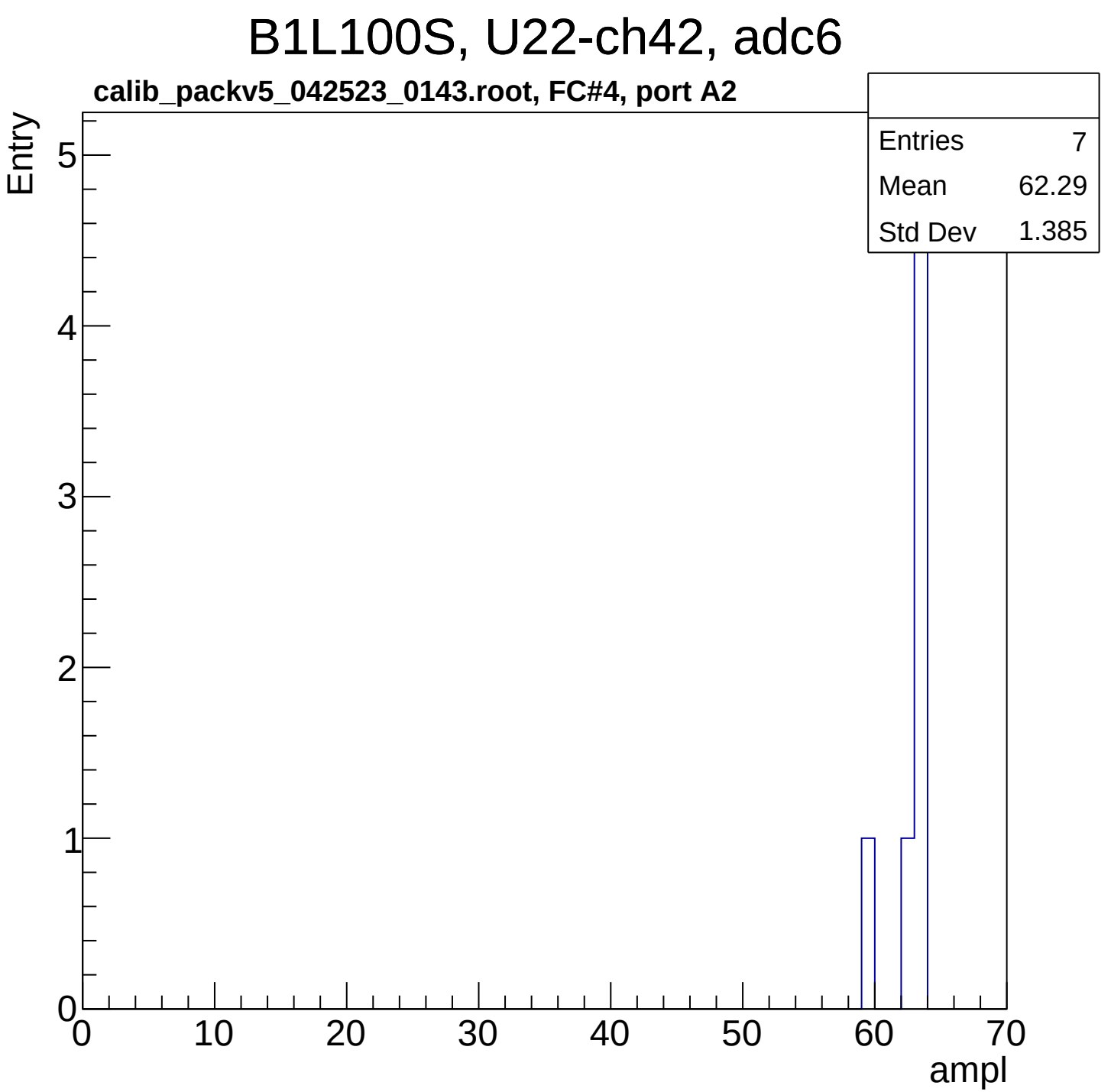
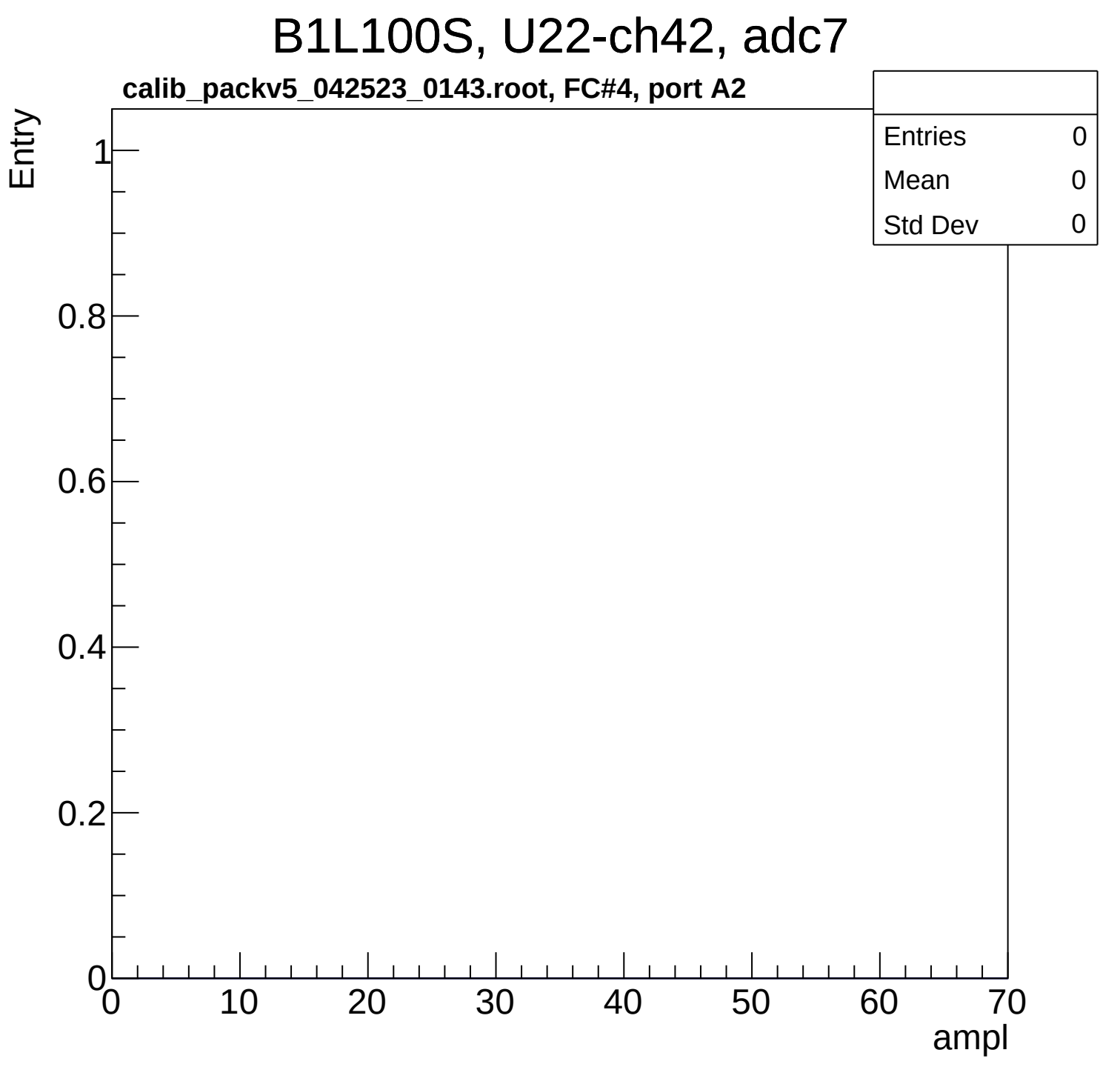
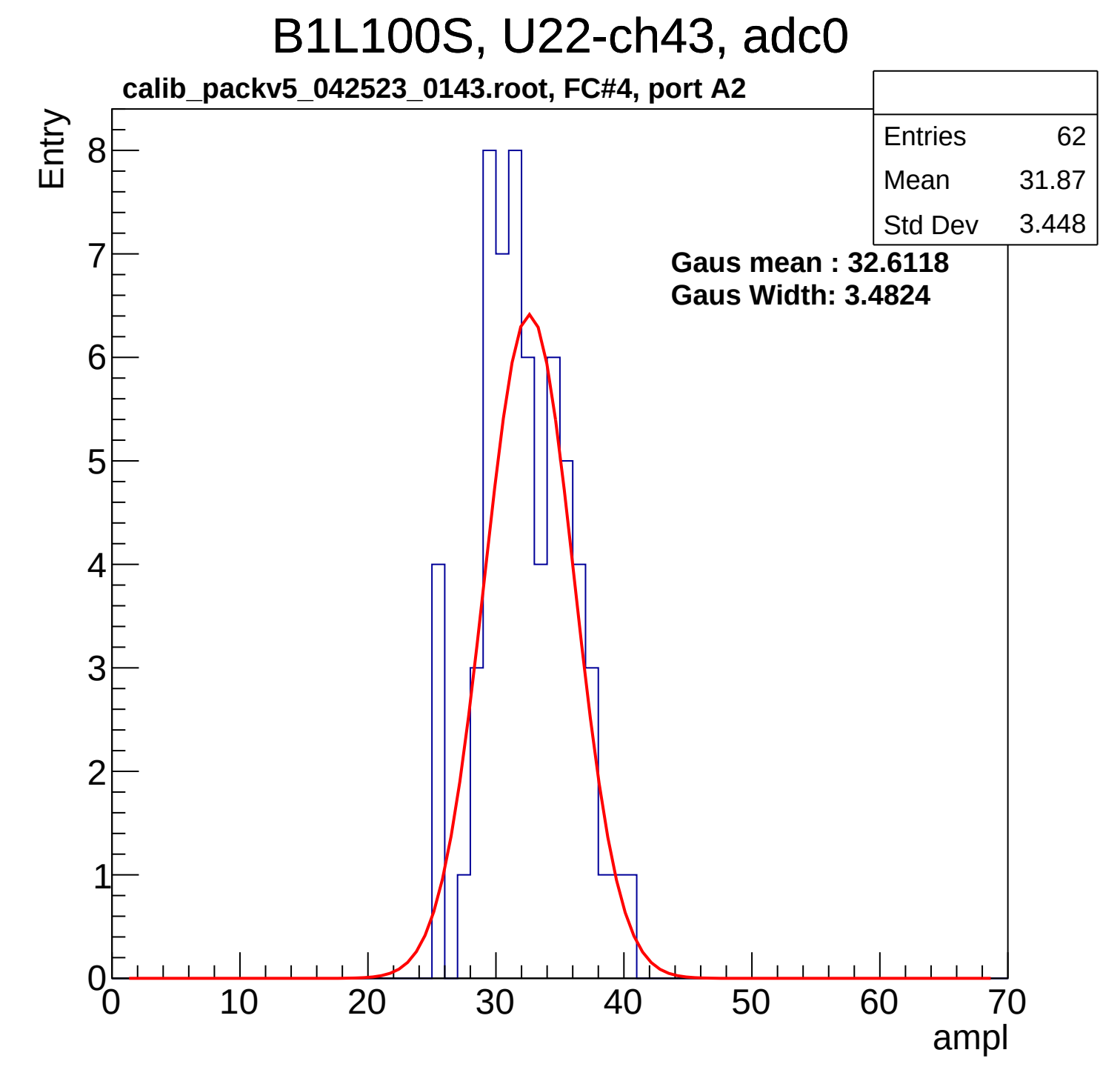
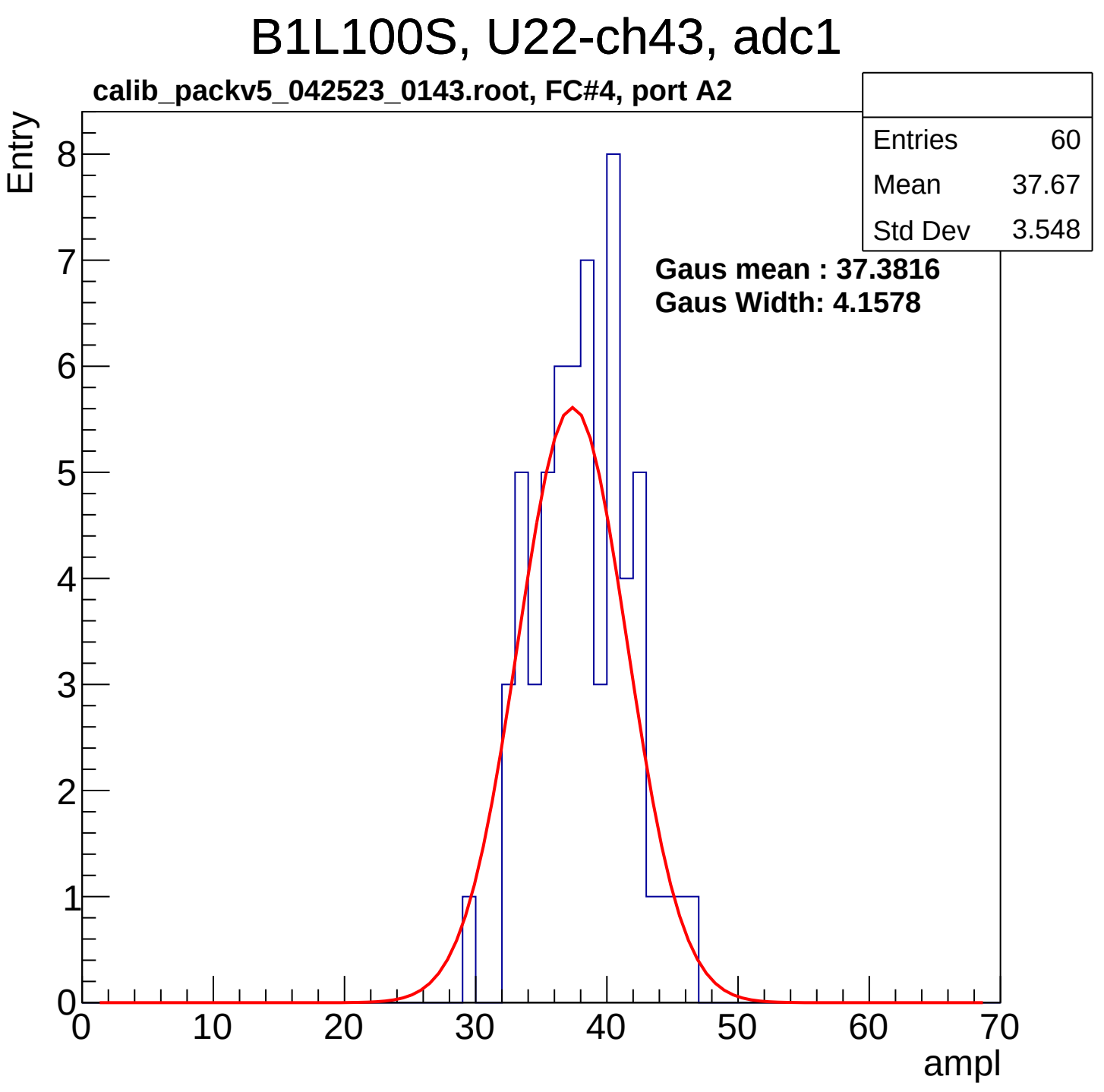

B1L100S, U22-ch42, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 62.29
Std Dev 1.385
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch42, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch43, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 31.87
Std Dev 3.448
Gaus mean : 32.6118
Gaus Width: 3.4824
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch43, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 37.67
Std Dev 3.548
Gaus mean : 37.3816
Gaus Width: 4.1578
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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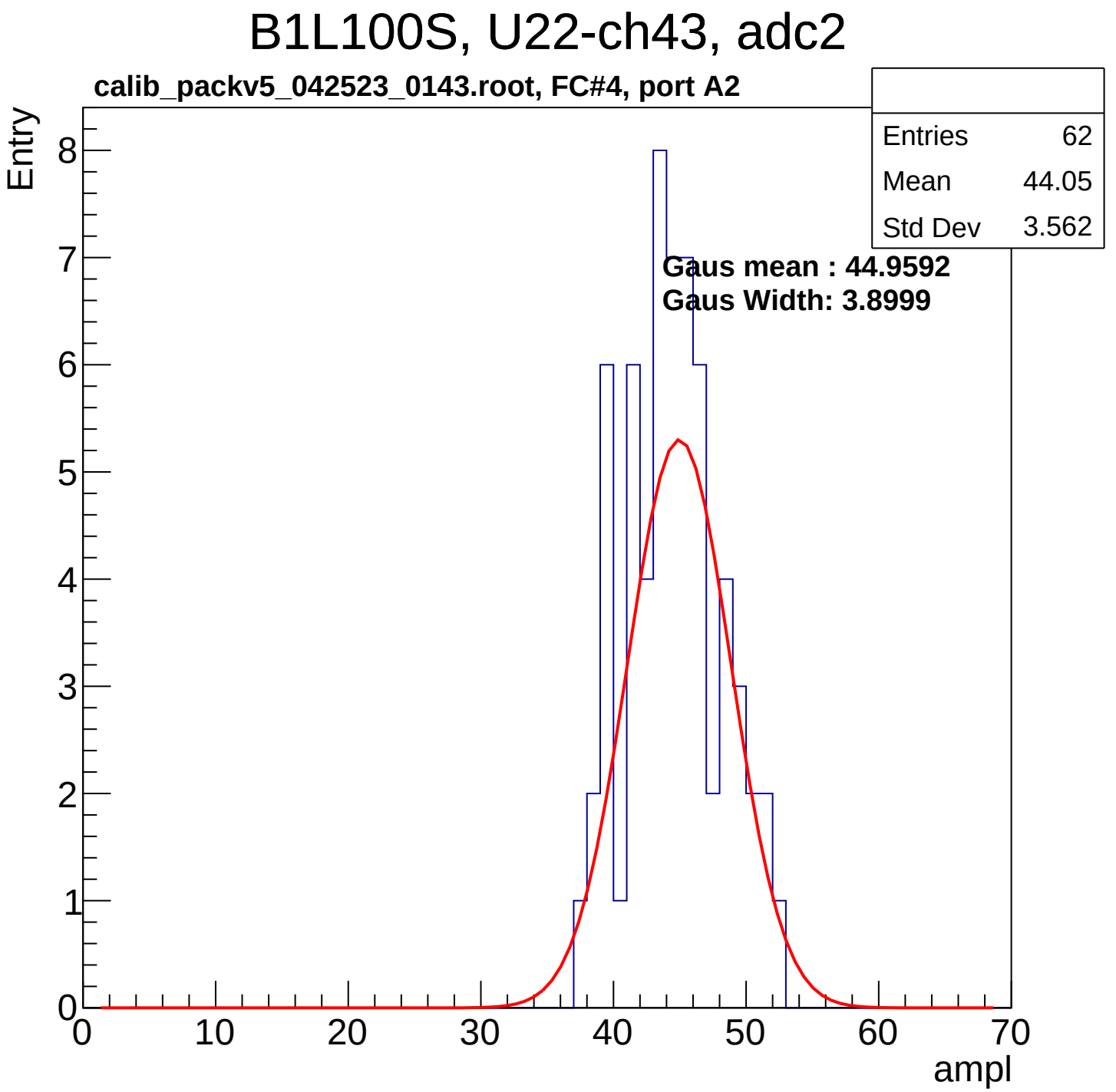
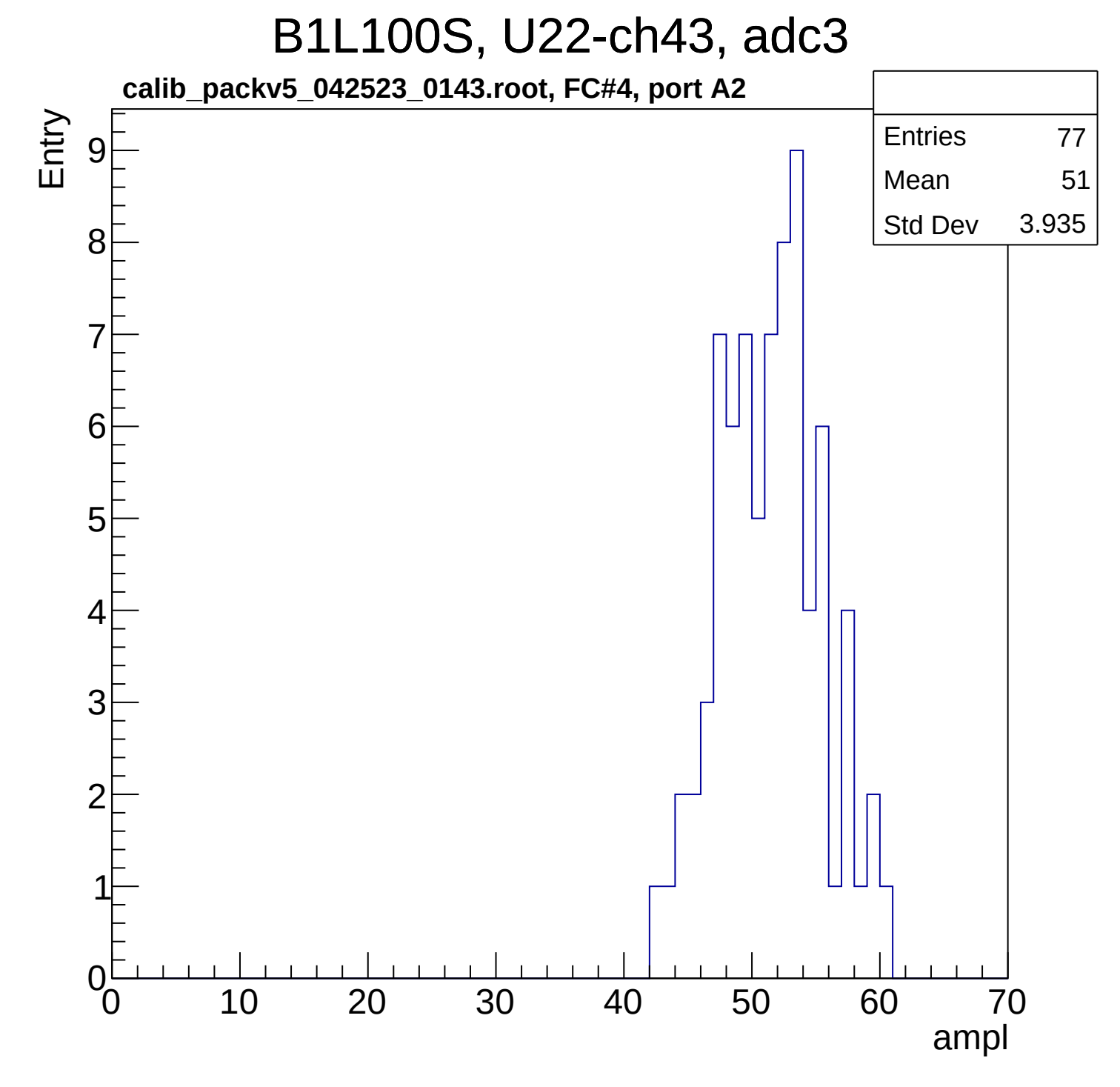

B1L100S, U22-ch43, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 44.05
Std Dev 3.562
Gaus mean : 44.9592
Gaus Width: 3.8999
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch43, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 51
Std Dev 3.935
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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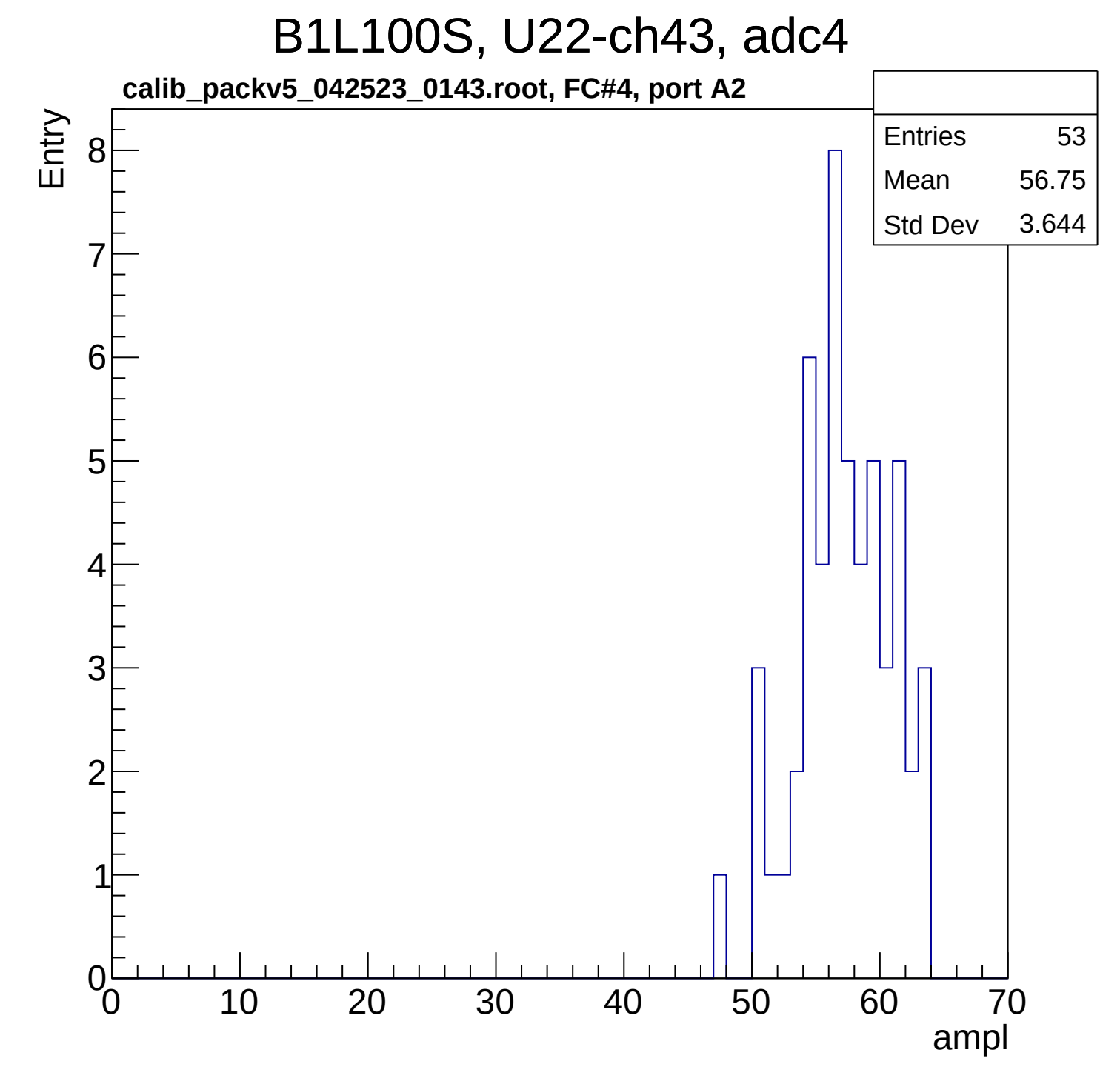
B1L100S, U22-ch43, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.75
Std Dev 3.644
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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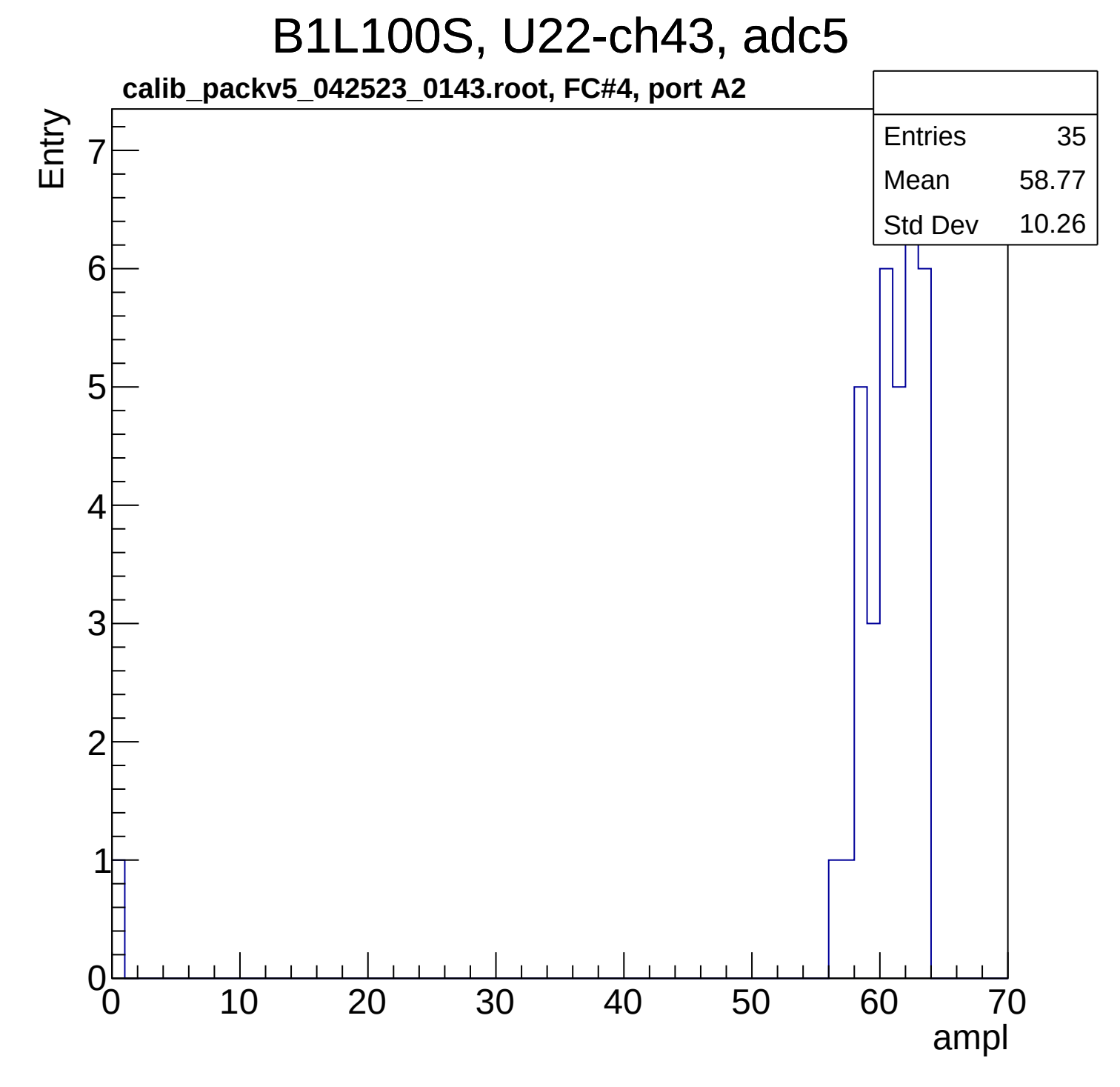
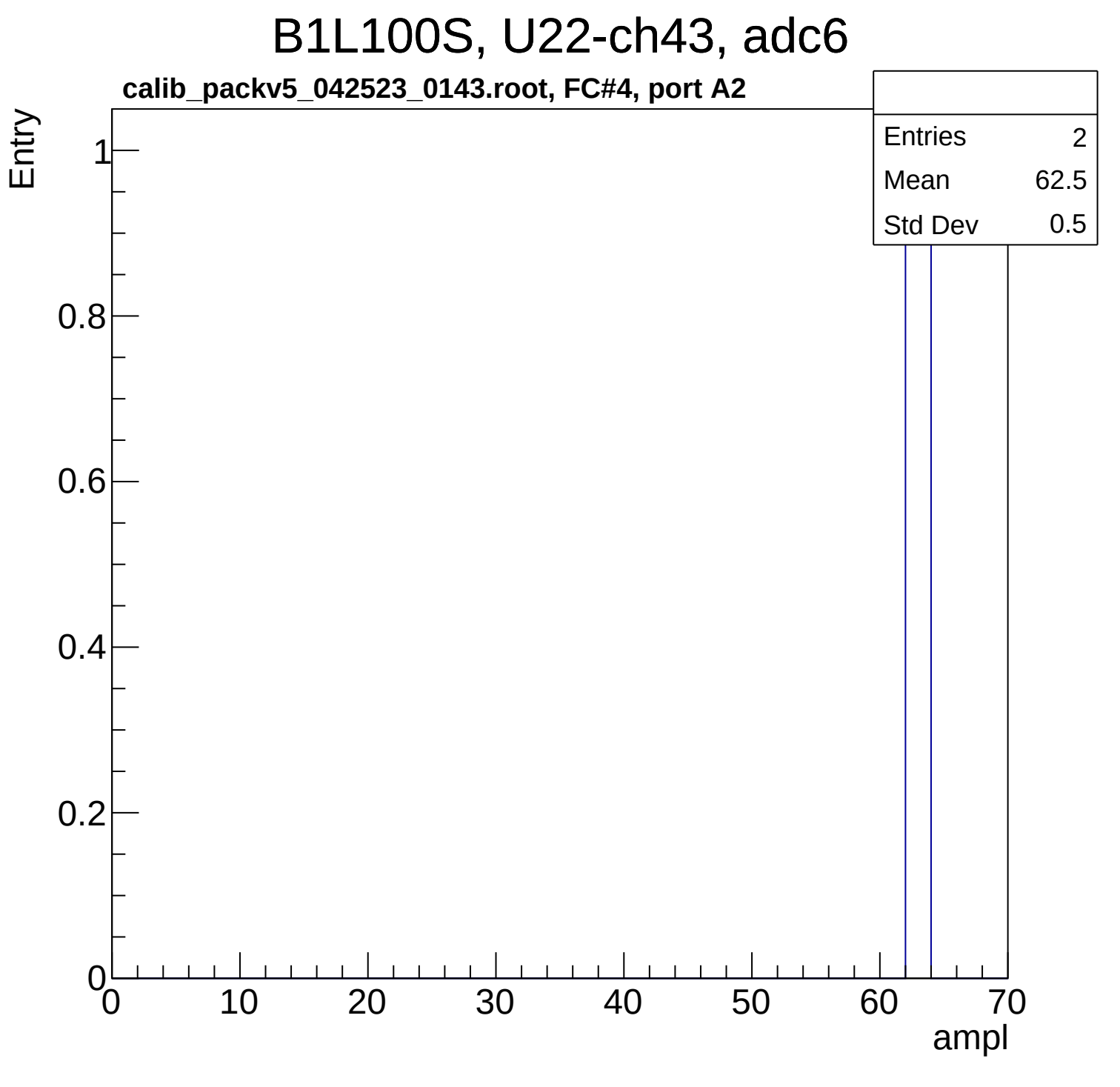
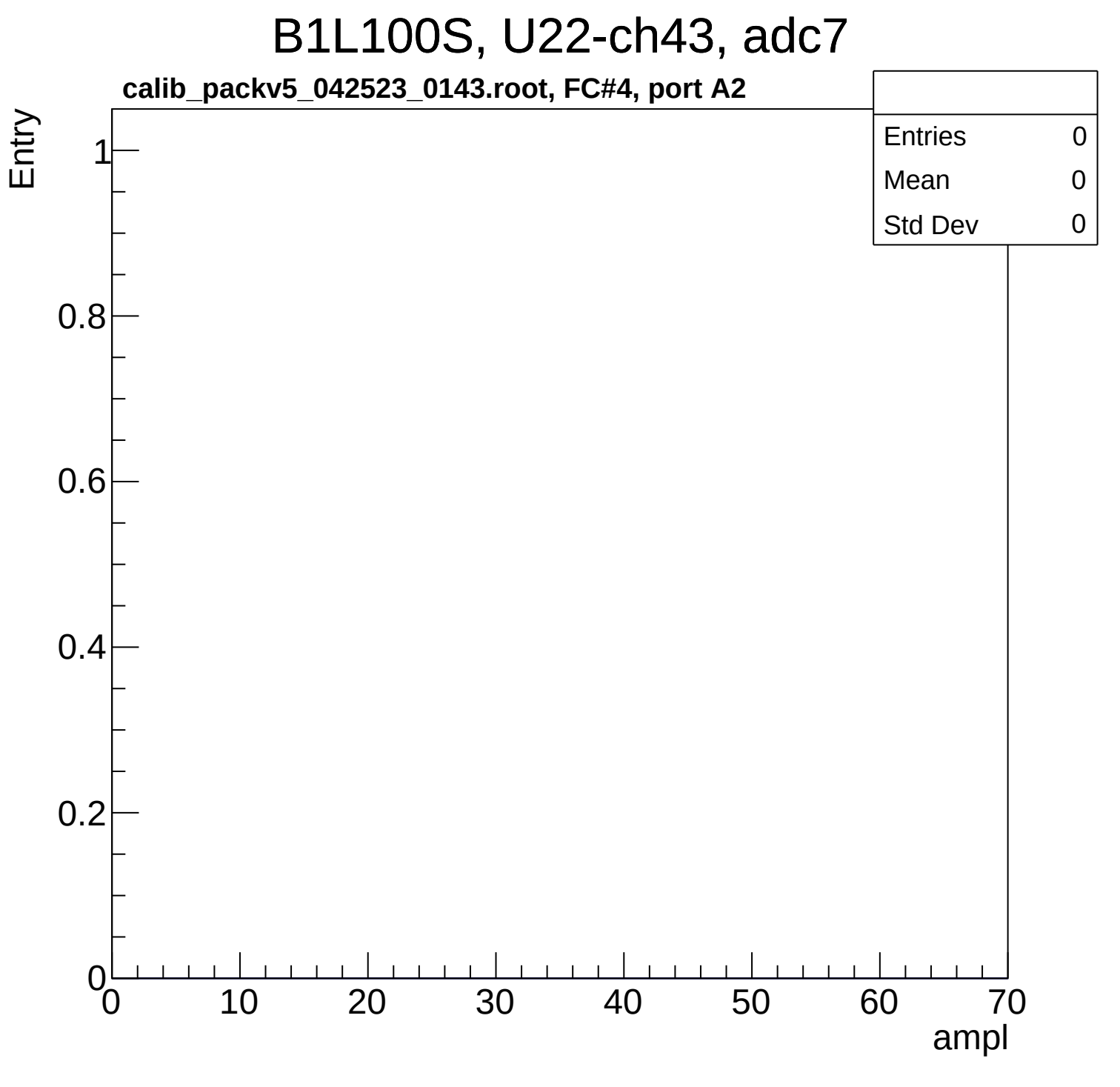
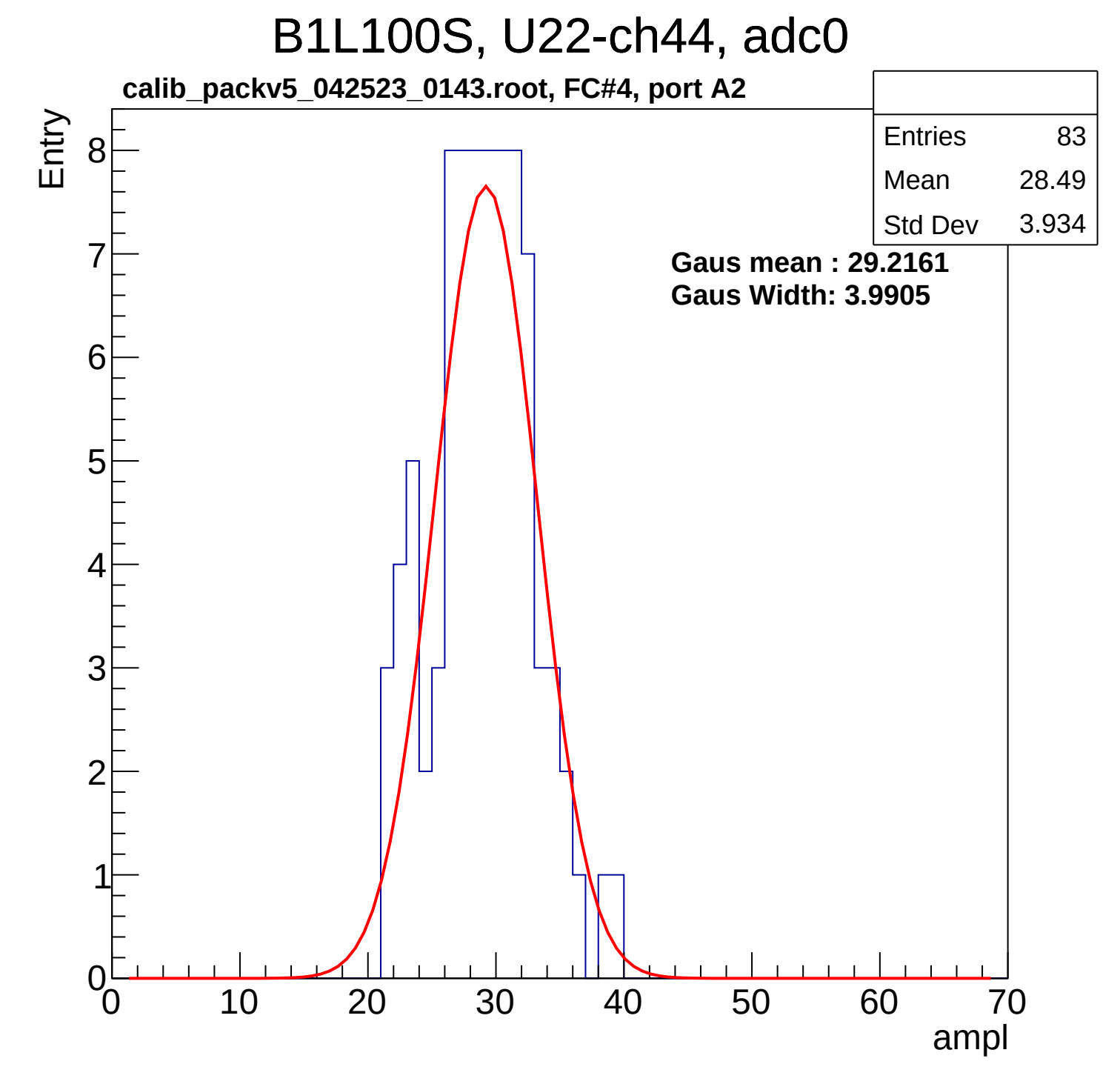
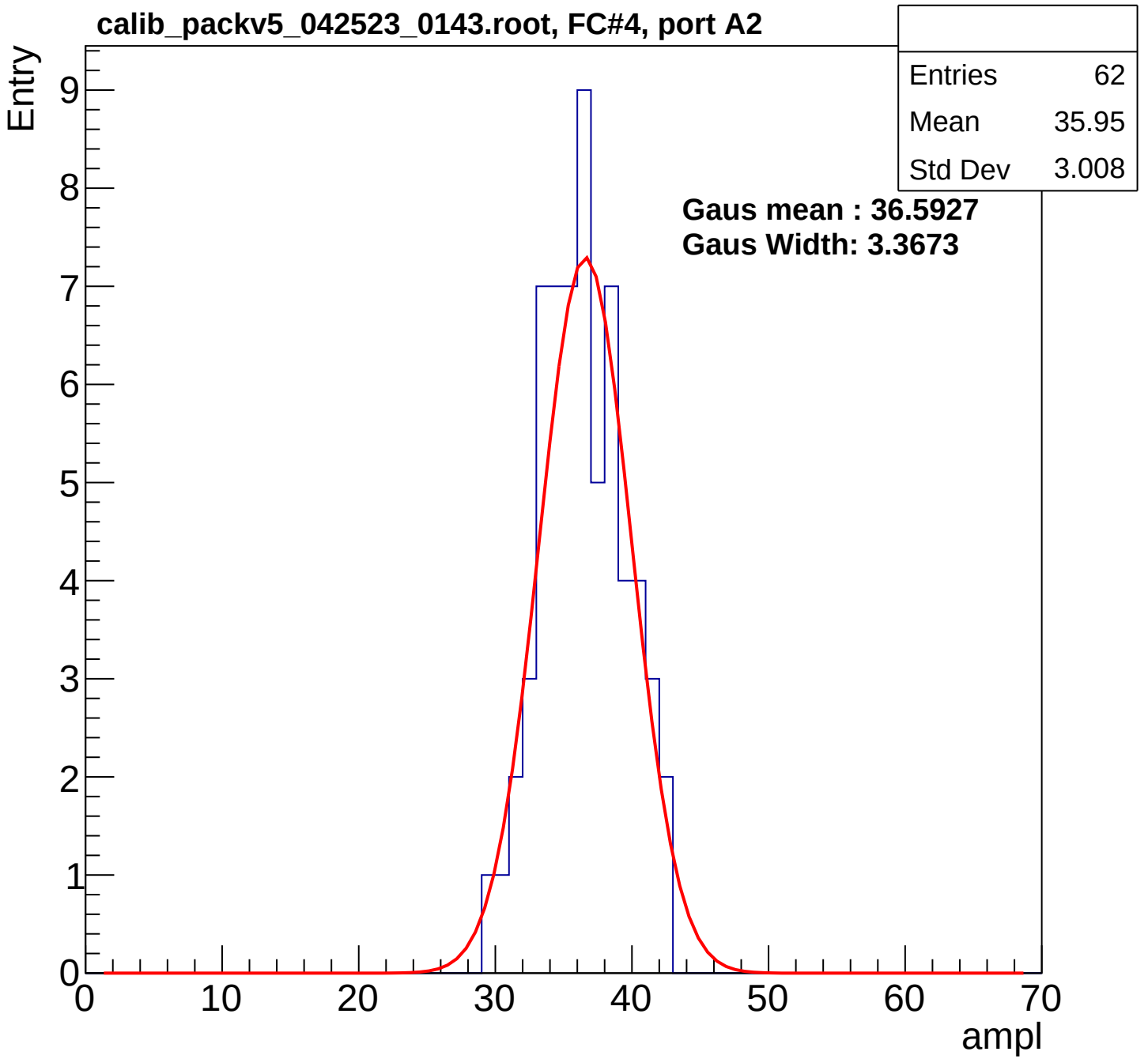
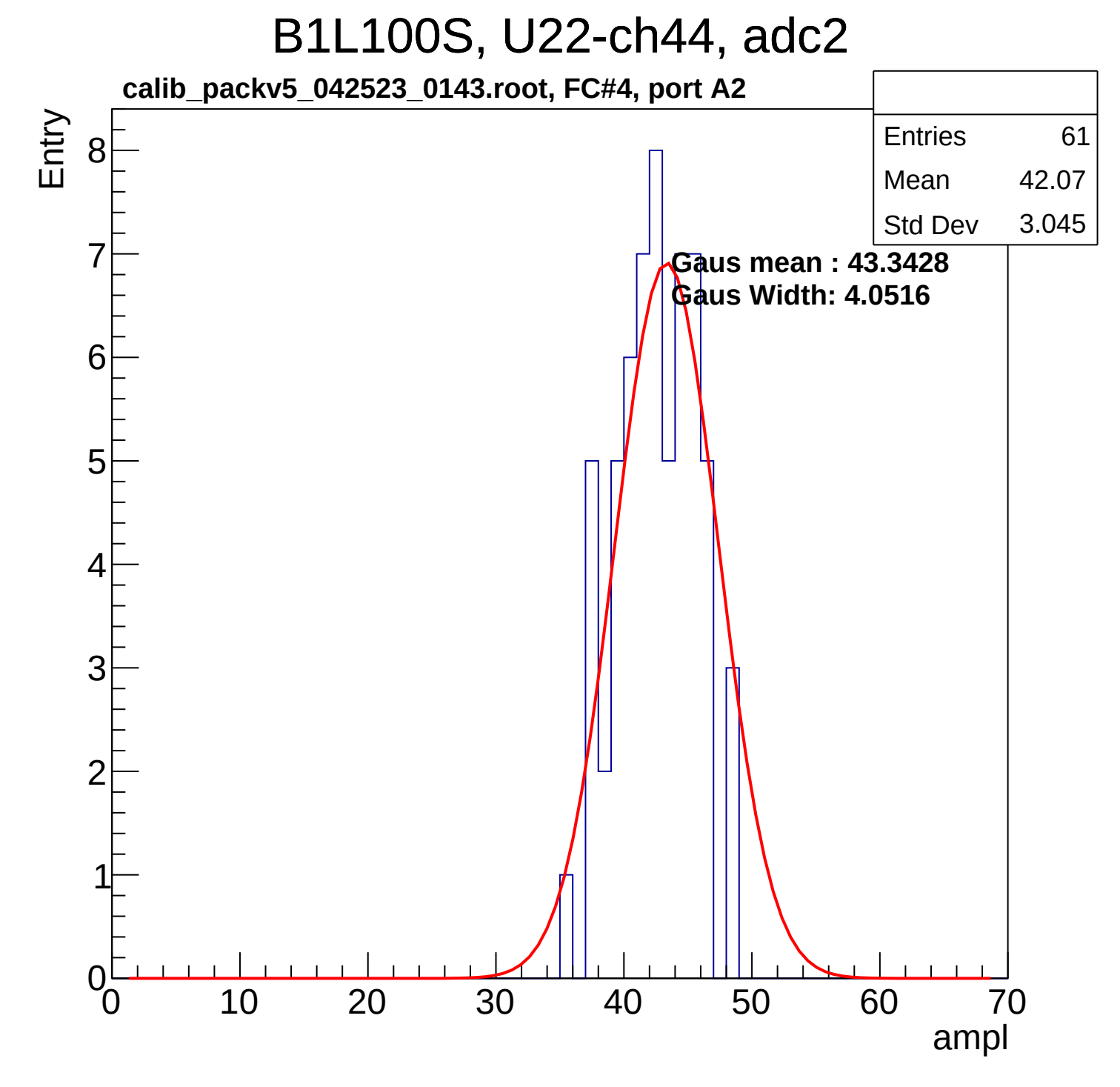

B1L100S, U22-ch43, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 58.77
Std Dev 10.26
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

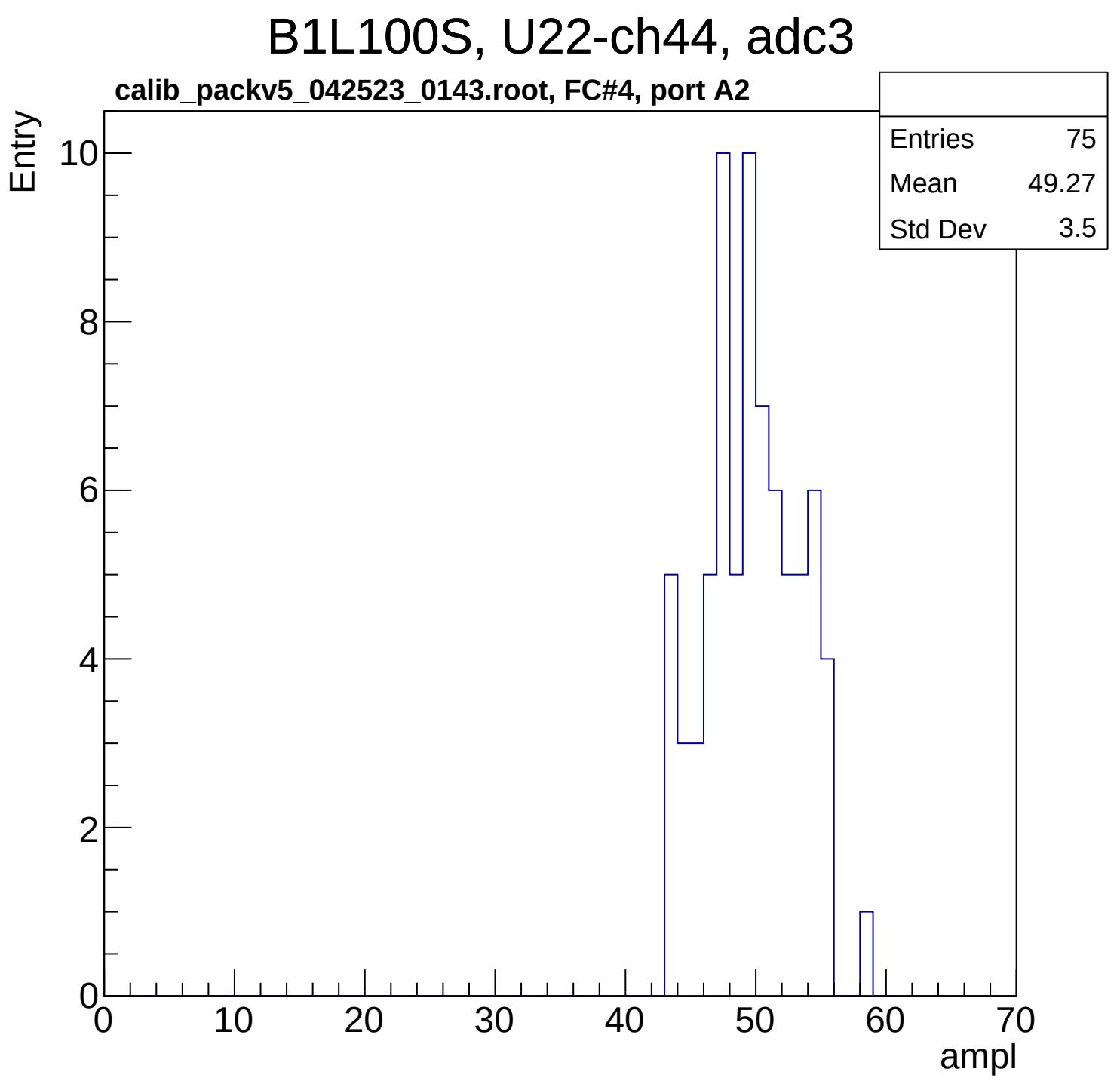

B1L100S, U22-ch43, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

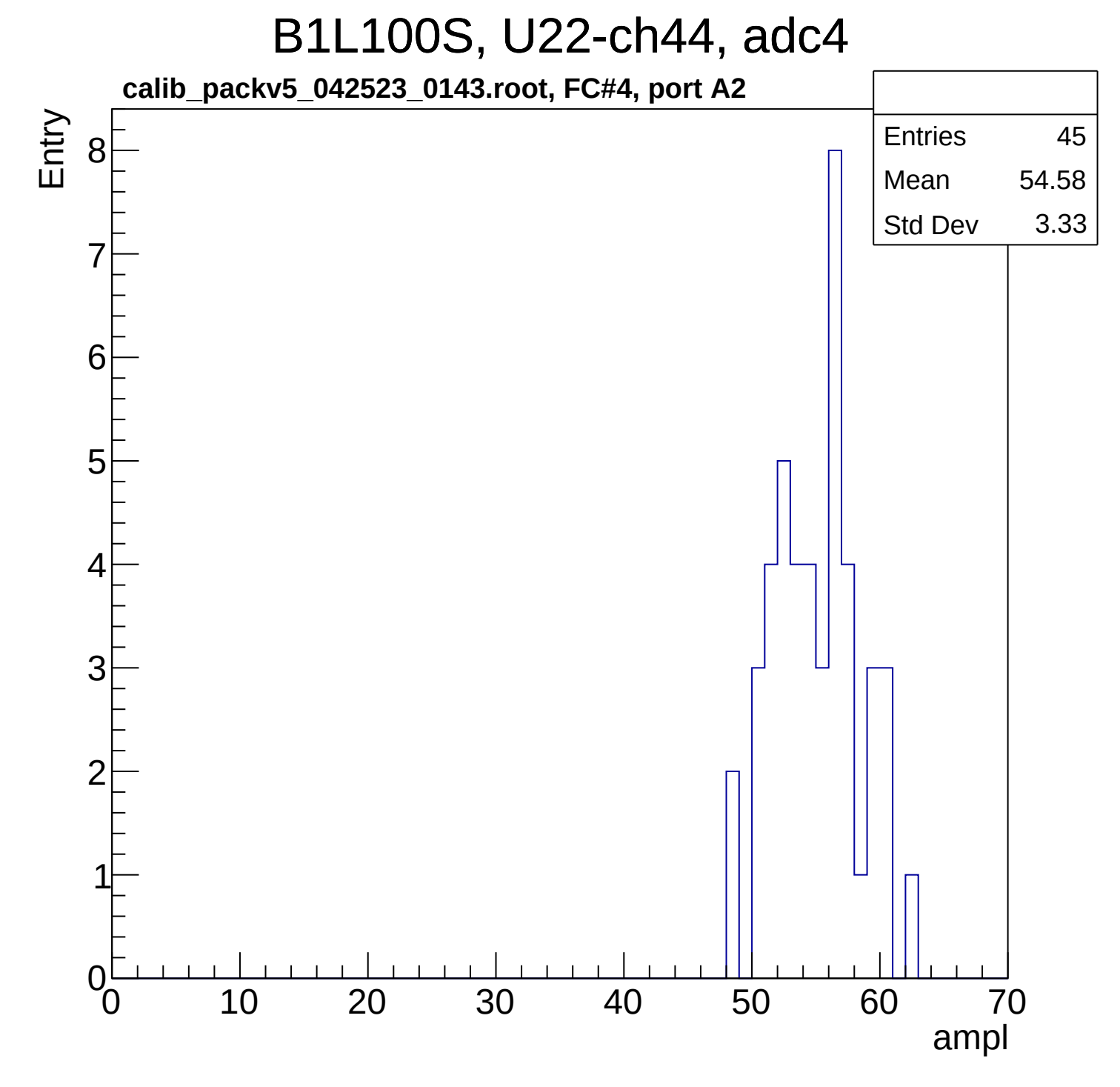

B1L100S, U22-ch43, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

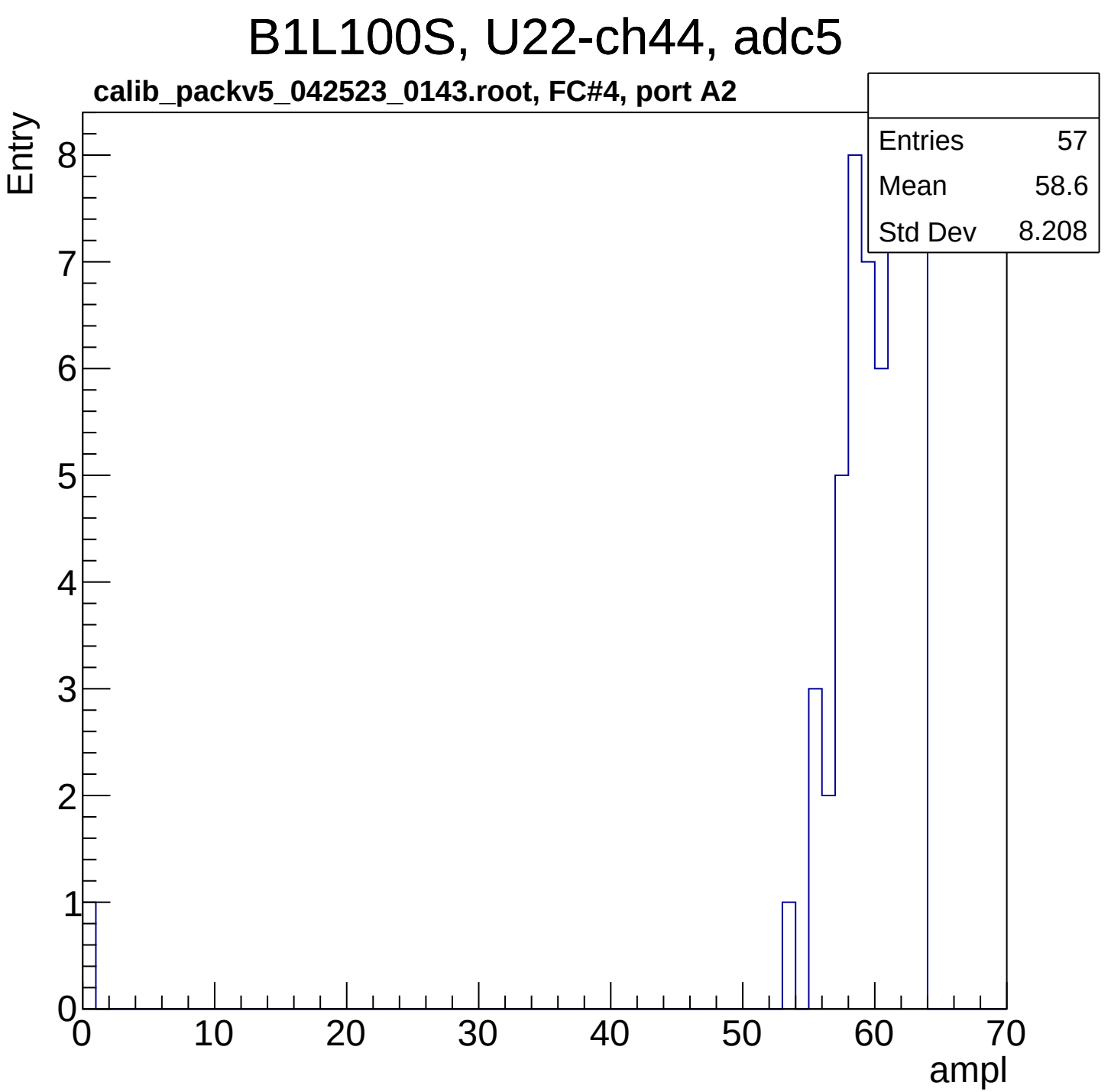

B1L100S, U22-ch44, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 28.49
Std Dev 3.934
Gaus mean : 29.2161
Gaus Width: 3.9905
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch44, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 35.95
Std Dev 3.008
Gaus mean : 36.5927
Gaus Width: 3.3673
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch44, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 42.07
Std Dev 3.045
Gaus mean : 43.3428
Gaus Width: 4.0516
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch44, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 49.27
Std Dev 3.5
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch44, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 54.58
Std Dev 3.33
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch44, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 58.6
Std Dev 8.208
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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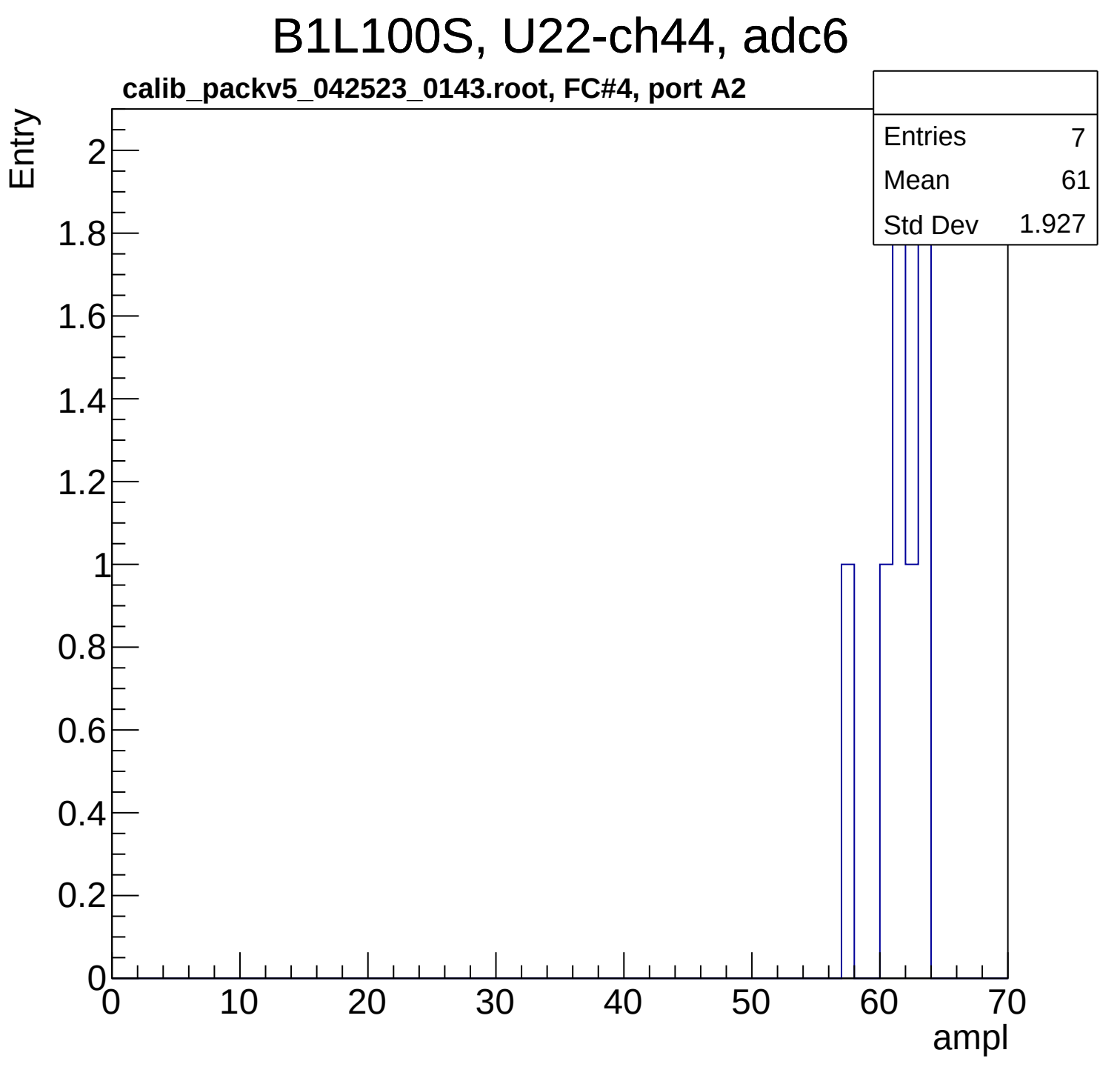
B1L100S, U22-ch44, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61
Std Dev 1.927
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
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60
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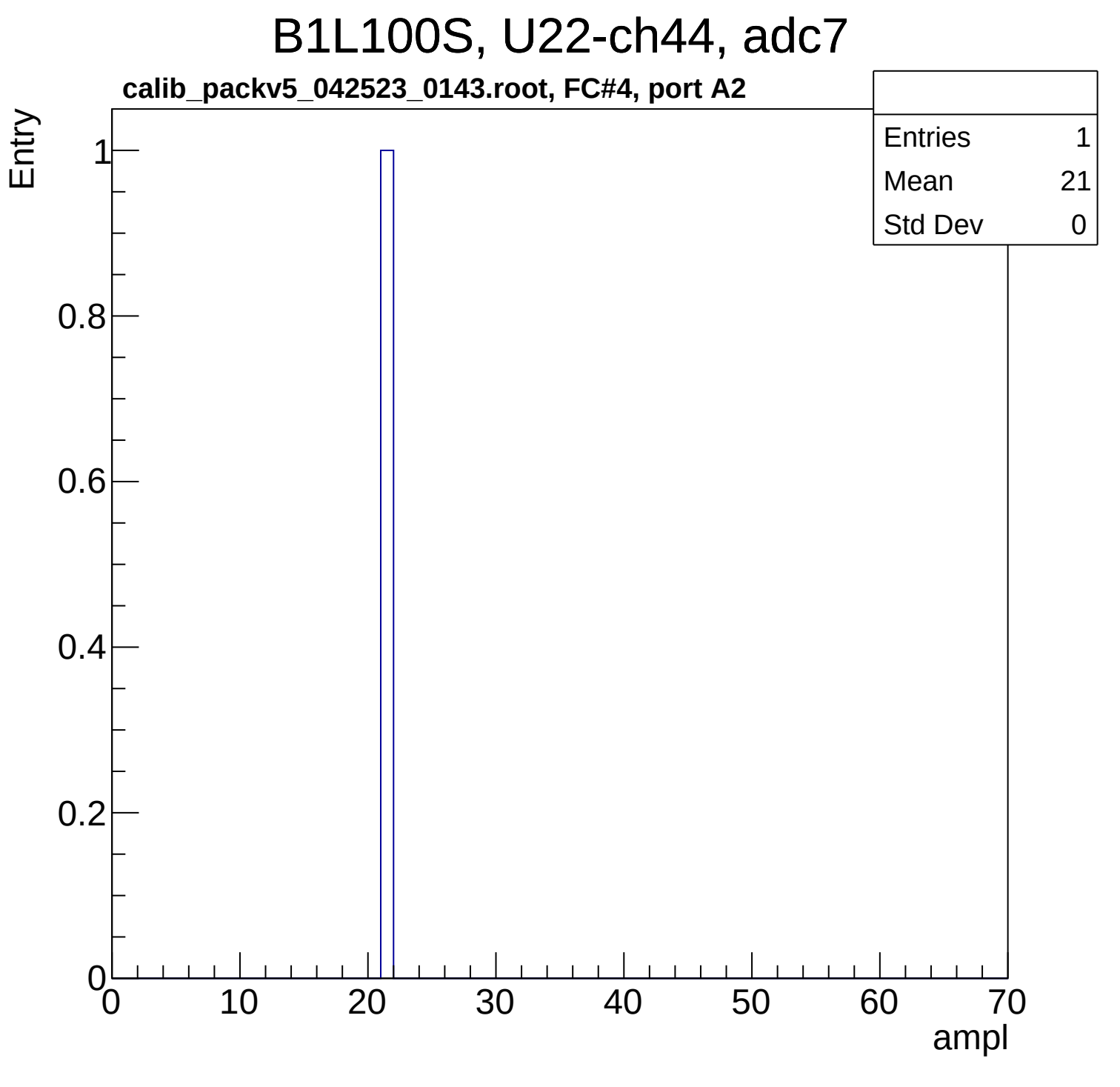
B1L100S, U22-ch44, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
ampl
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0.2
0.4
0.6
0.8
1
0
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40
50
60
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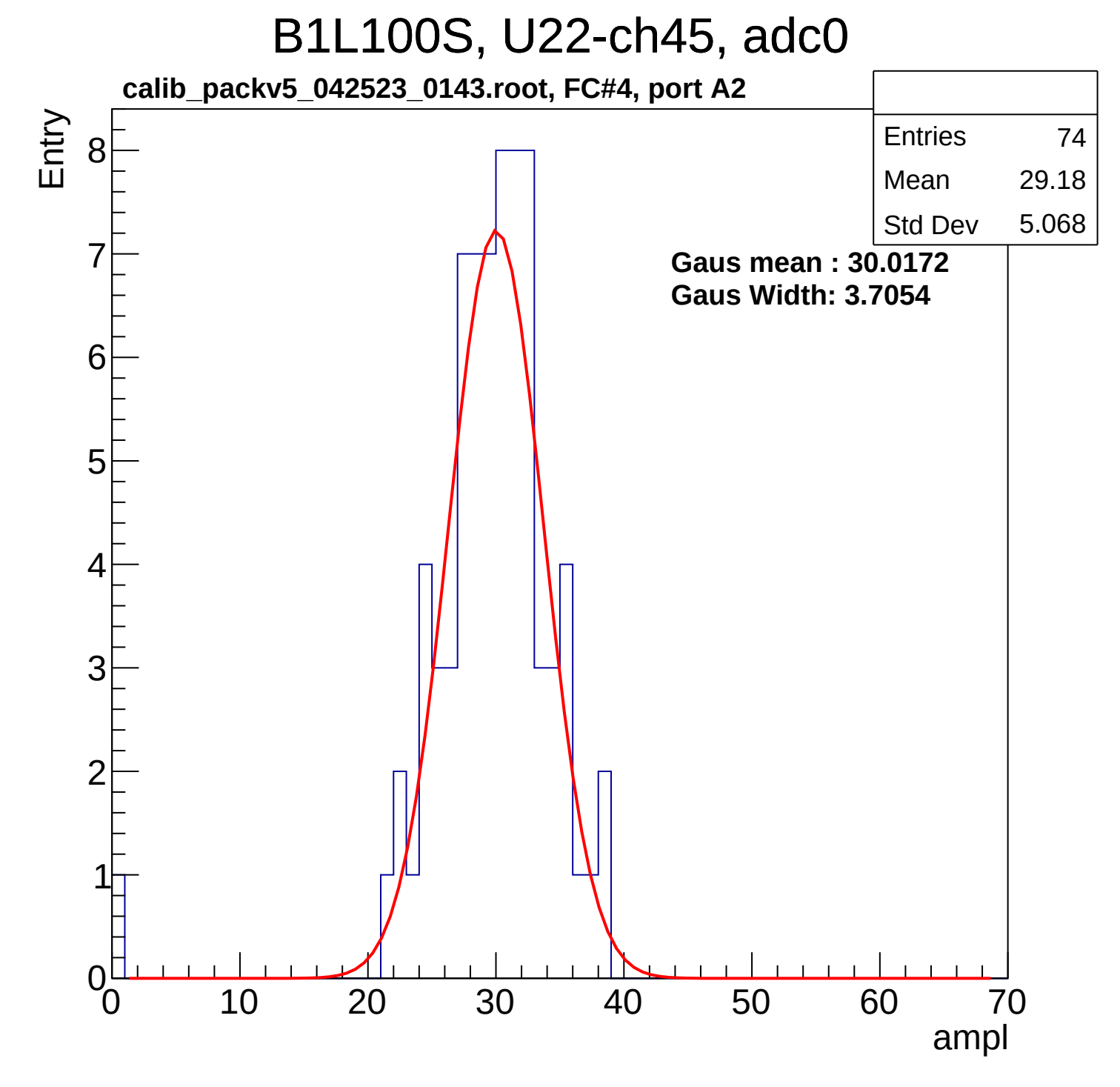
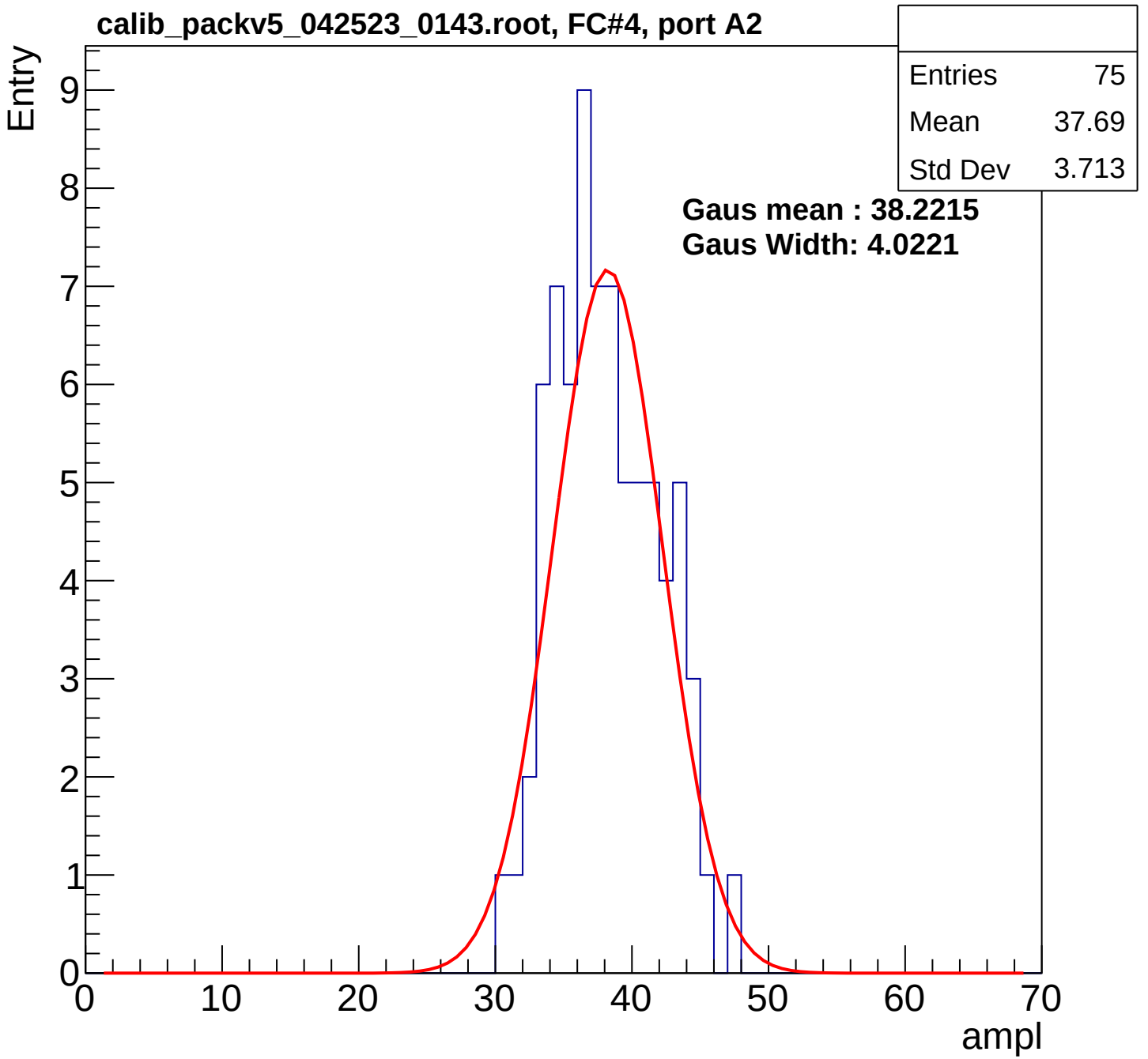
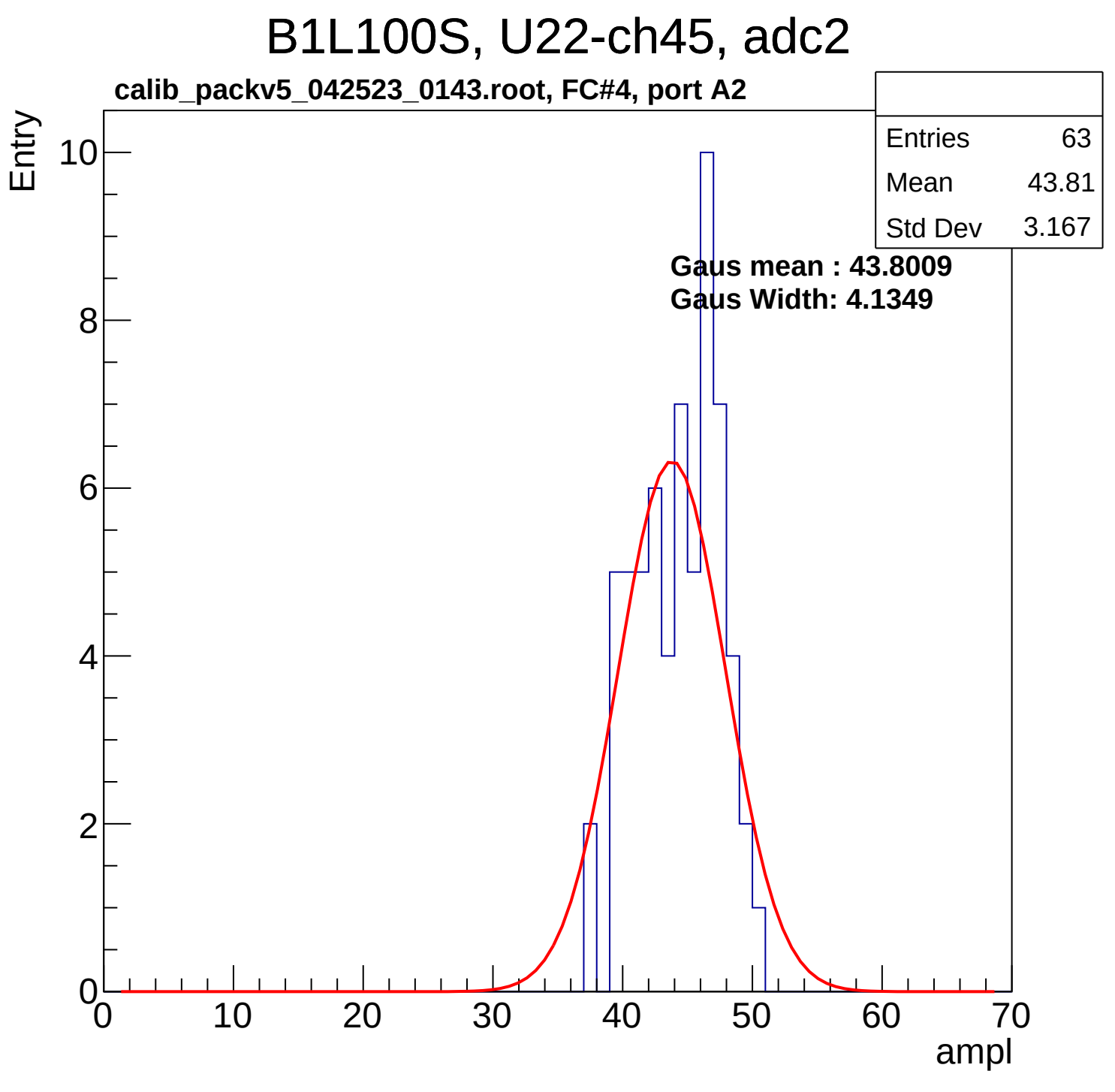
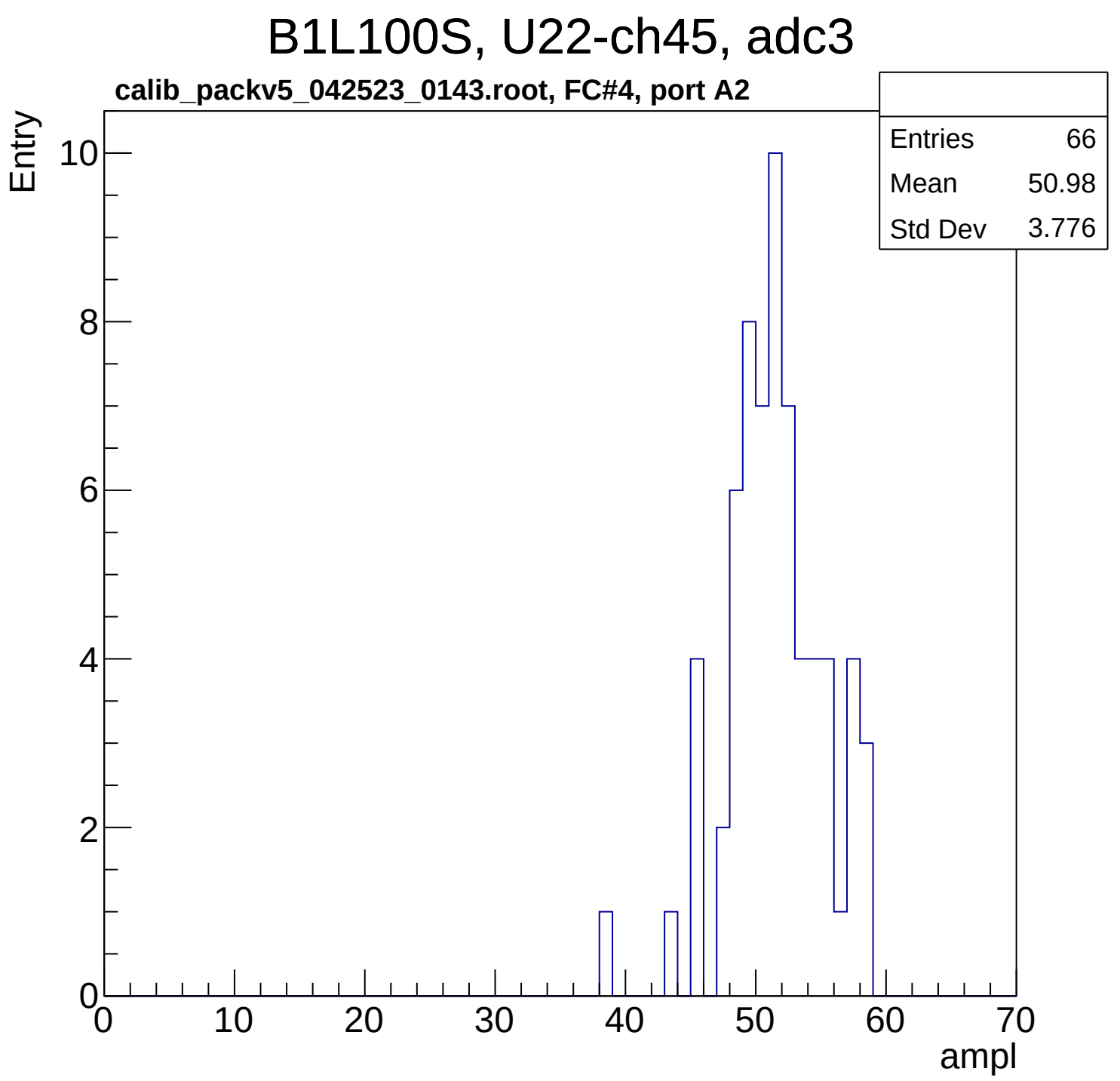
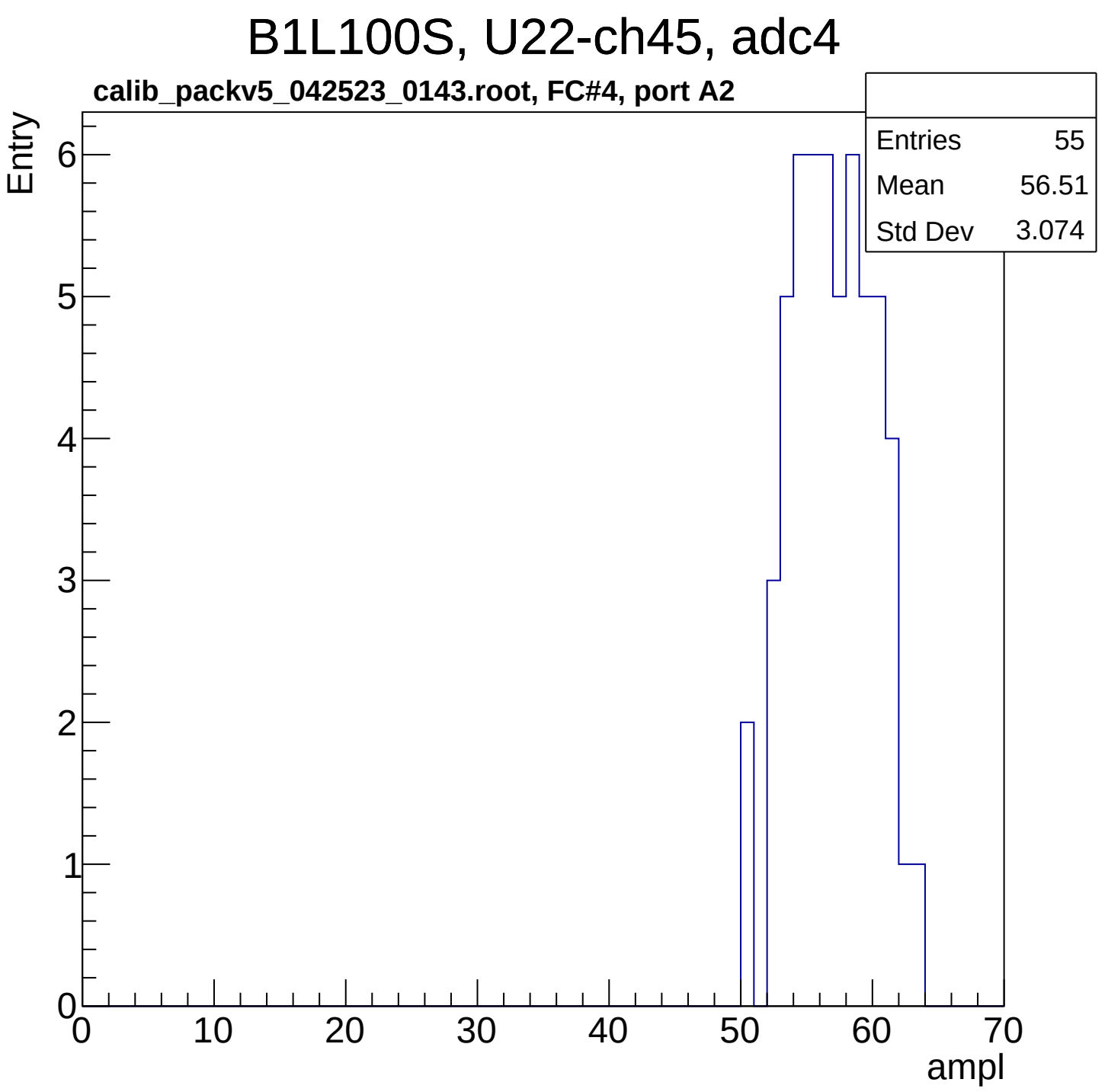

B1L100S, U22-ch45, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.18
Std Dev 5.068
Gaus mean : 30.0172
Gaus Width: 3.7054
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch45, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.69
Std Dev 3.713
Gaus mean : 38.2215
Gaus Width: 4.0221
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch45, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 43.81
Std Dev 3.167
Gaus mean : 43.8009
Gaus Width: 4.1349
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch45, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.98
Std Dev 3.776
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch45, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.51
Std Dev 3.074
Entry
0
1
2
3
4
5
6
0
10
20
30
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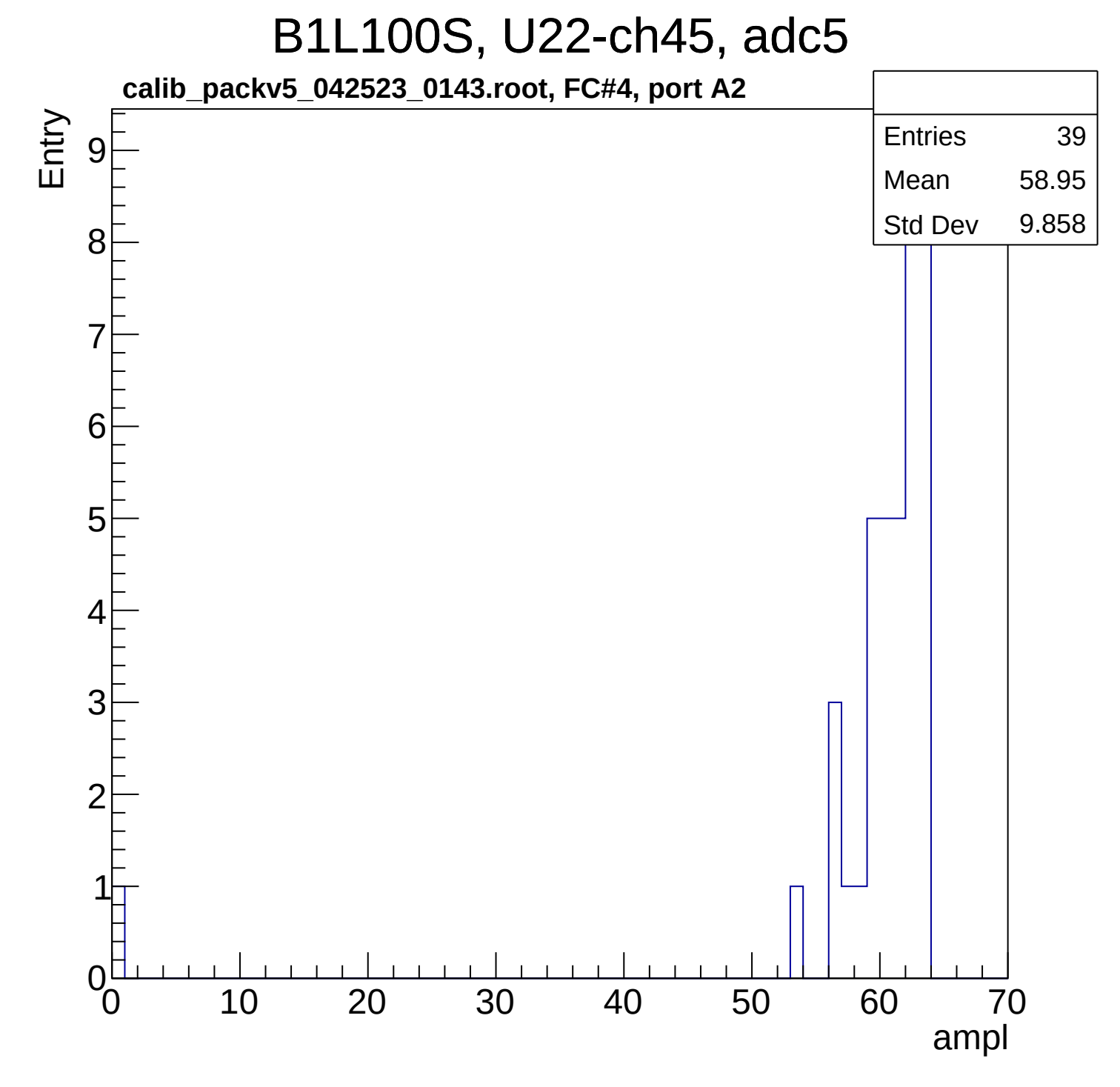
50
60
70
ampl


B1L100S, U22-ch45, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.95
Std Dev 9.858
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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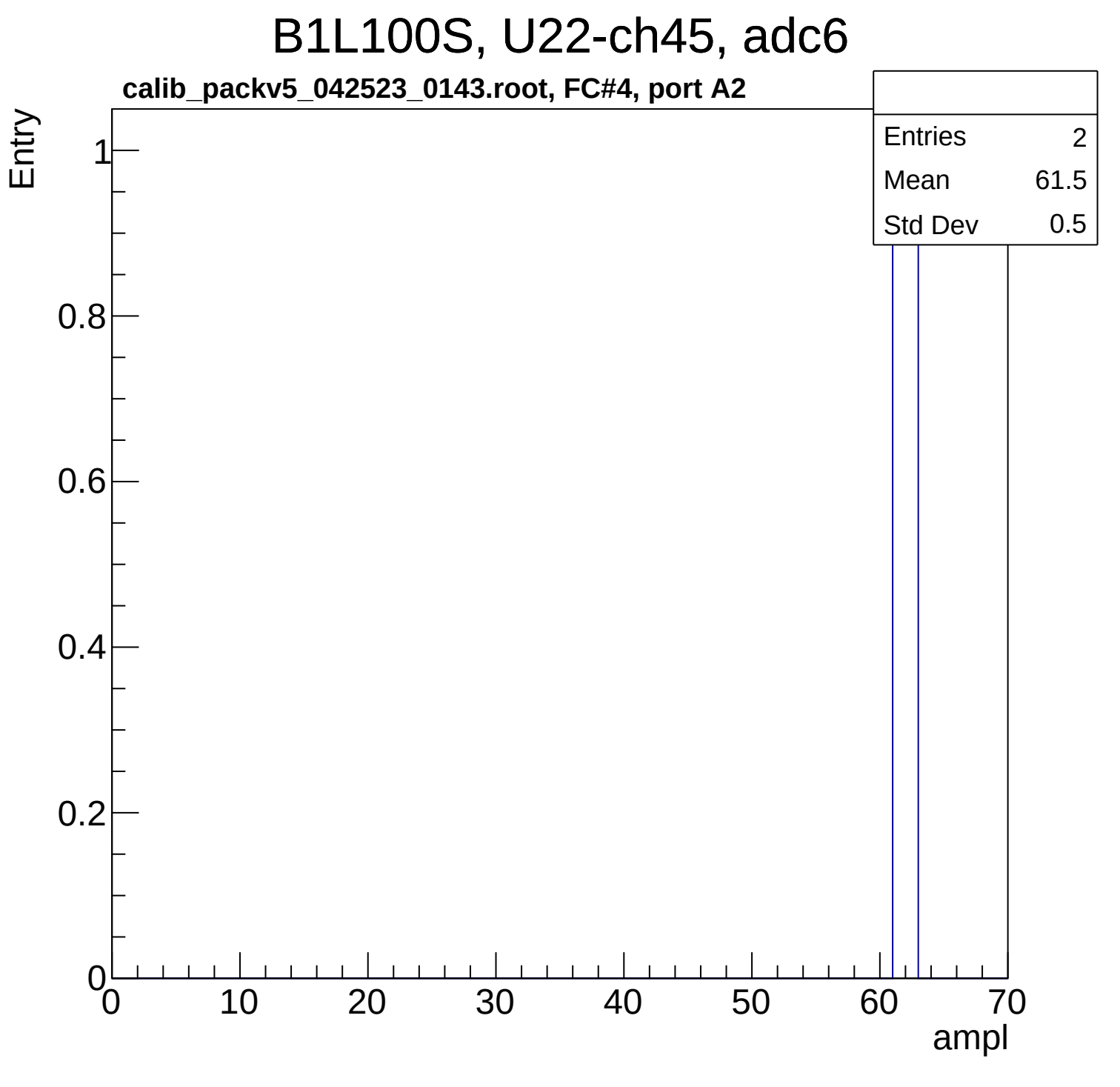
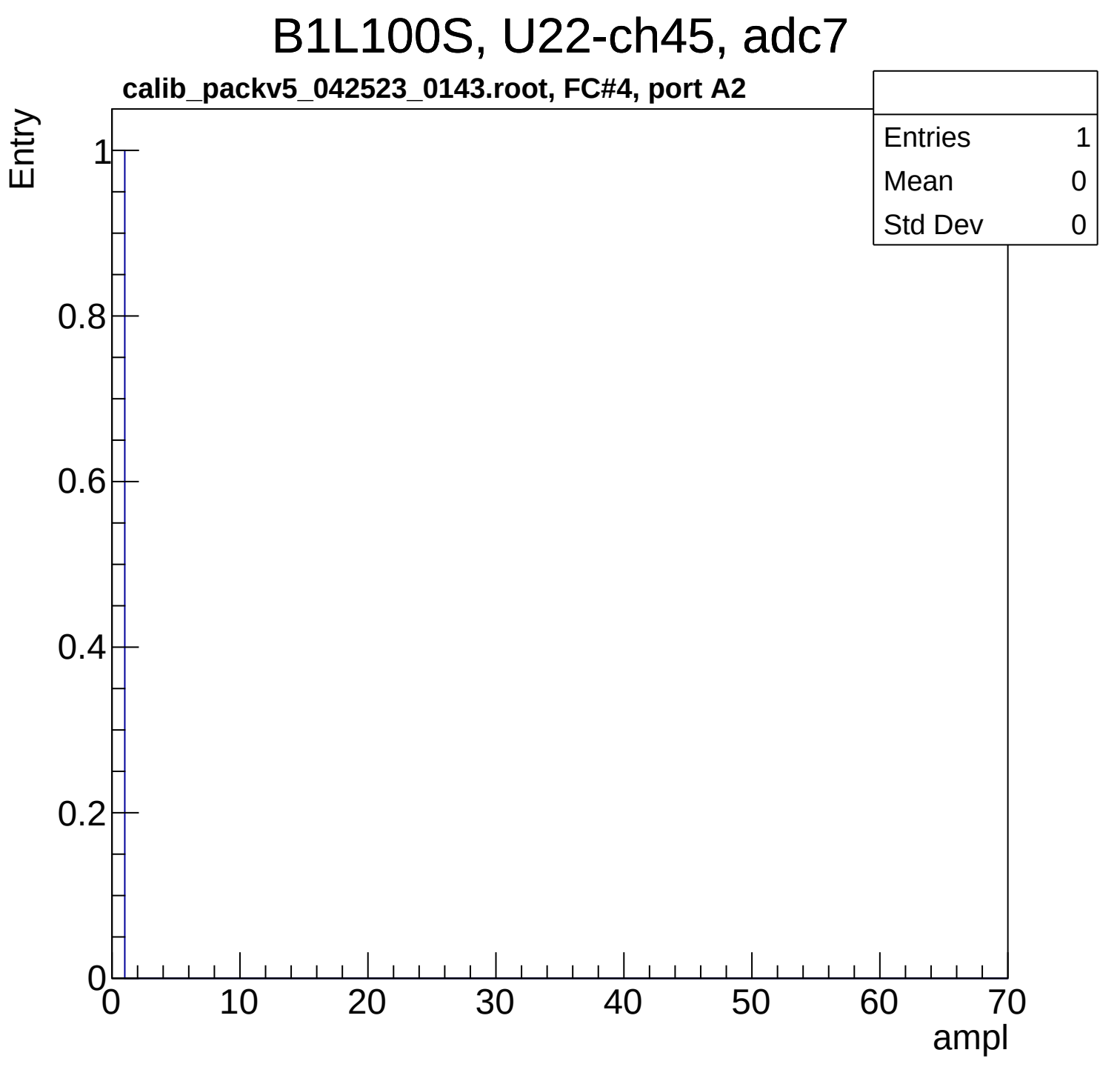
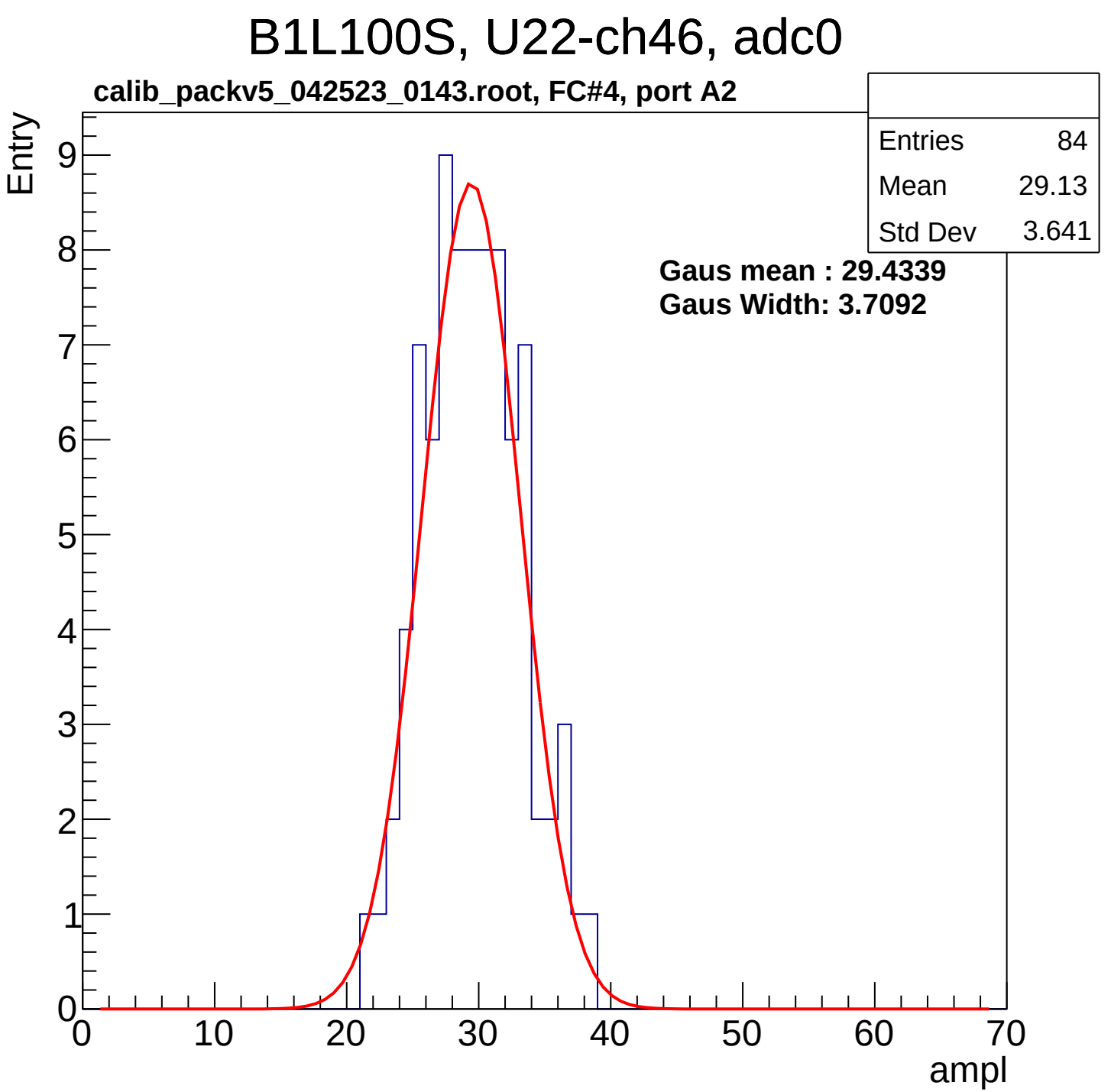

B1L100S, U22-ch45, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch45, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch46, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 29.13
Std Dev 3.641
Gaus mean : 29.4339
Gaus Width: 3.7092
Entry
ampl
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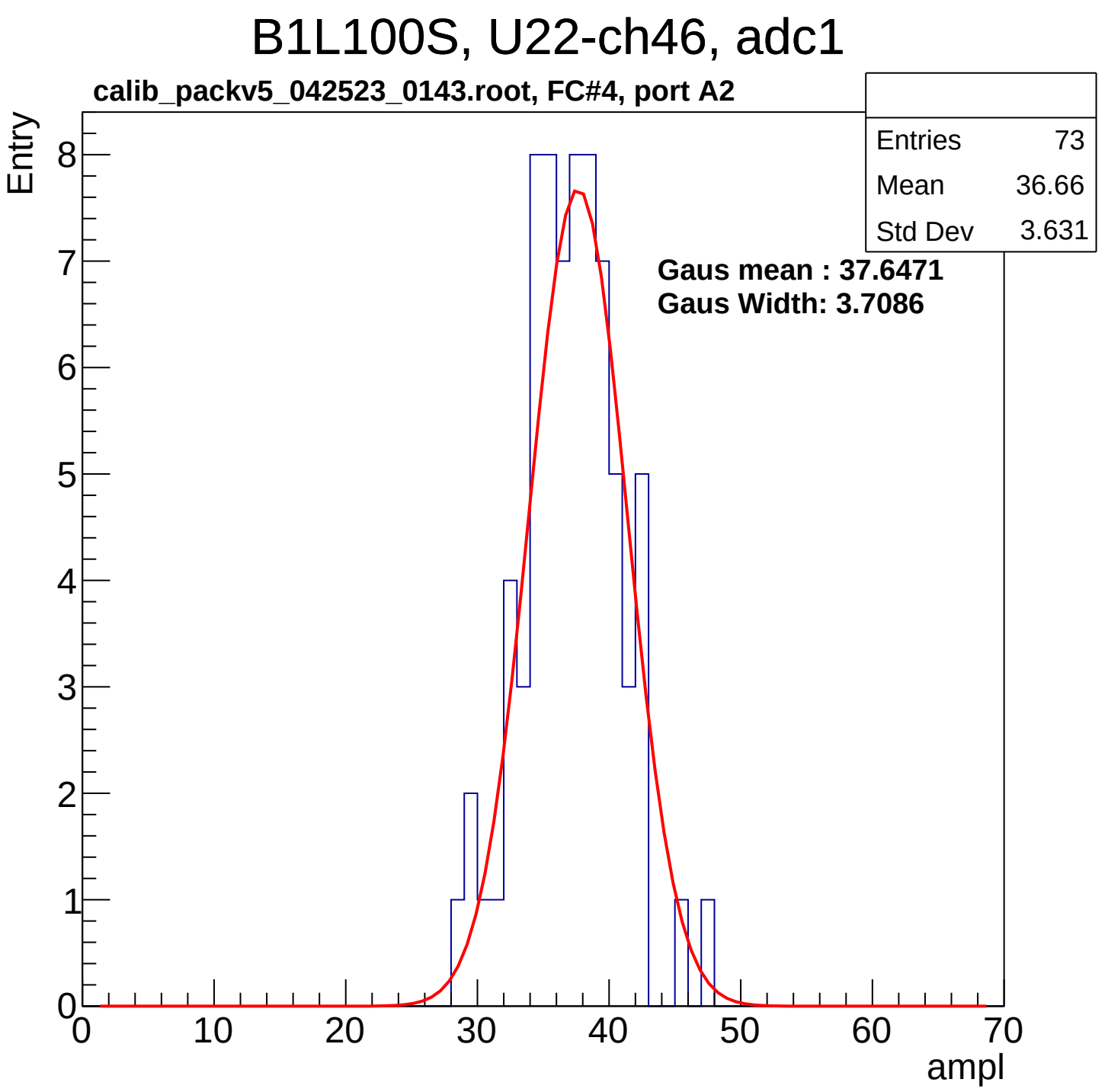
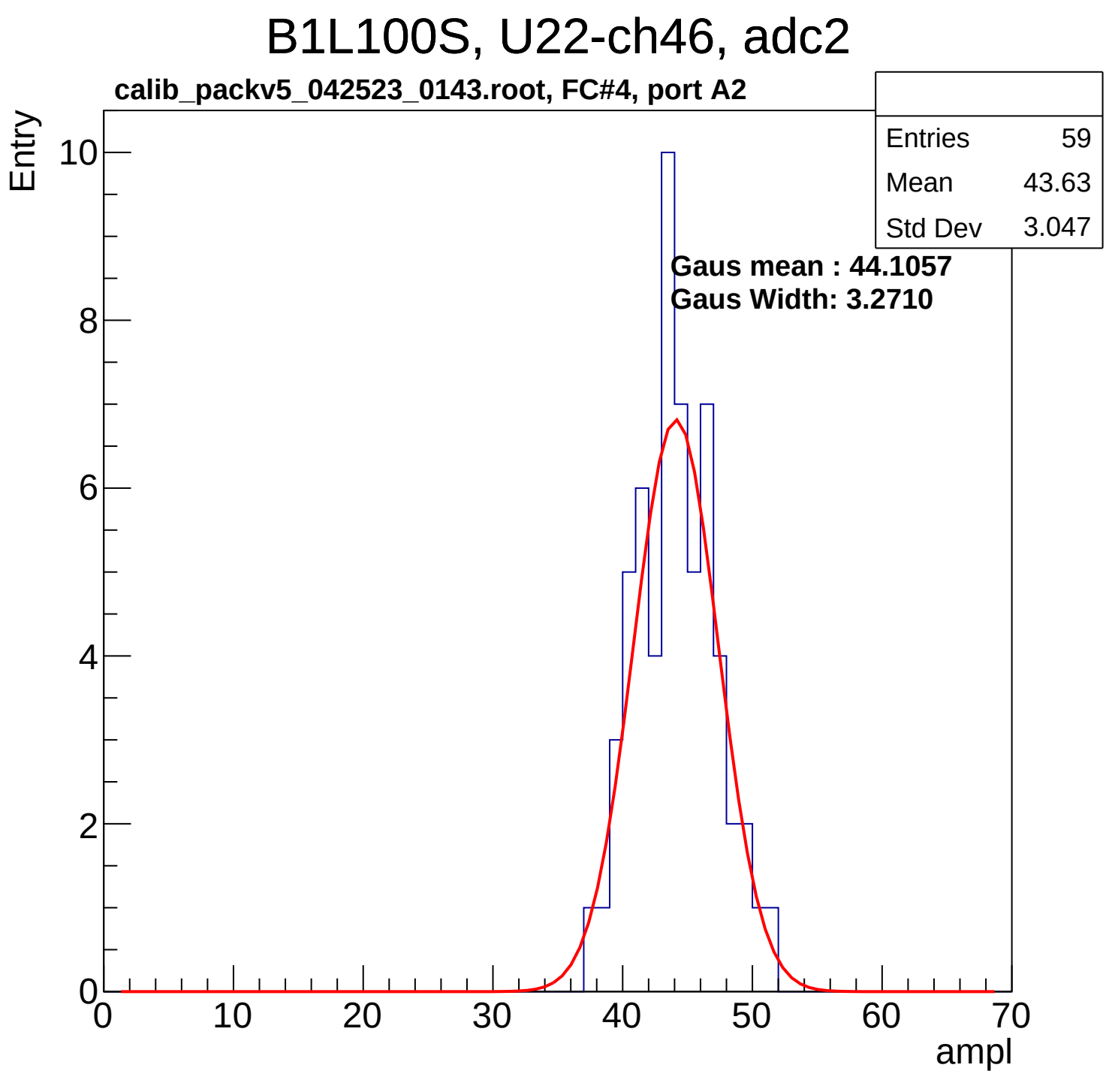
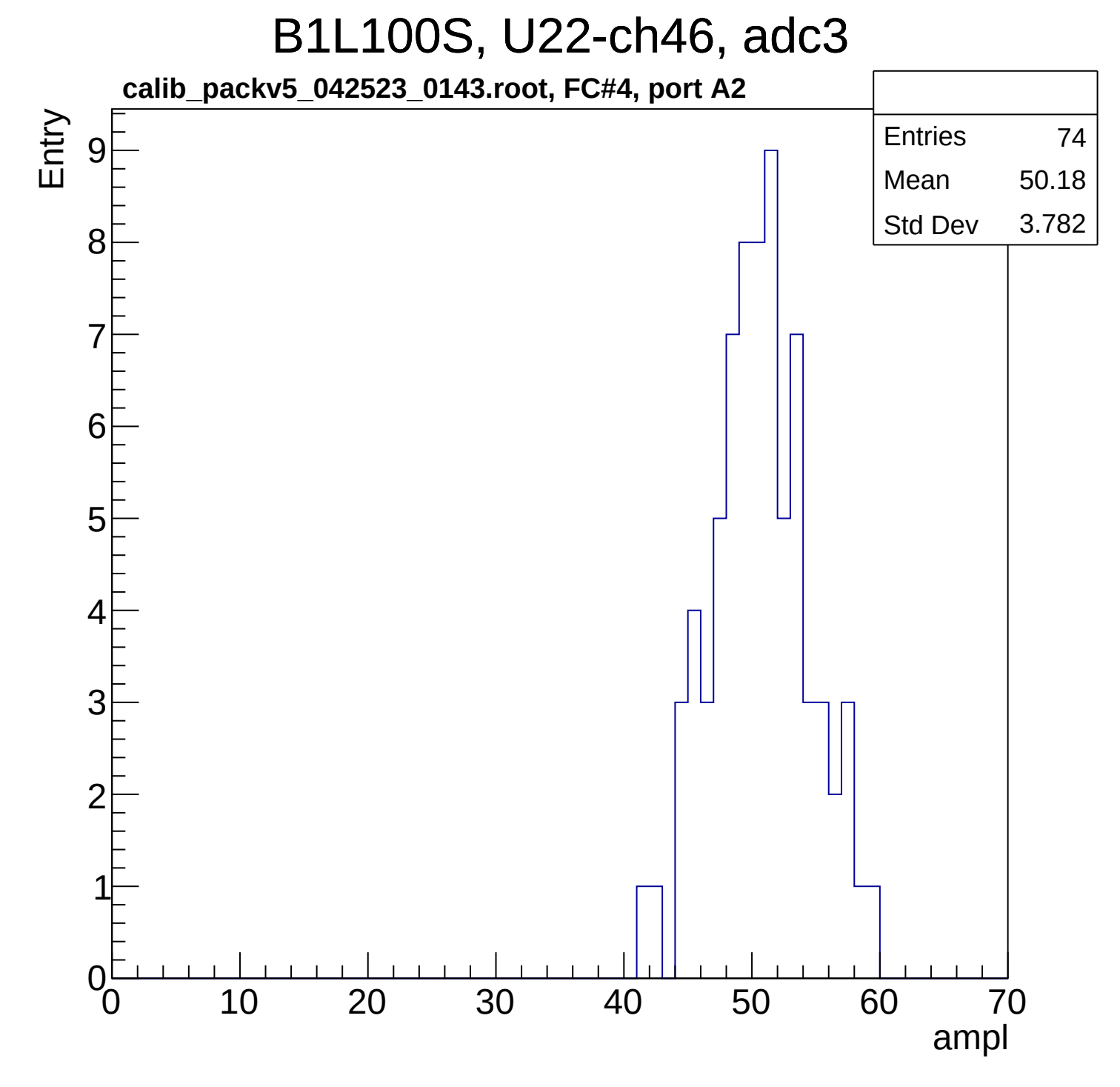
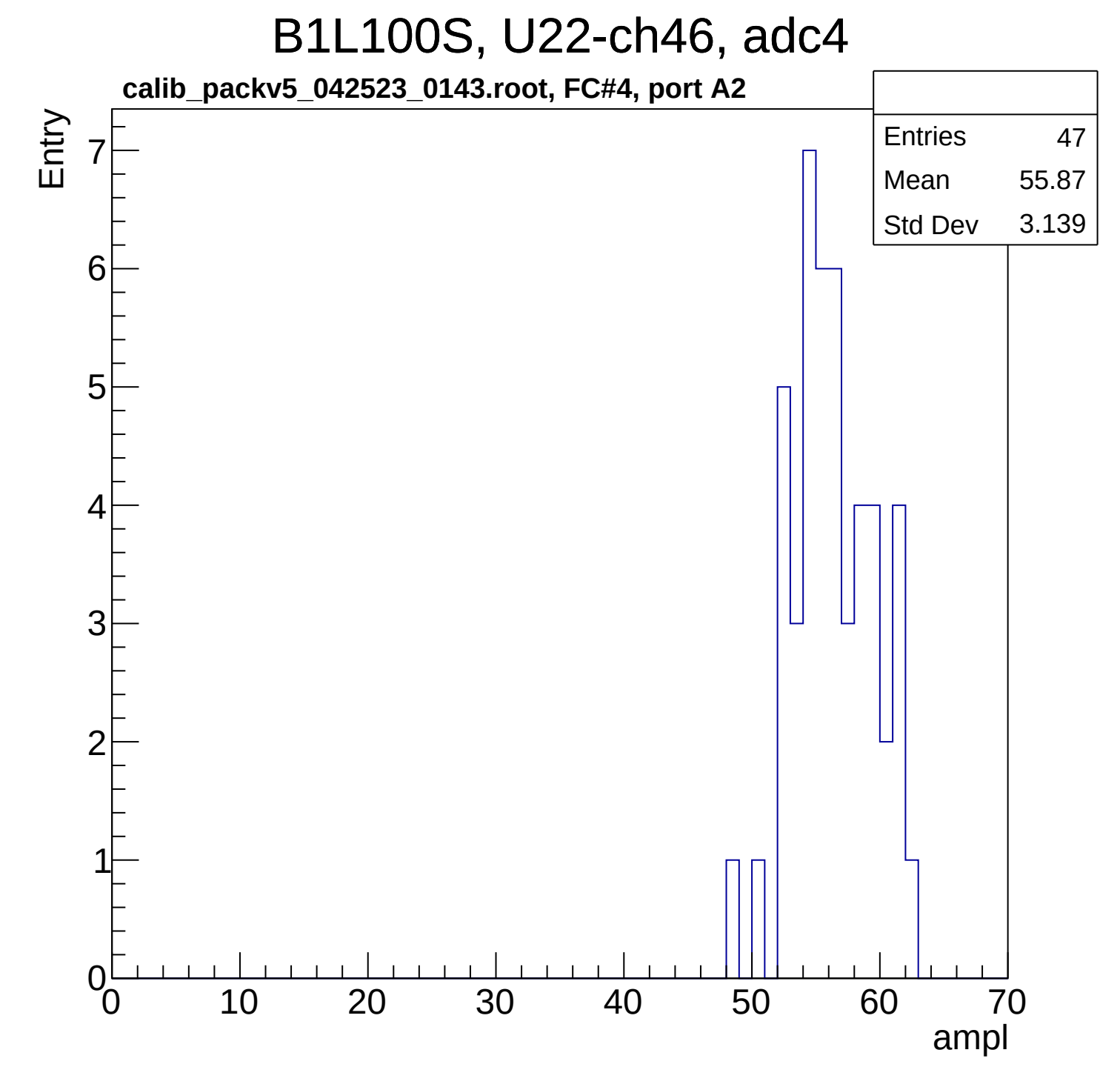
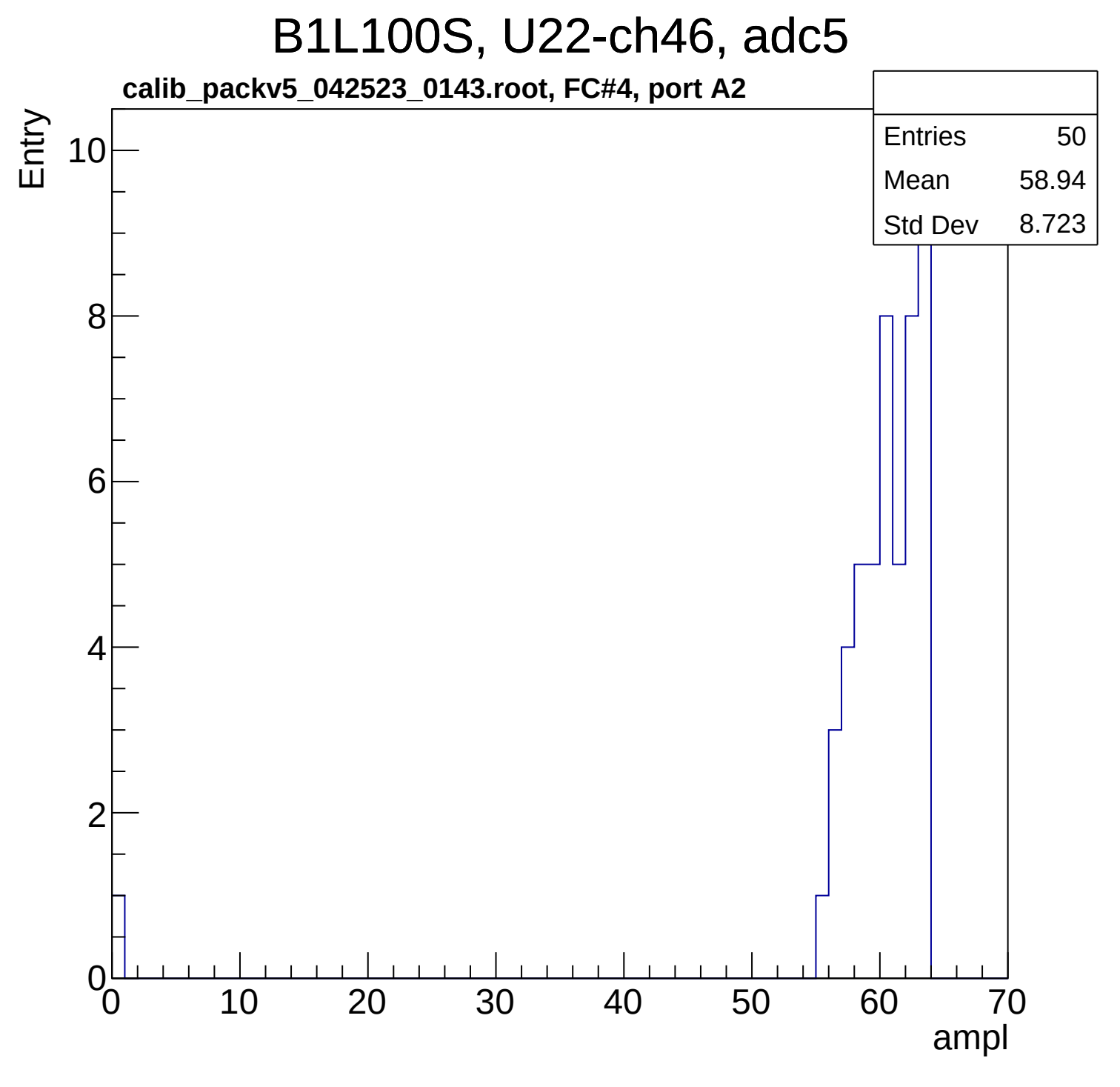
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

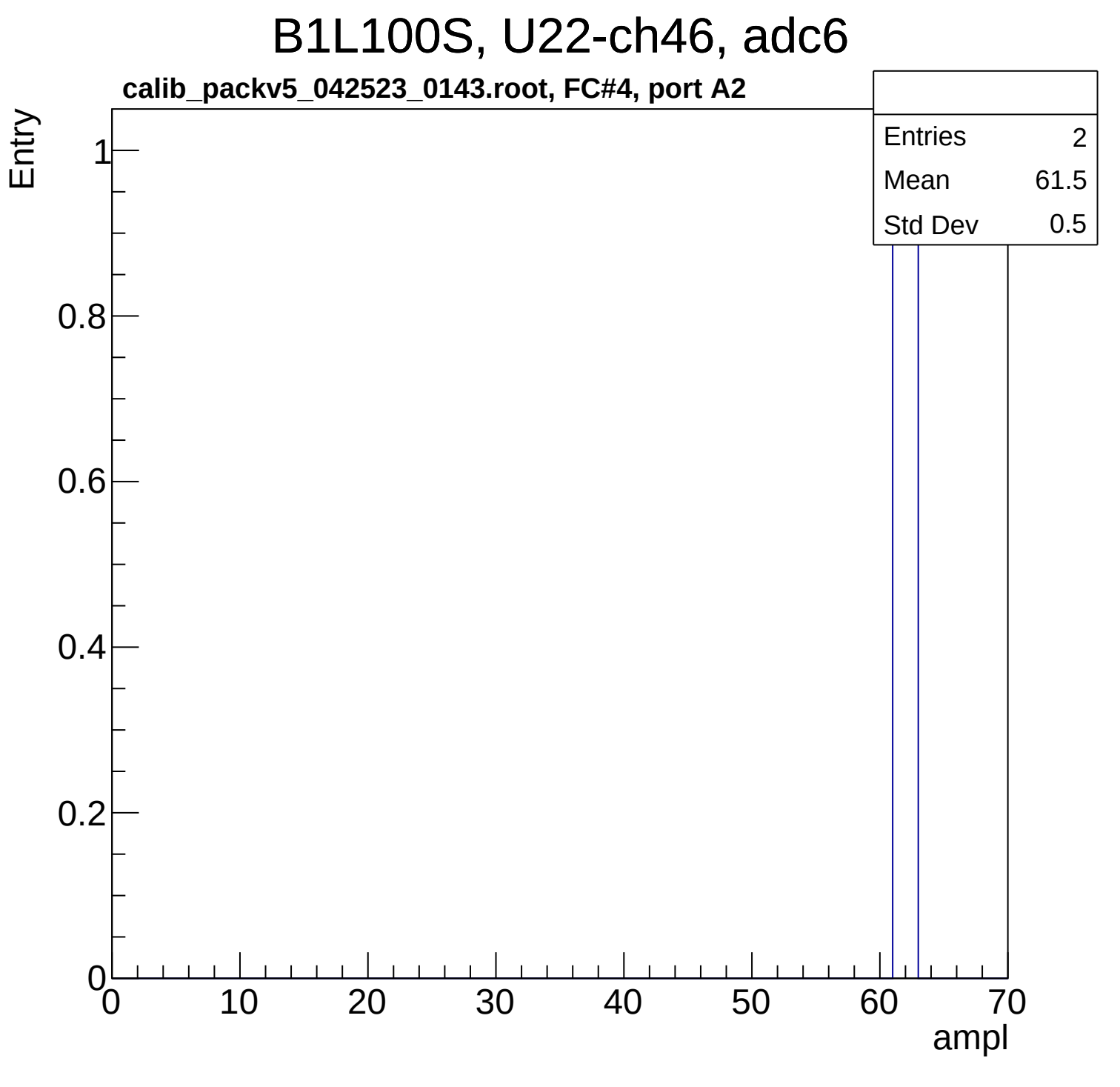

B1L100S, U22-ch46, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.66
Std Dev 3.631
Gaus mean : 37.6471
Gaus Width: 3.7086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

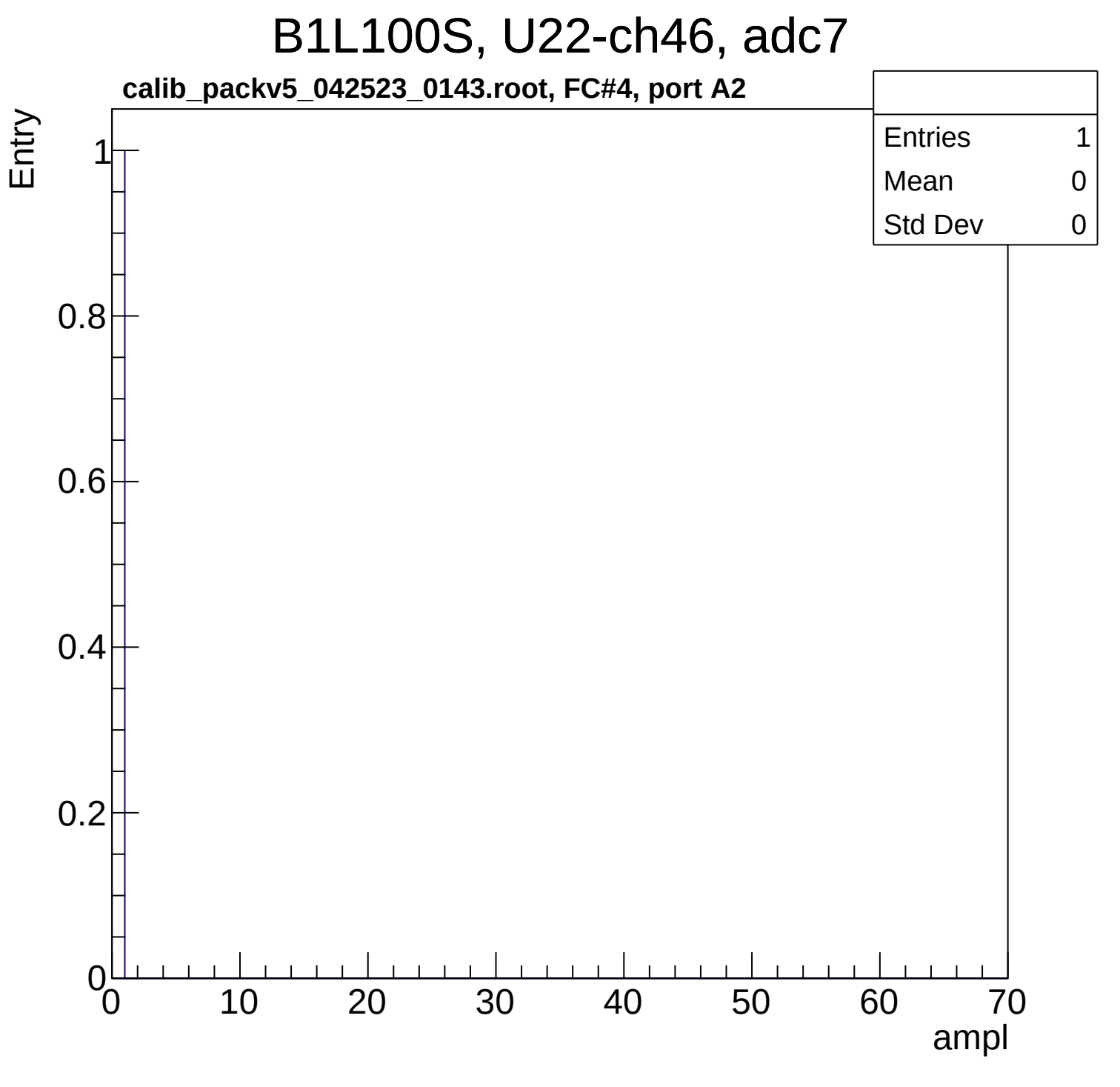

B1L100S, U22-ch46, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 43.63
Std Dev 3.047
Gaus mean : 44.1057
Gaus Width: 3.2710
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

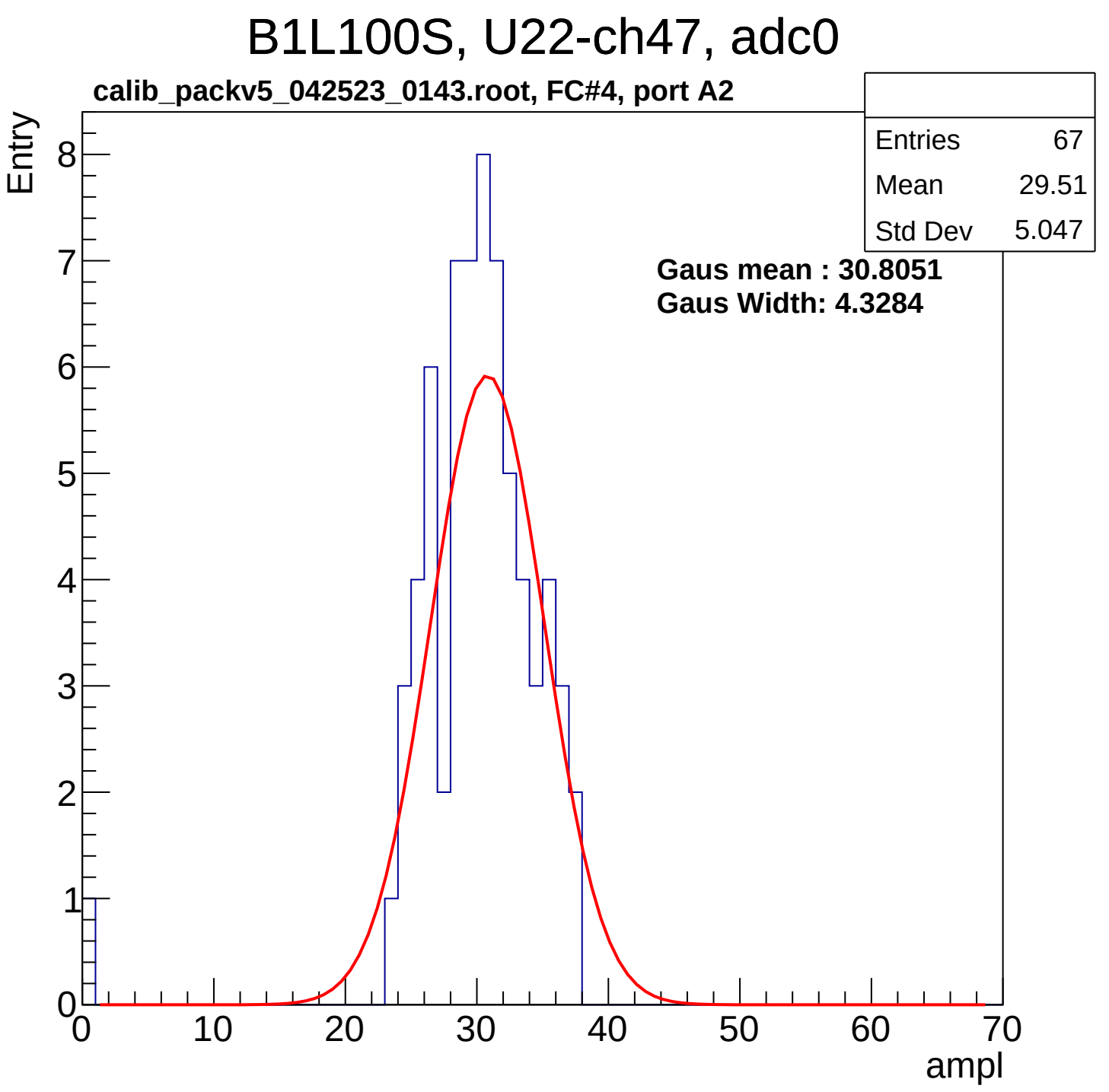

B1L100S, U22-ch46, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.18
Std Dev 3.782
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

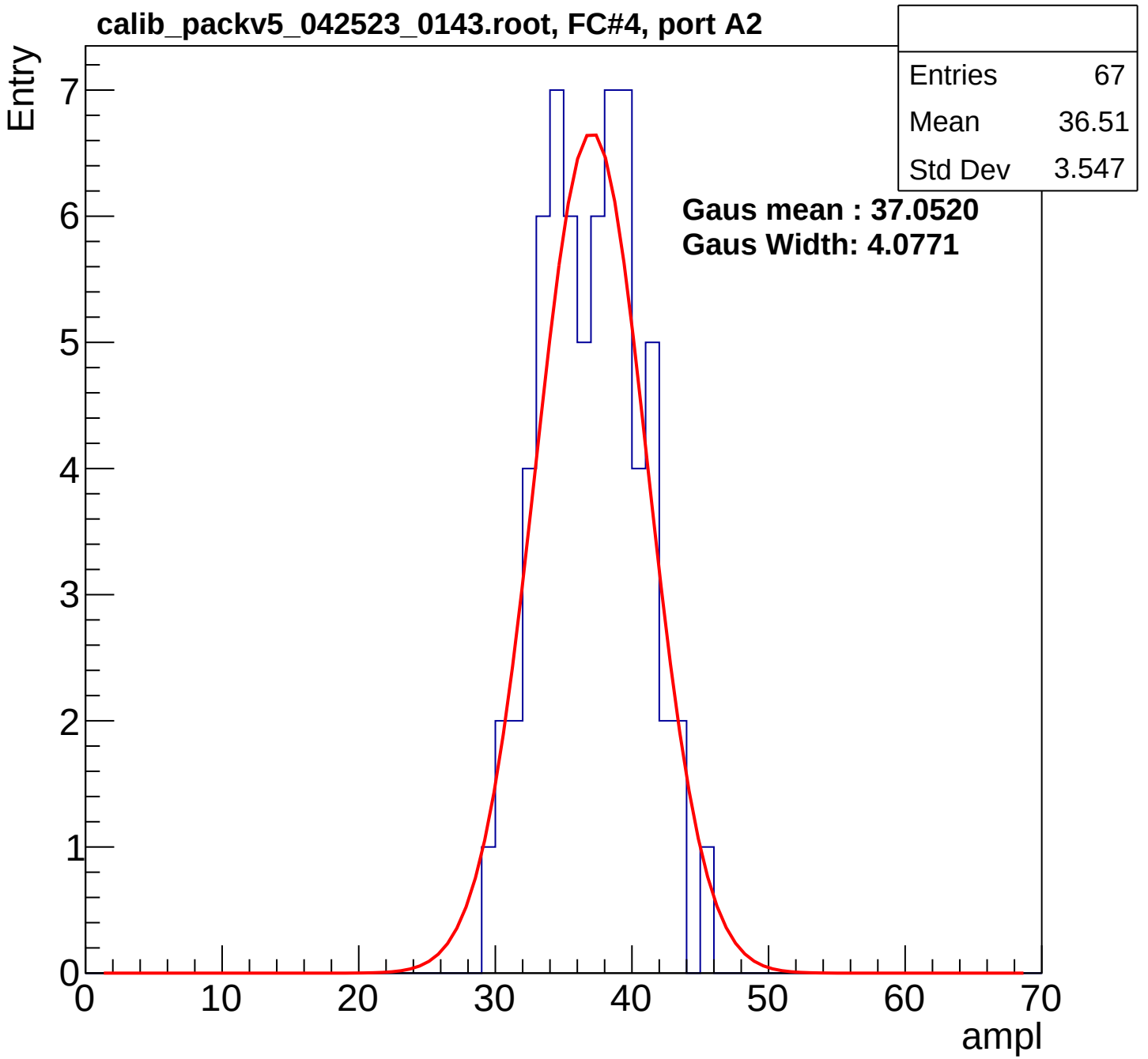

B1L100S, U22-ch46, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.87
Std Dev 3.139
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

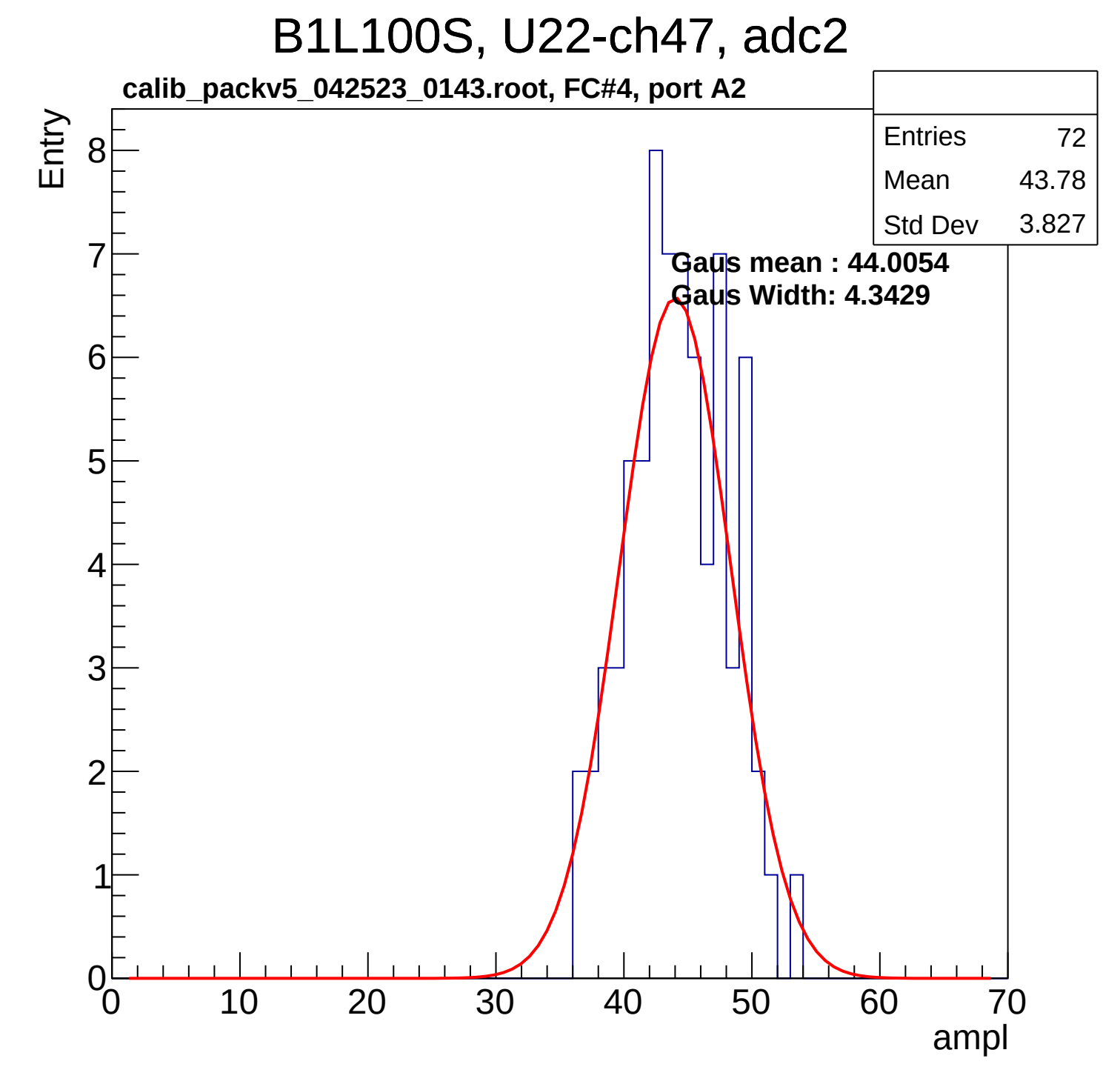

B1L100S, U22-ch46, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.94
Std Dev 8.723
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

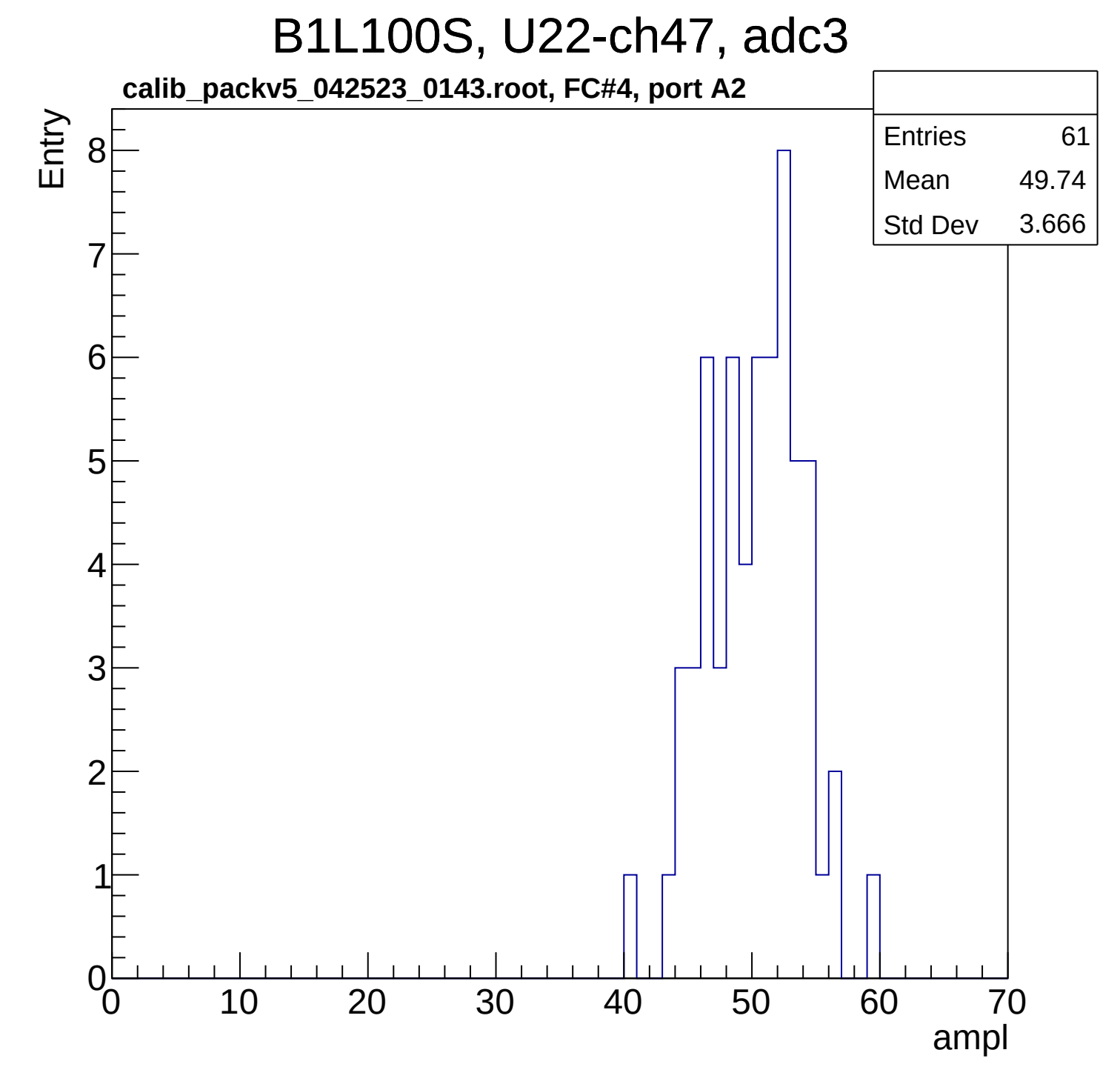

B1L100S, U22-ch46, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

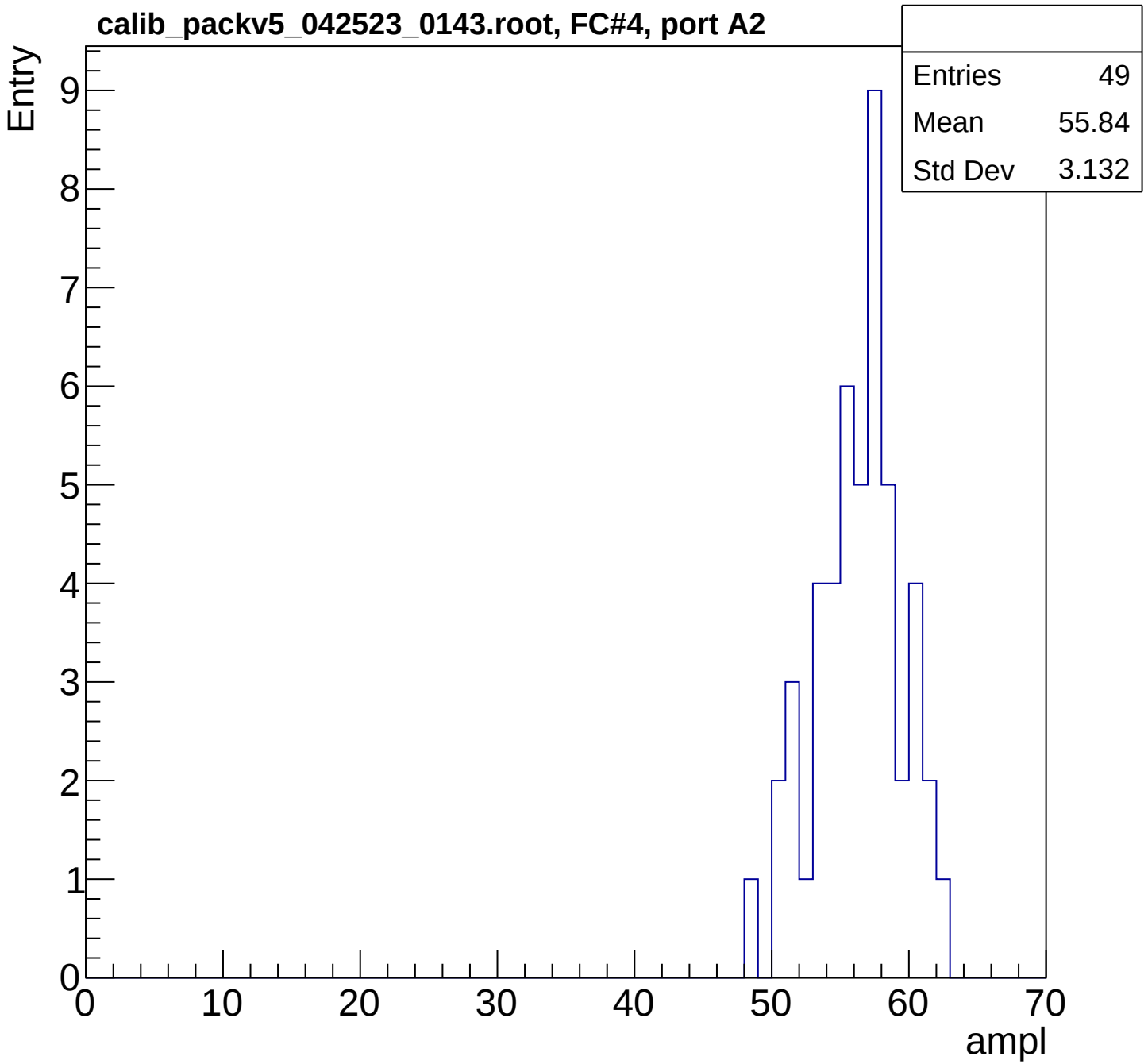

B1L100S, U22-ch46, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

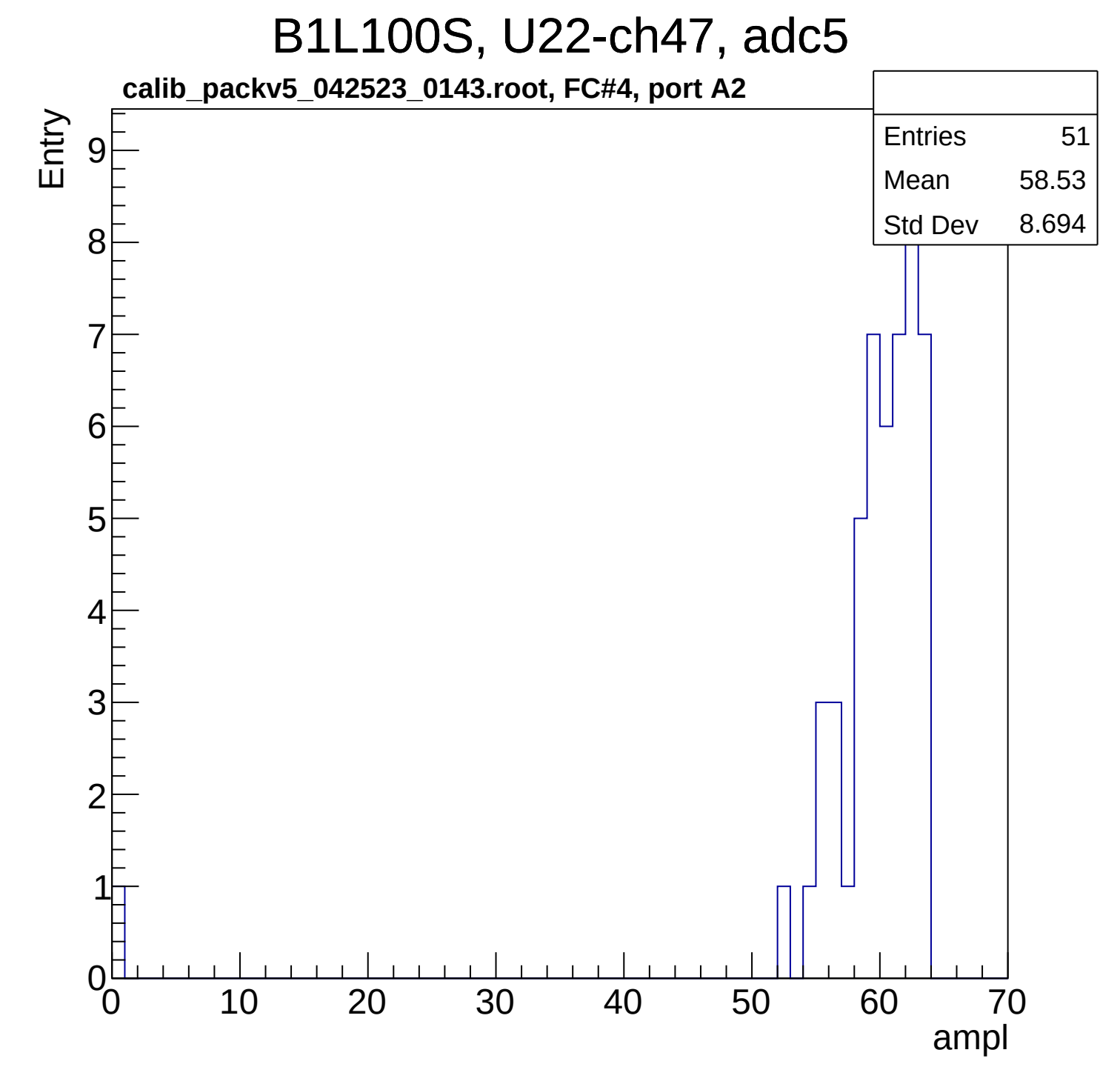

B1L100S, U22-ch47, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.51
Std Dev 5.047
Gaus mean : 30.8051
Gaus Width: 4.3284
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch47, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.51
Std Dev 3.547
Gaus mean : 37.0520
Gaus Width: 4.0771
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch47, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 43.78
Std Dev 3.827
Gaus mean : 44.0054
Gaus Width: 4.3429
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch47, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.74
Std Dev 3.666
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch47, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.84
Std Dev 3.132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch47, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.53
Std Dev 8.694
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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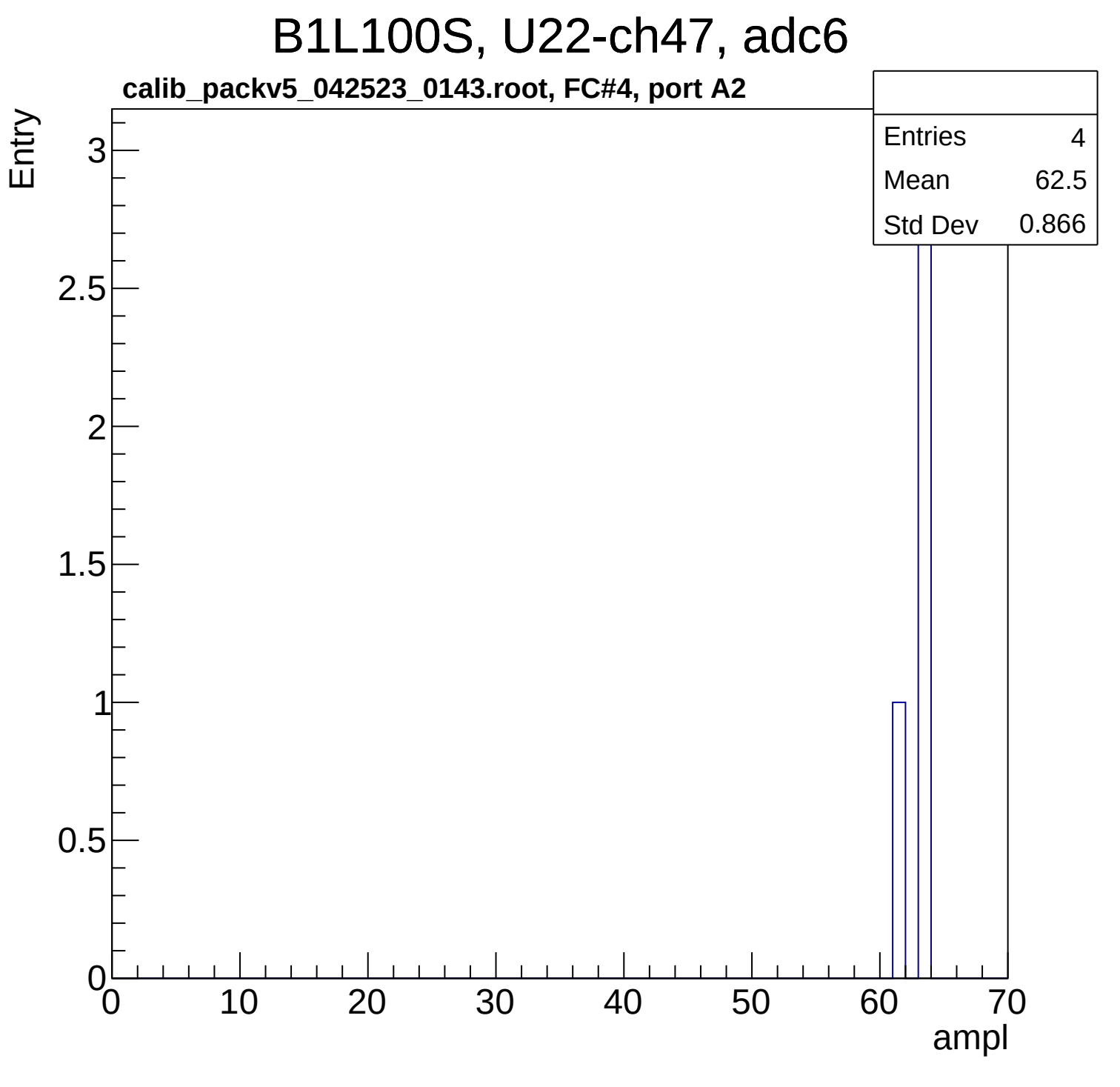
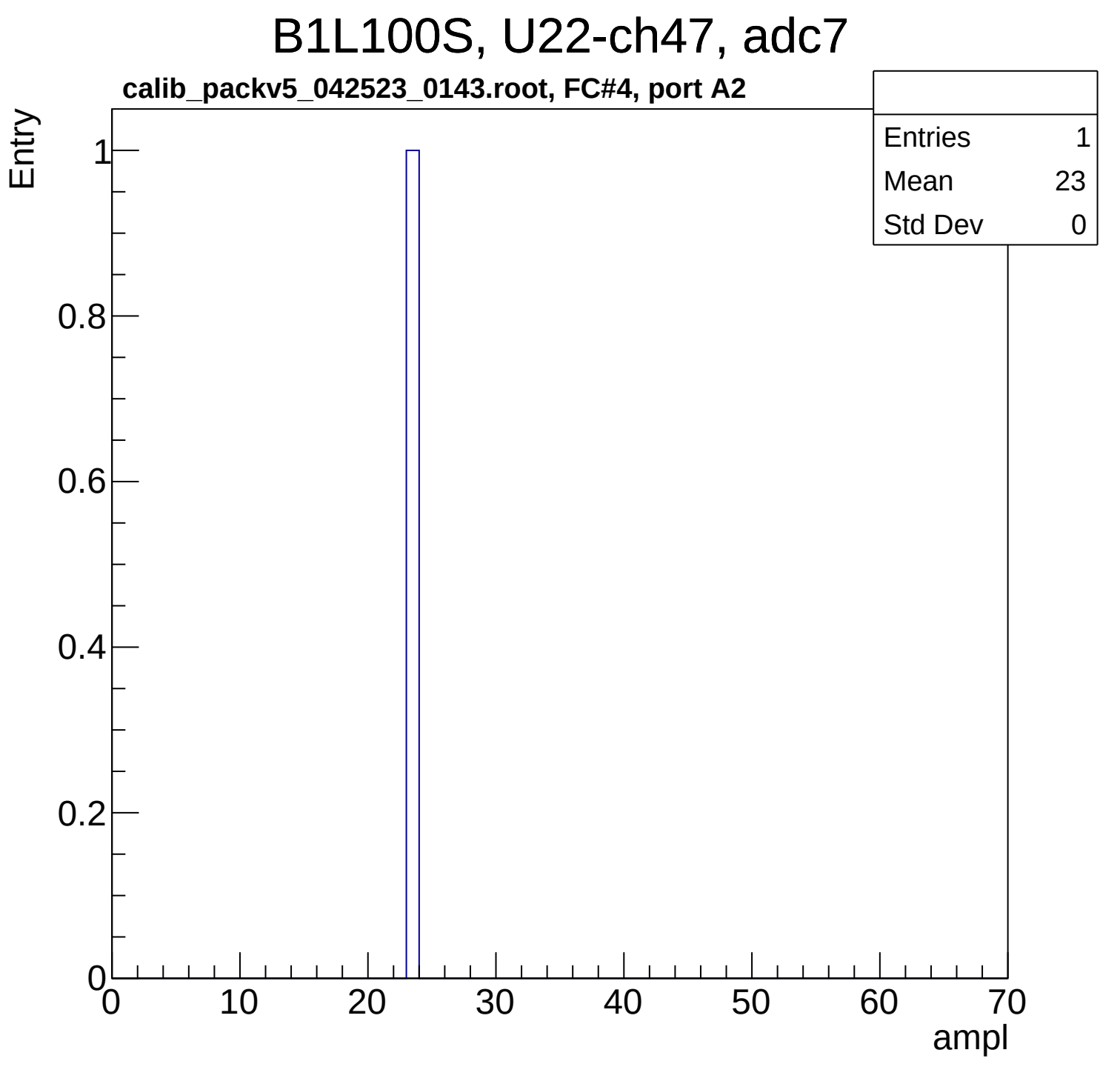
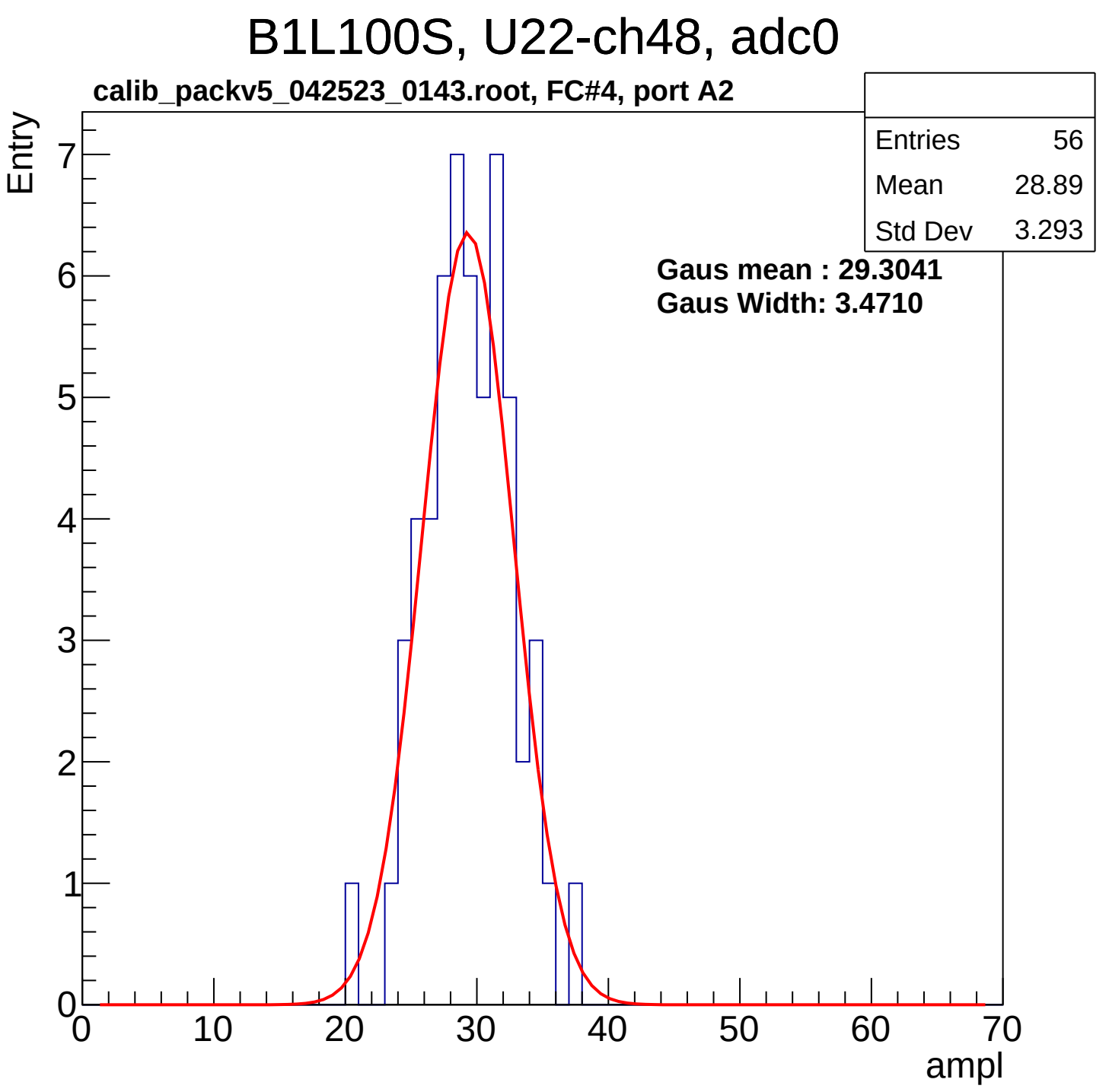
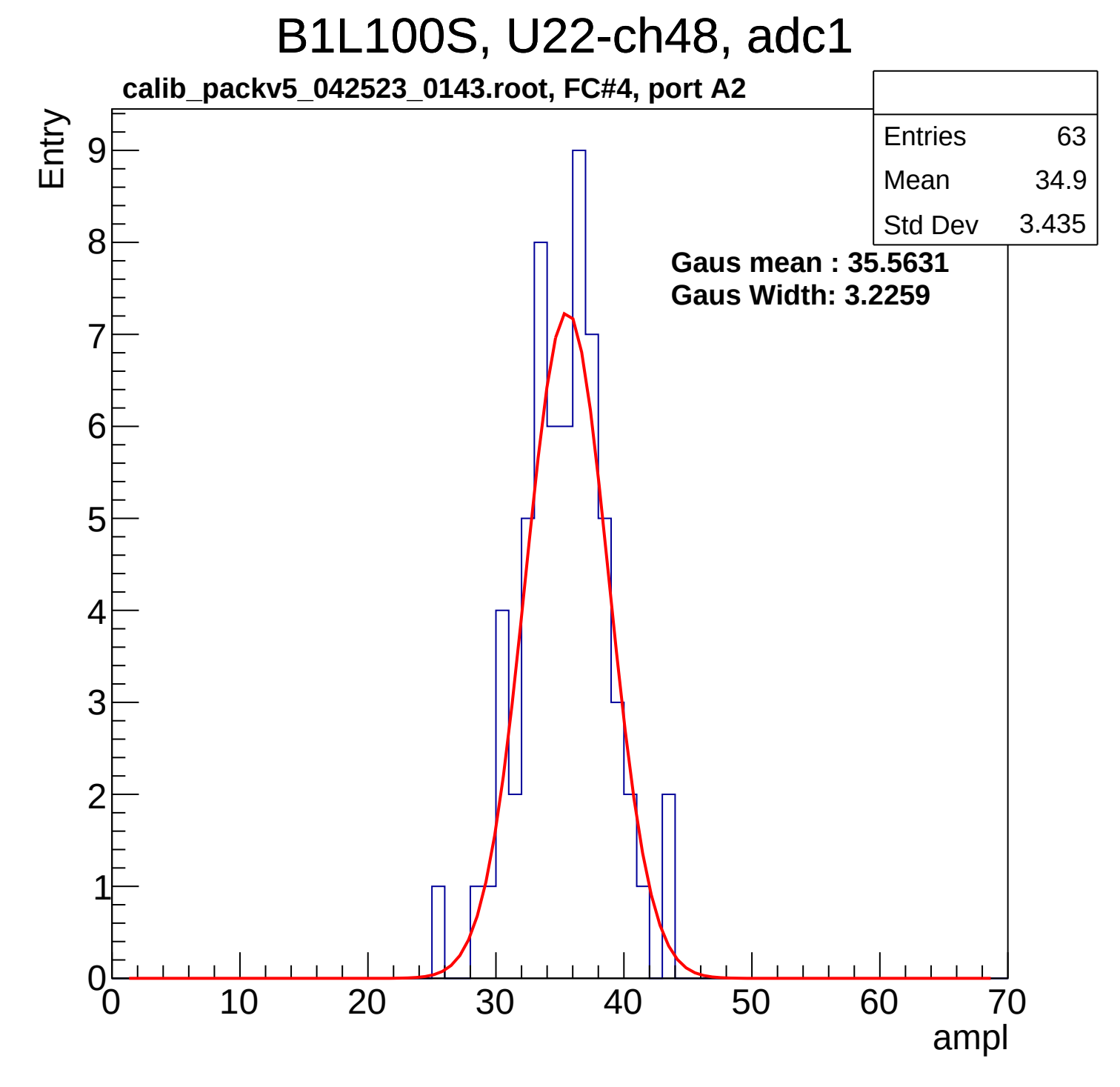
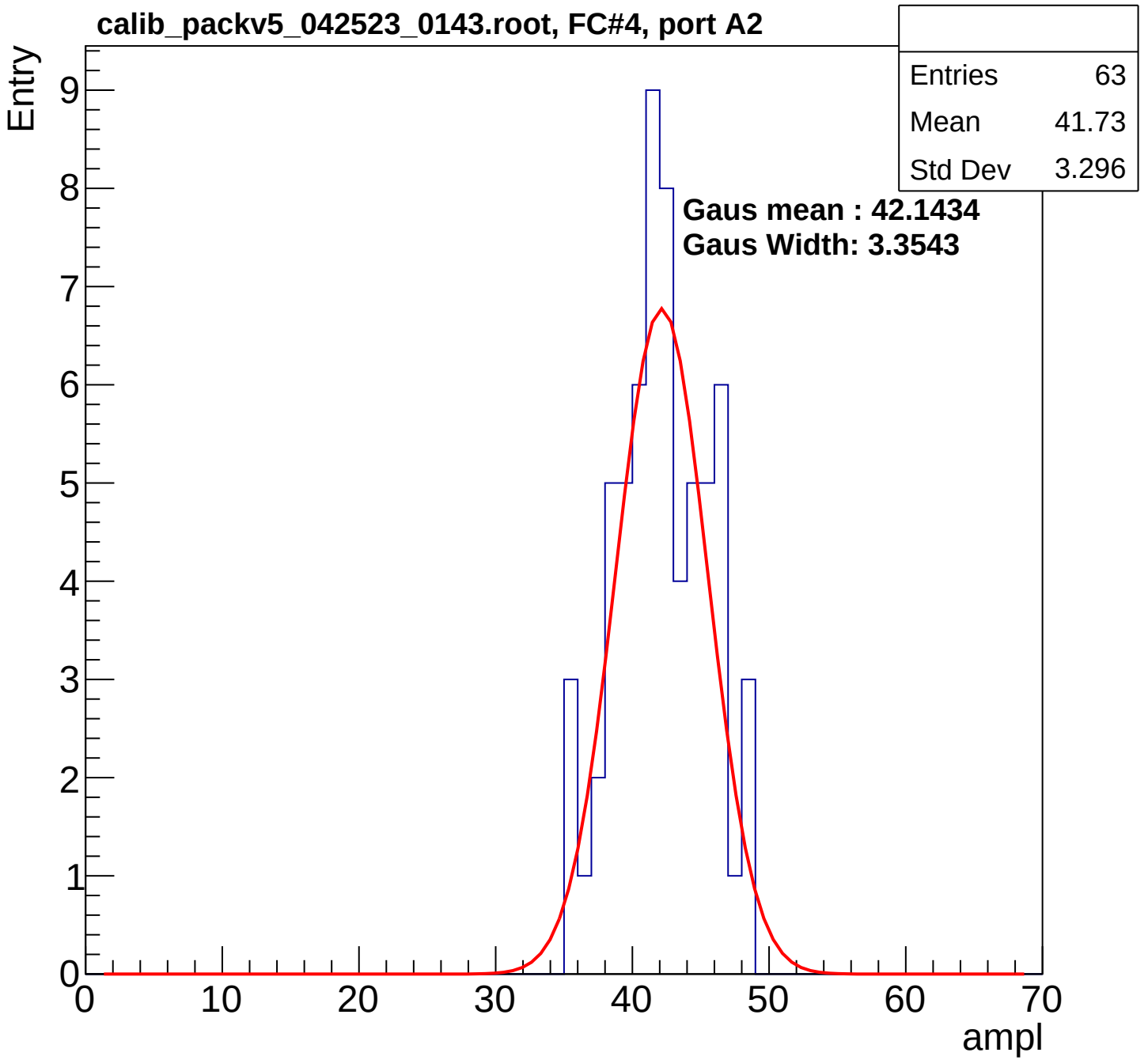
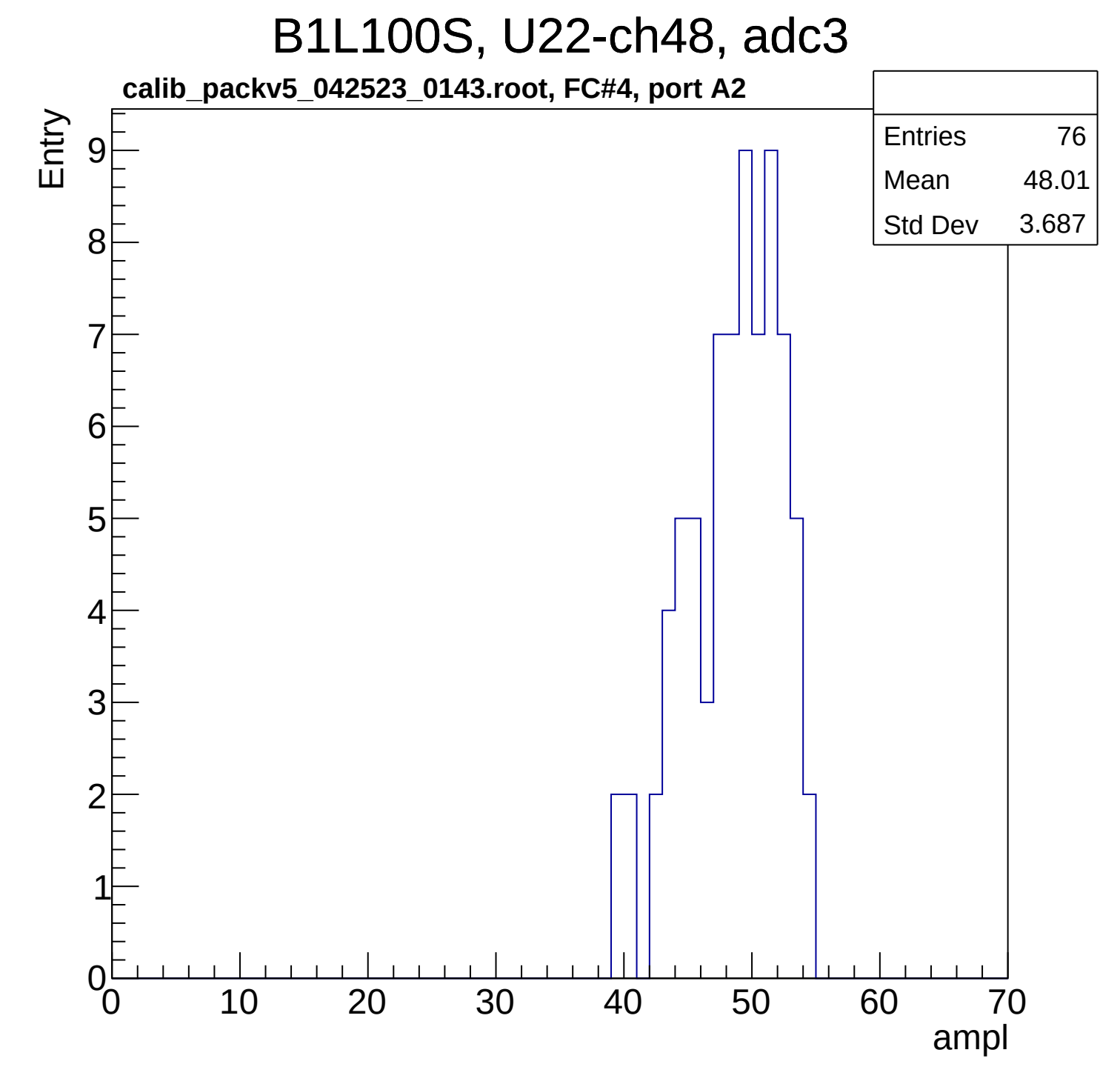

B1L100S, U22-ch47, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.5
Std Dev 0.866
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

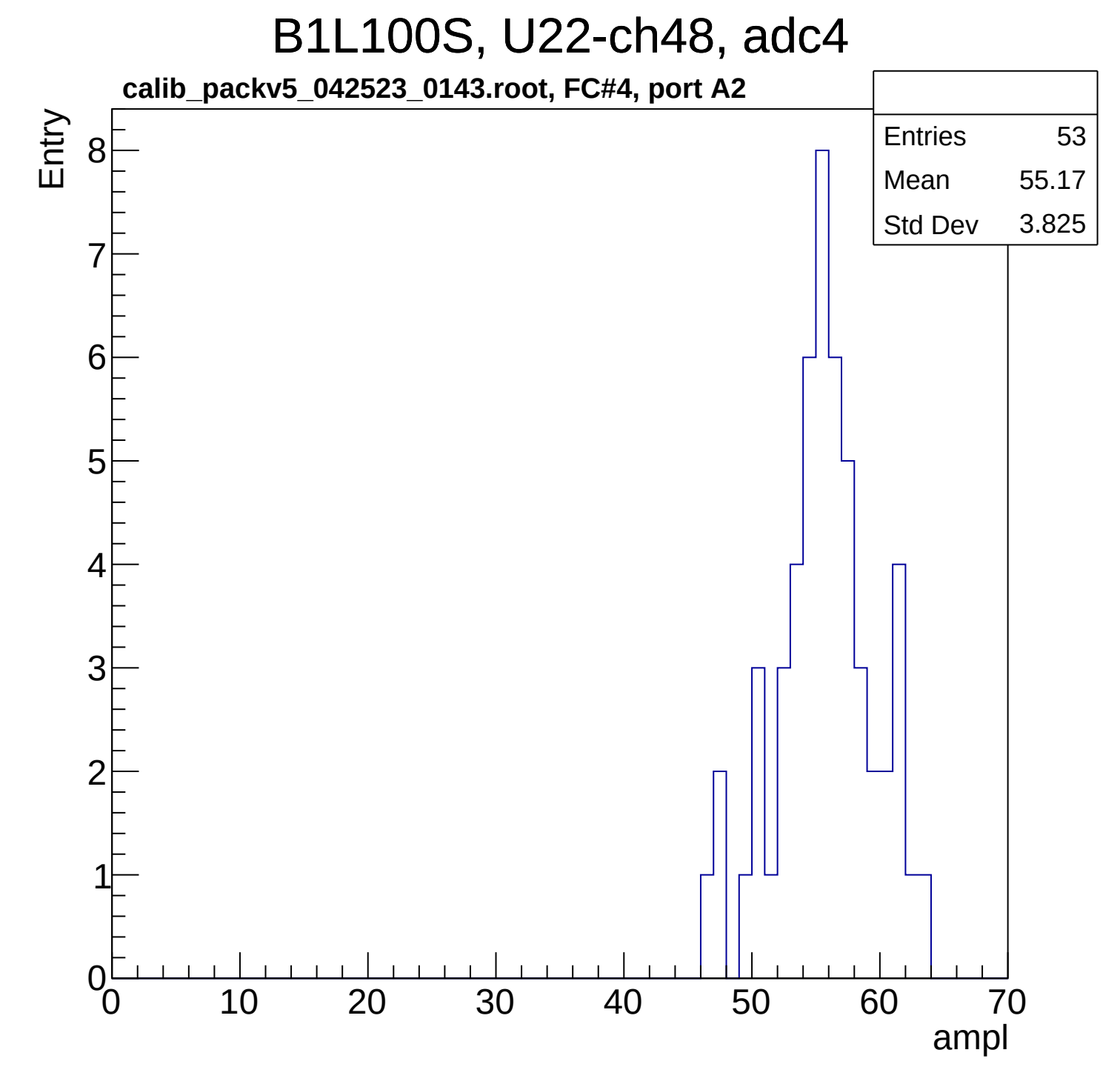

B1L100S, U22-ch47, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 23
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

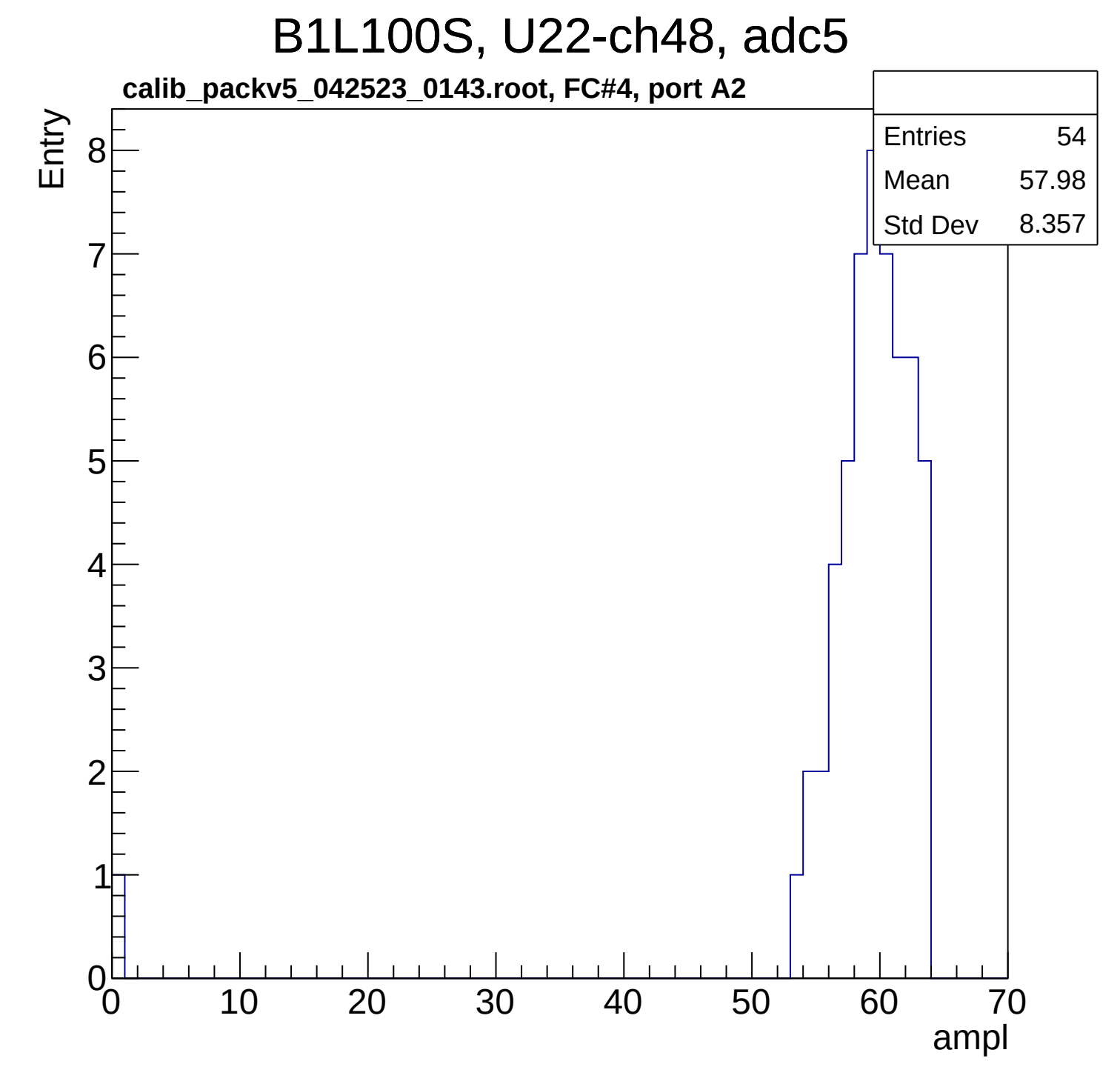

B1L100S, U22-ch48, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 28.89
Std Dev 3.293
Gaus mean : 29.3041
Gaus Width: 3.4710
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

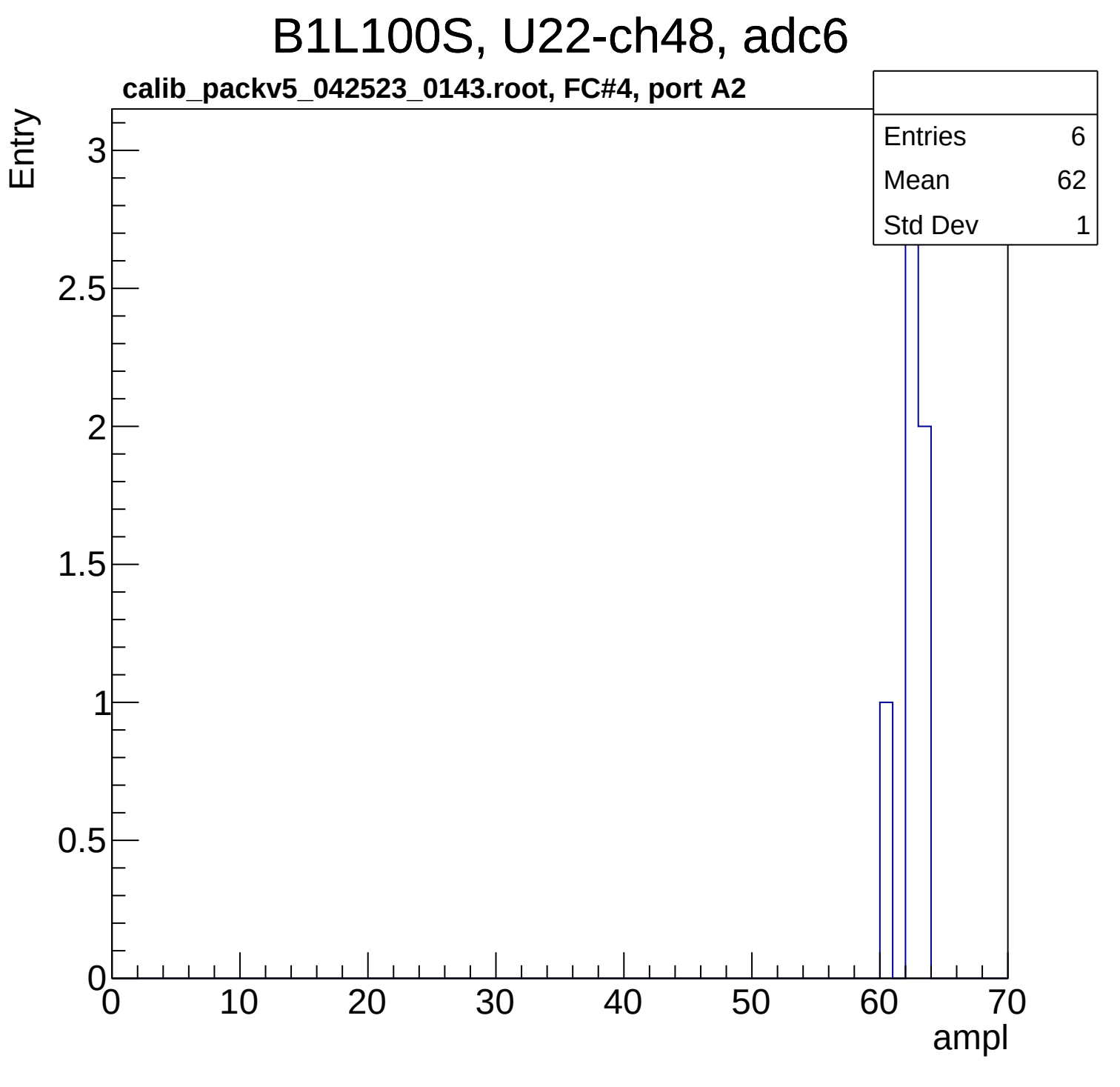

B1L100S, U22-ch48, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 34.9
Std Dev 3.435
Gaus mean : 35.5631
Gaus Width: 3.2259
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

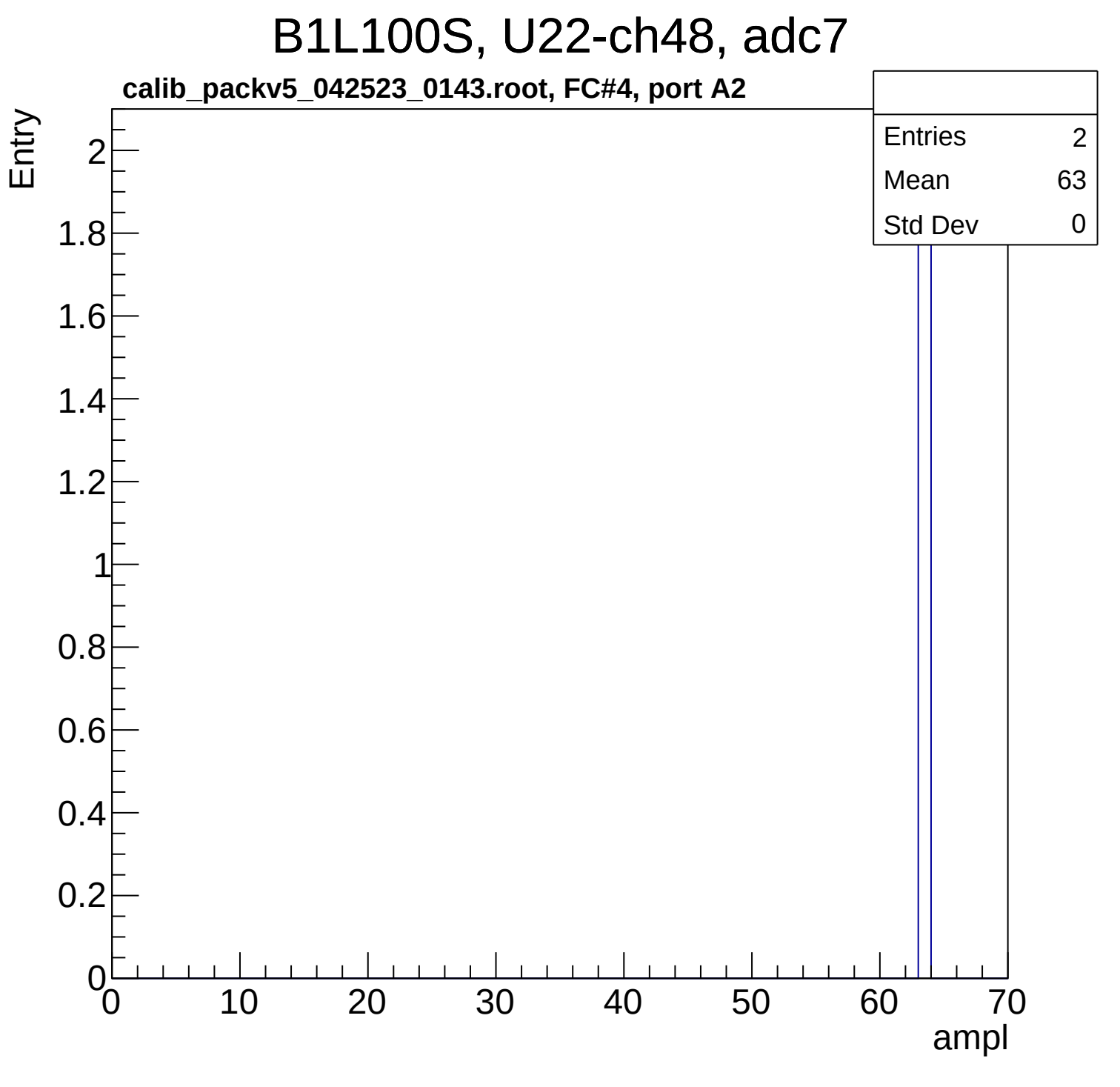

B1L100S, U22-ch48, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 41.73
Std Dev 3.296
Gaus mean : 42.1434
Gaus Width: 3.3543
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

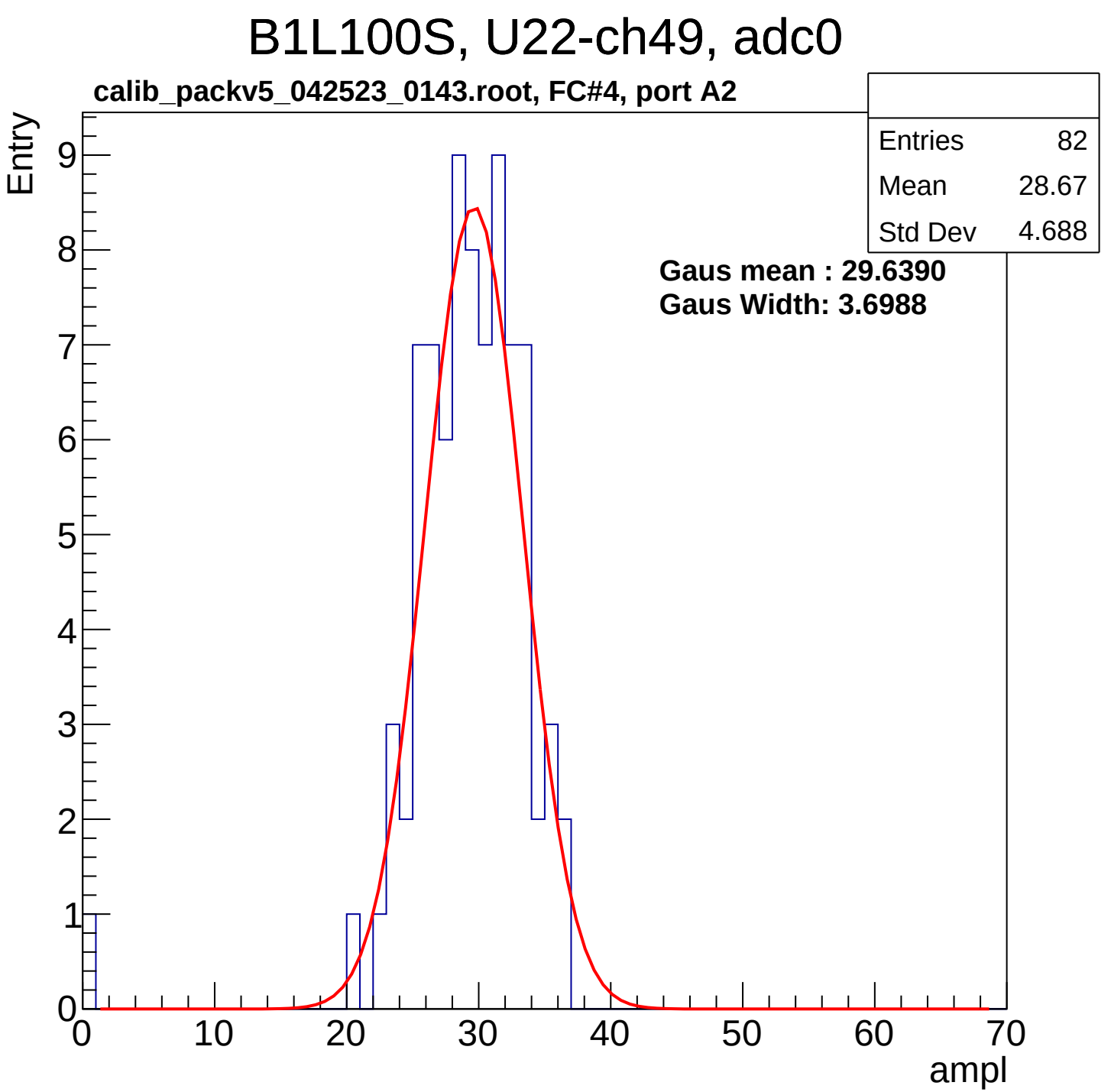

B1L100S, U22-ch48, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 48.01
Std Dev 3.687
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

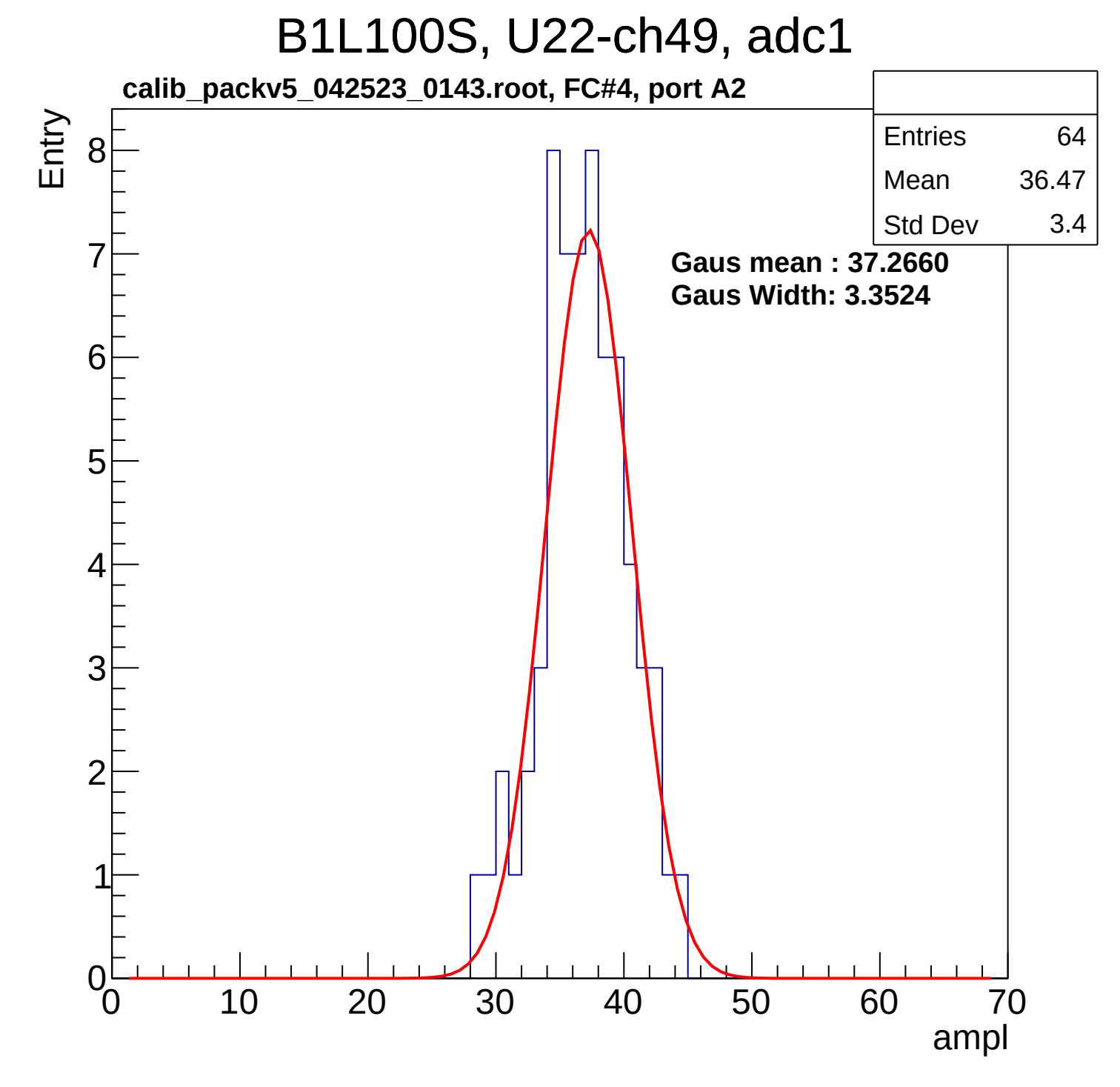

B1L100S, U22-ch48, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.17
Std Dev 3.825
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

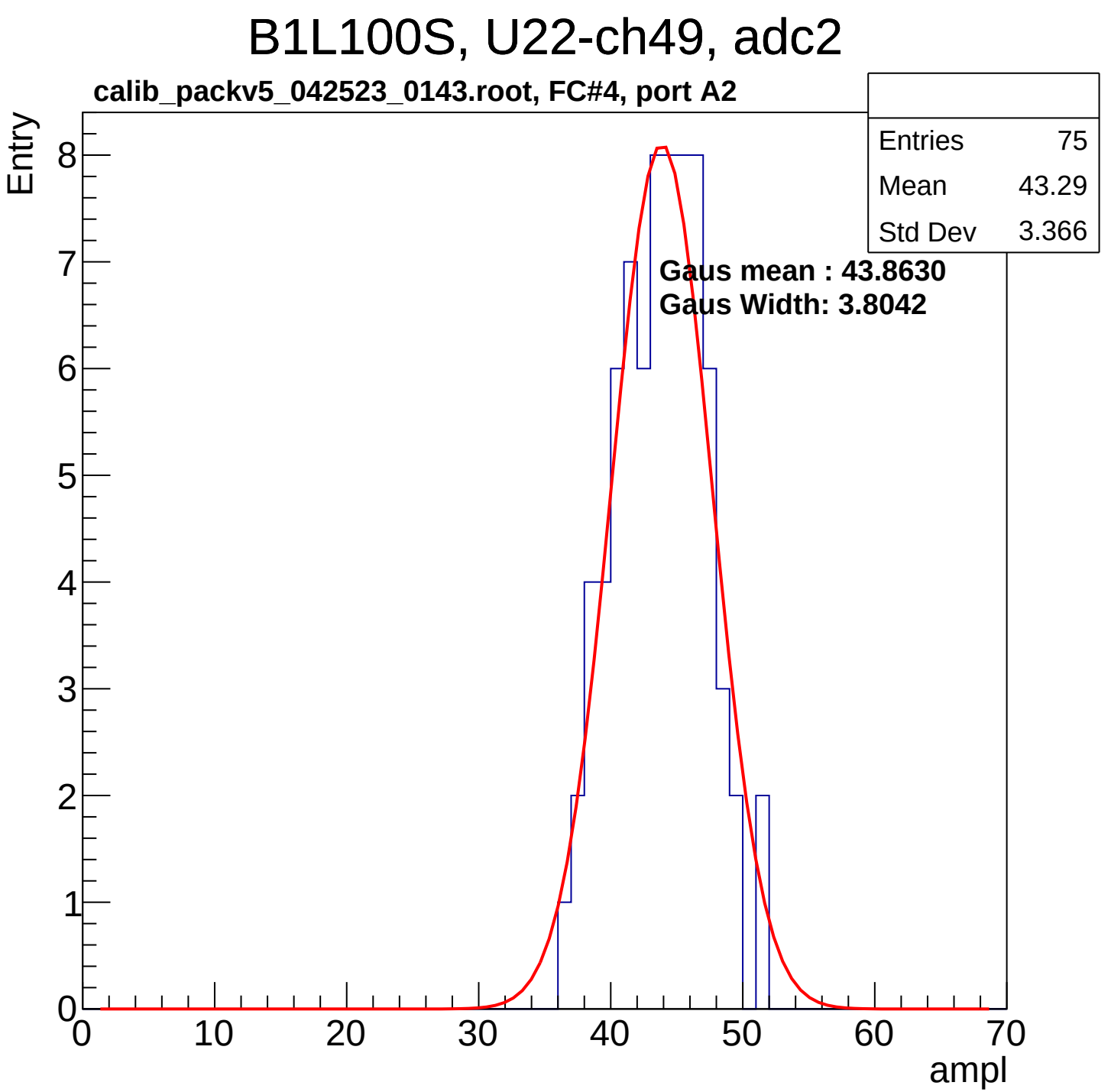

B1L100S, U22-ch48, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 57.98
Std Dev 8.357
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

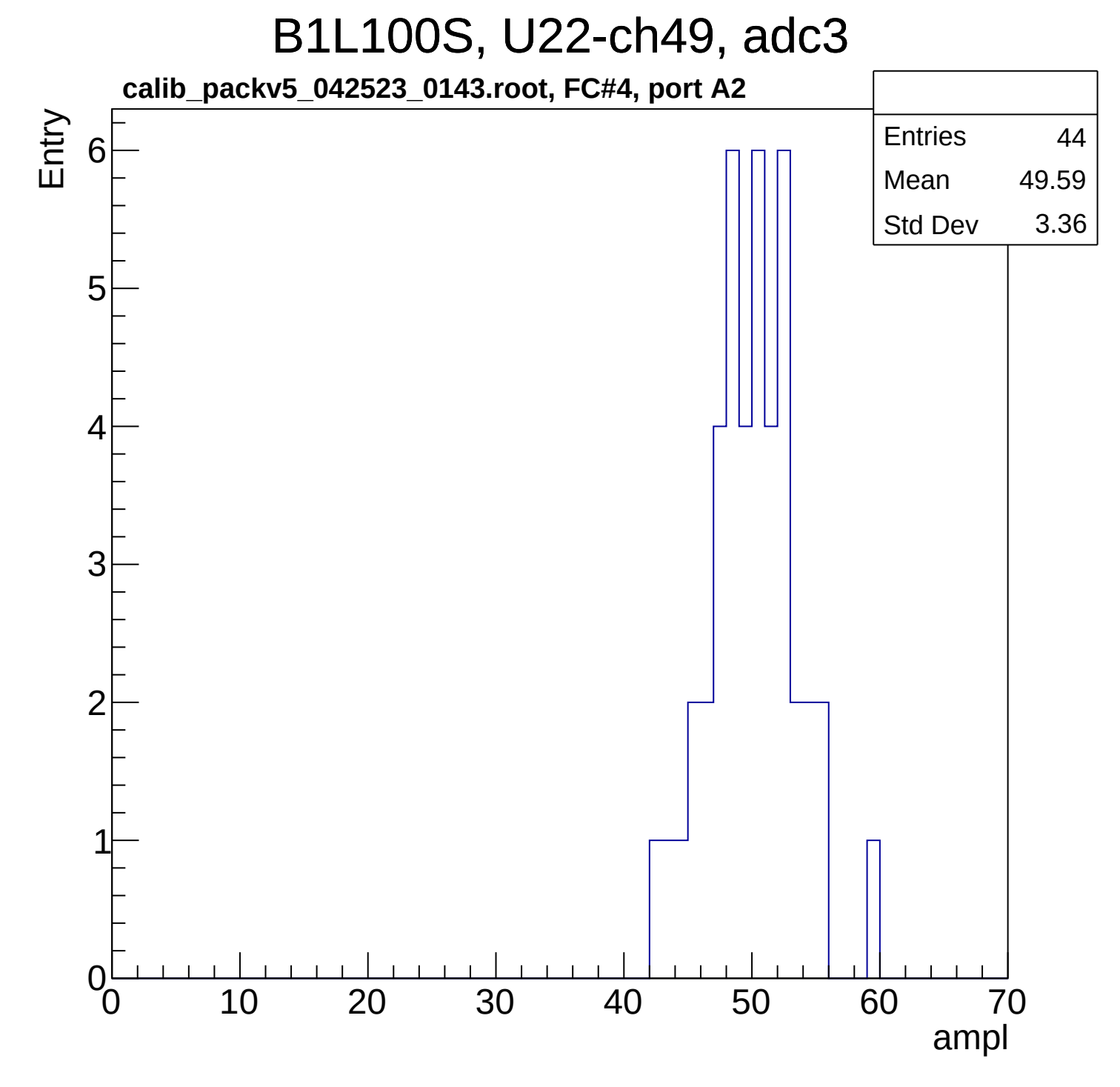

B1L100S, U22-ch48, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62
Std Dev 1
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

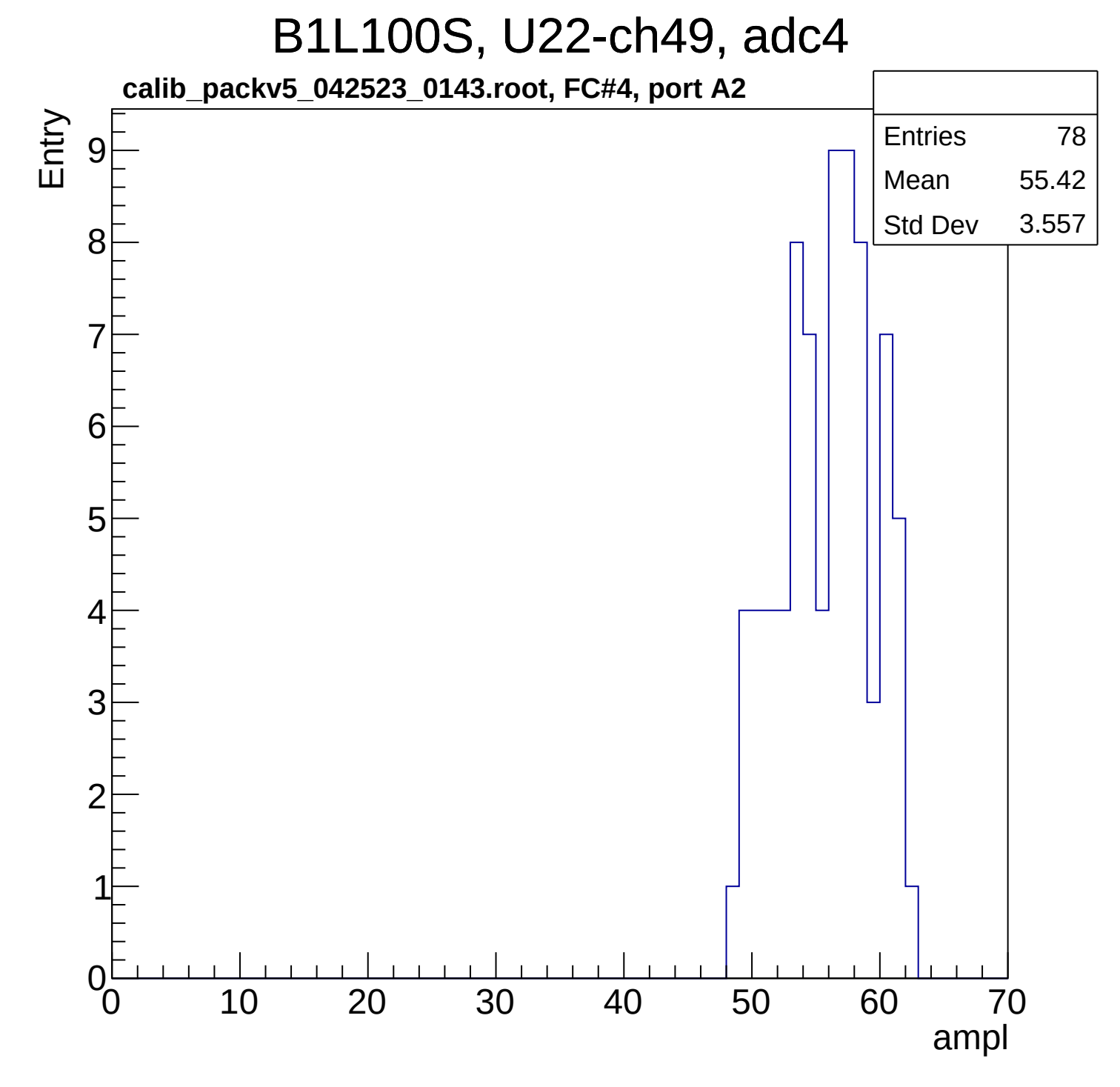

B1L100S, U22-ch48, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch49, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 28.67
Std Dev 4.688
Gaus mean : 29.6390
Gaus Width: 3.6988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch49, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 36.47
Std Dev 3.4
Gaus mean : 37.2660
Gaus Width: 3.3524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch49, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.29
Std Dev 3.366
Gaus mean : 43.8630
Gaus Width: 3.8042
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch49, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 49.59
Std Dev 3.36
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch49, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 55.42
Std Dev 3.557
Entry
ampl
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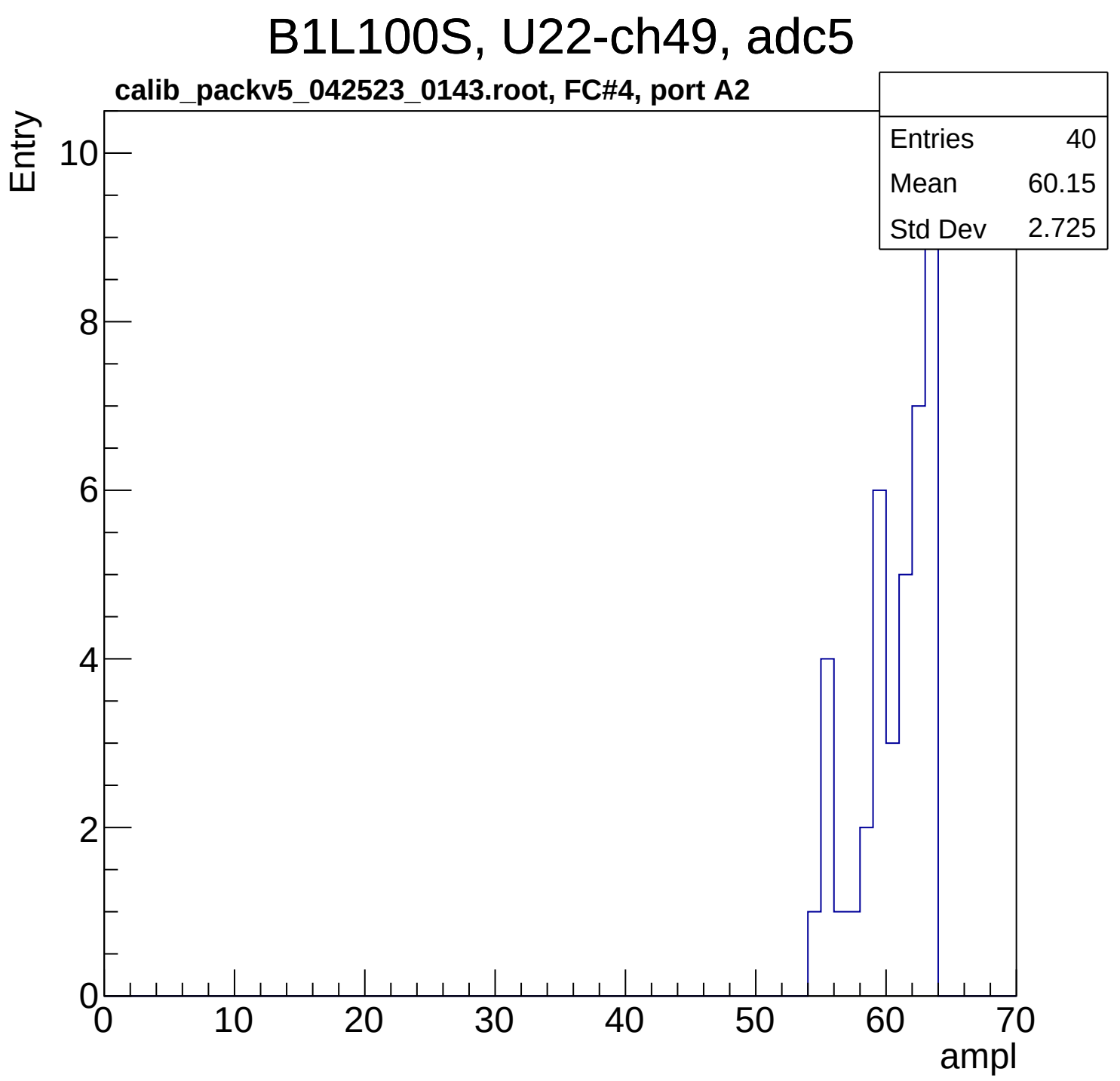
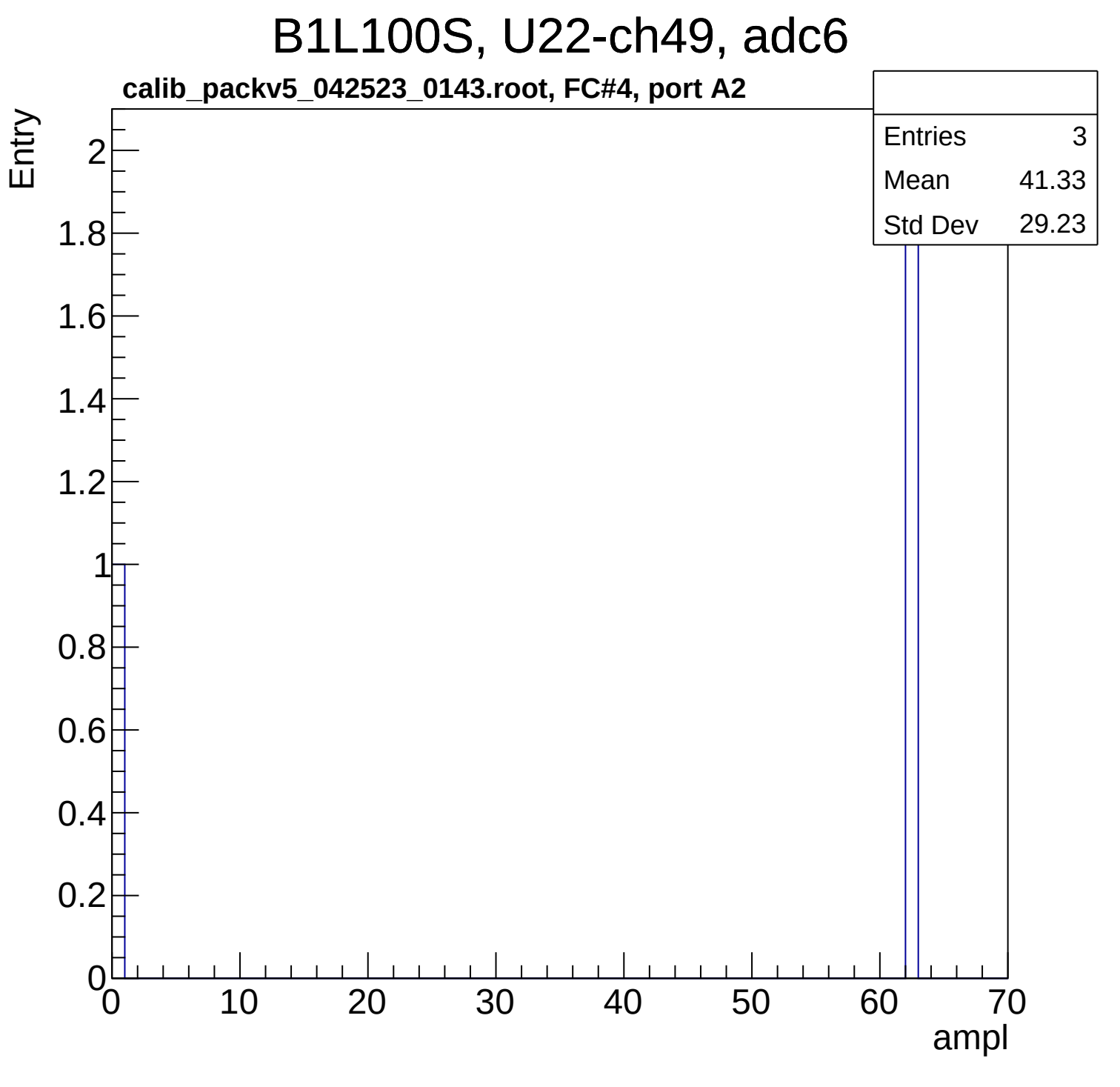
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch49, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 60.15
Std Dev 2.725
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch49, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.33
Std Dev 29.23
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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40
50
60
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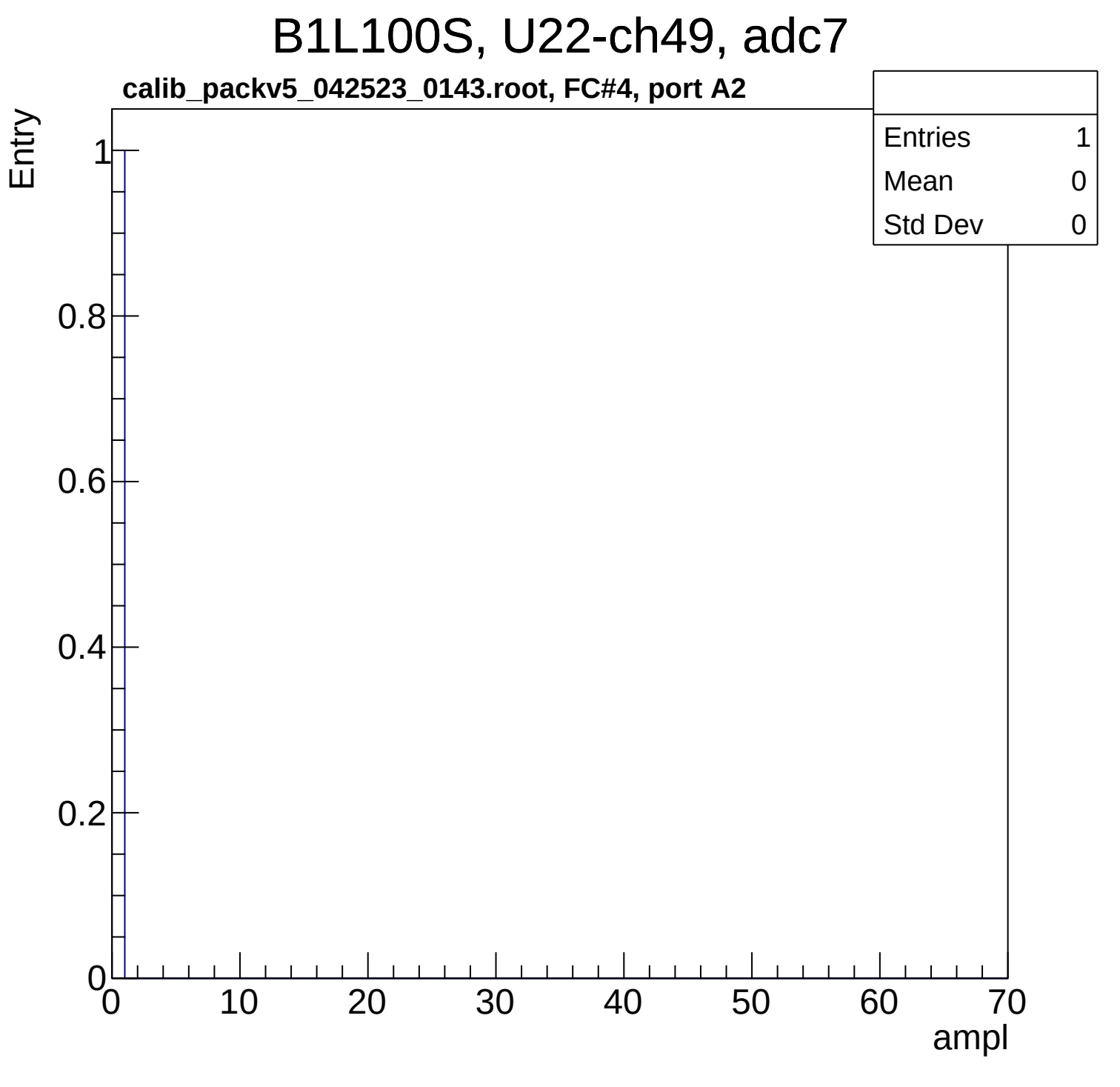
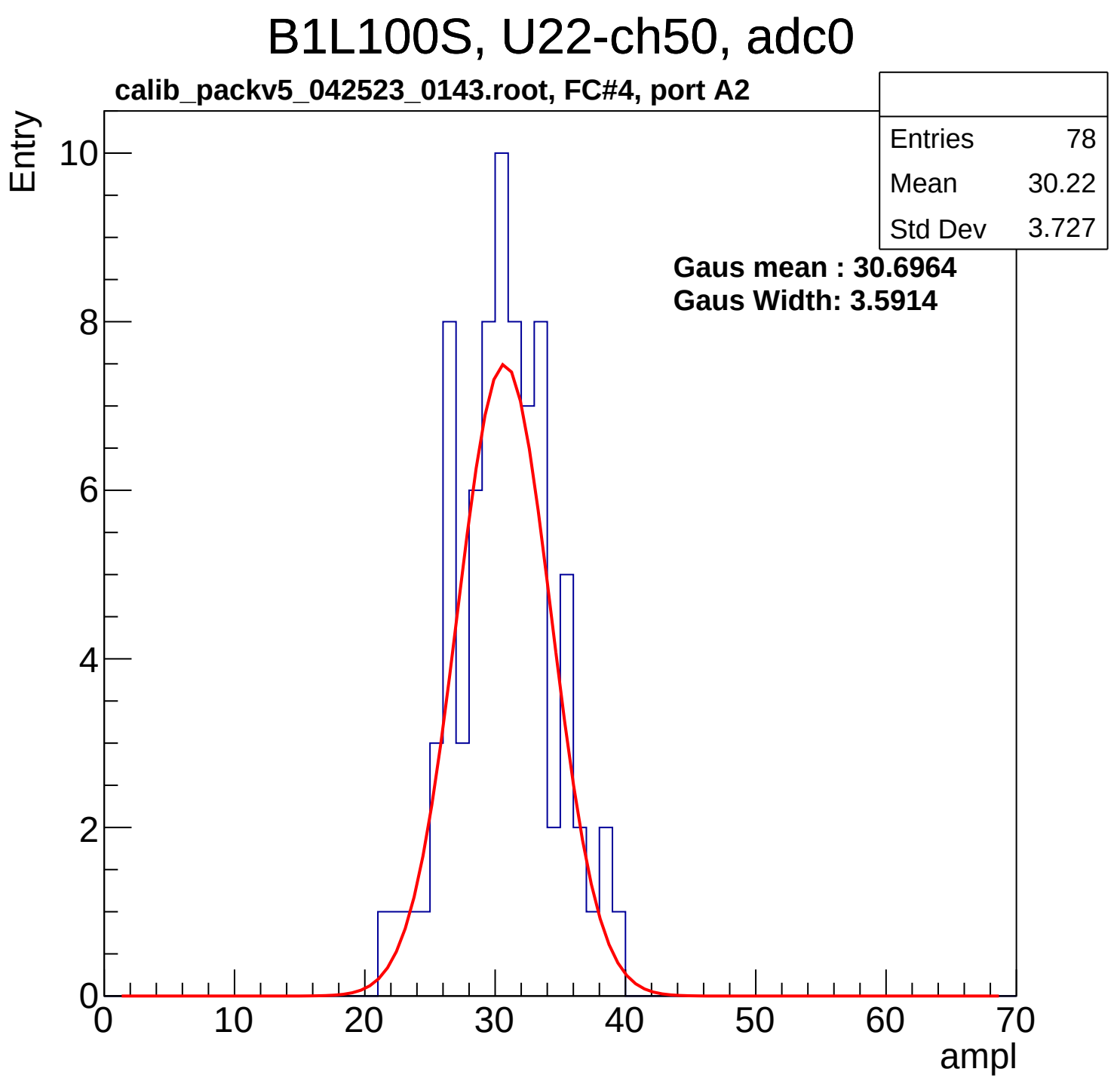
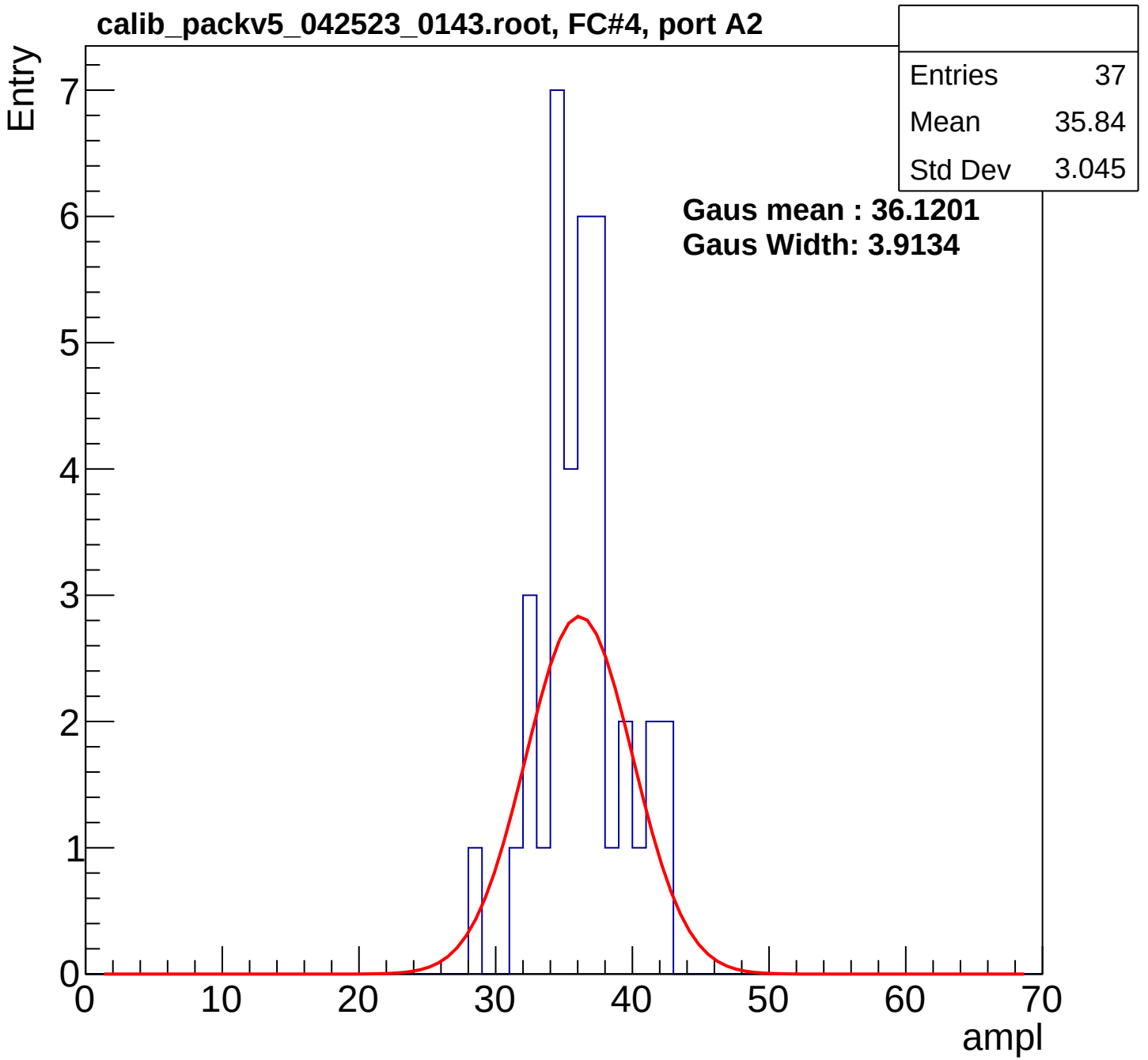
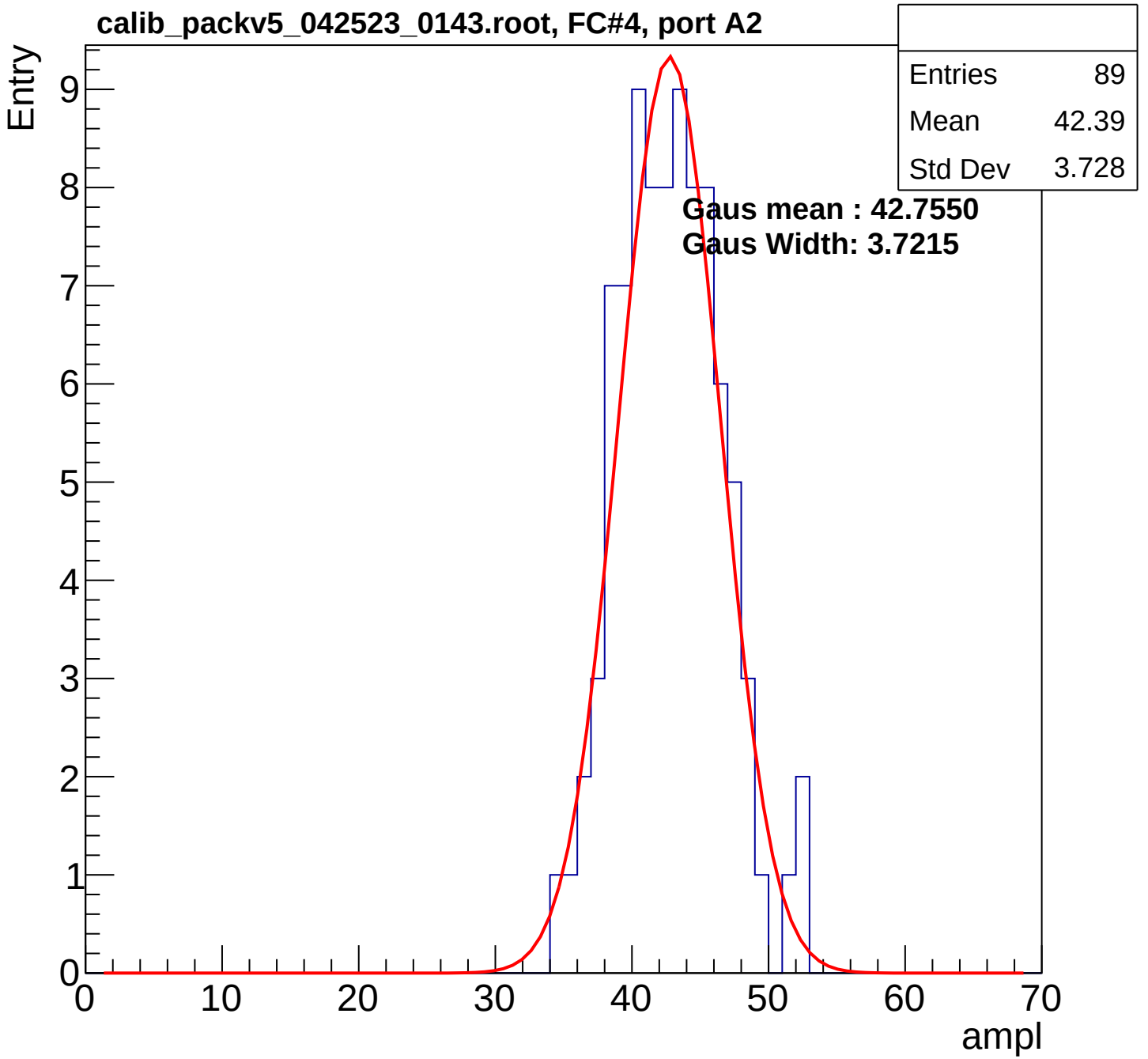

B1L100S, U22-ch49, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch50, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 30.22
Std Dev 3.727
Gaus mean : 30.6964
Gaus Width: 3.5914
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch50, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 35.84
Std Dev 3.045
Gaus mean : 36.1201
Gaus Width: 3.9134
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch50, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 42.39
Std Dev 3.728
Gaus mean : 42.7550
Gaus Width: 3.7215
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
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60
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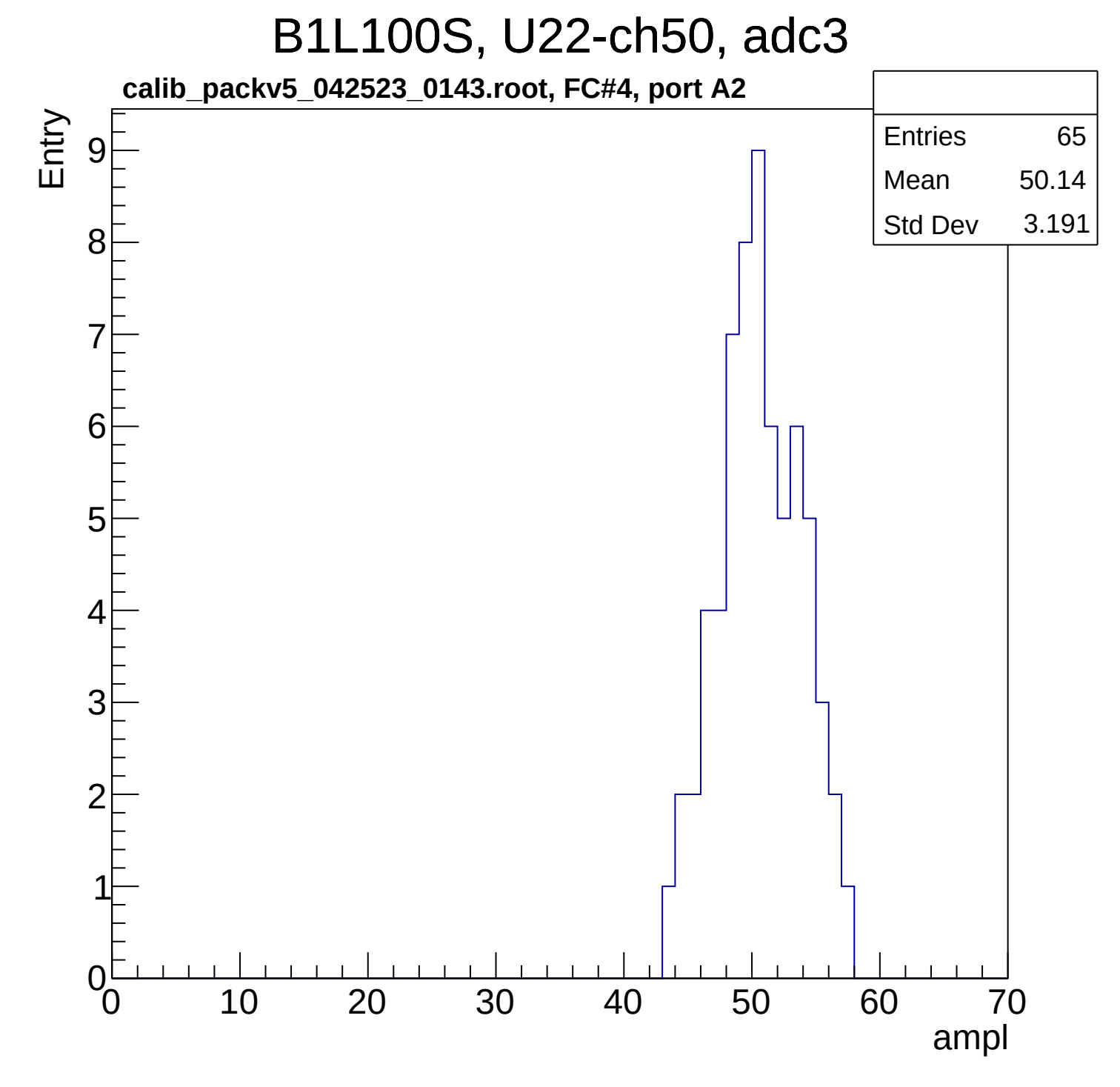
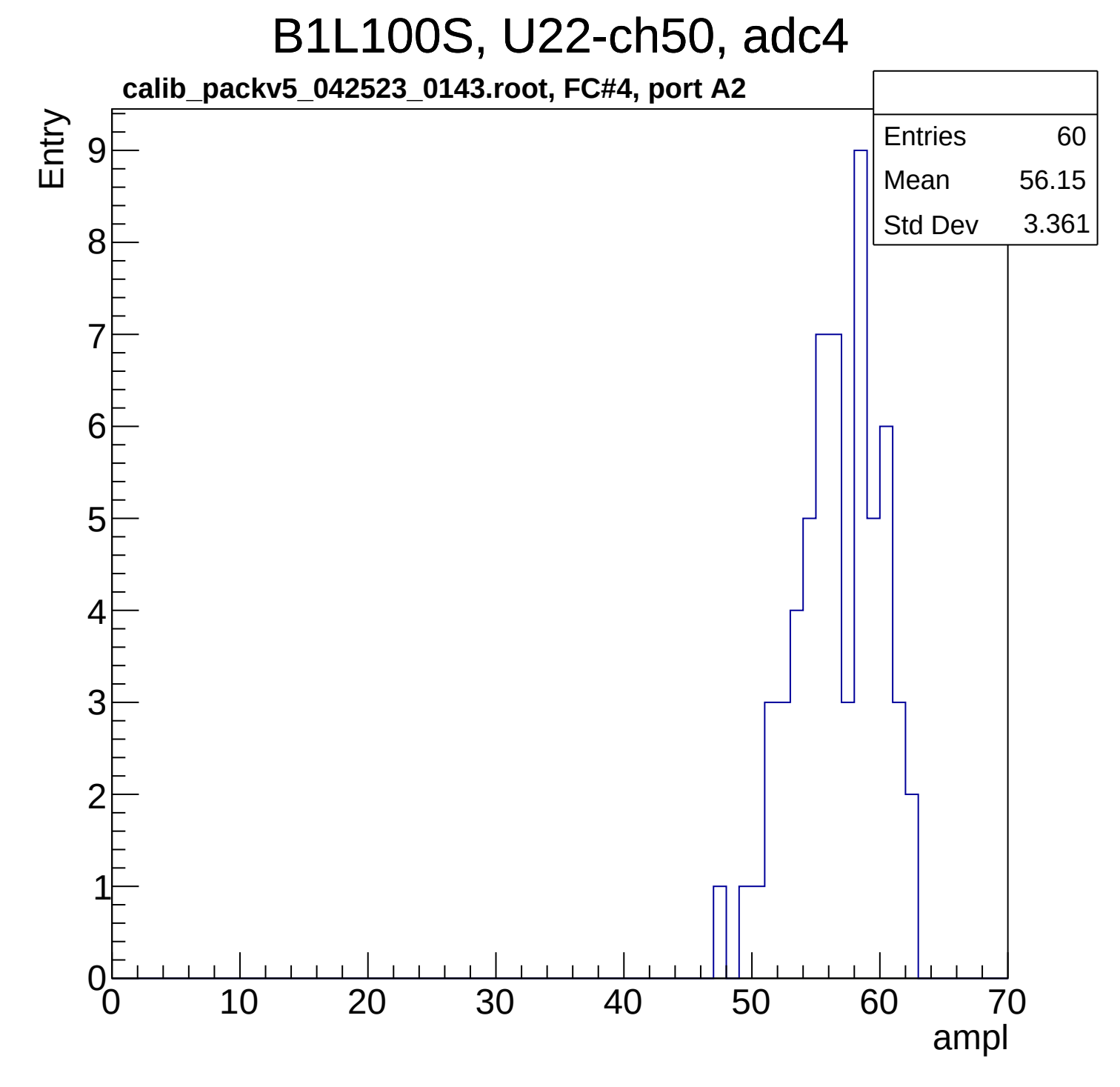
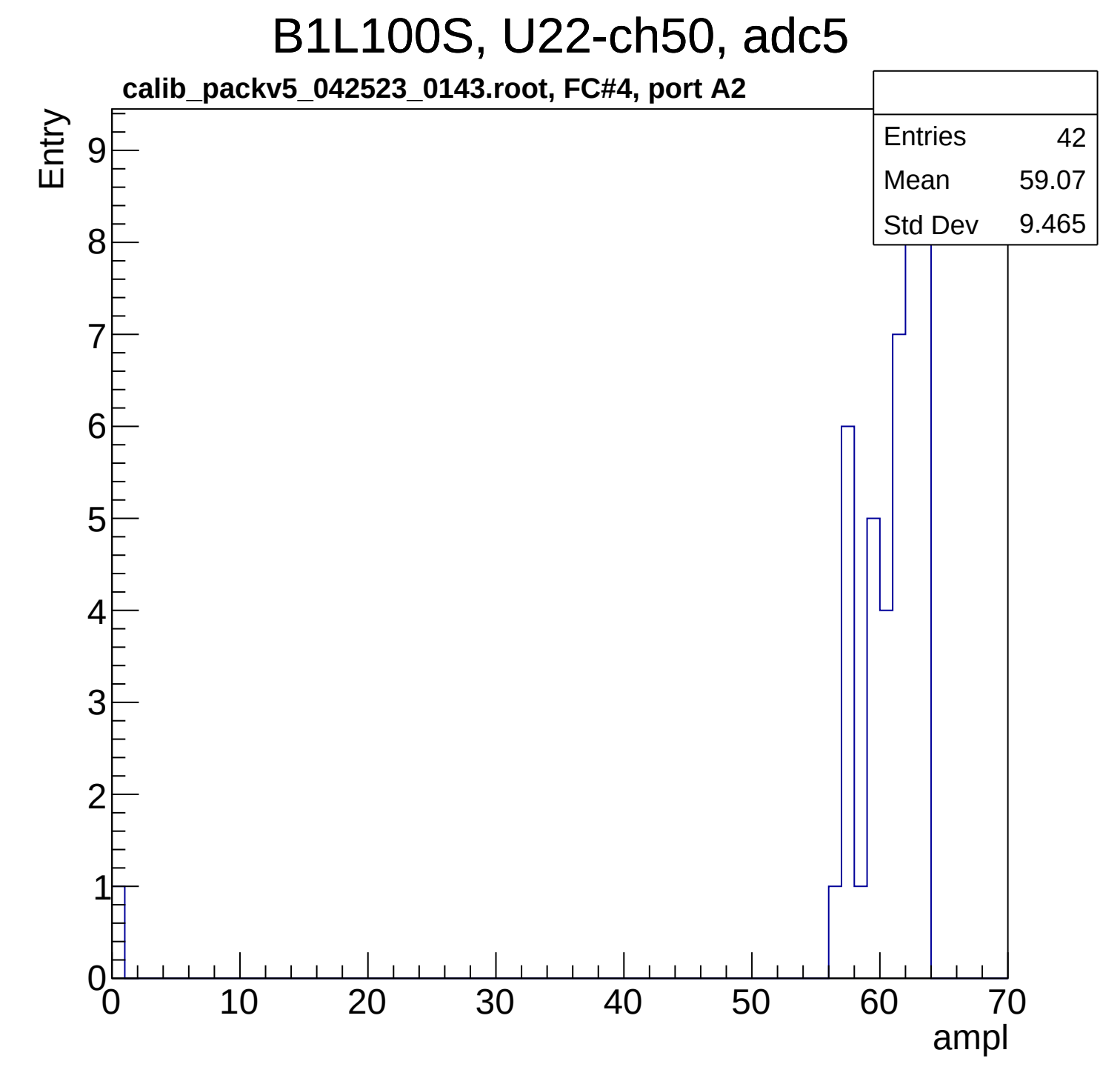
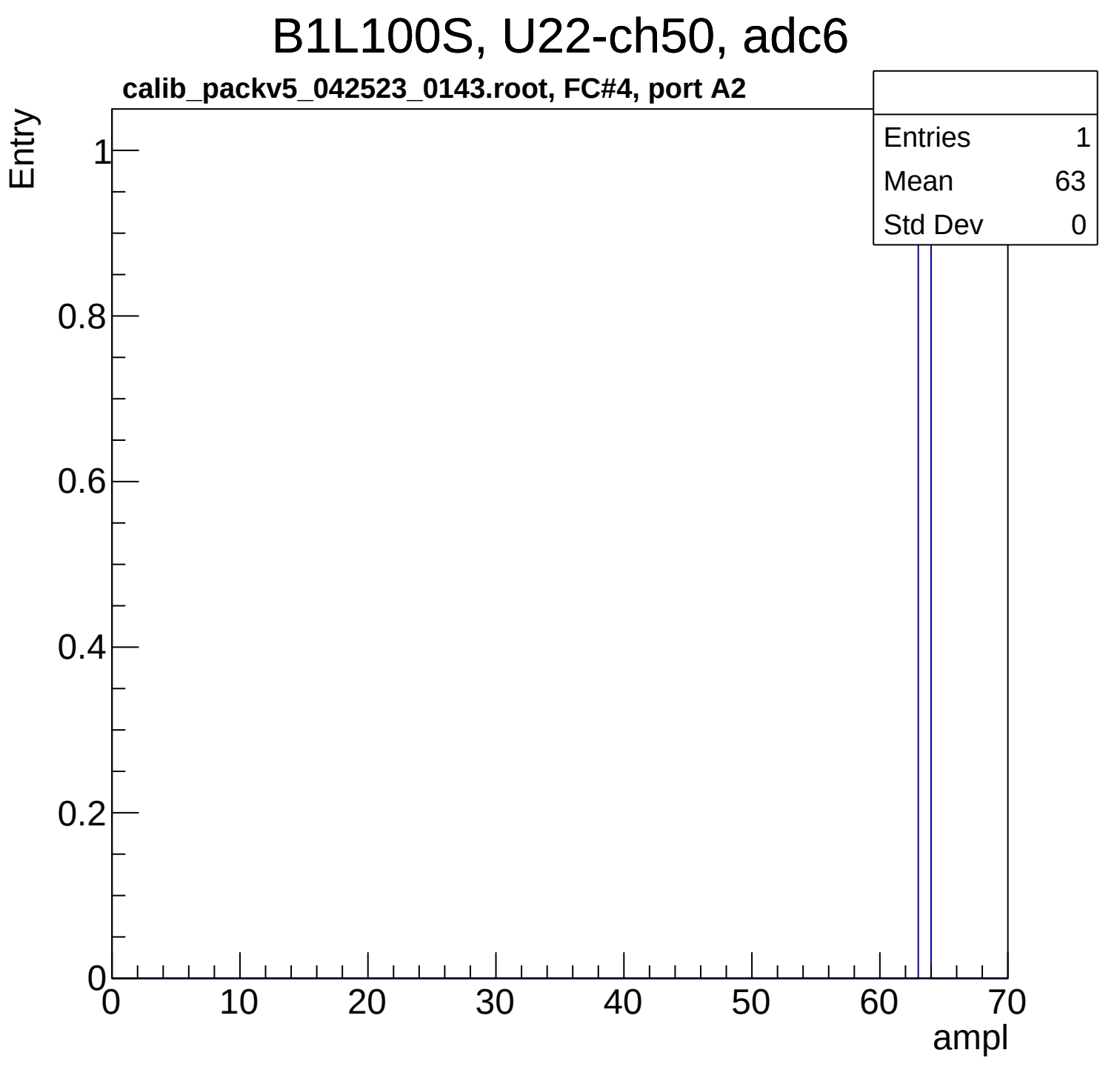
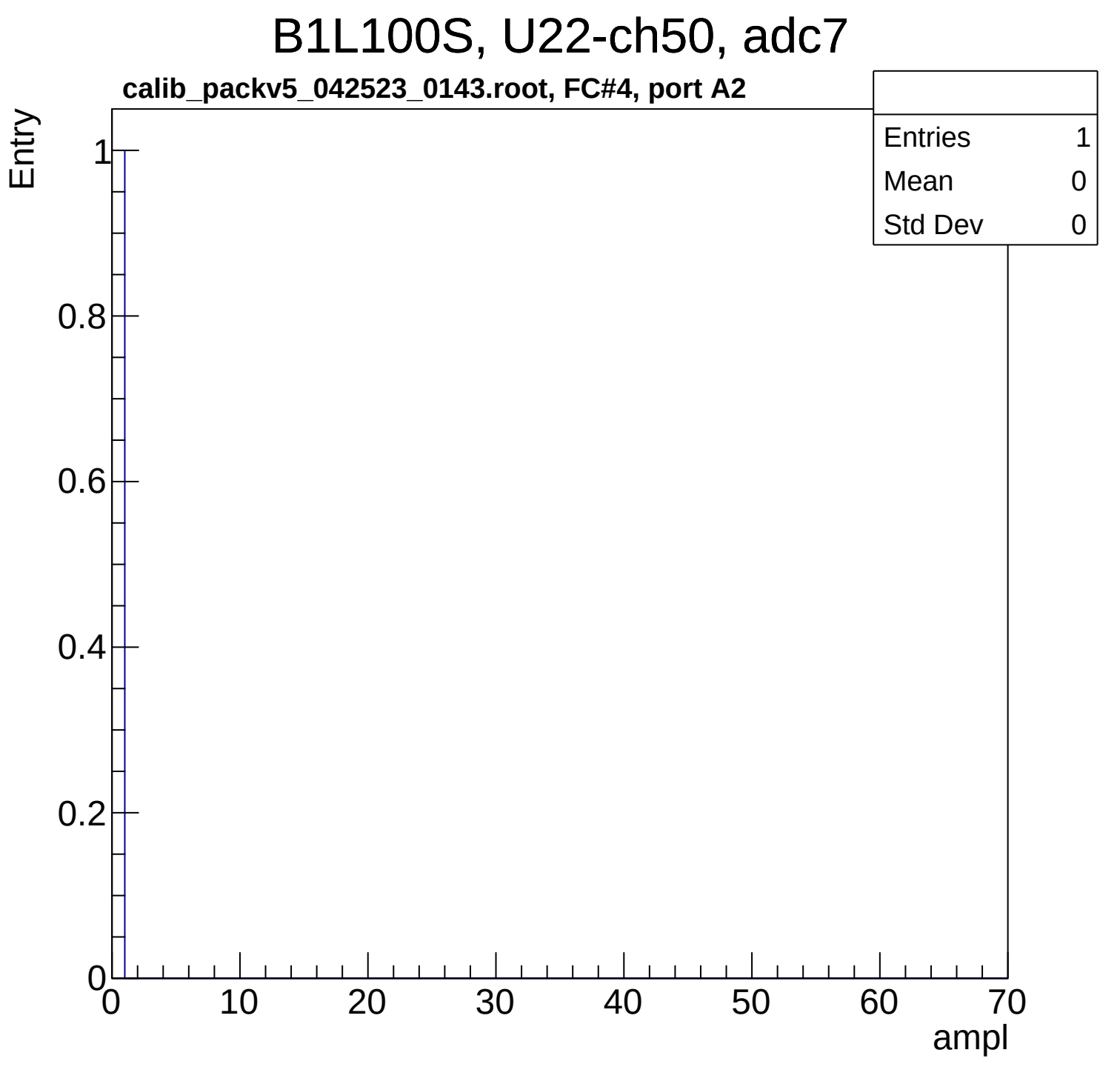
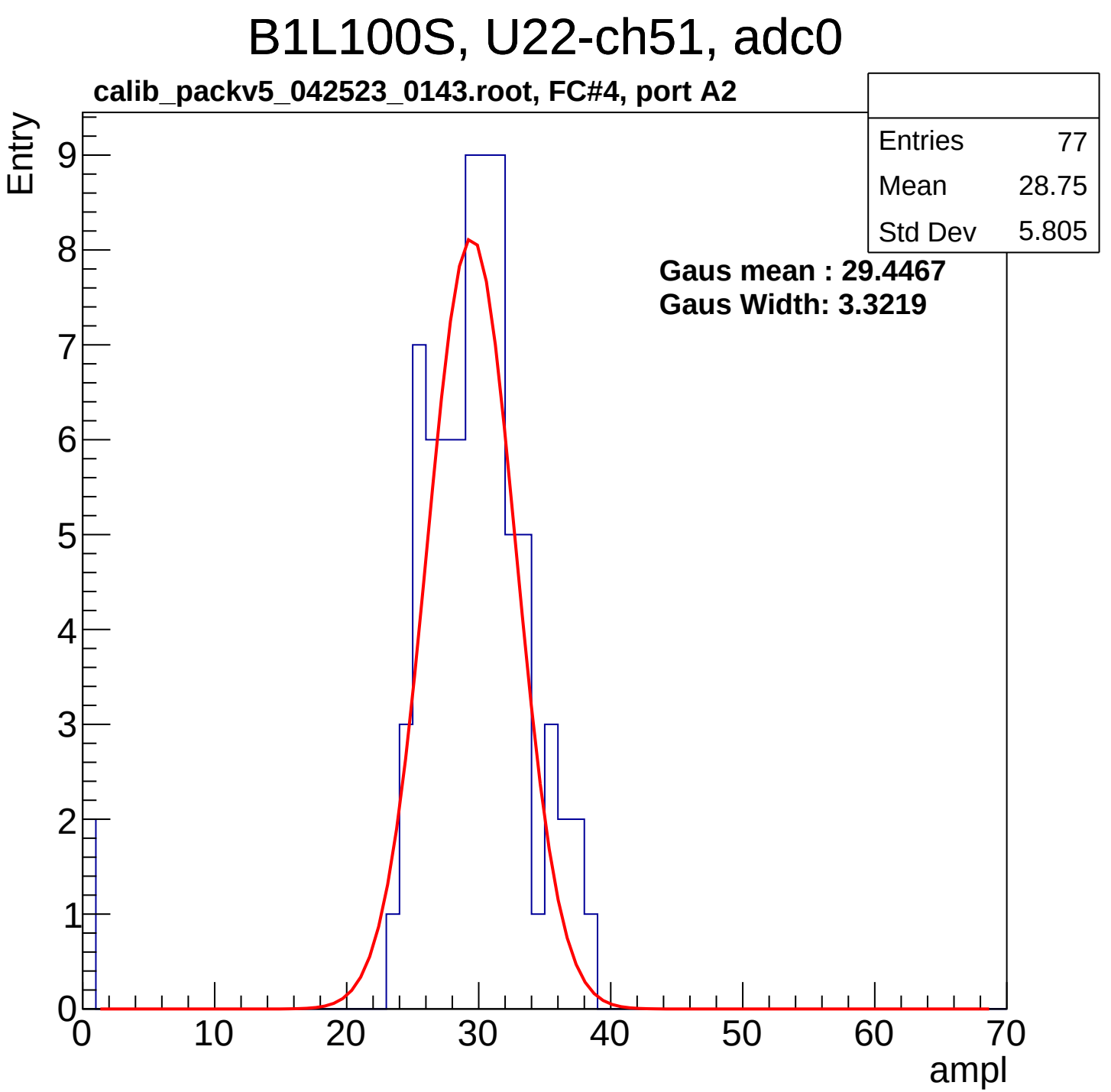

B1L100S, U22-ch50, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.14
Std Dev 3.191
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch50, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 56.15
Std Dev 3.361
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch50, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.07
Std Dev 9.465
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch50, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch50, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch51, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 28.75
Std Dev 5.805
Gaus mean : 29.4467
Gaus Width: 3.3219
Entry
ampl
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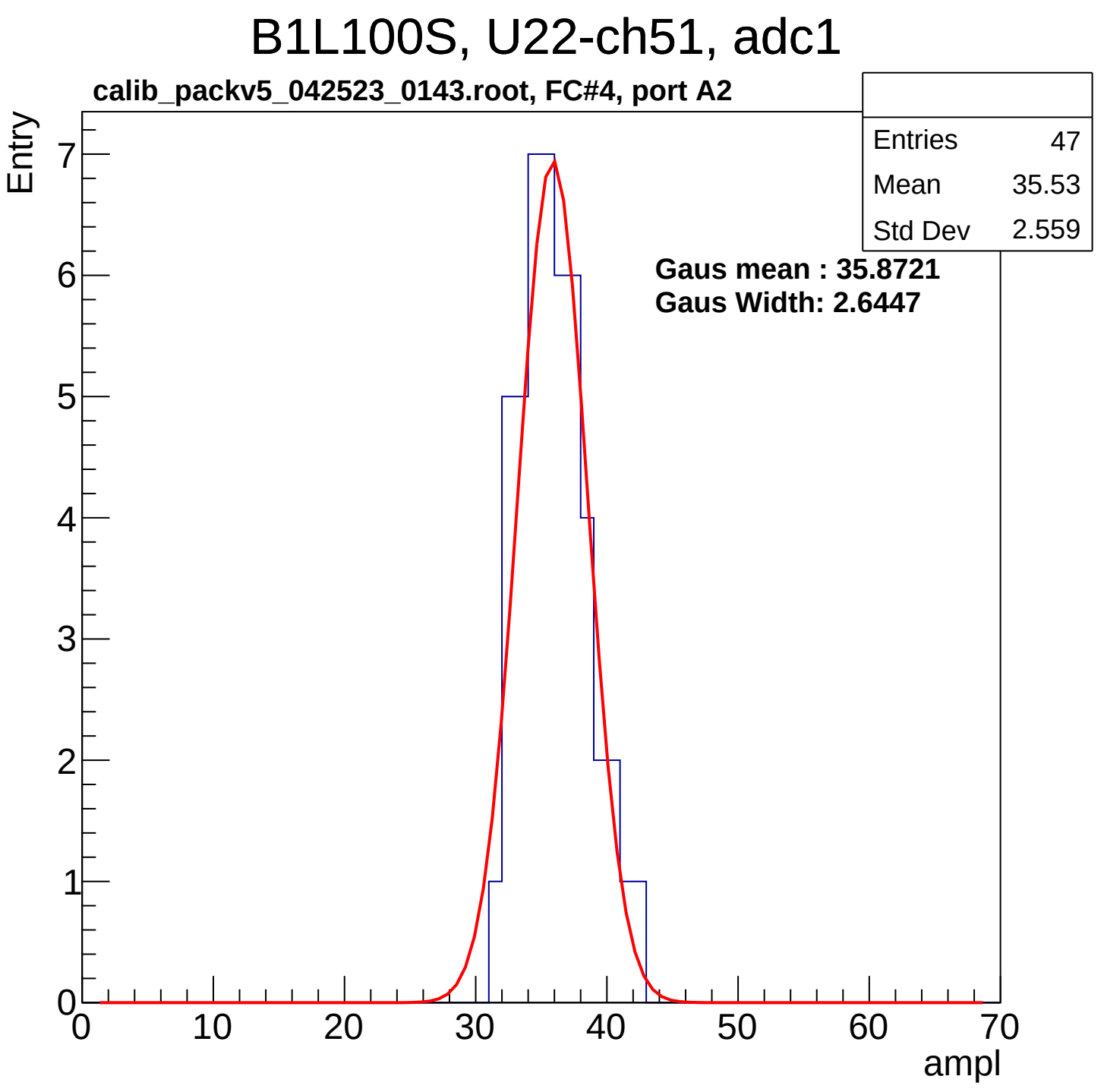
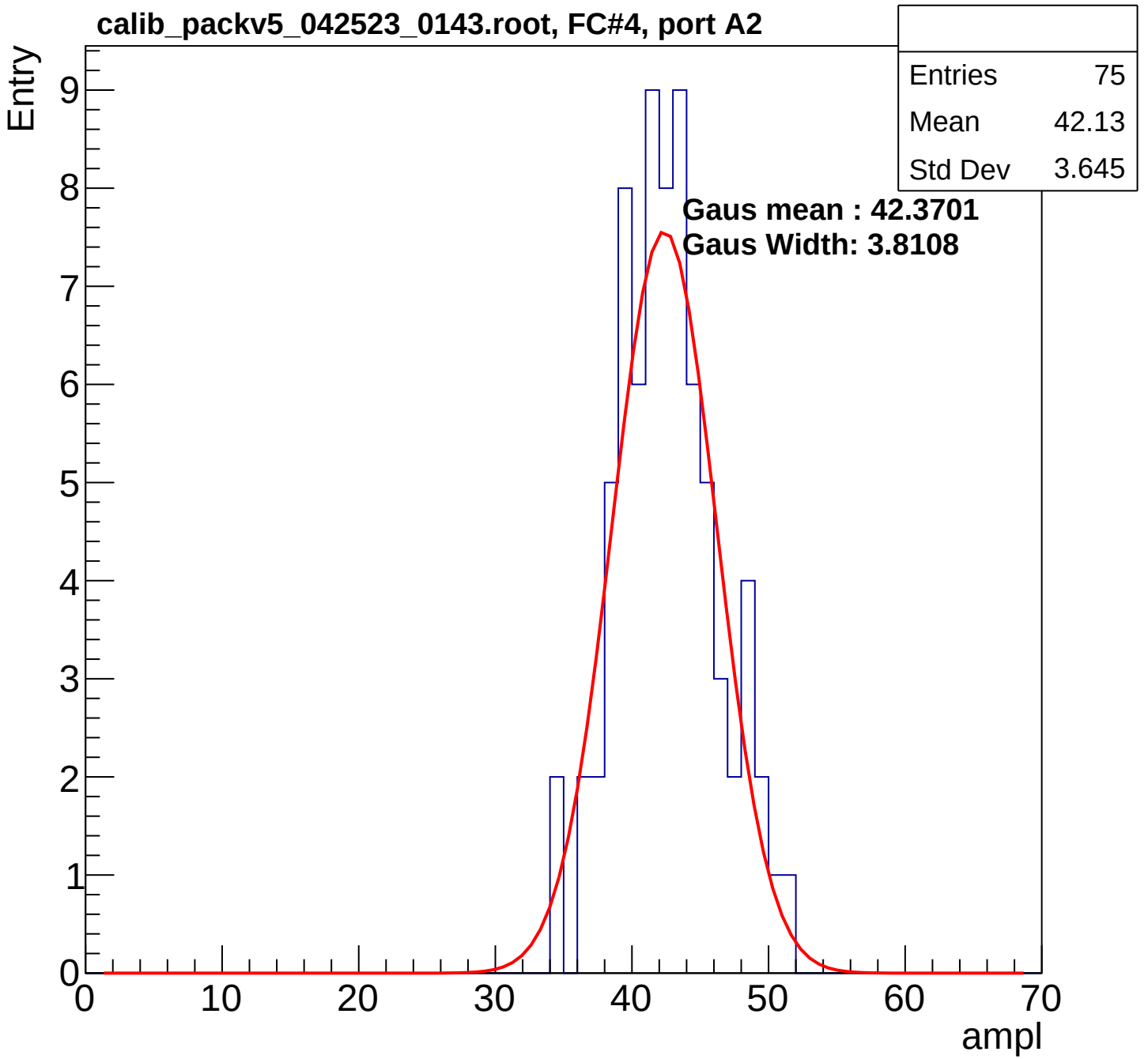
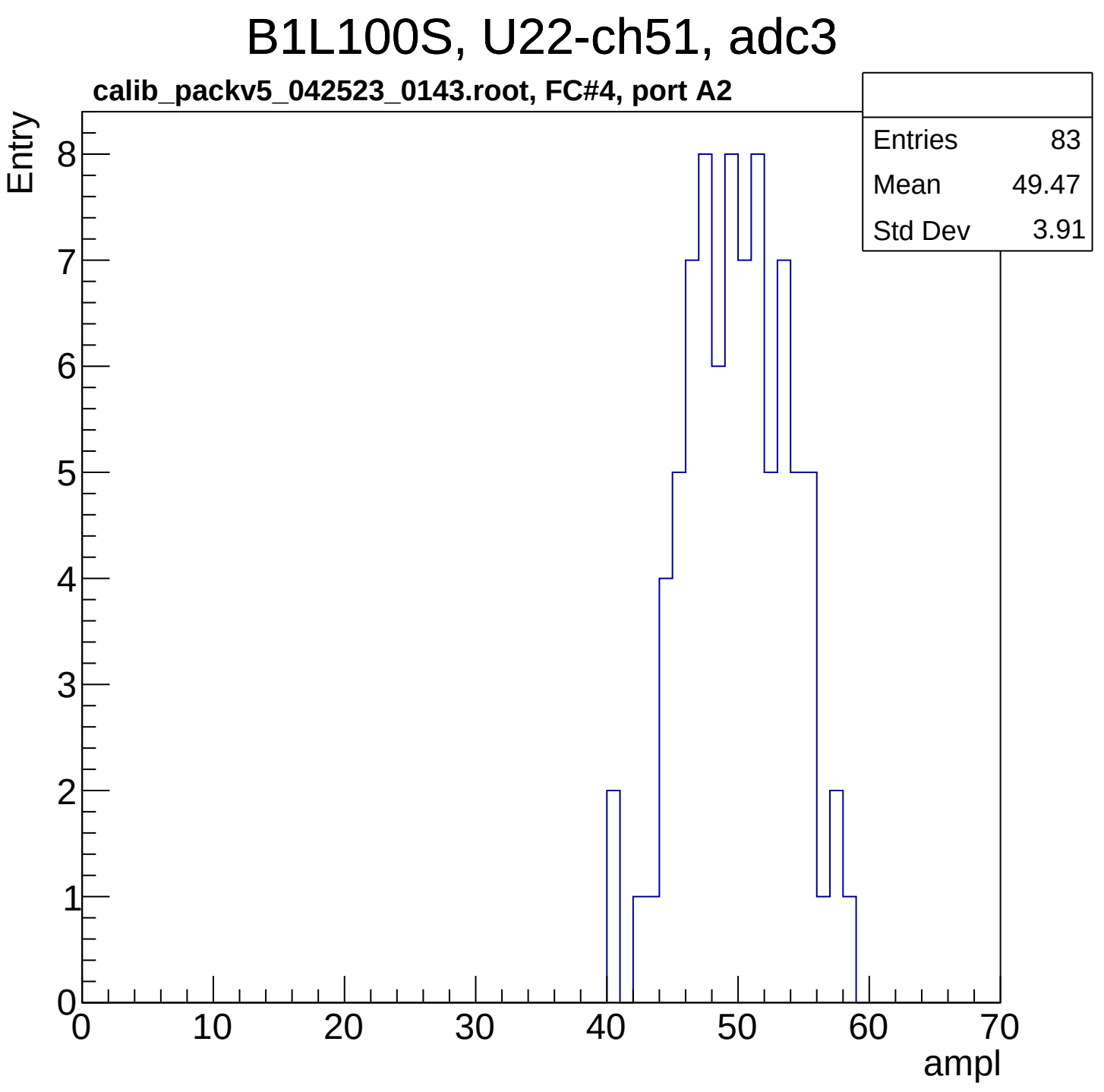
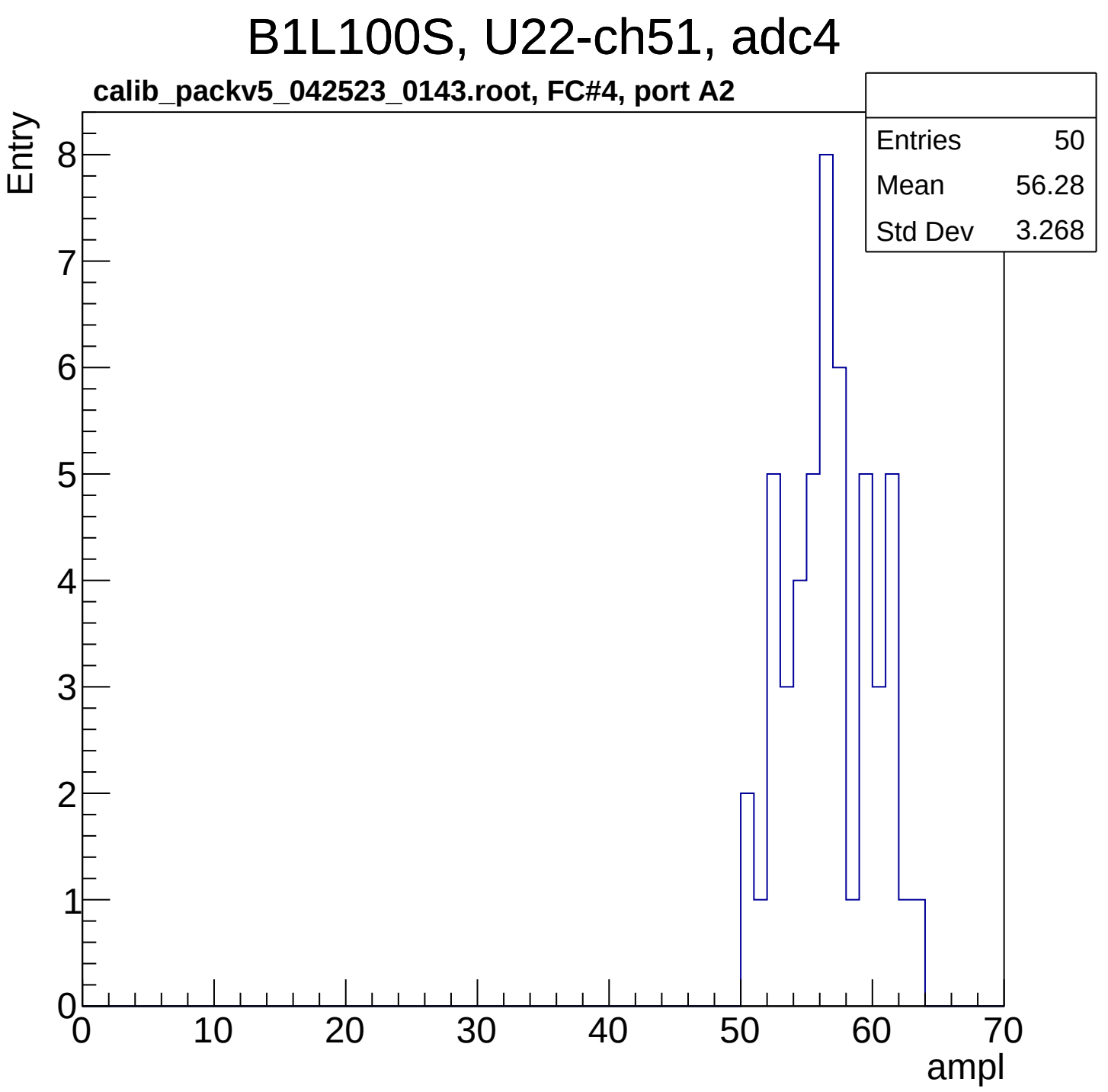
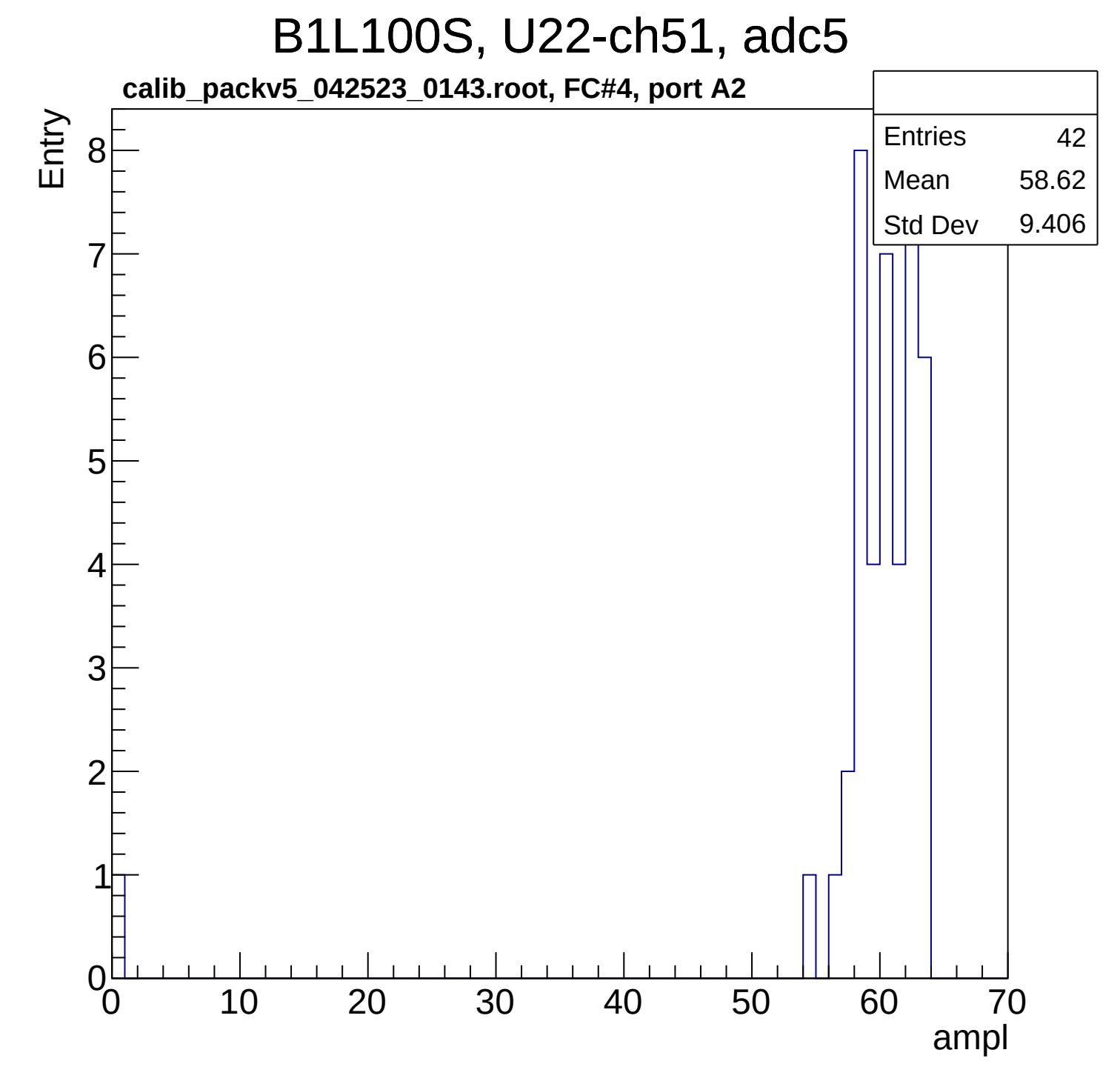
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 35.53
Std Dev 2.559
Gaus mean : 35.8721
Gaus Width: 2.6447
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 42.13
Std Dev 3.645
Gaus mean : 42.3701
Gaus Width: 3.8108
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 49.47
Std Dev 3.91
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 56.28
Std Dev 3.268
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.62
Std Dev 9.406
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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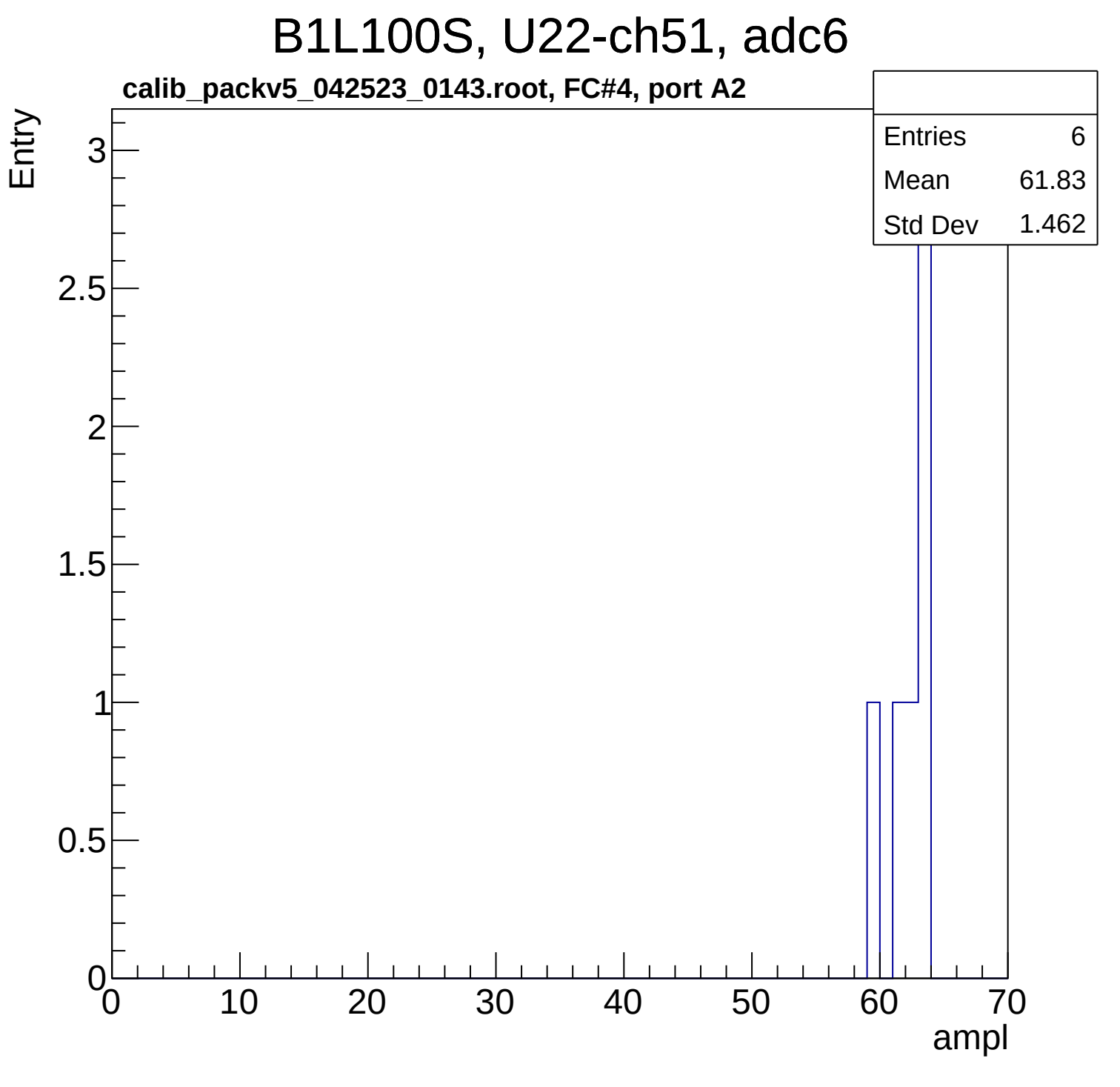
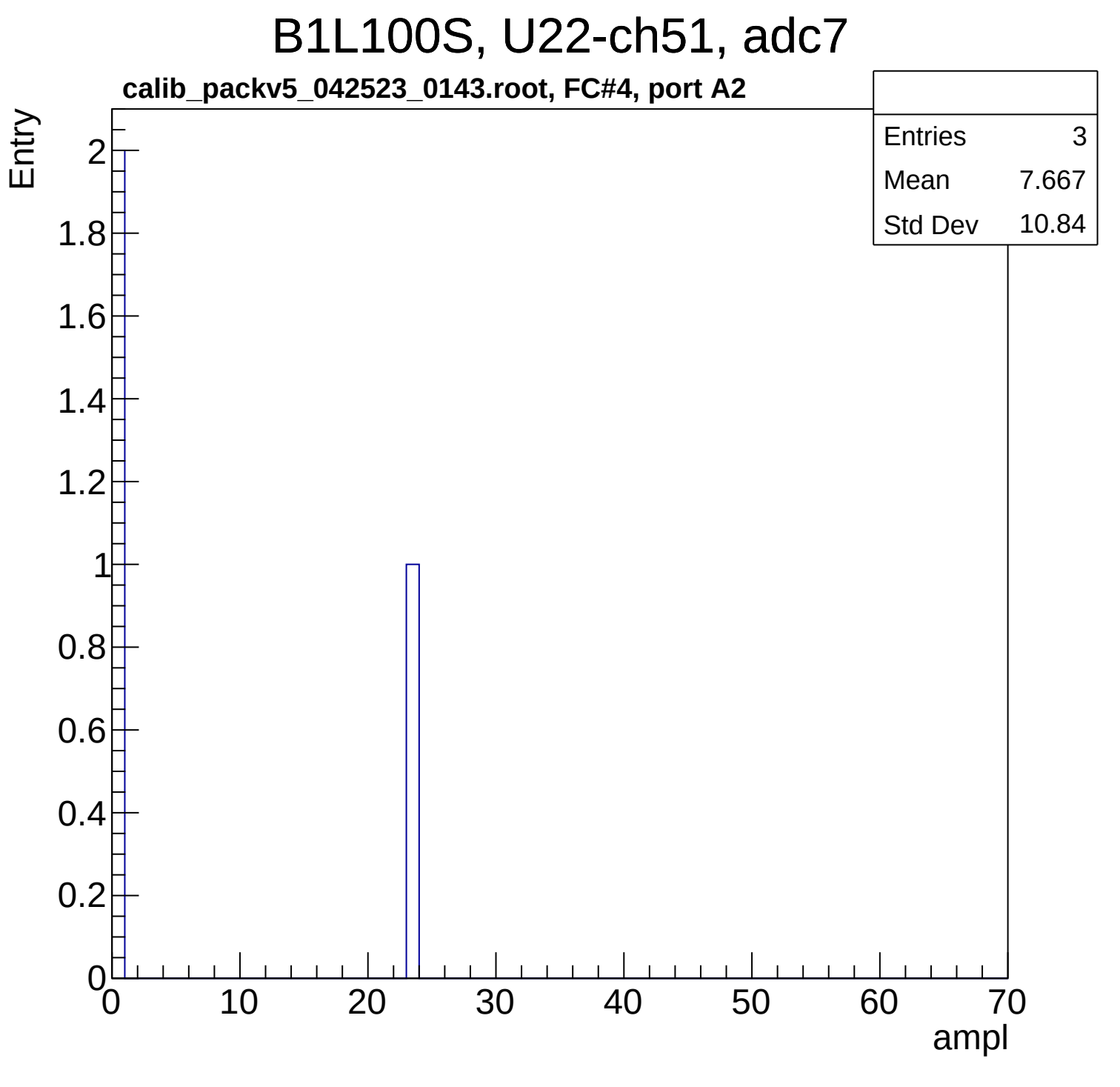
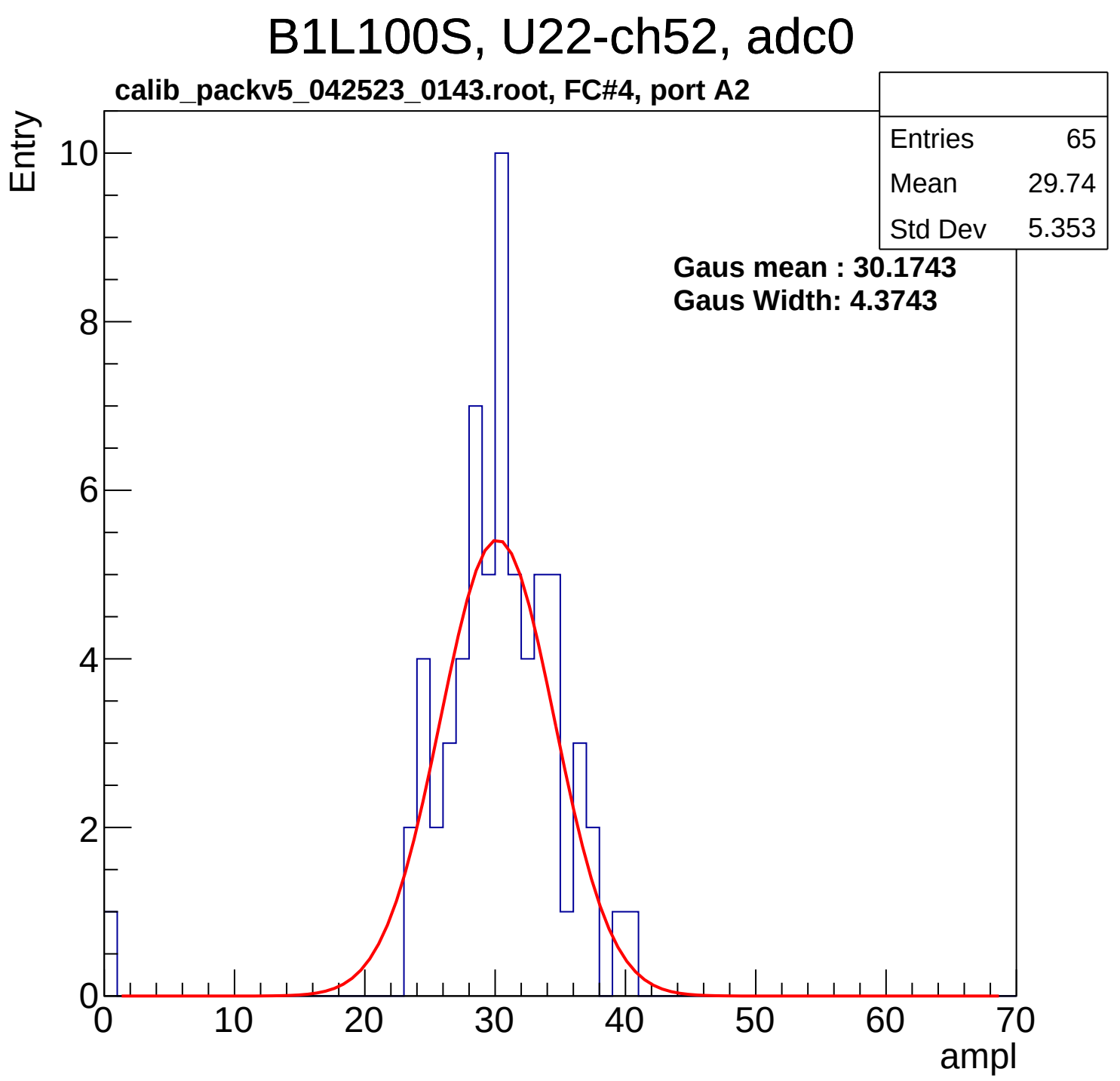
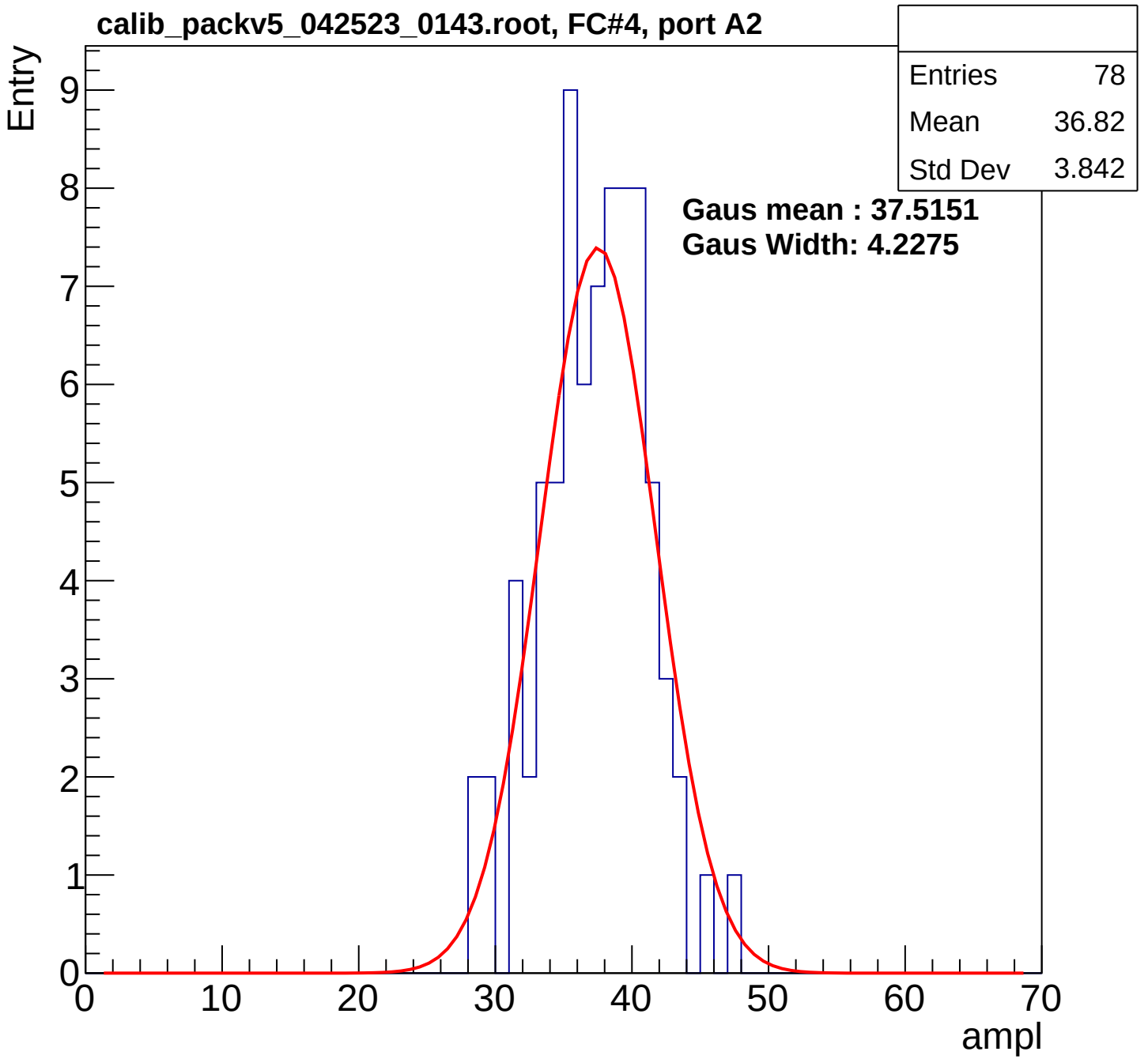
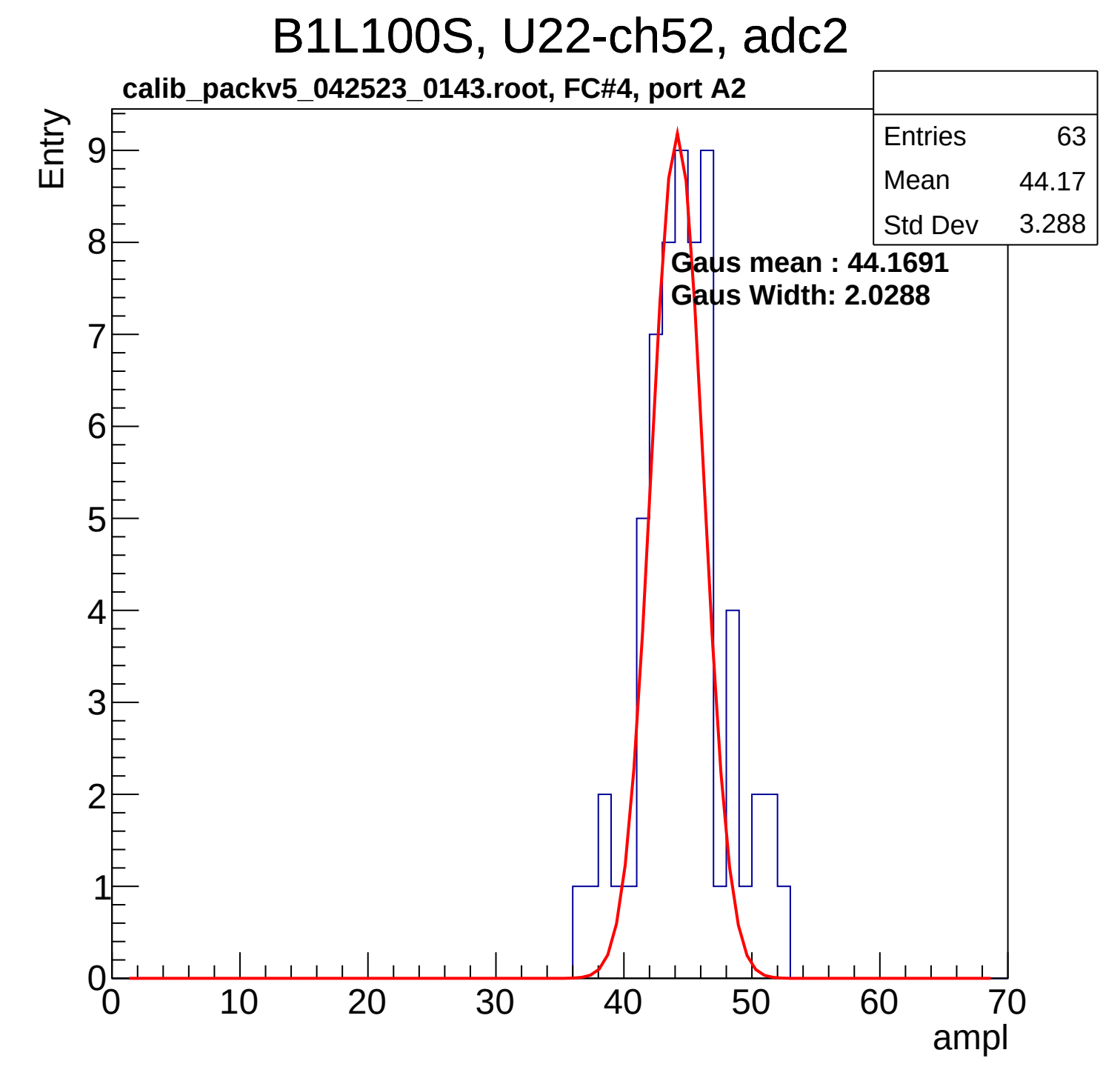
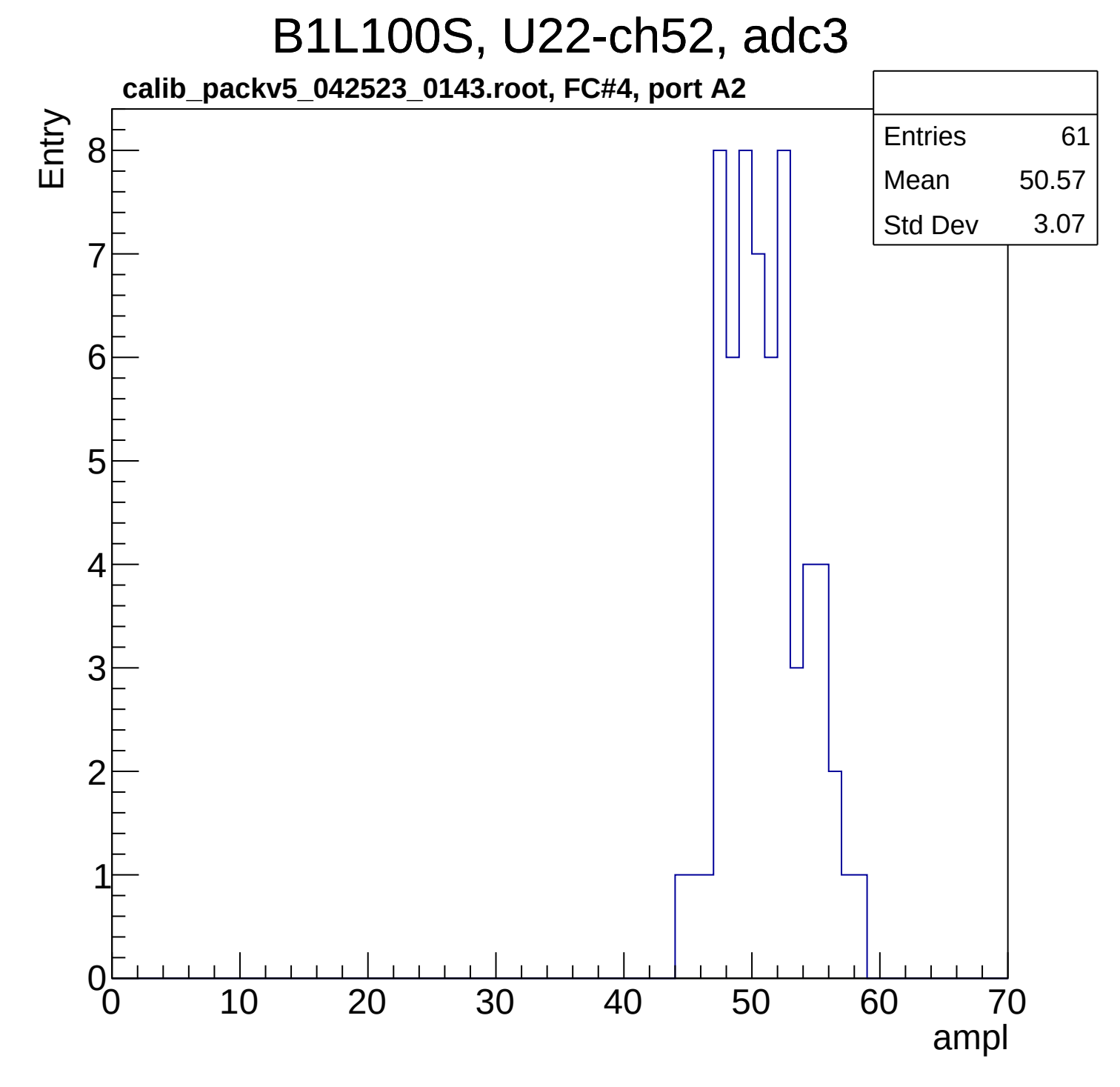

B1L100S, U22-ch51, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.83
Std Dev 1.462
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch51, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 7.667
Std Dev 10.84
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 29.74
Std Dev 5.353
Gaus mean : 30.1743
Gaus Width: 4.3743
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.82
Std Dev 3.842
Gaus mean : 37.5151
Gaus Width: 4.2275
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.17
Std Dev 3.288
Gaus mean : 44.1691
Gaus Width: 2.0288
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 50.57
Std Dev 3.07
Entry
ampl
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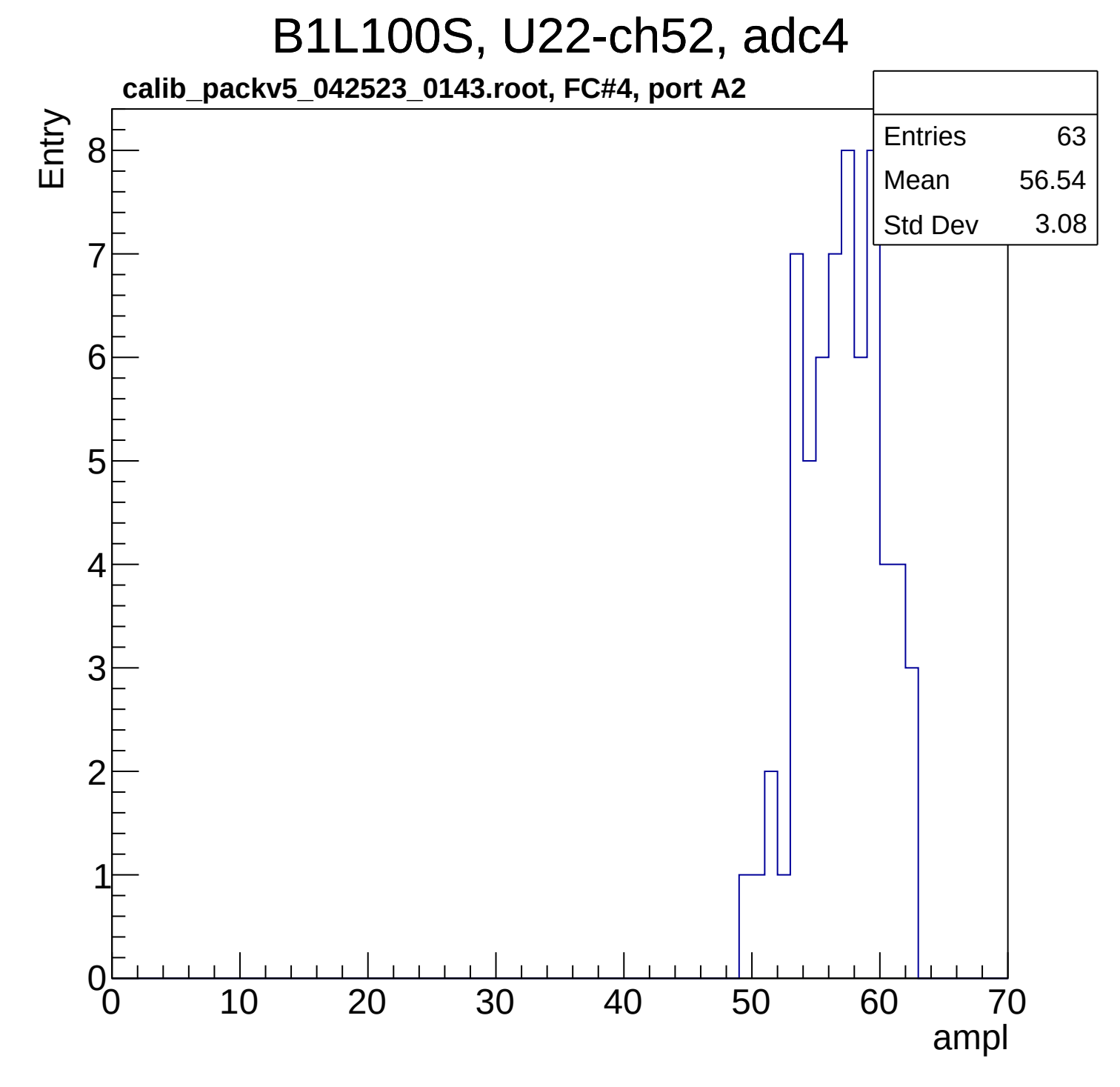
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.54
Std Dev 3.08
Entry
ampl
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2
3
4
5
6
7
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0
10
20
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40
50
60
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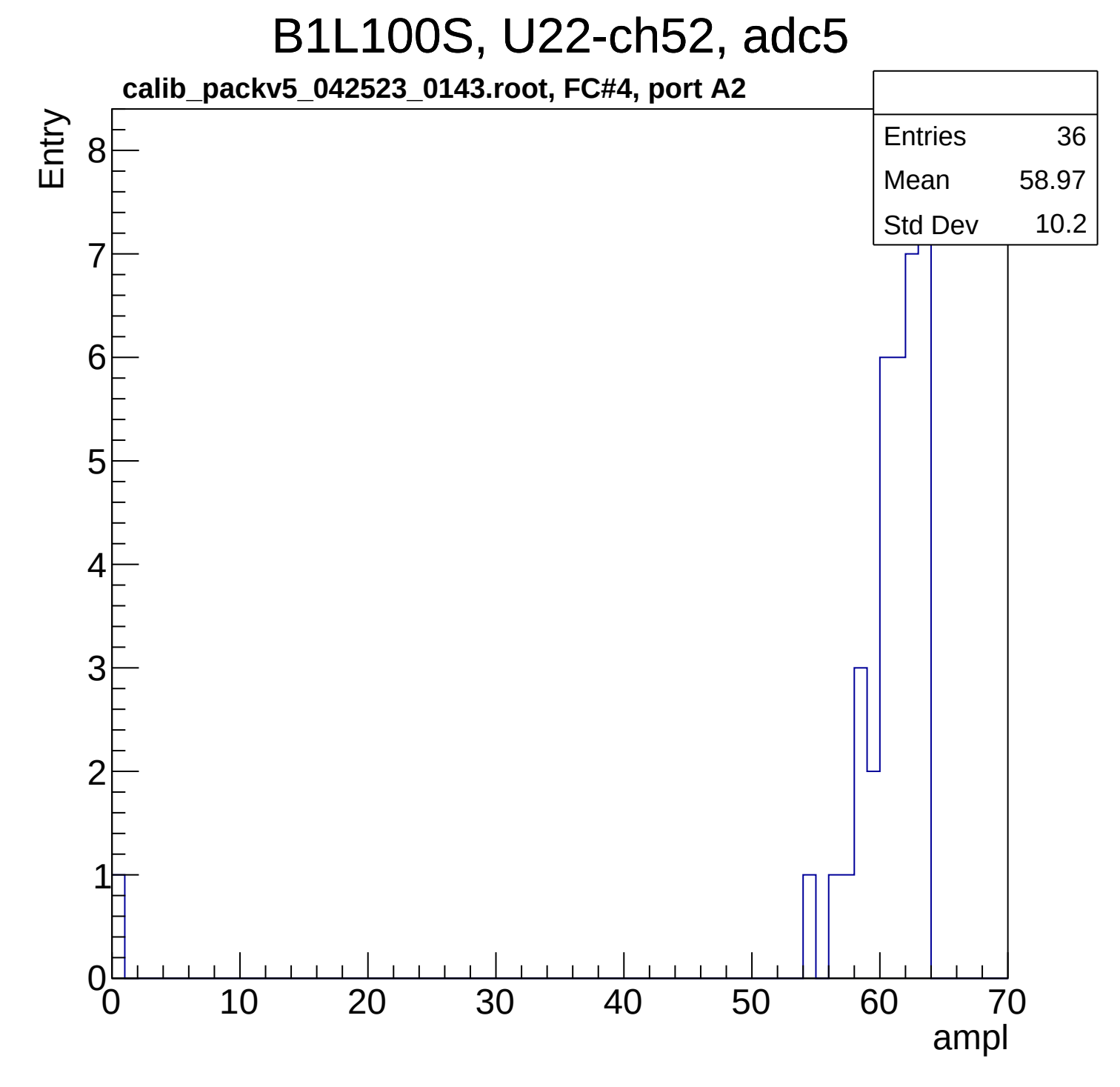
B1L100S, U22-ch52, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.97
Std Dev 10.2
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
30
40
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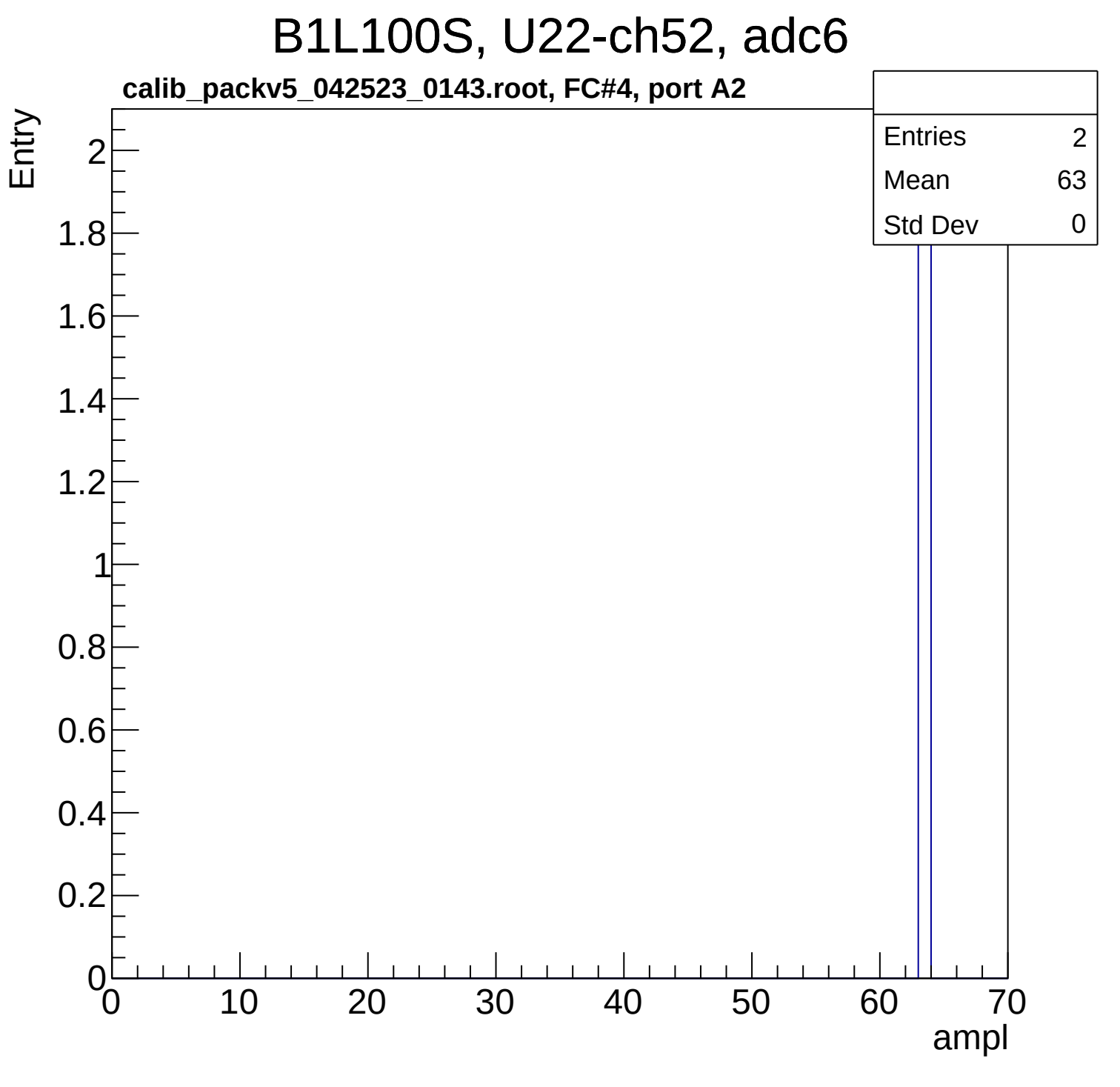
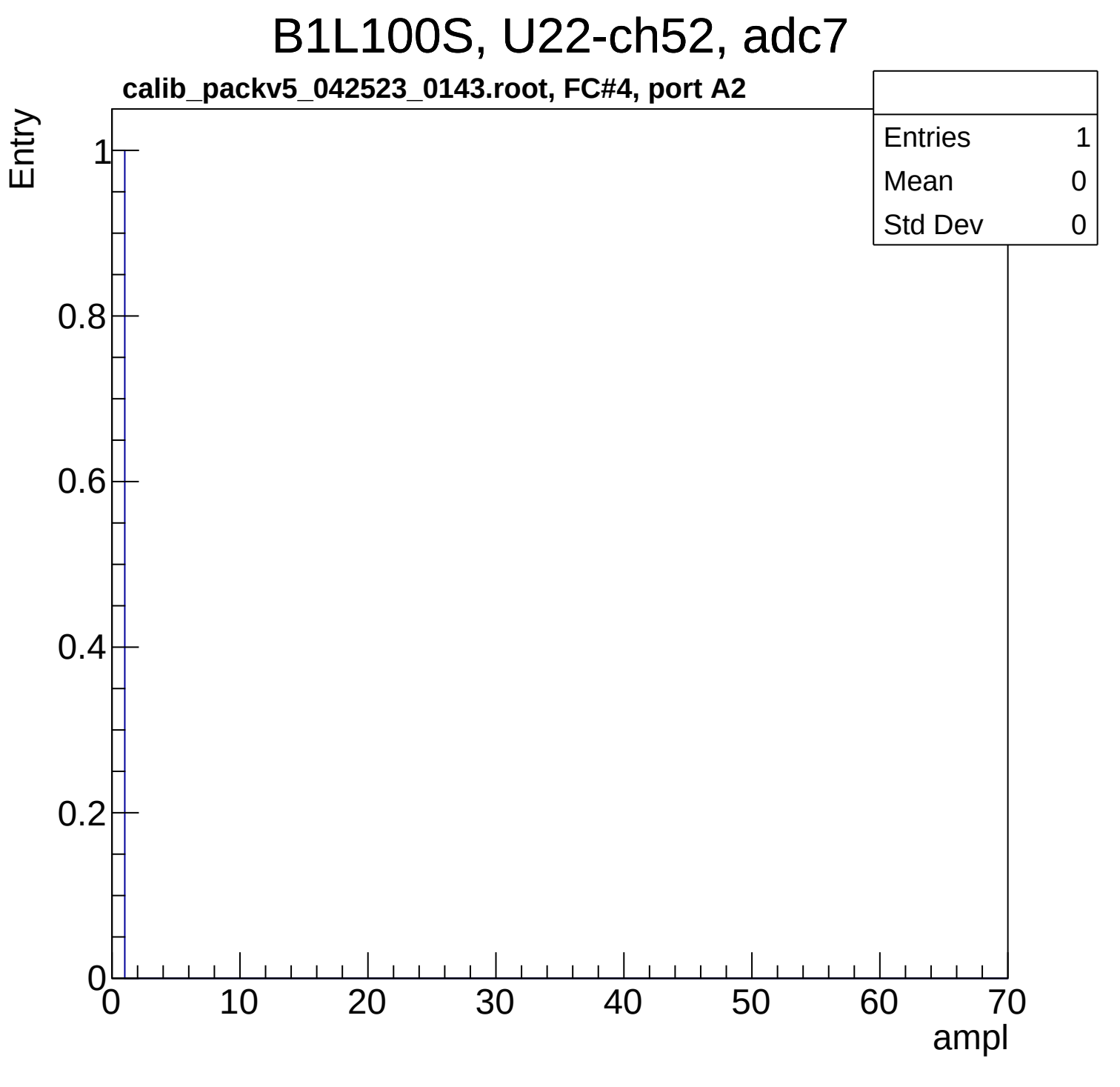
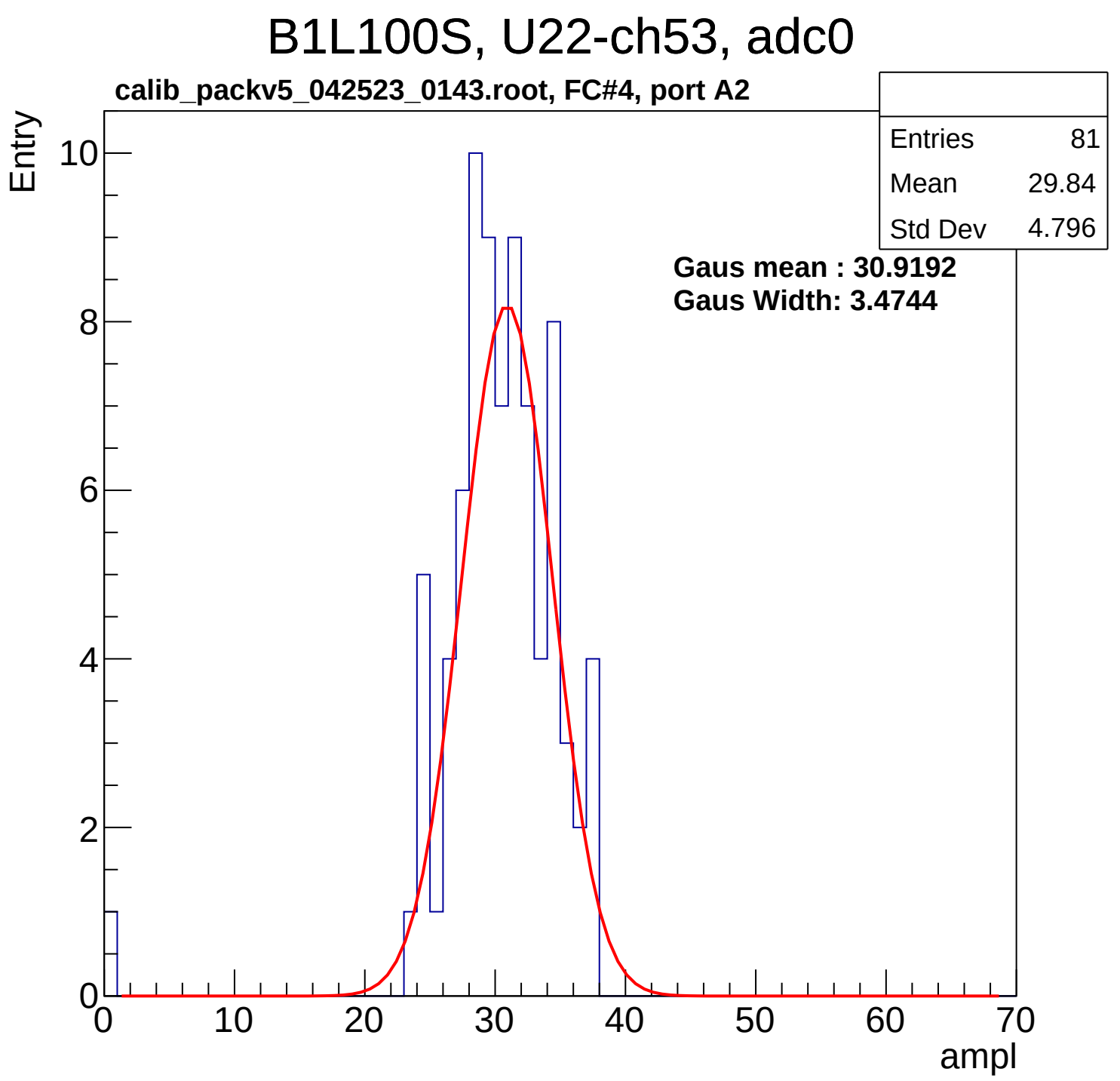
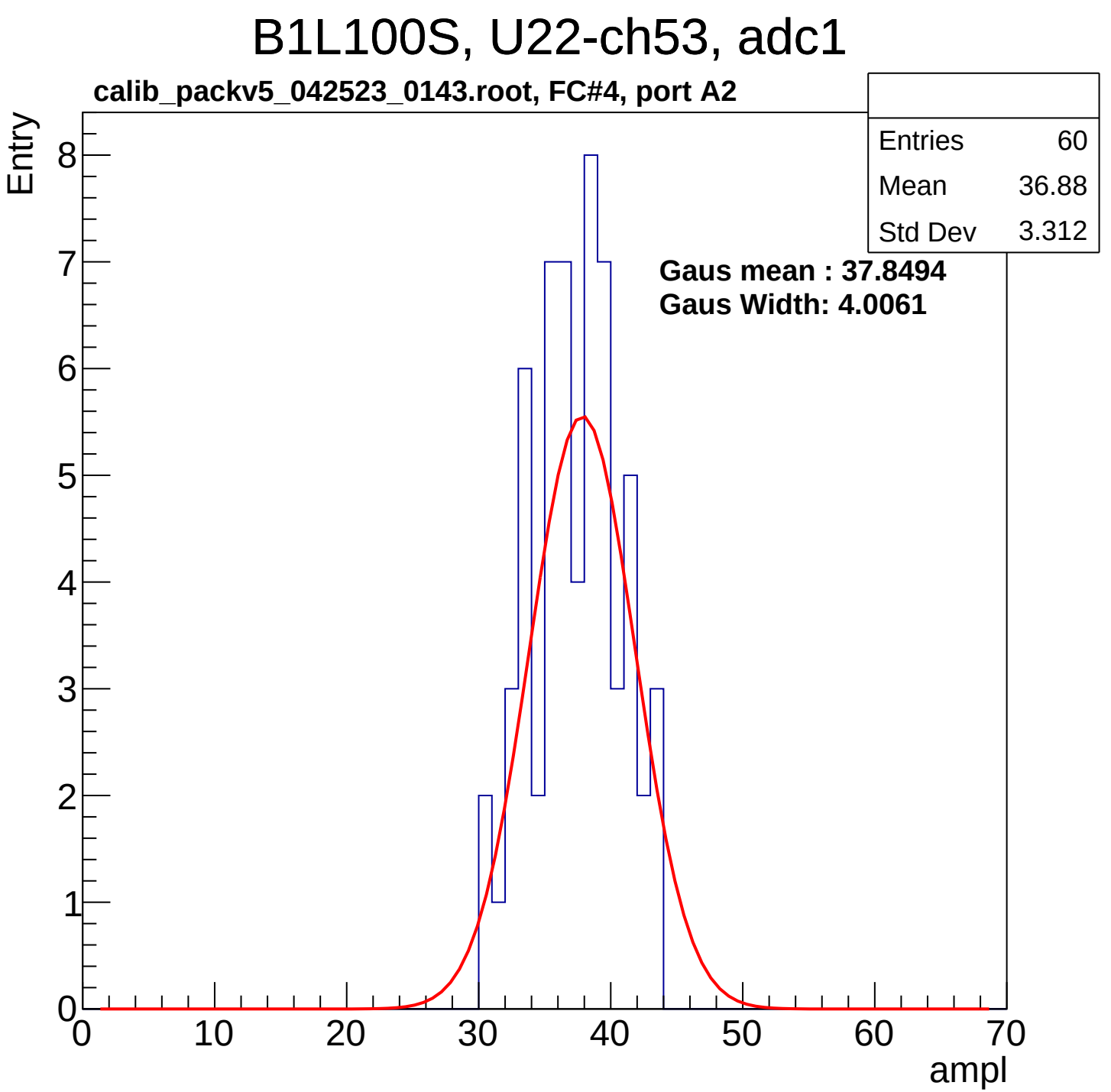
60
70


B1L100S, U22-ch52, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 63
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch52, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch53, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 29.84
Std Dev 4.796
Gaus mean : 30.9192
Gaus Width: 3.4744
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch53, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.88
Std Dev 3.312
Gaus mean : 37.8494
Gaus Width: 4.0061
Entry
ampl
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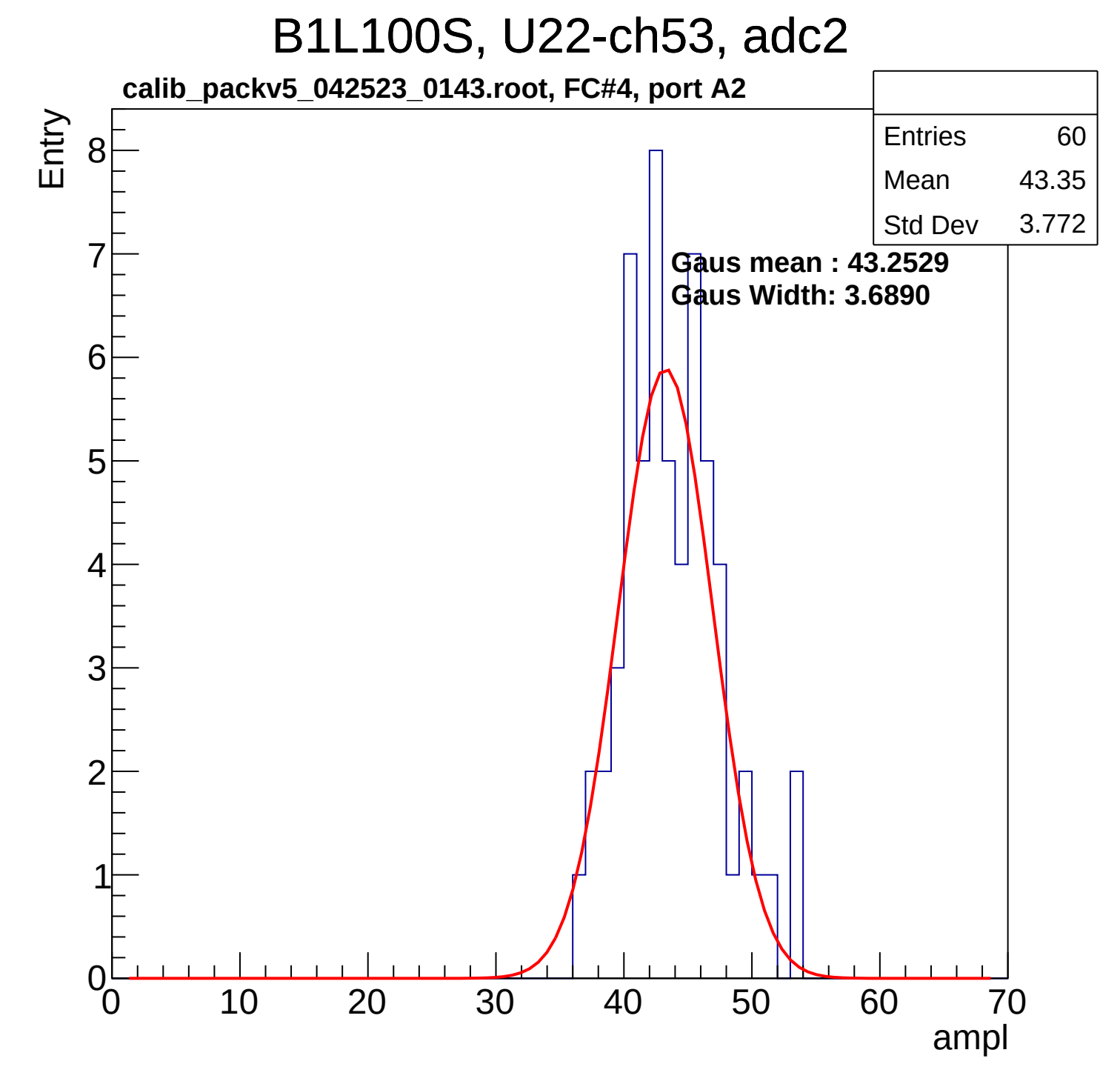
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch53, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.35
Std Dev 3.772
Gaus mean : 43.2529
Gaus Width: 3.6890
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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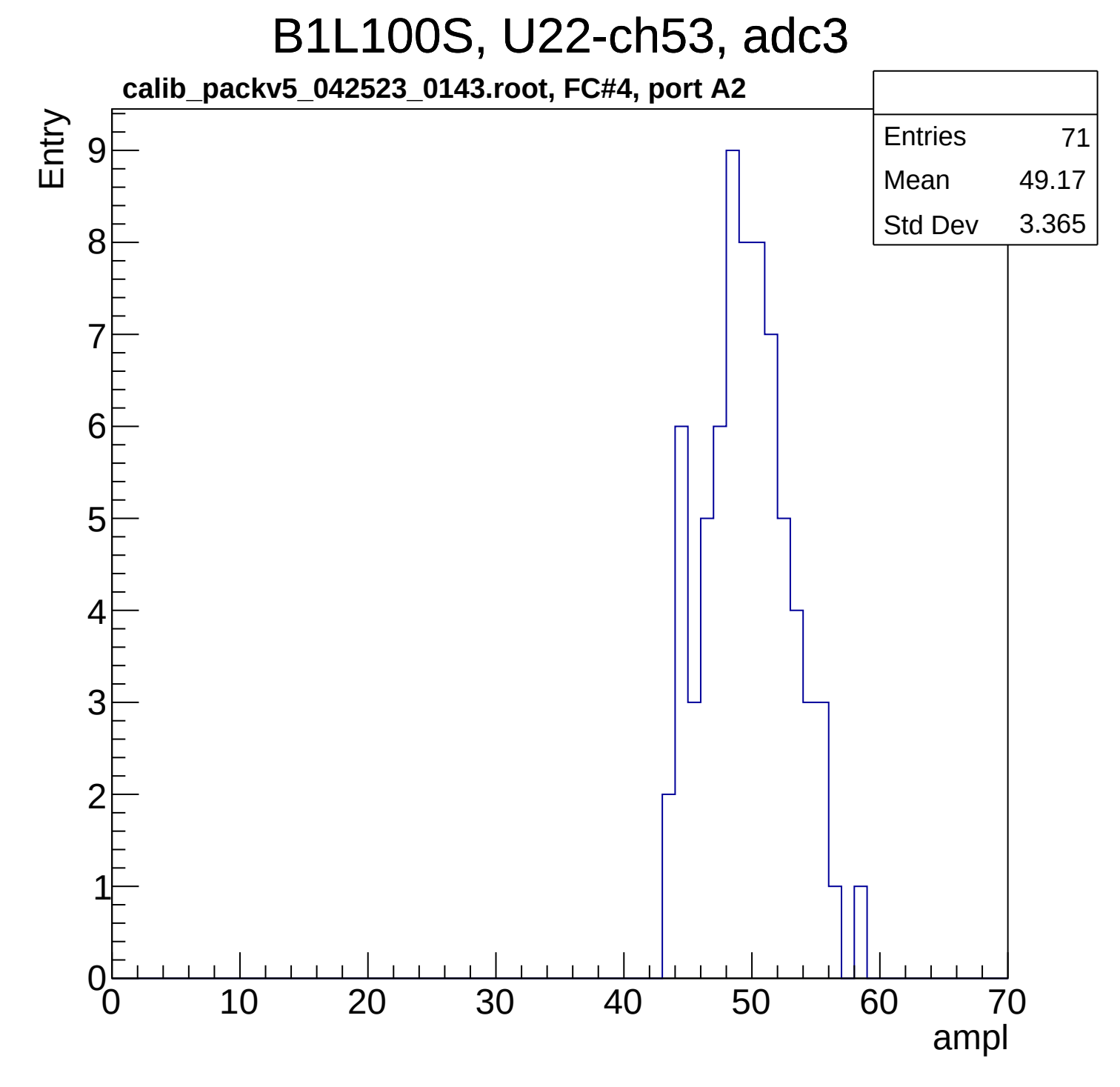
B1L100S, U22-ch53, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 49.17
Std Dev 3.365
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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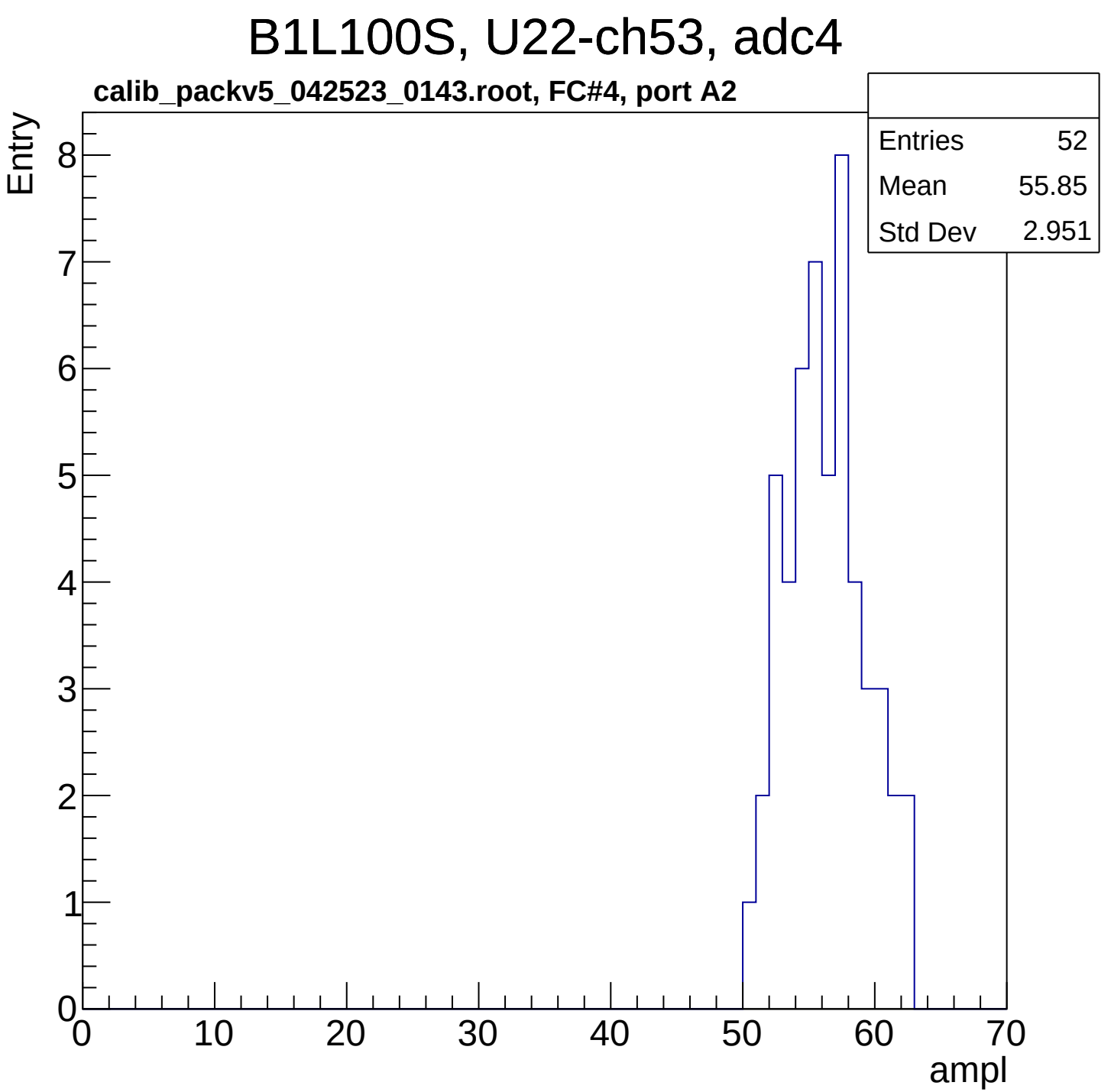
40
50
60
70


B1L100S, U22-ch53, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.85
Std Dev 2.951
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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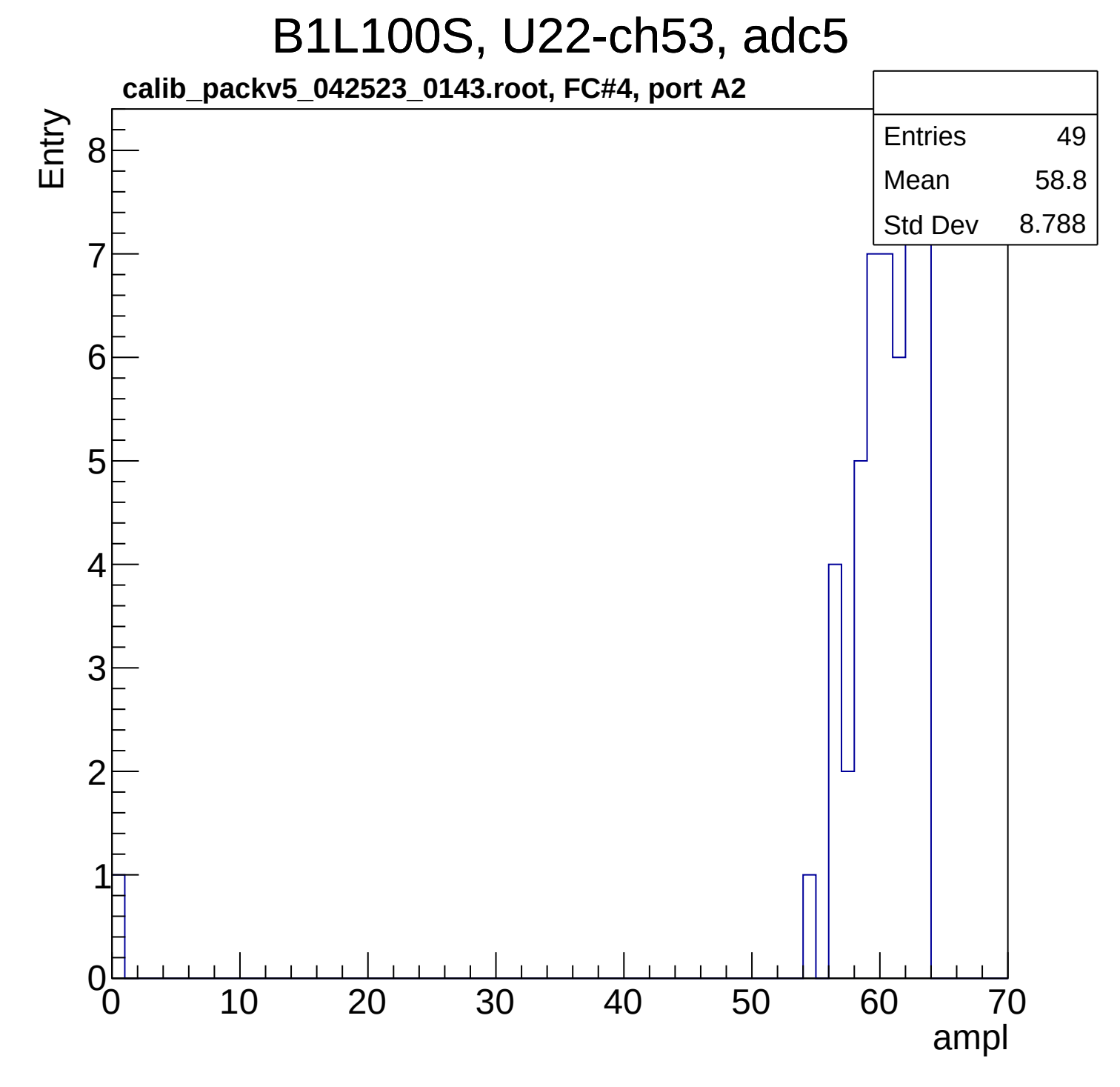
B1L100S, U22-ch53, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 58.8
Std Dev 8.788
Entry
ampl
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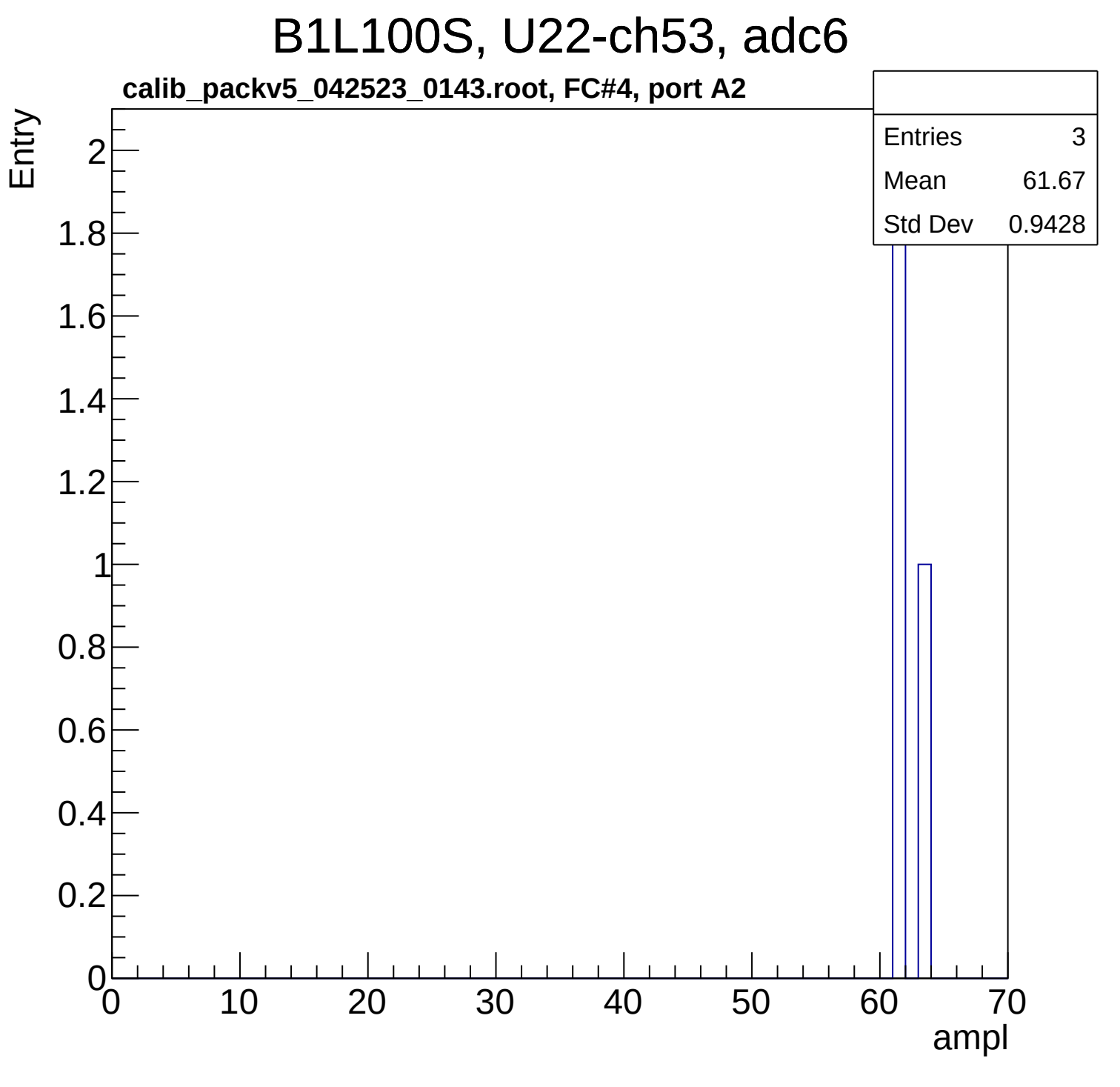
B1L100S, U22-ch53, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
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1.2
1.4
1.6
1.8
2
0
10
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40
50
60
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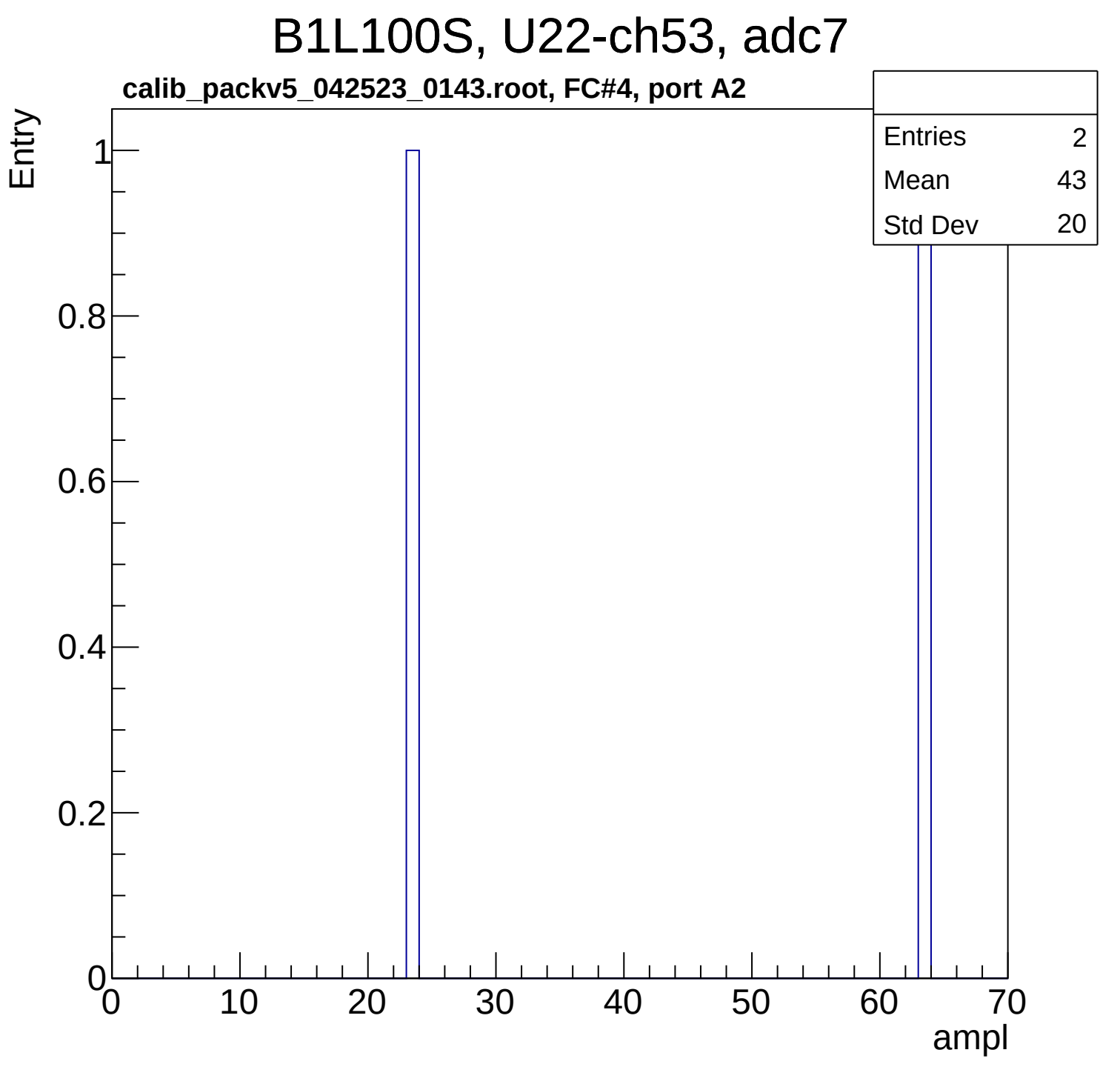
B1L100S, U22-ch53, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 43
Std Dev 20
Entry
ampl
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0.4
0.6
0.8
1
0
10
20
30
40
50
60
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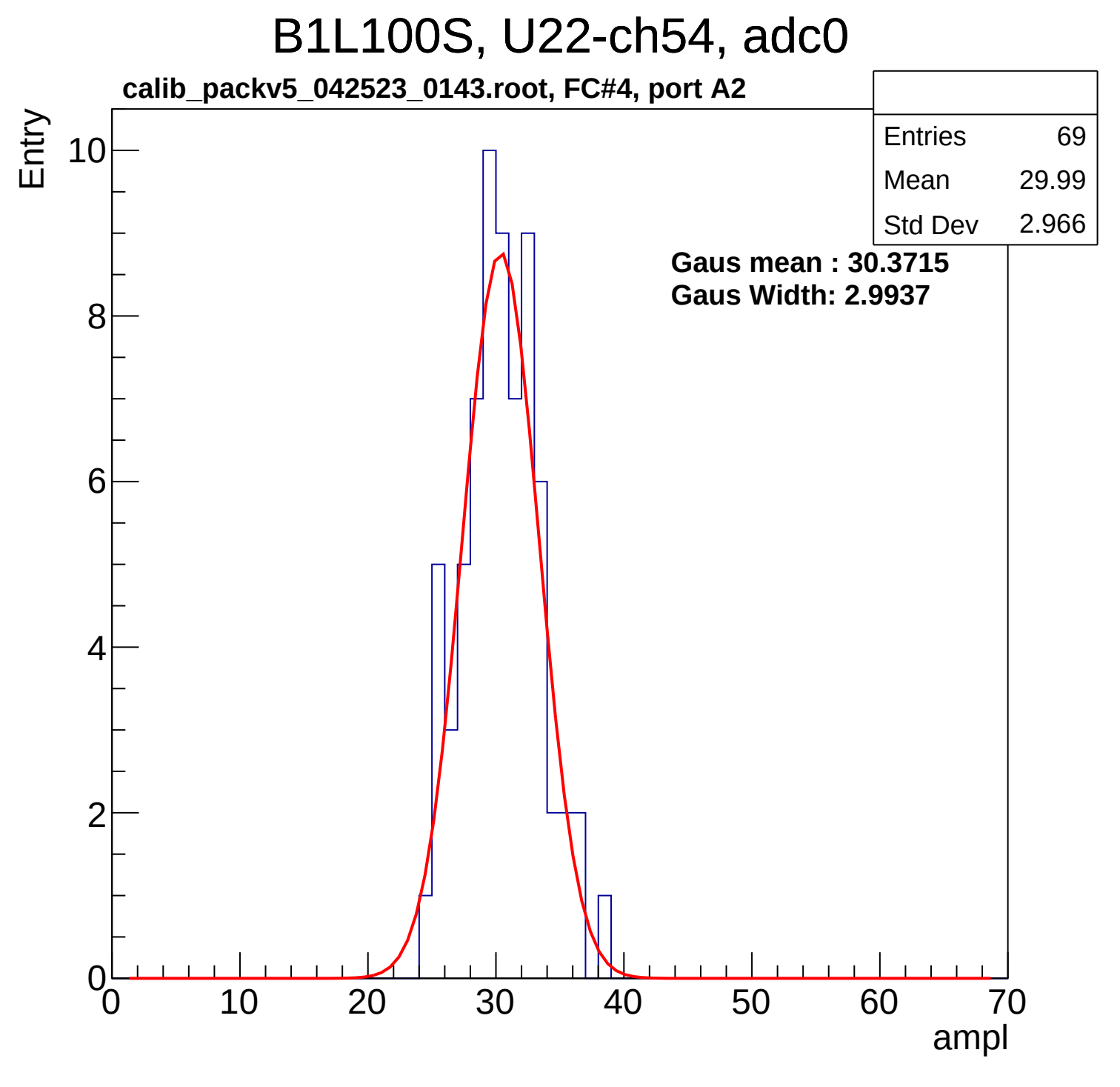
B1L100S, U22-ch54, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.99
Std Dev 2.966
Gaus mean : 30.3715
Gaus Width: 2.9937
Entry
ampl
0
2
4
6
8
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0
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20
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40
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60
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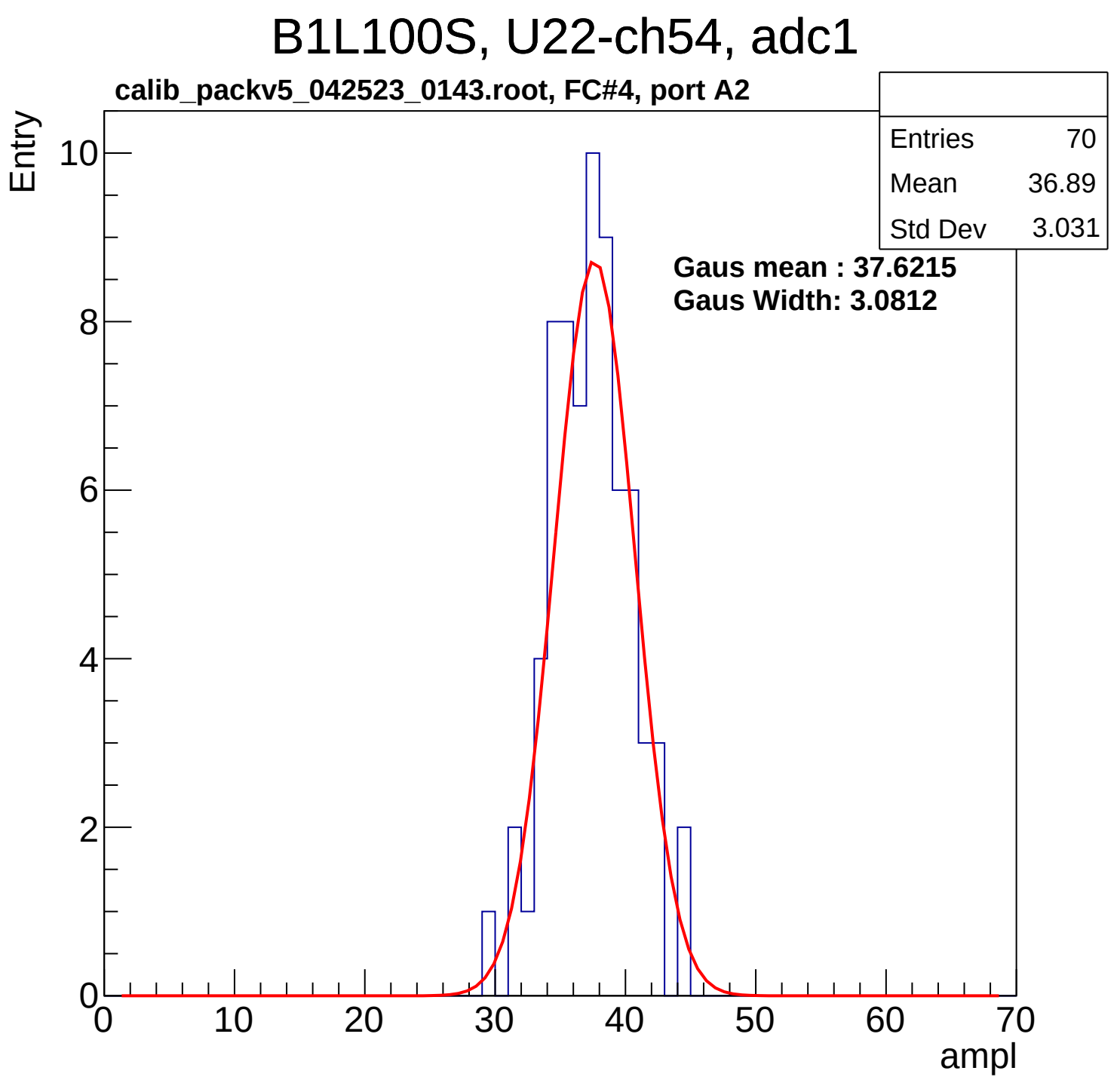
B1L100S, U22-ch54, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.89
Std Dev 3.031
Gaus mean : 37.6215
Gaus Width: 3.0812
Entry
ampl
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4
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0
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60
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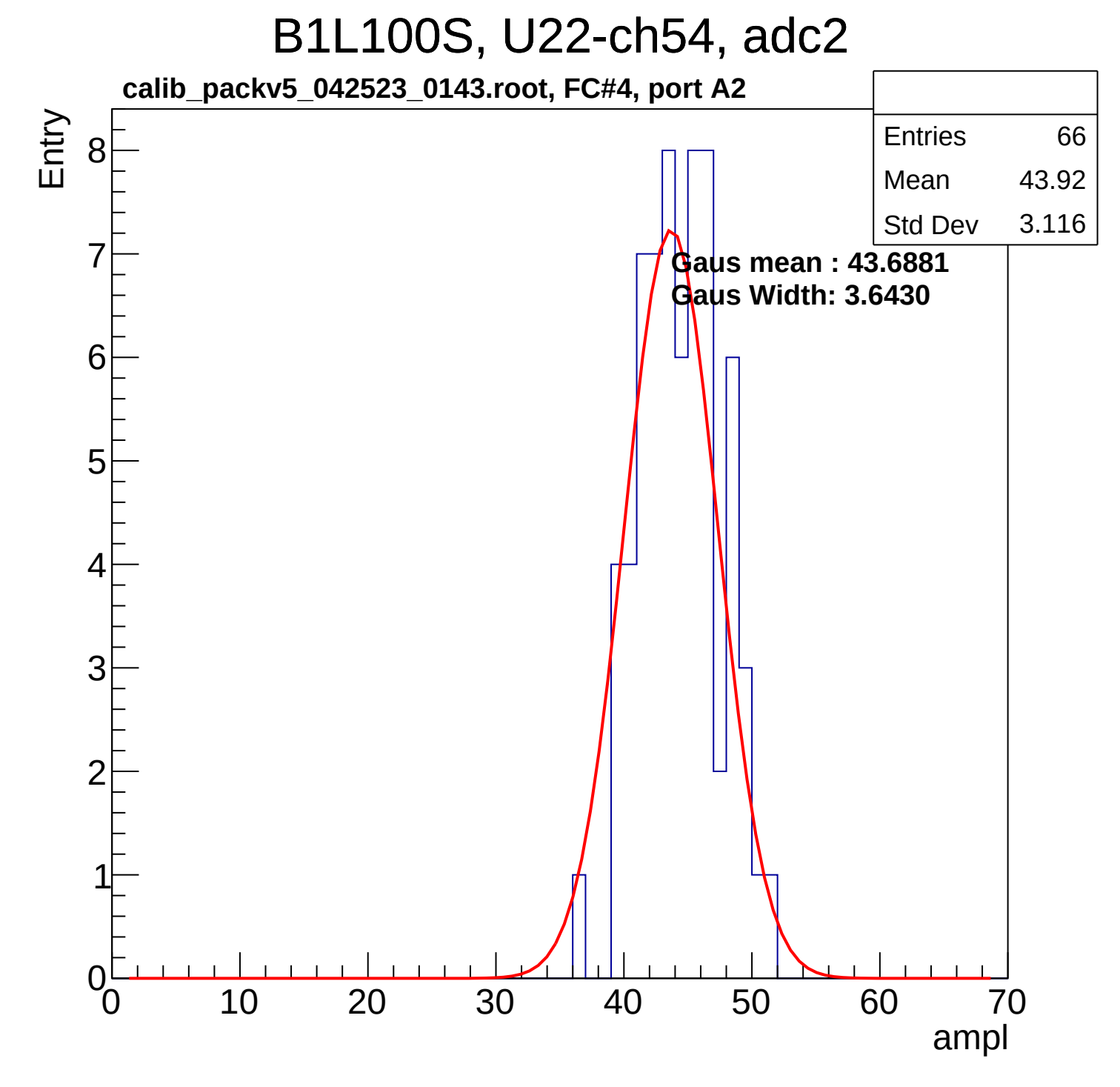
B1L100S, U22-ch54, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.92
Std Dev 3.116
Gaus mean : 43.6881
Gaus Width: 3.6430
Entry
ampl
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1
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3
4
5
6
7
8
0
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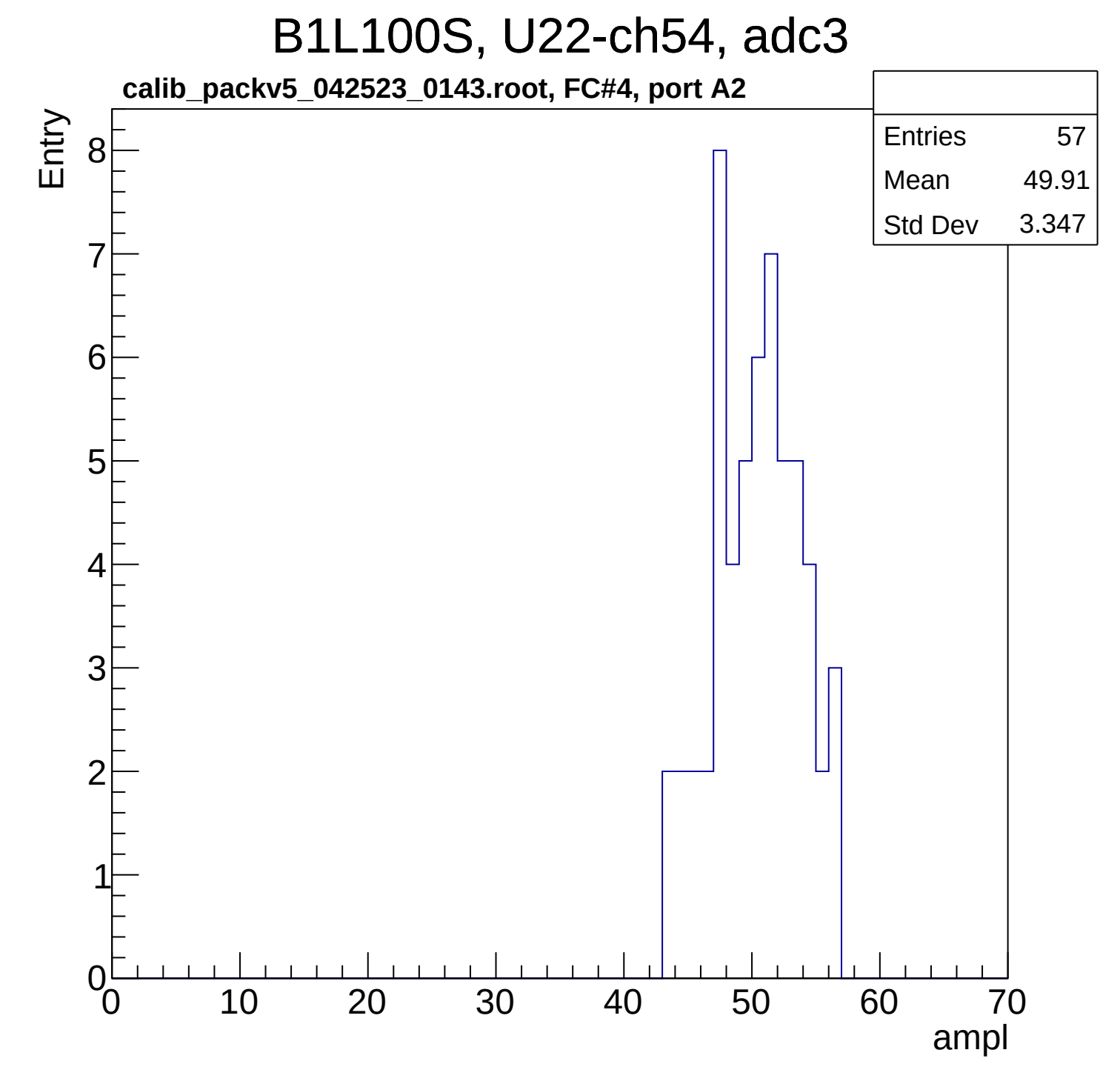
B1L100S, U22-ch54, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.91
Std Dev 3.347
Entry
ampl
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1
2
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4
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40
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60
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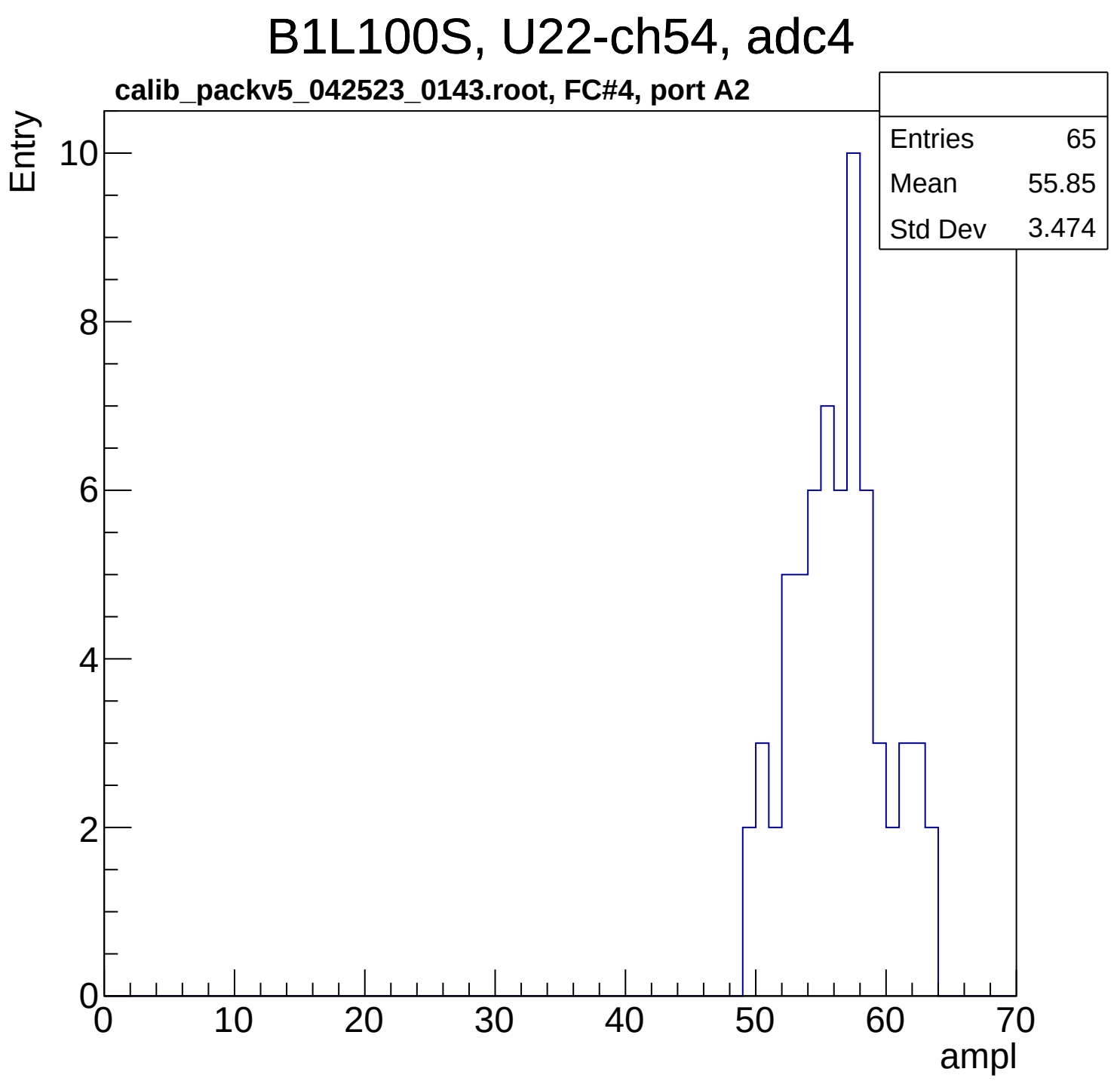
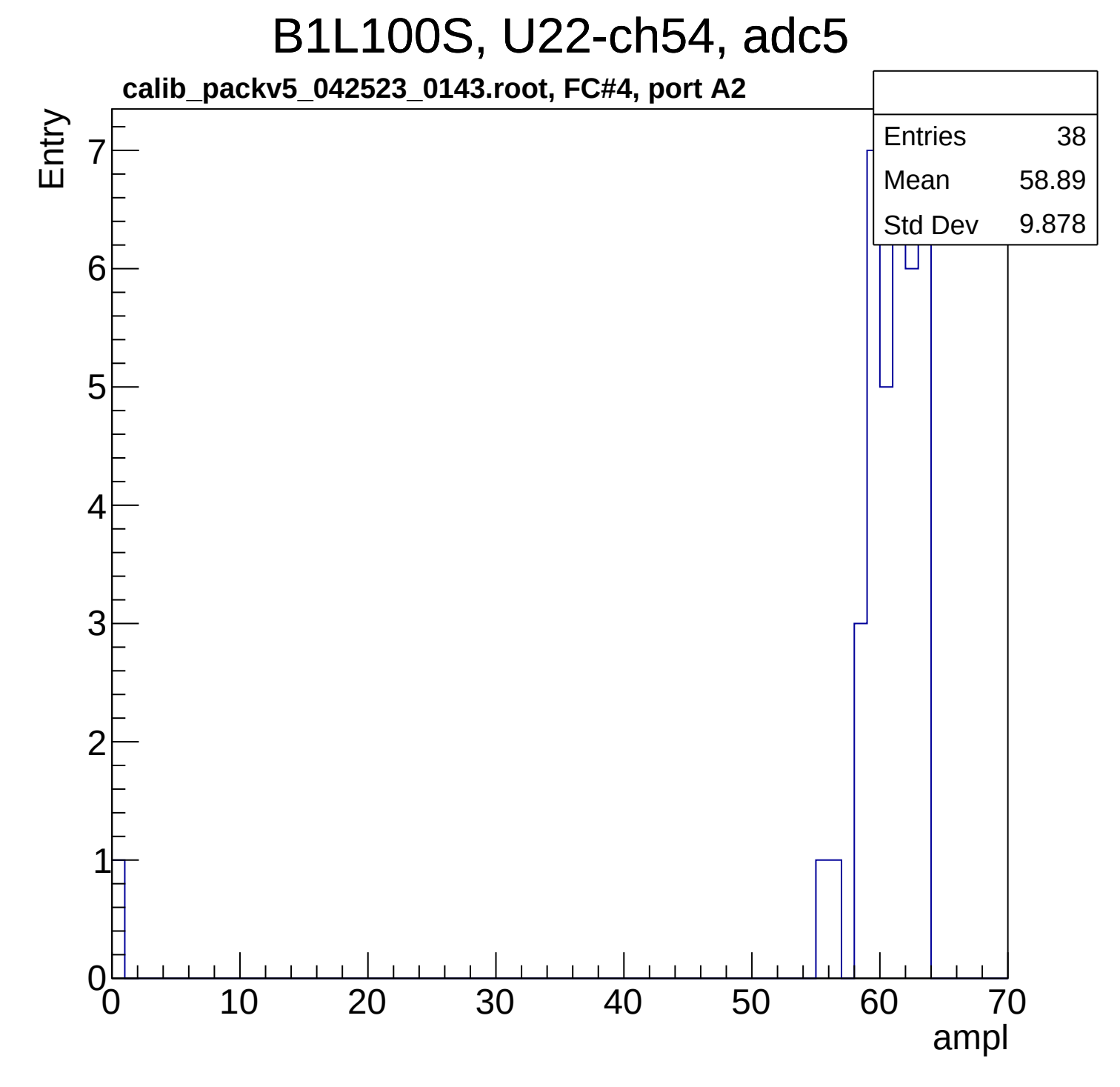

B1L100S, U22-ch54, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.85
Std Dev 3.474
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch54, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.89
Std Dev 9.878
Entry
ampl
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1
2
3
4
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0
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20
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60
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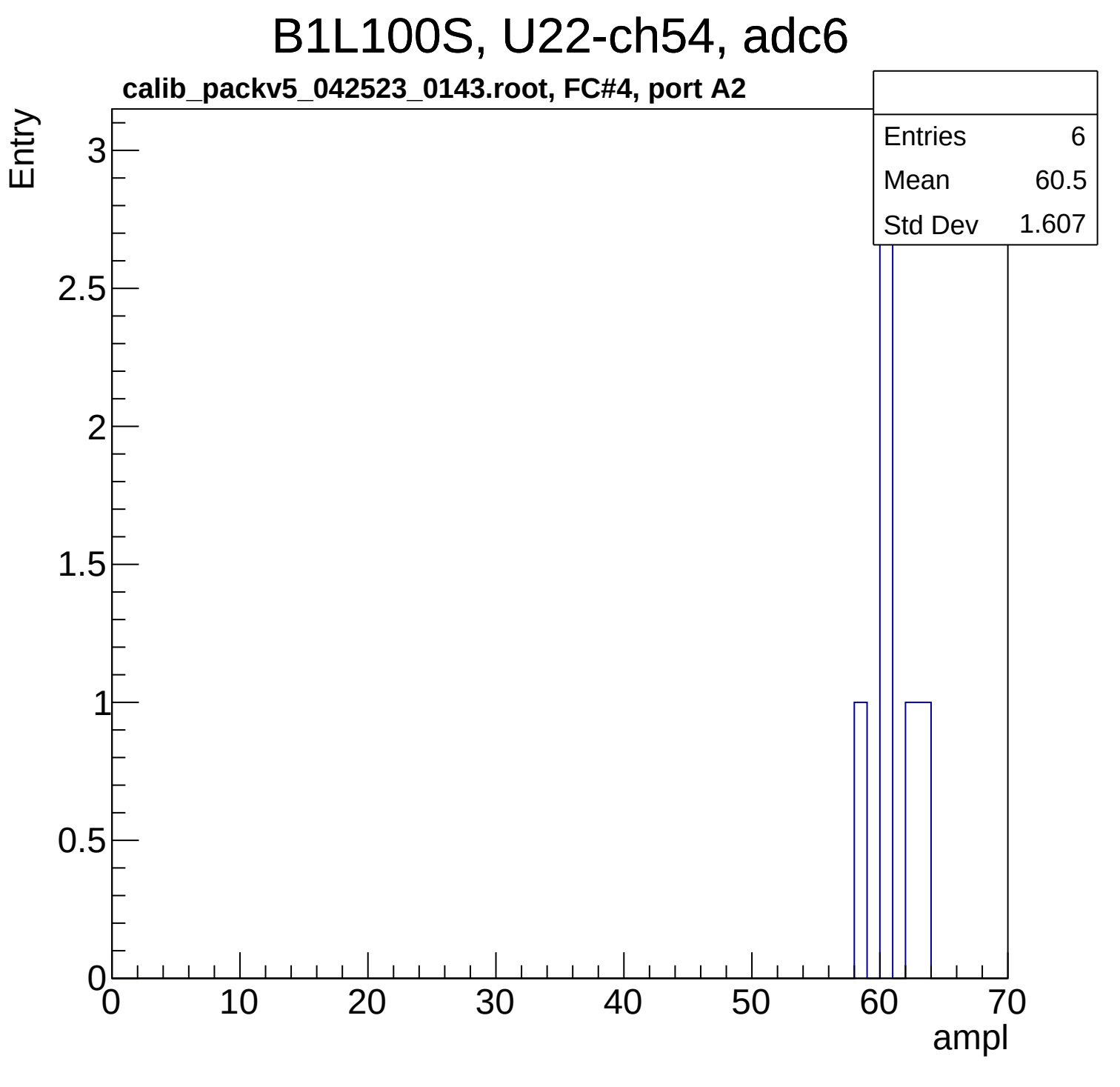
B1L100S, U22-ch54, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 60.5
Std Dev 1.607
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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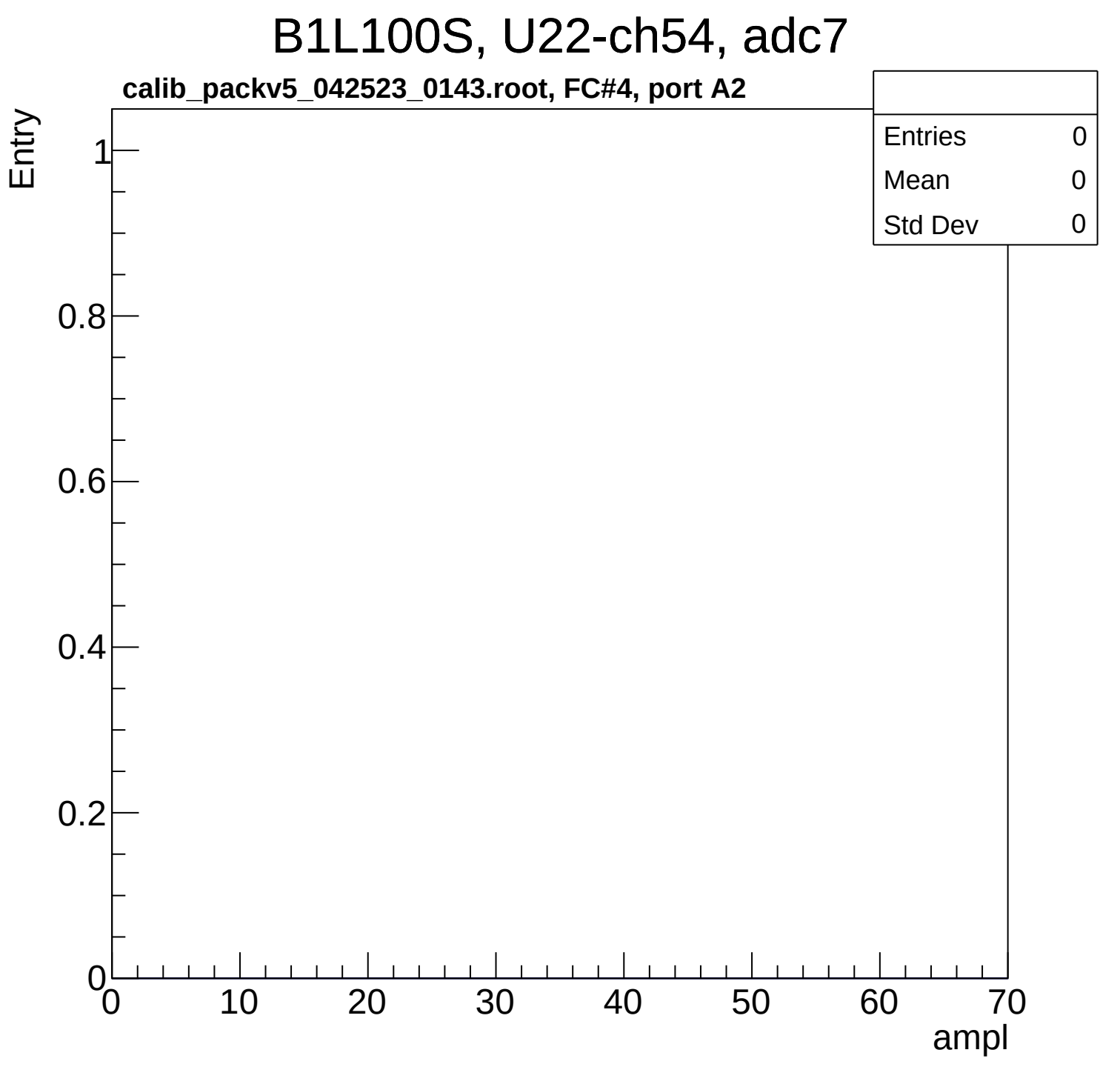
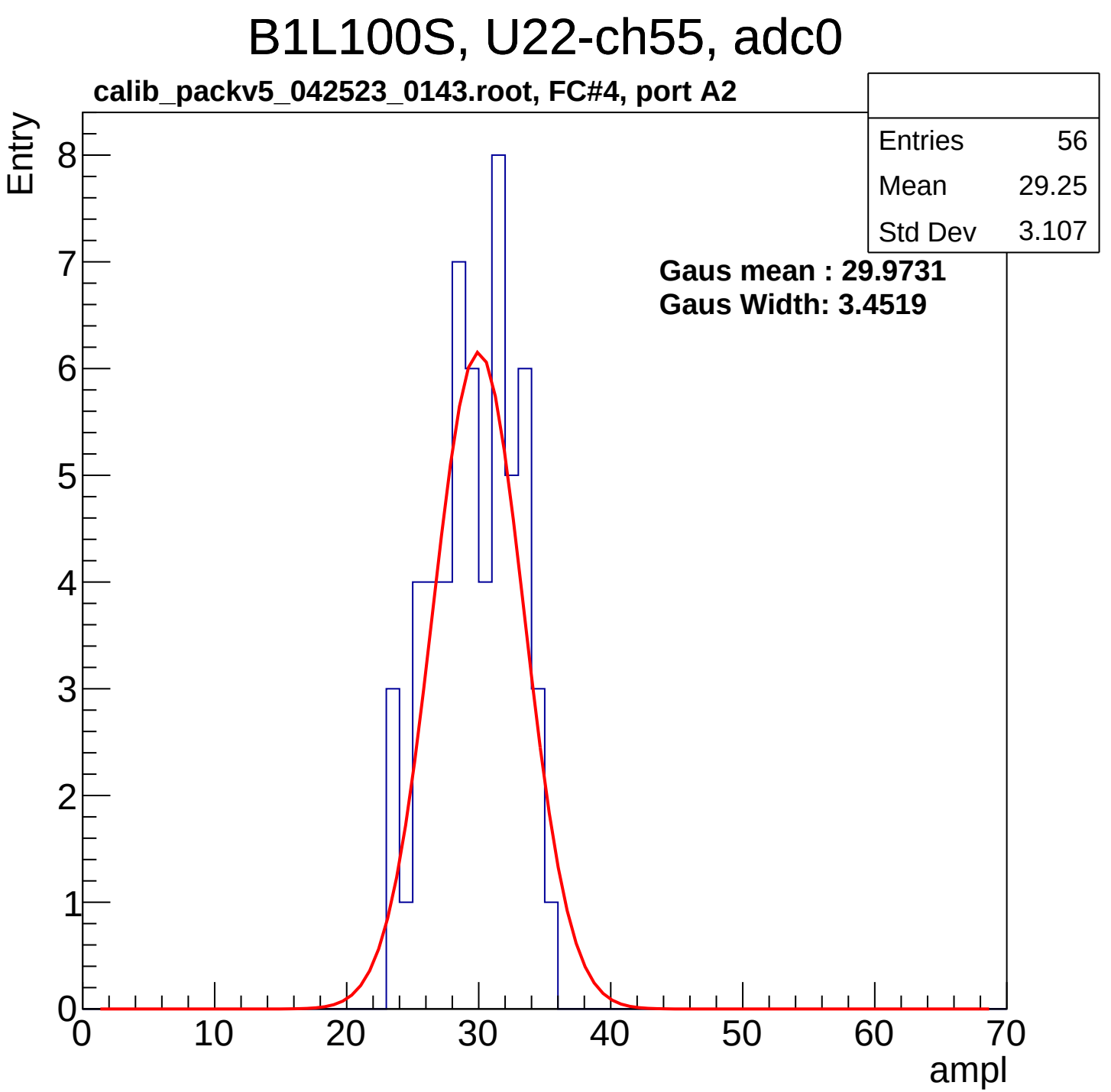
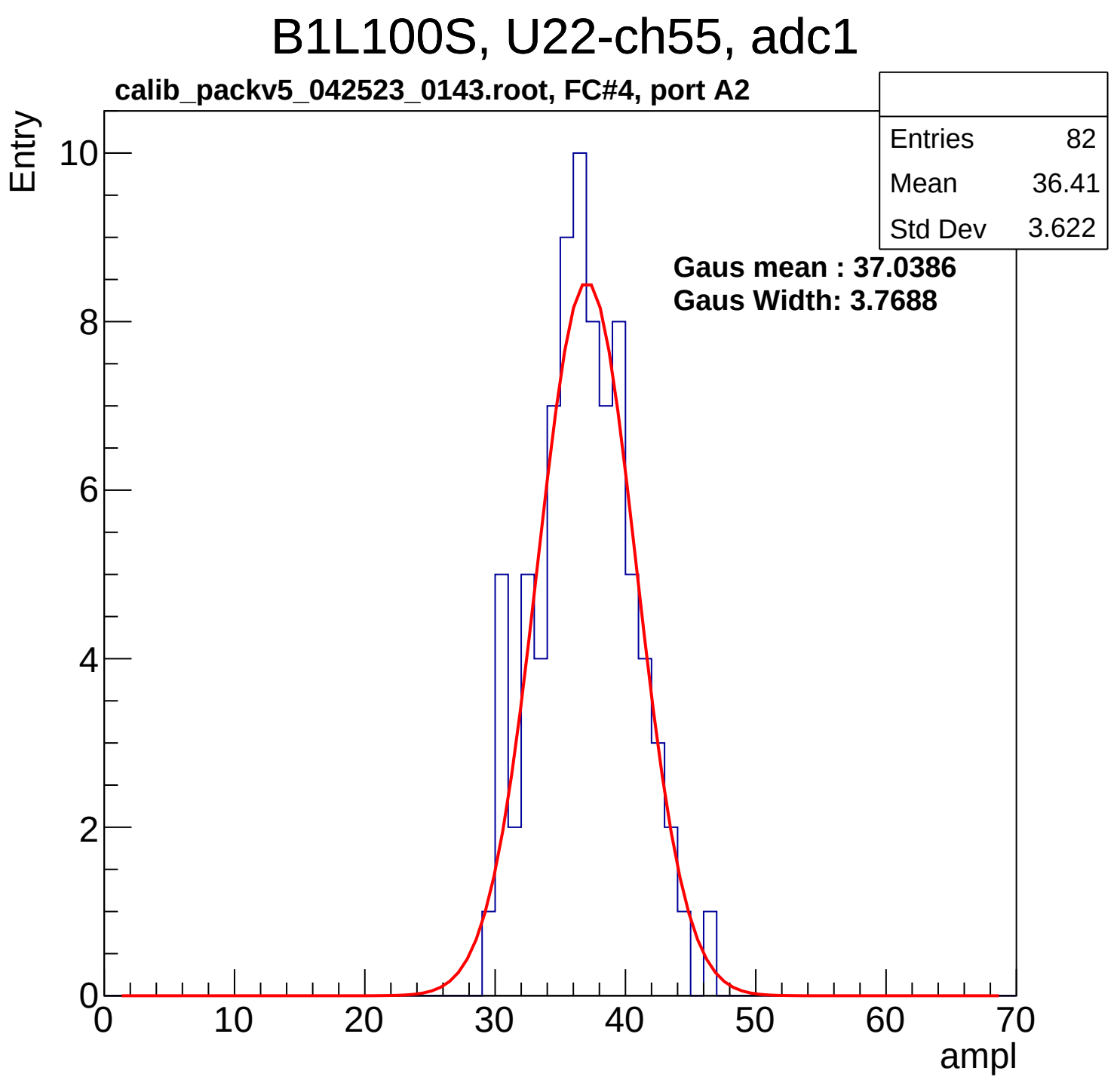
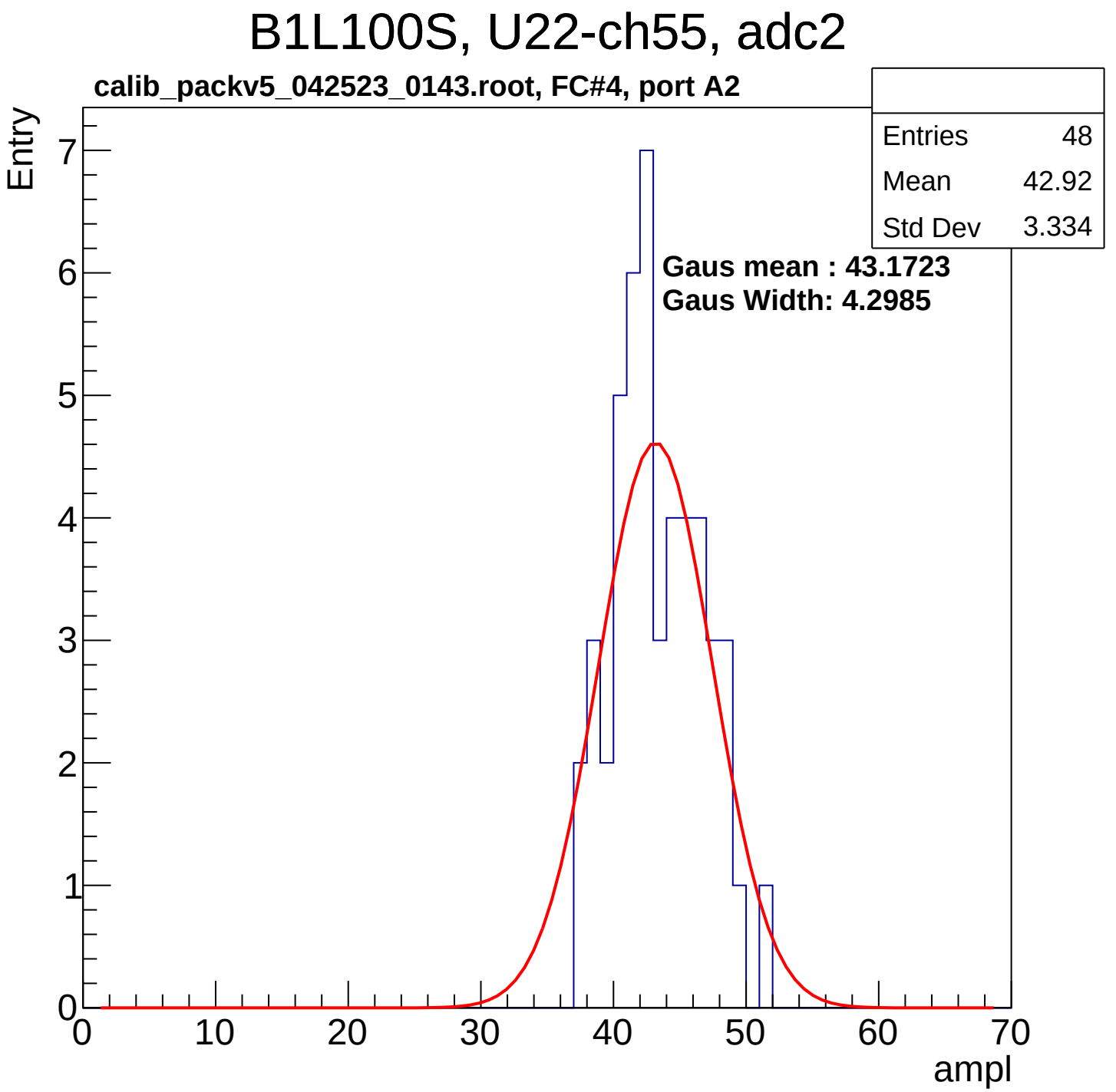
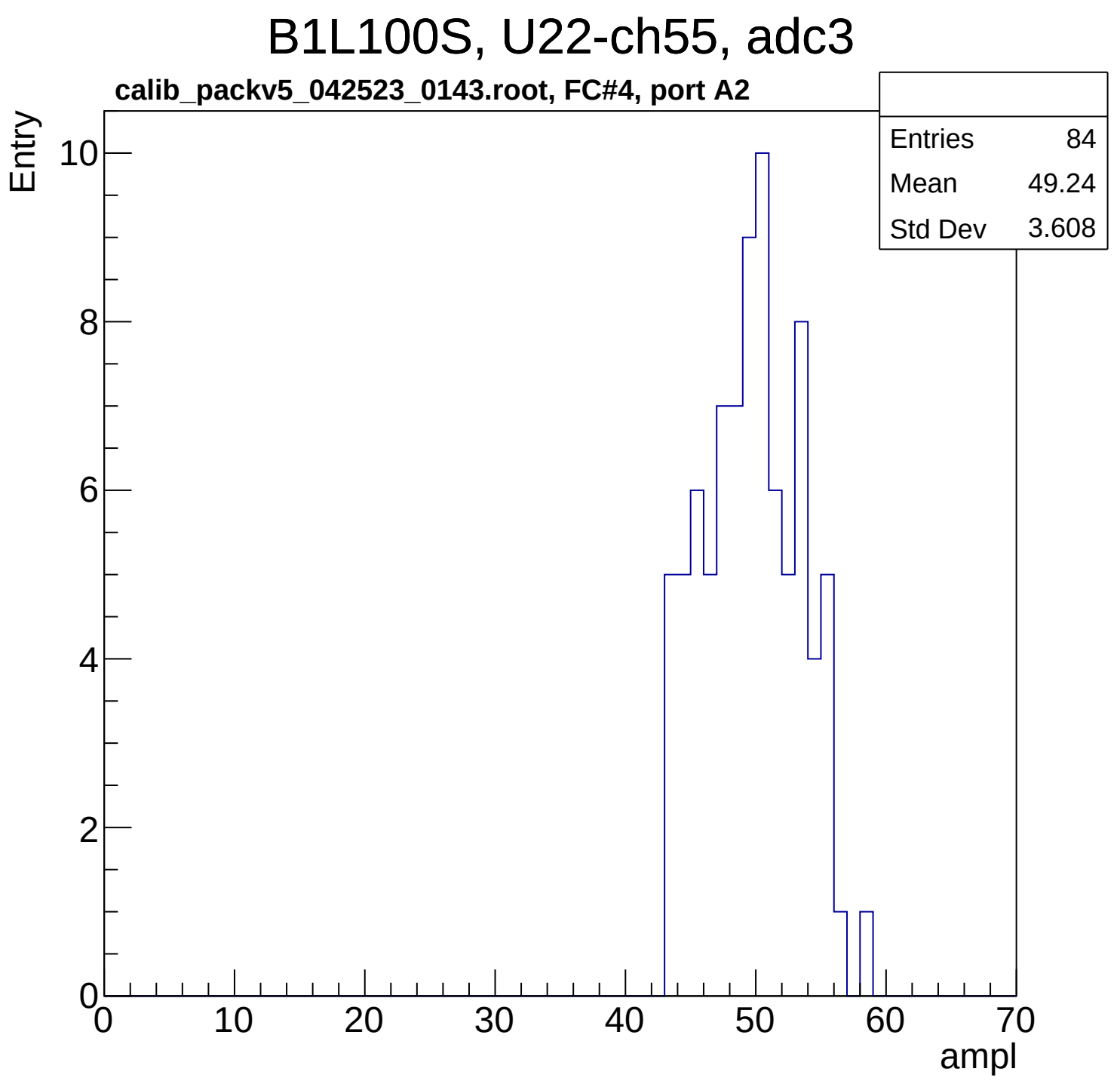
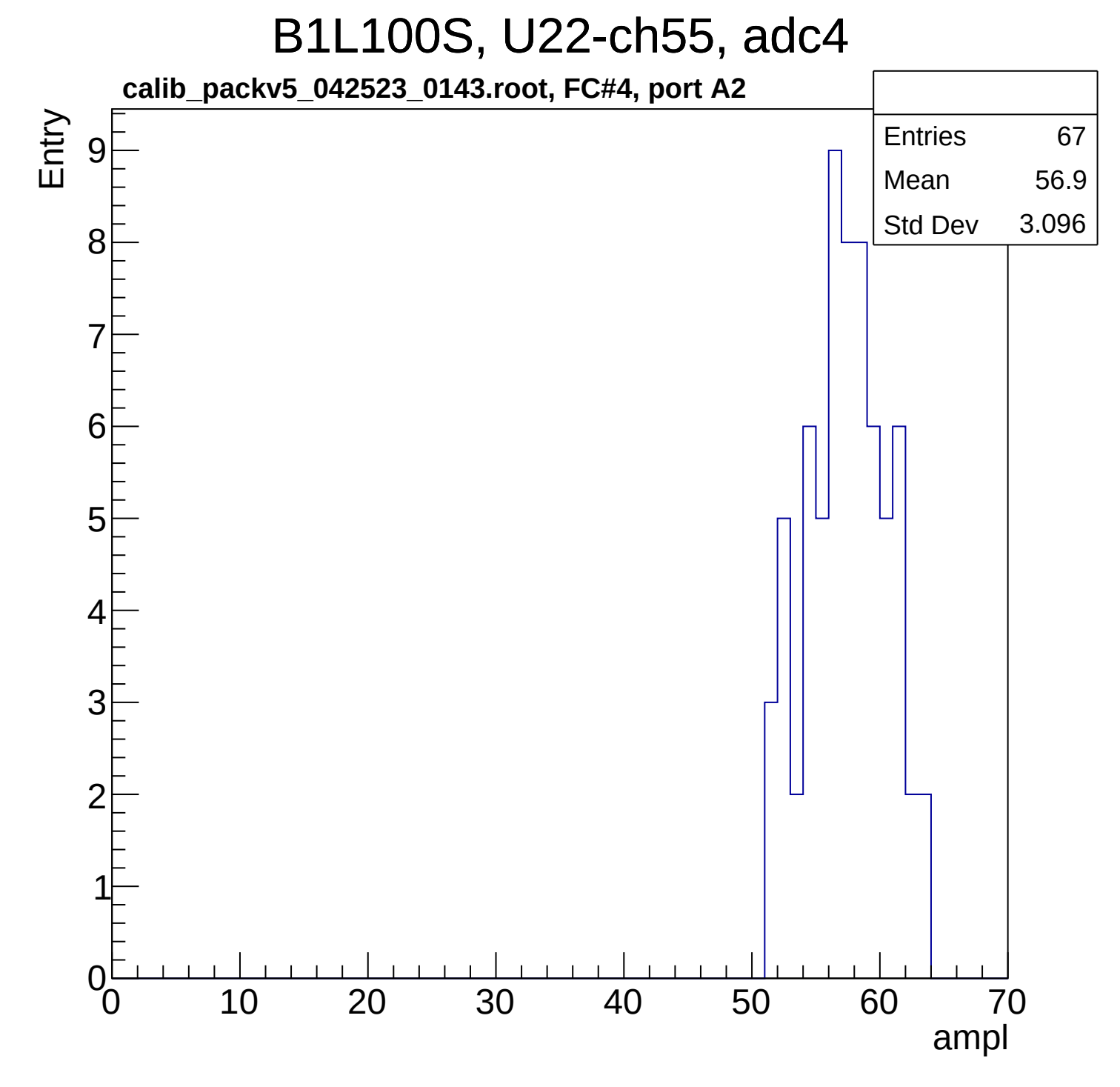

B1L100S, U22-ch54, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

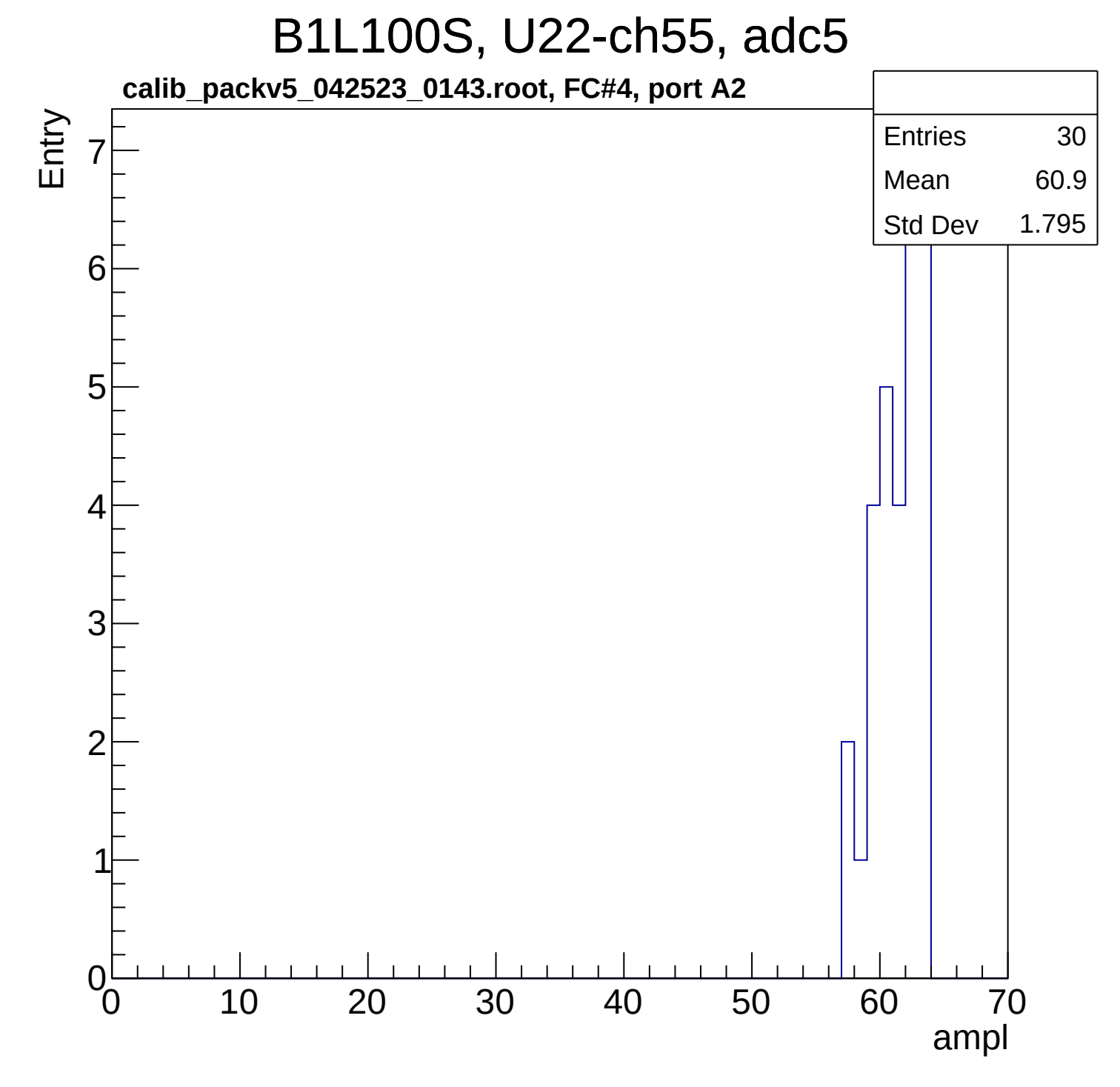

B1L100S, U22-ch55, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 29.25
Std Dev 3.107
Gaus mean : 29.9731
Gaus Width: 3.4519
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch55, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 36.41
Std Dev 3.622
Gaus mean : 37.0386
Gaus Width: 3.7688
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch55, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 42.92
Std Dev 3.334
Gaus mean : 43.1723
Gaus Width: 4.2985
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch55, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 49.24
Std Dev 3.608
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch55, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 56.9
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch55, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 30
Mean 60.9
Std Dev 1.795
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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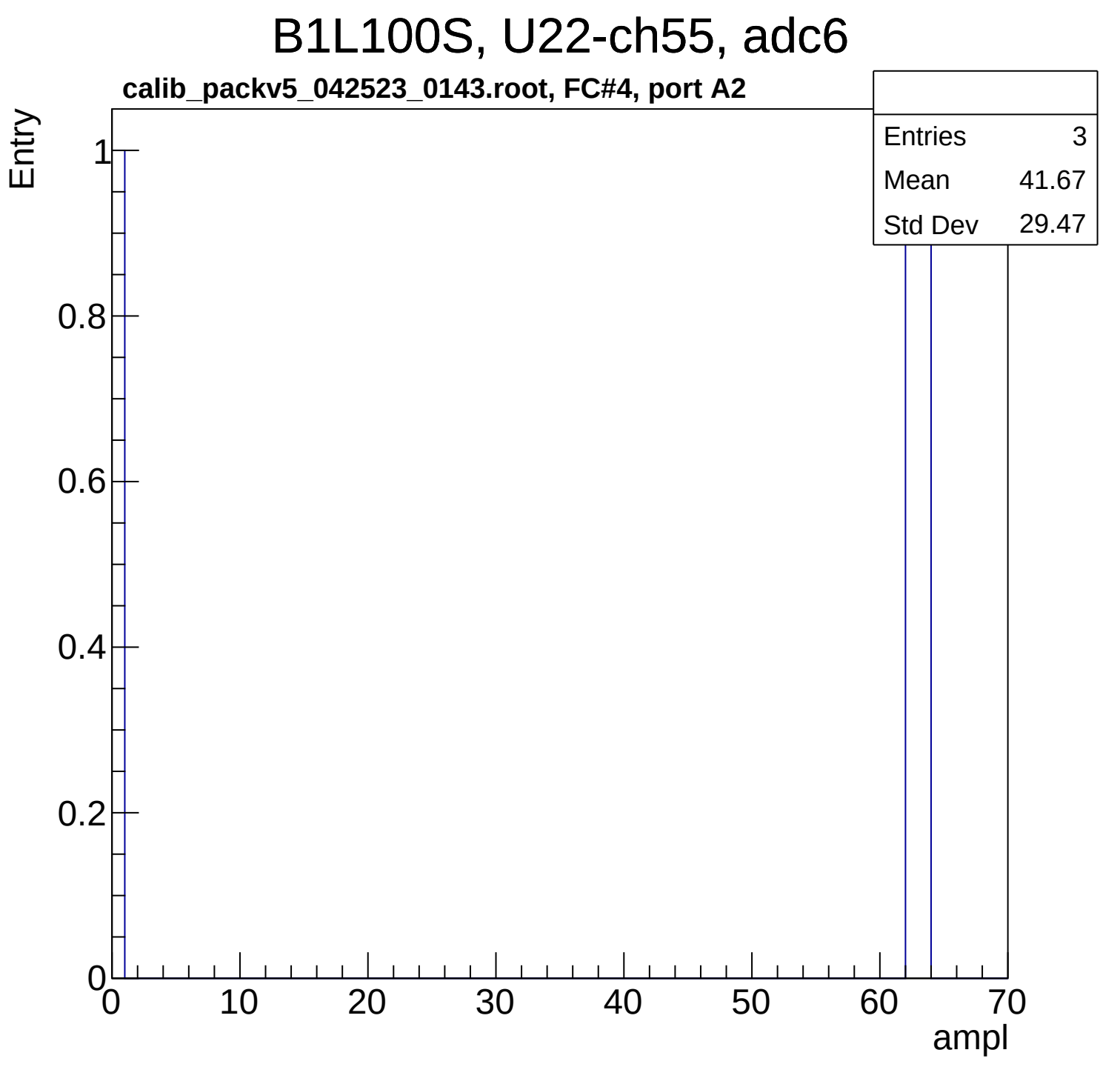
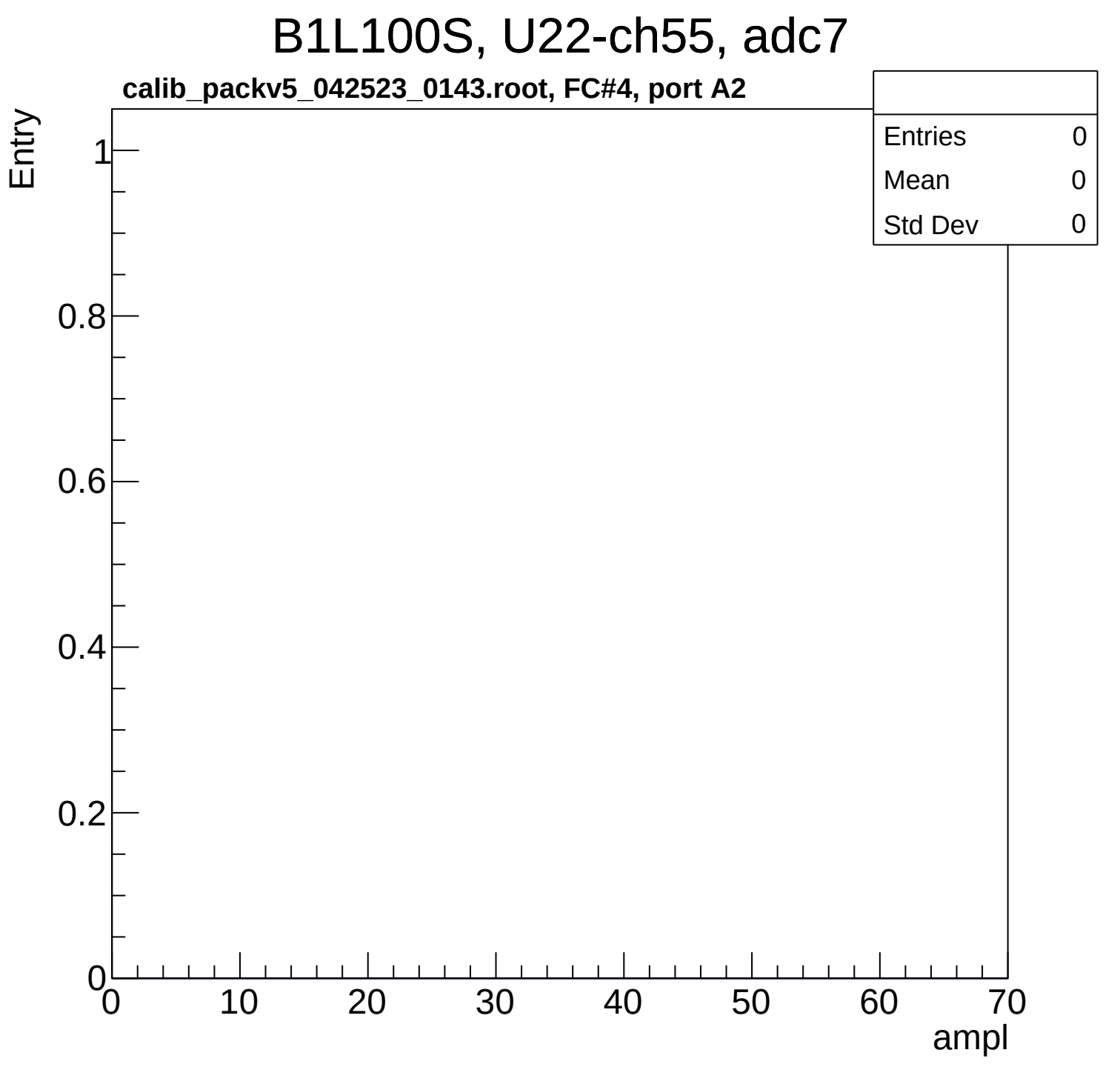
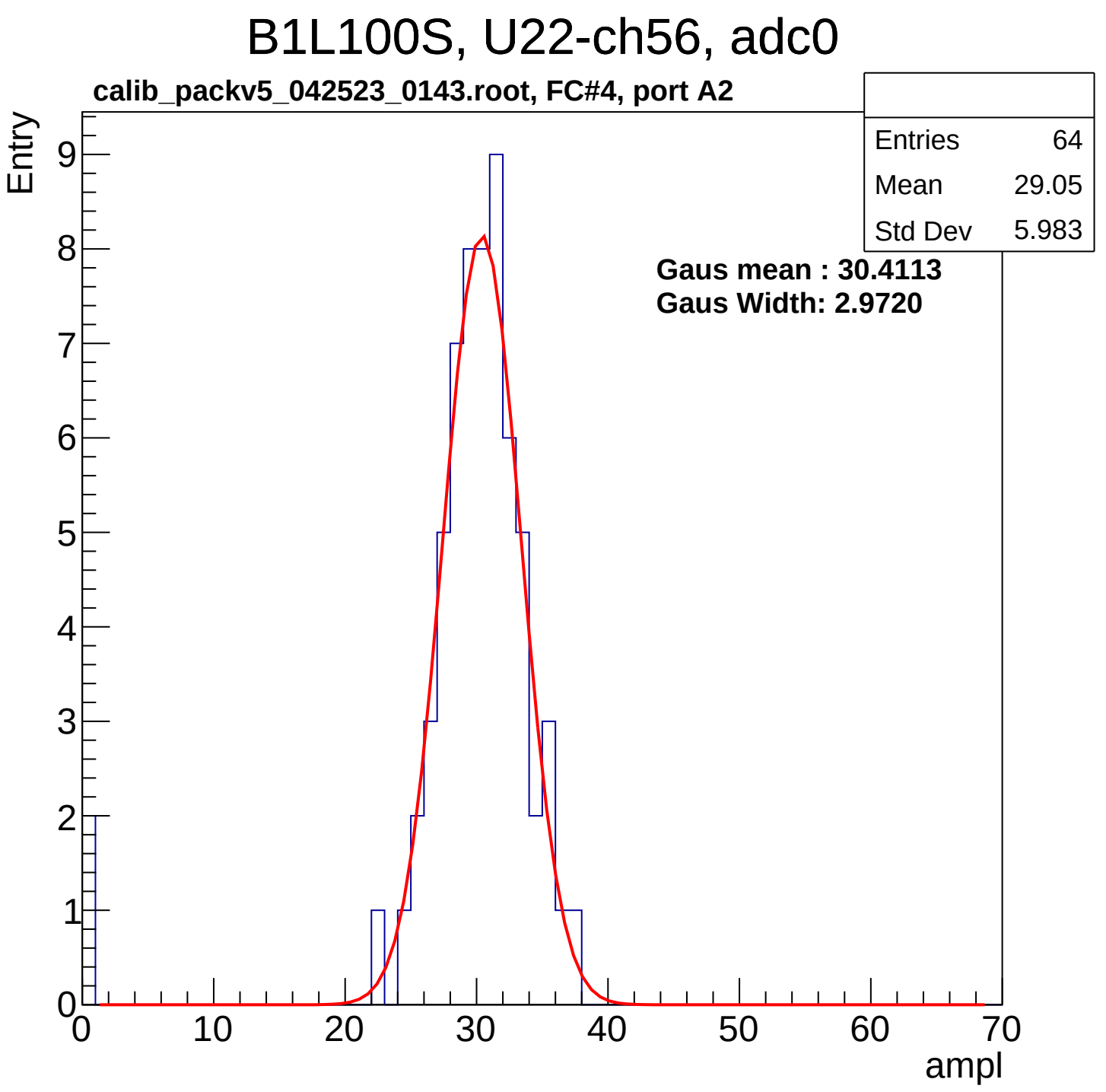

B1L100S, U22-ch55, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch55, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch56, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.05
Std Dev 5.983
Gaus mean : 30.4113
Gaus Width: 2.9720
Entry
ampl
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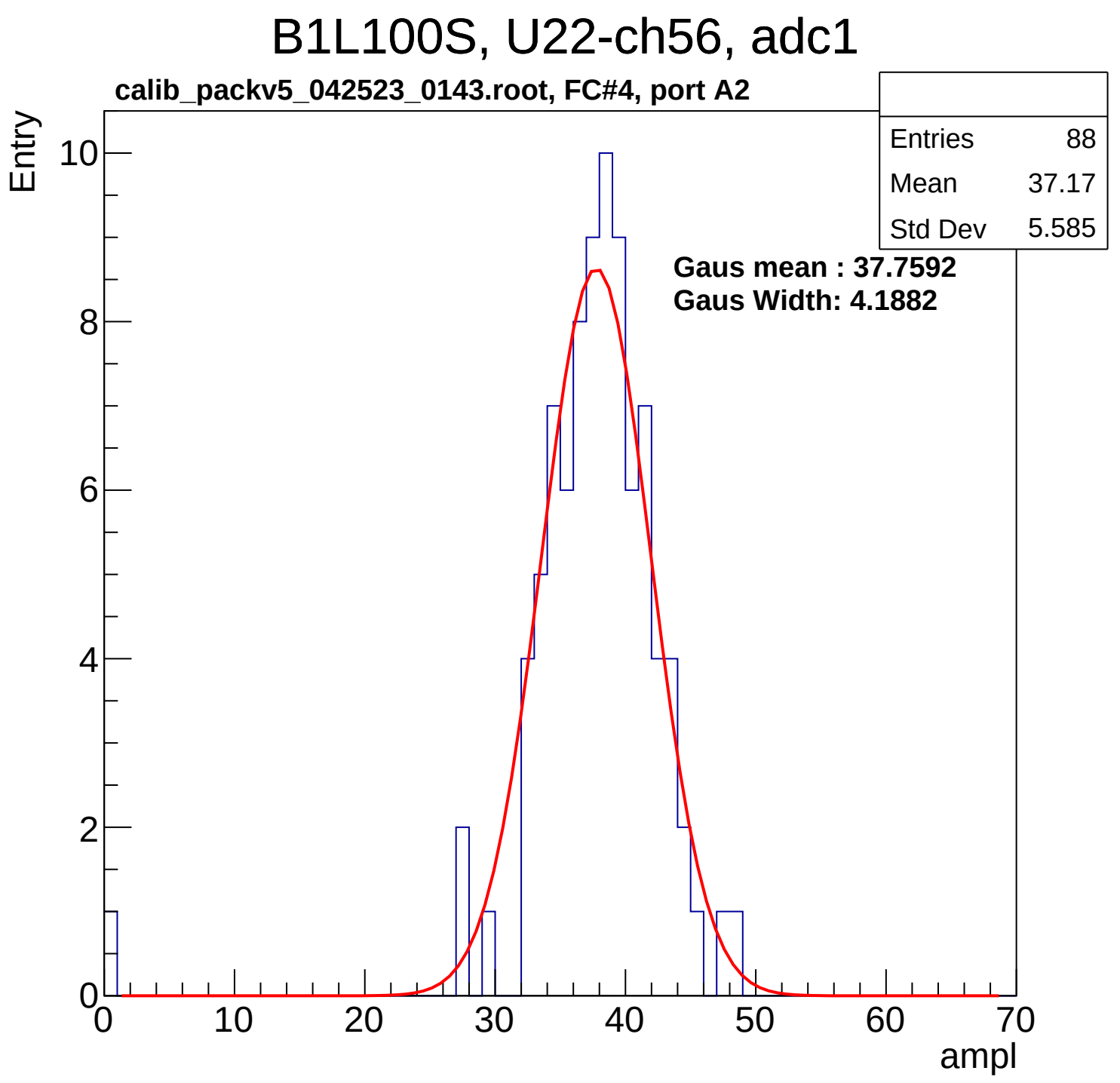
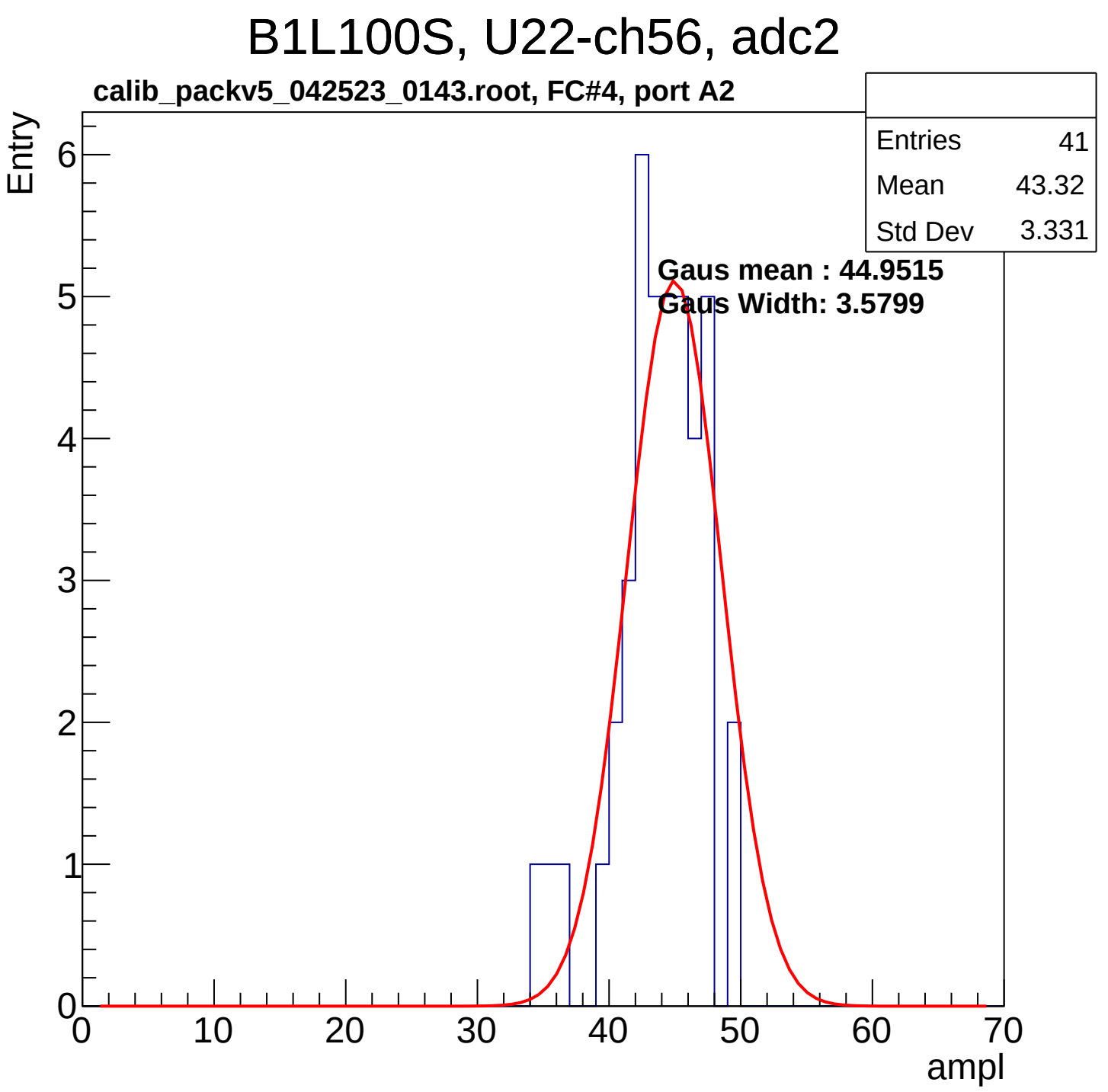
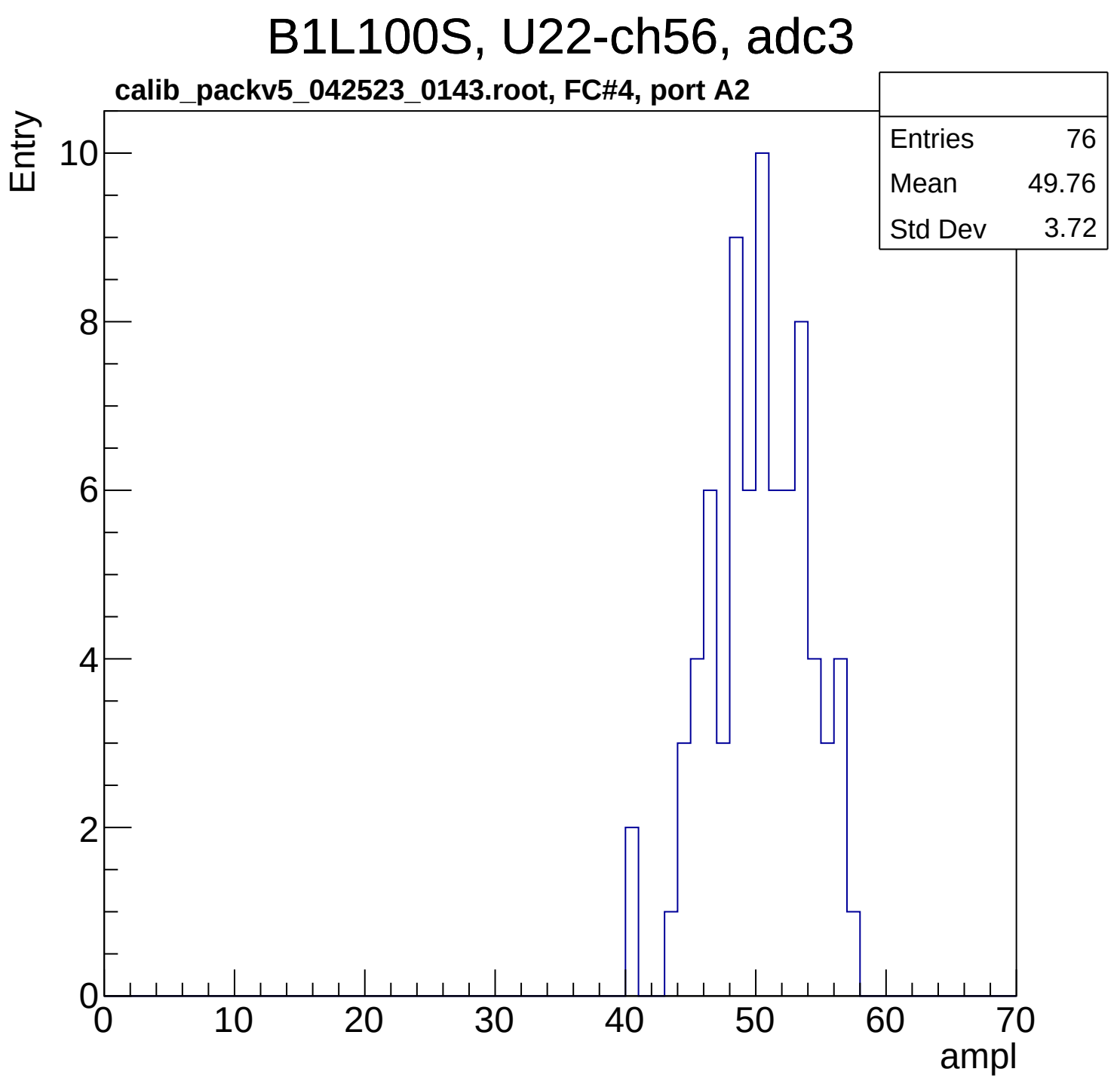
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch56, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 37.17
Std Dev 5.585
Gaus mean : 37.7592
Gaus Width: 4.1882
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch56, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 43.32
Std Dev 3.331
Gaus mean : 44.9515
Gaus Width: 3.5799
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch56, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 49.76
Std Dev 3.72
Entry
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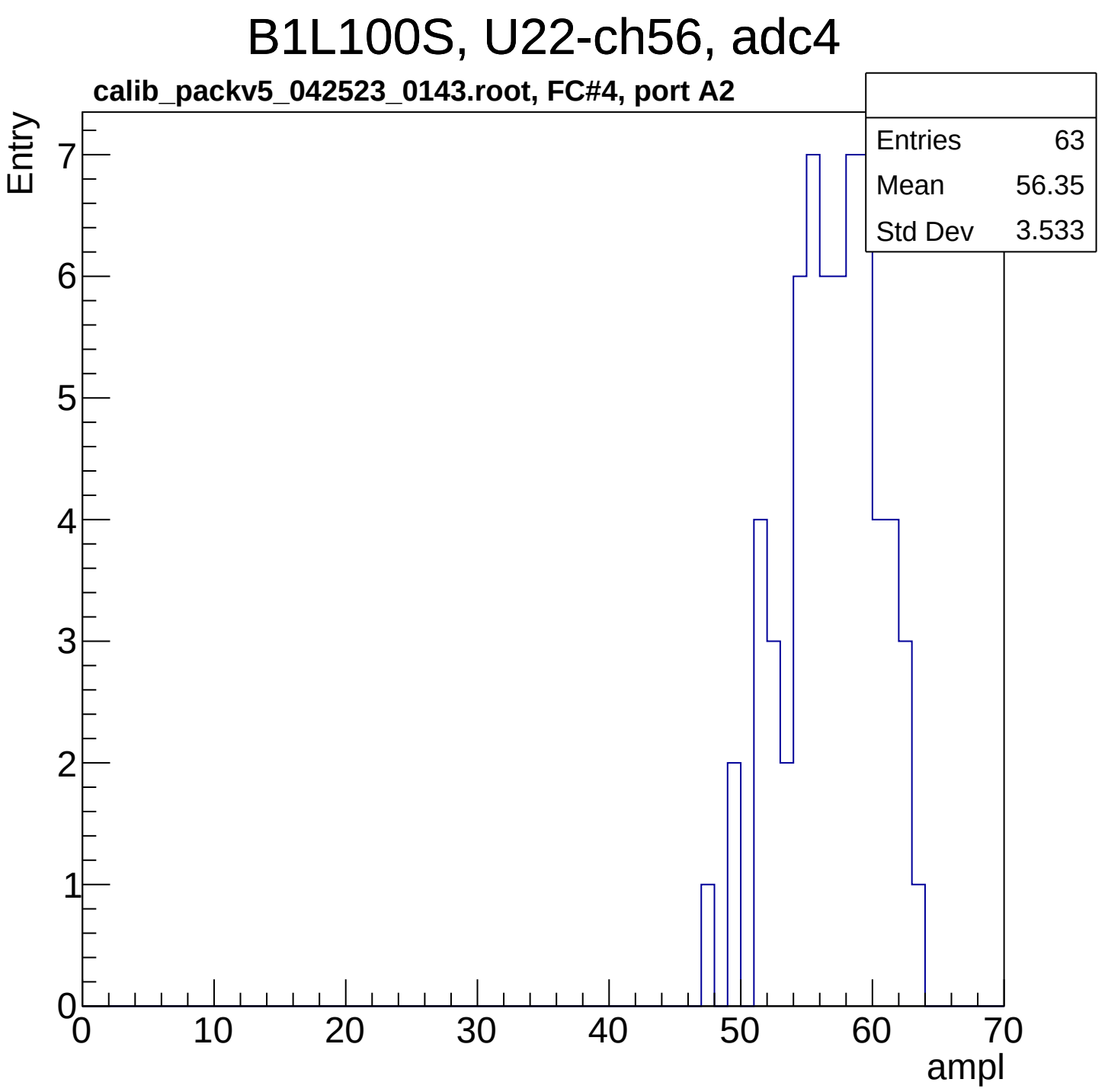
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch56, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 56.35
Std Dev 3.533
Entry
ampl
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6
7
0
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40
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60
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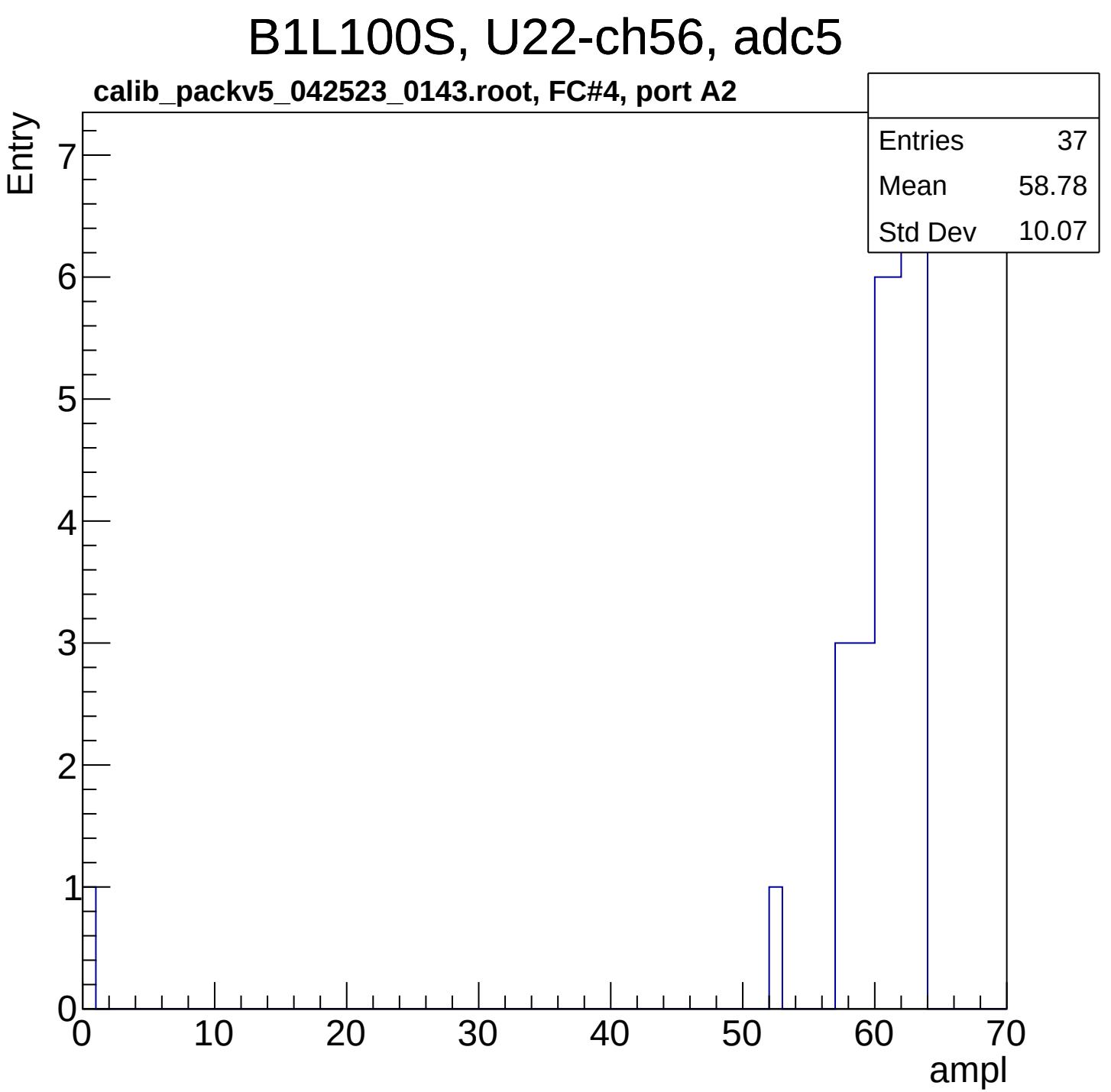
B1L100S, U22-ch56, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 58.78
Std Dev 10.07
Entry
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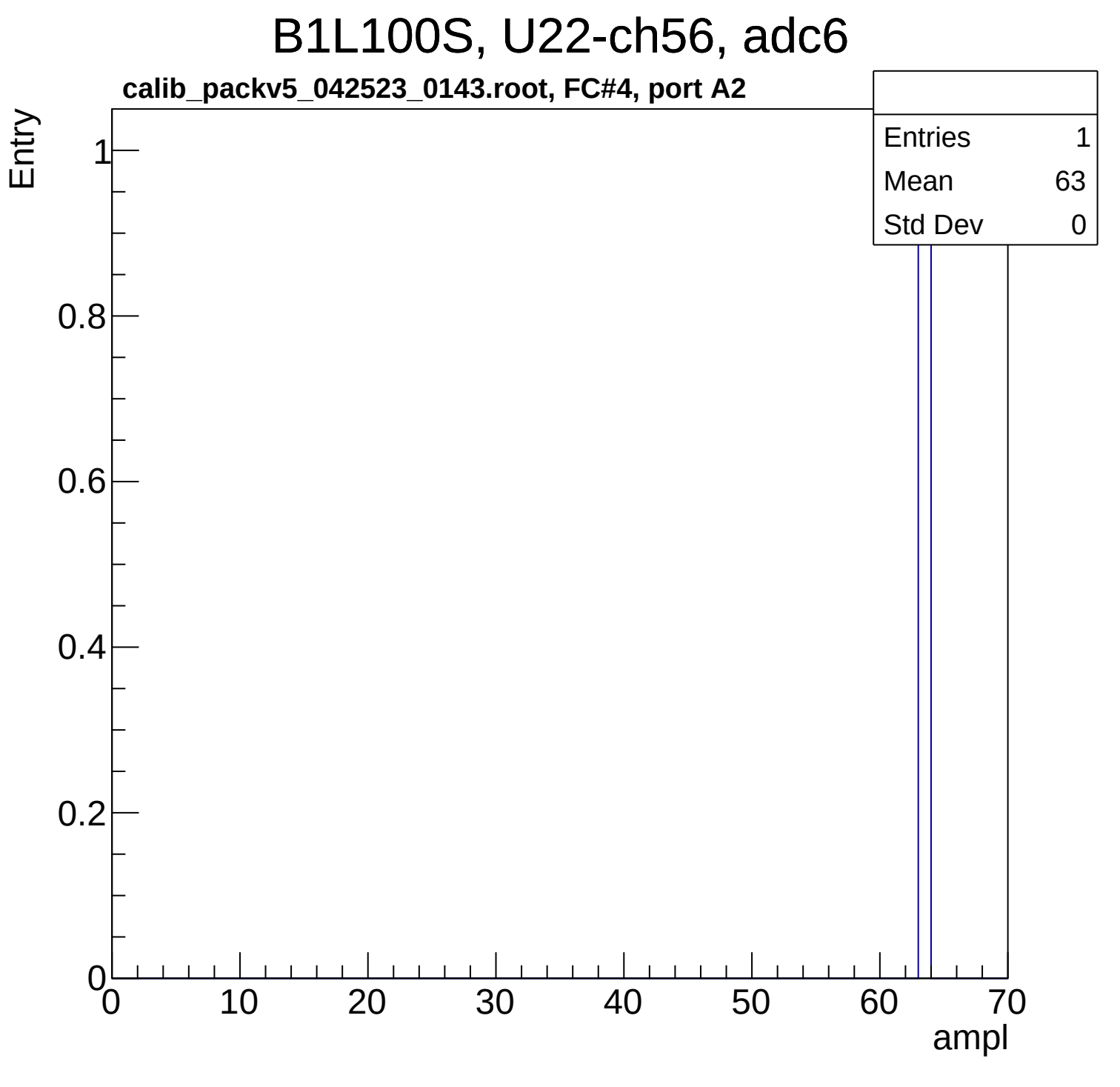
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch56, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
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0.6
0.4
0.2
0
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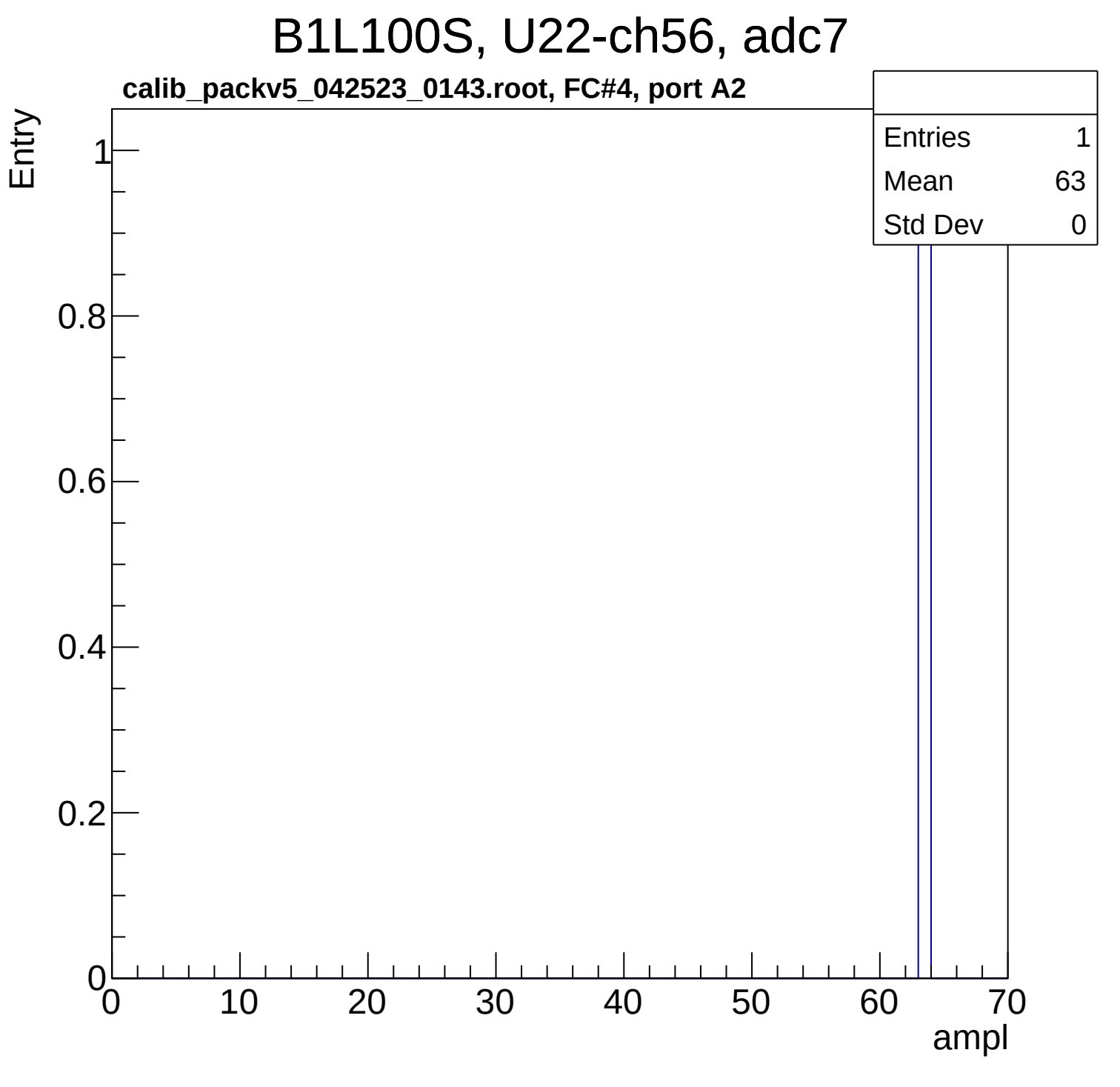
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch56, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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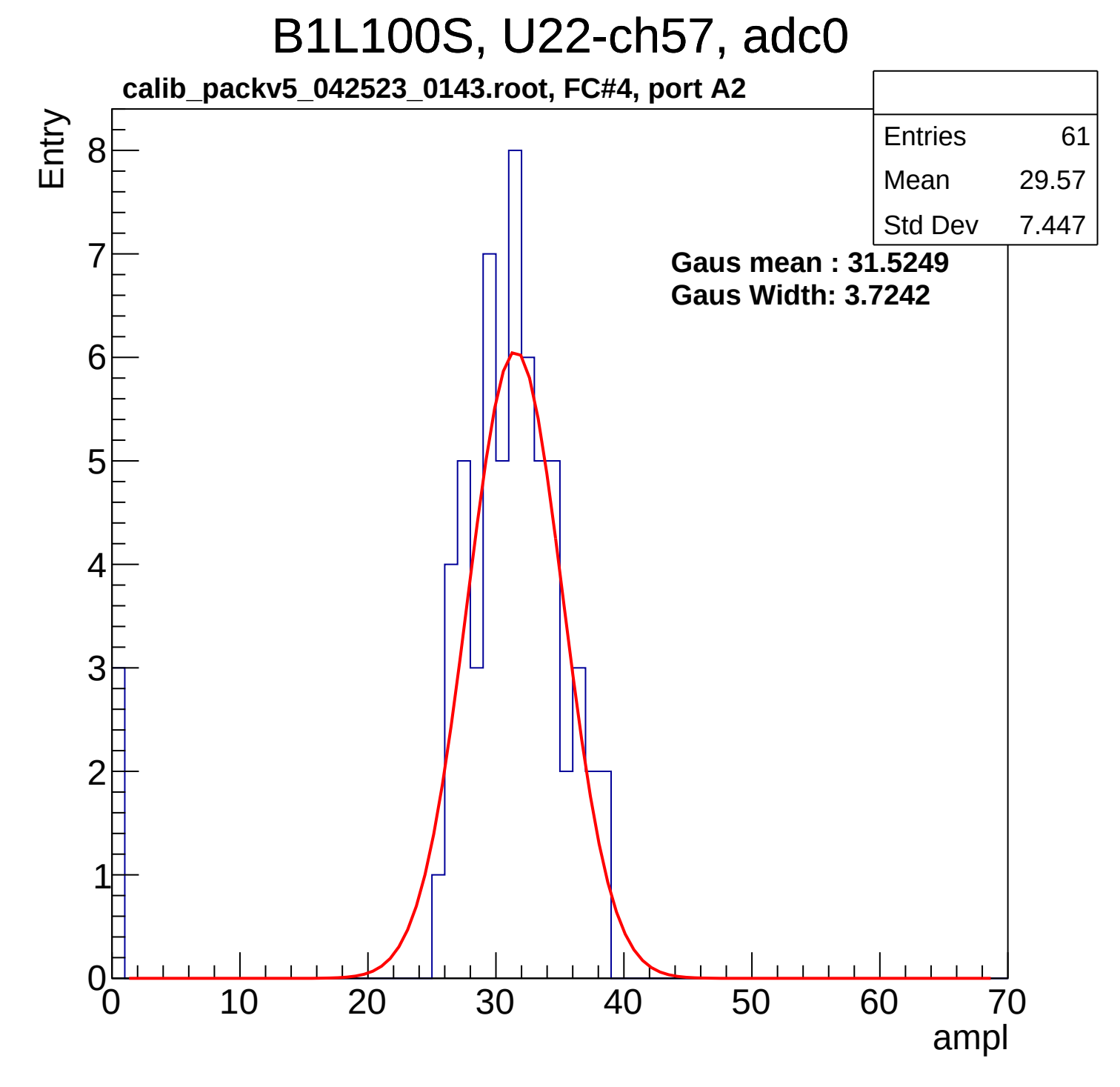
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch57, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.57
Std Dev 7.447
Gaus mean : 31.5249
Gaus Width: 3.7242
Entry
ampl
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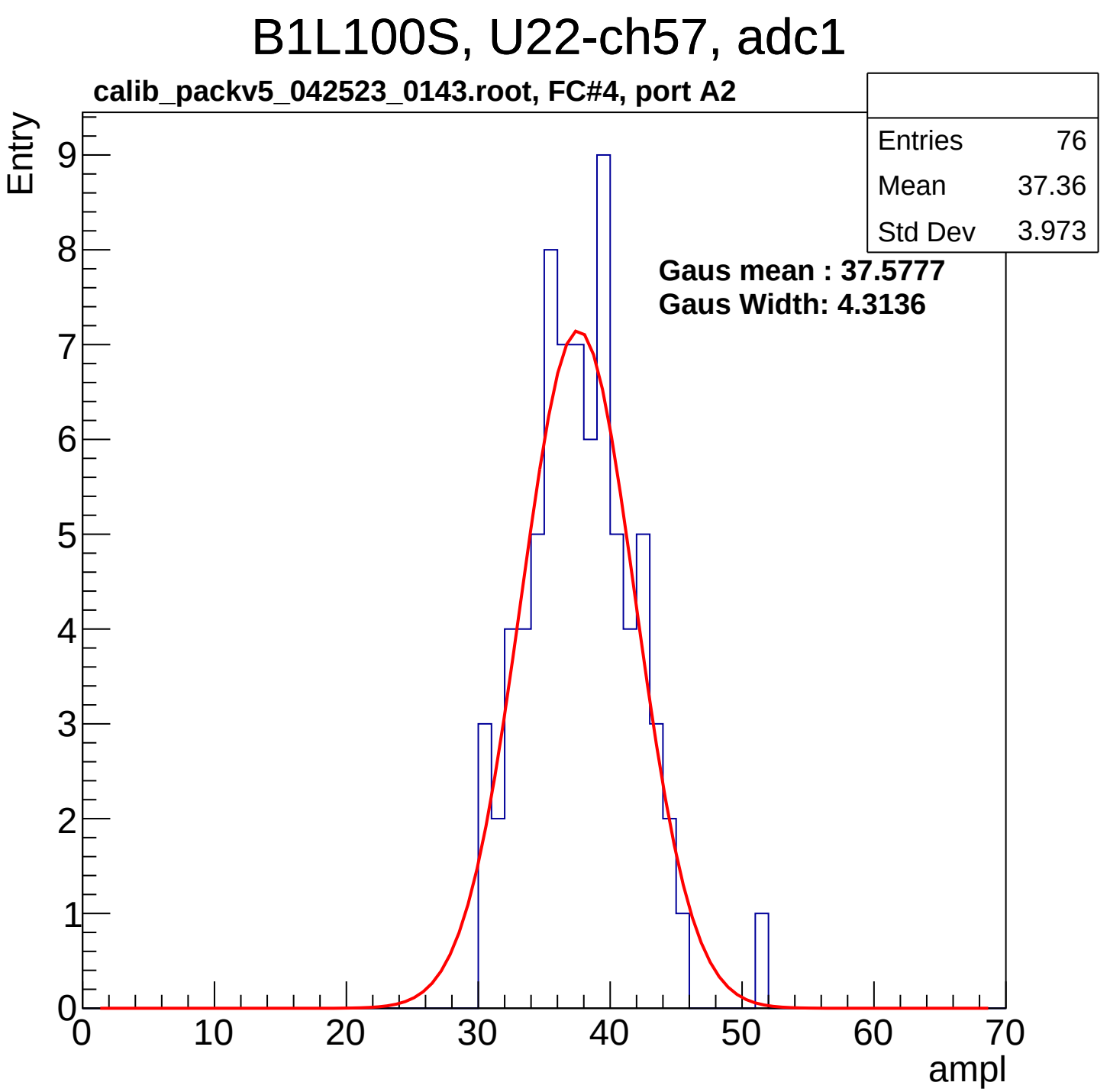
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch57, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 37.36
Std Dev 3.973
Gaus mean : 37.5777
Gaus Width: 4.3136
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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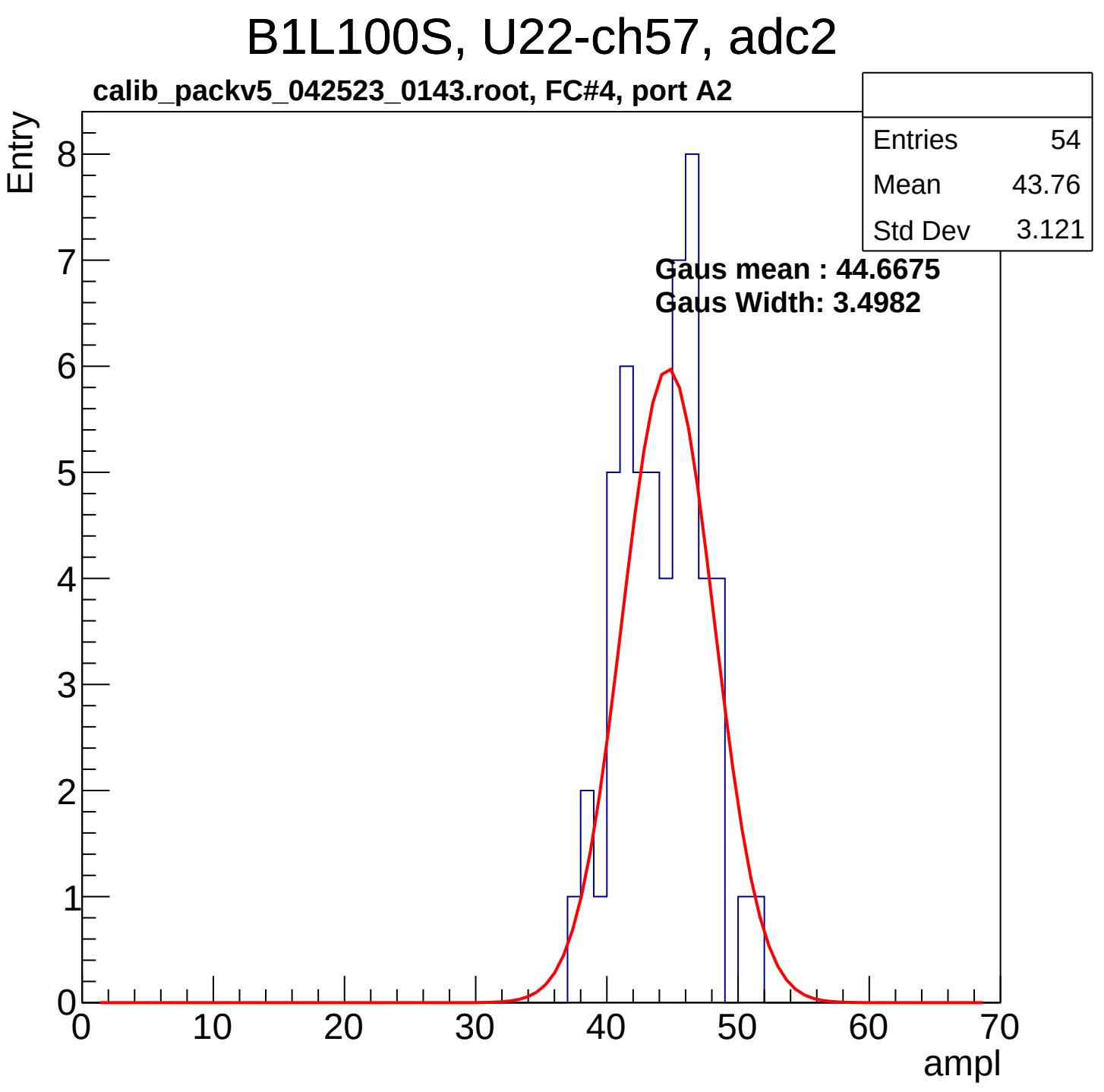
B1L100S, U22-ch57, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 43.76
Std Dev 3.121
Gaus mean : 44.6675
Gaus Width: 3.4982
Entry
ampl
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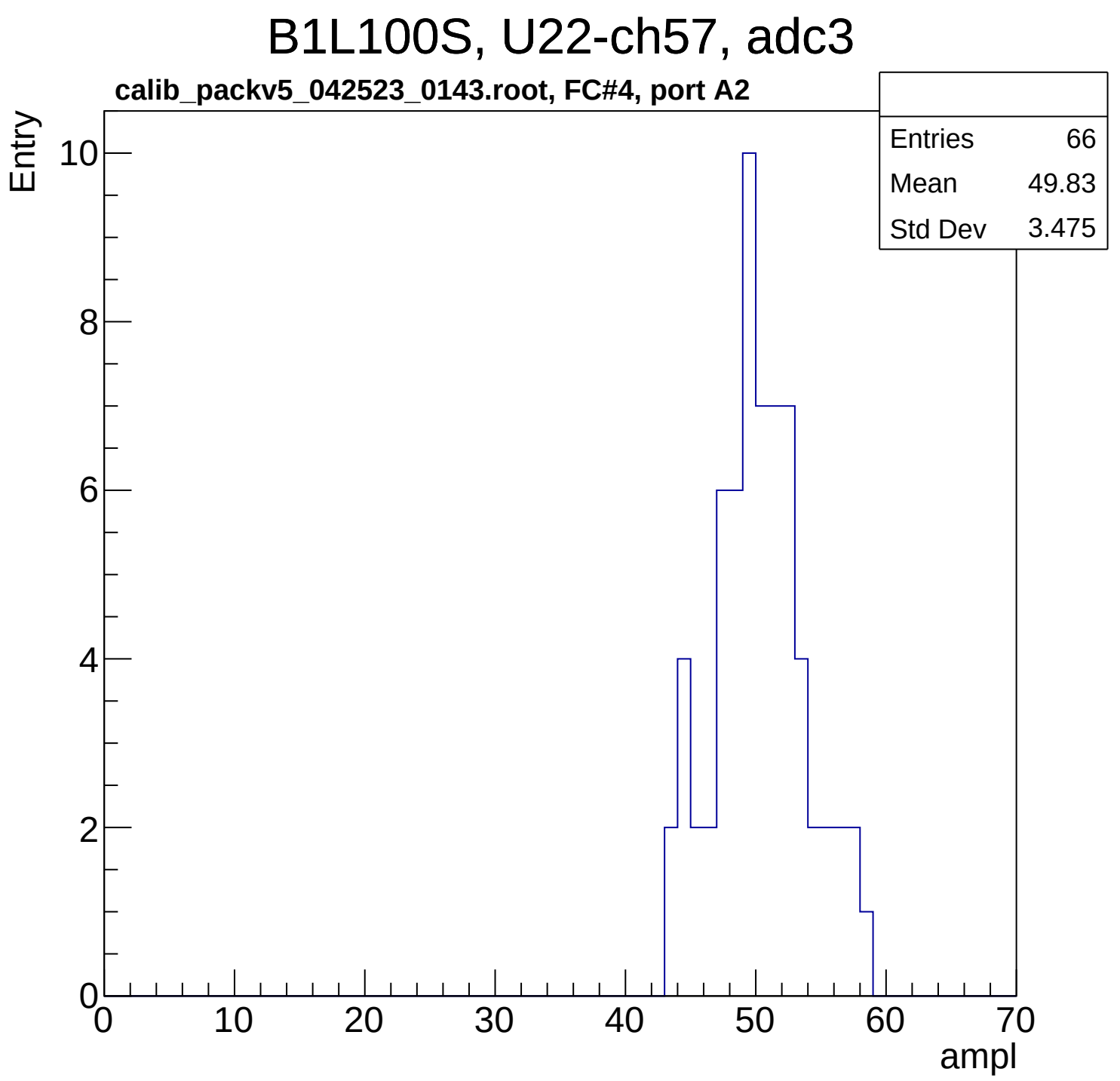
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch57, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.83
Std Dev 3.475
Entry
ampl
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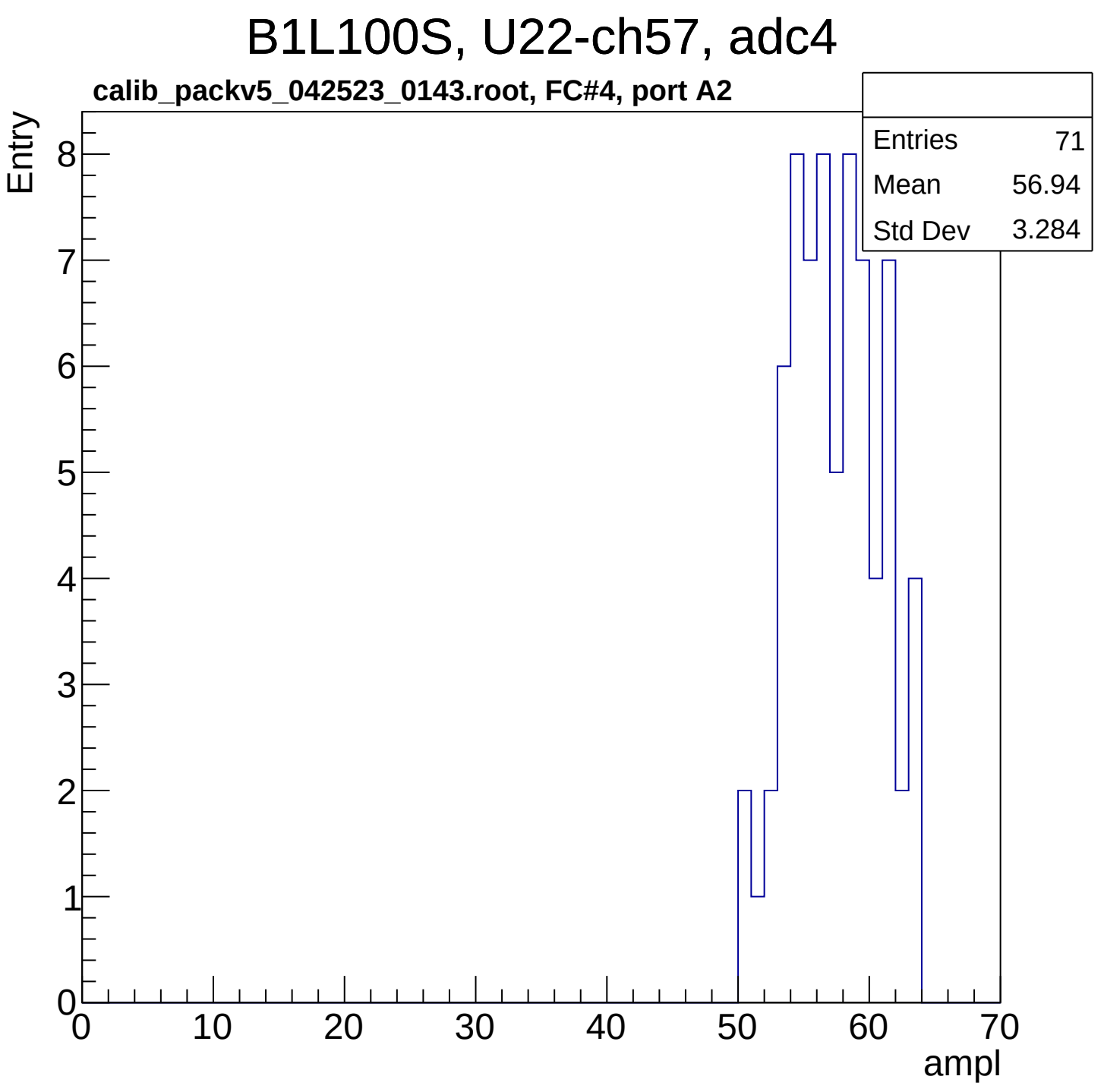
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch57, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 56.94
Std Dev 3.284
Entry
ampl
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1
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7
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20
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60
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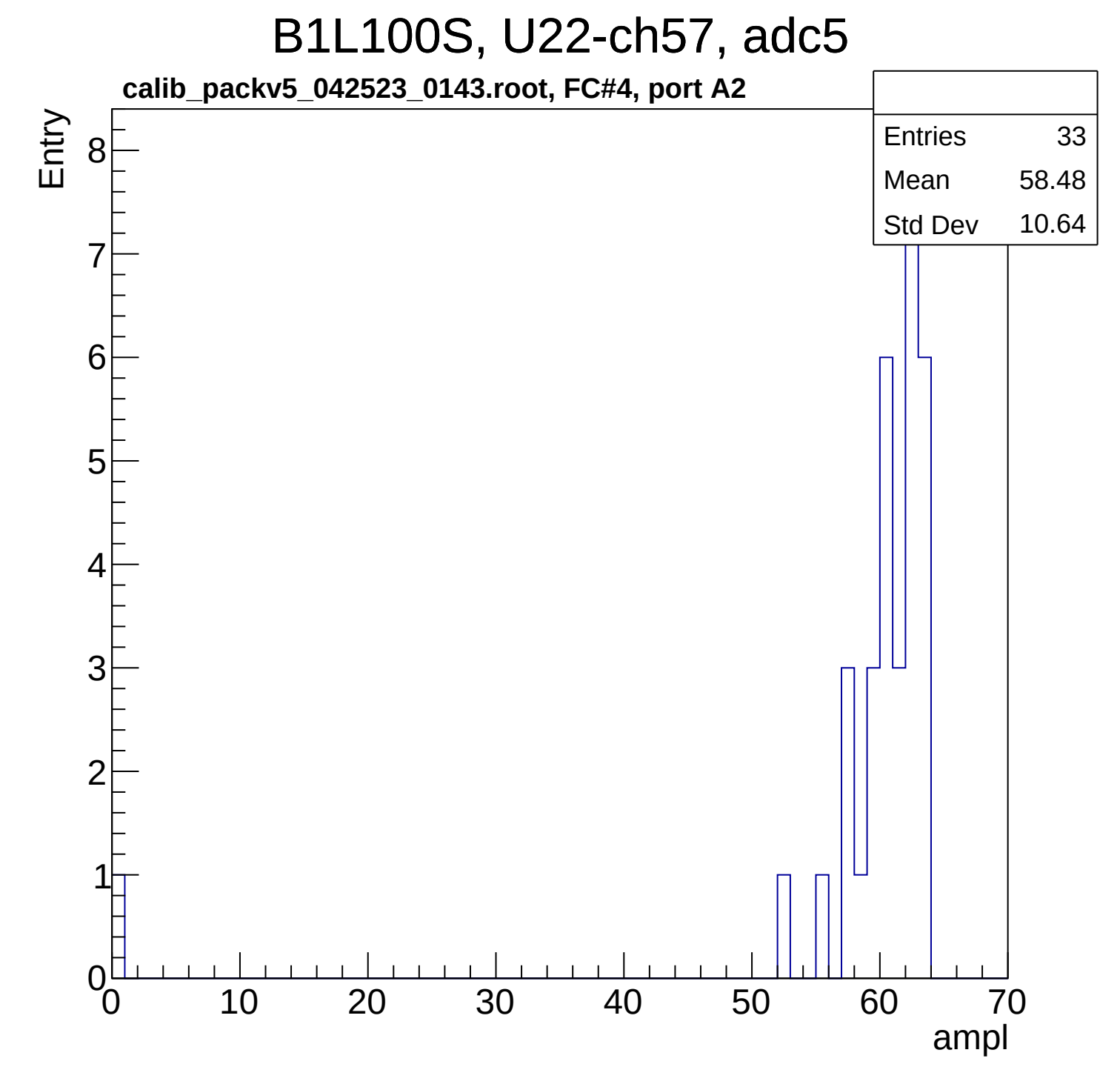
B1L100S, U22-ch57, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.48
Std Dev 10.64
Entry
ampl
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1
2
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4
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6
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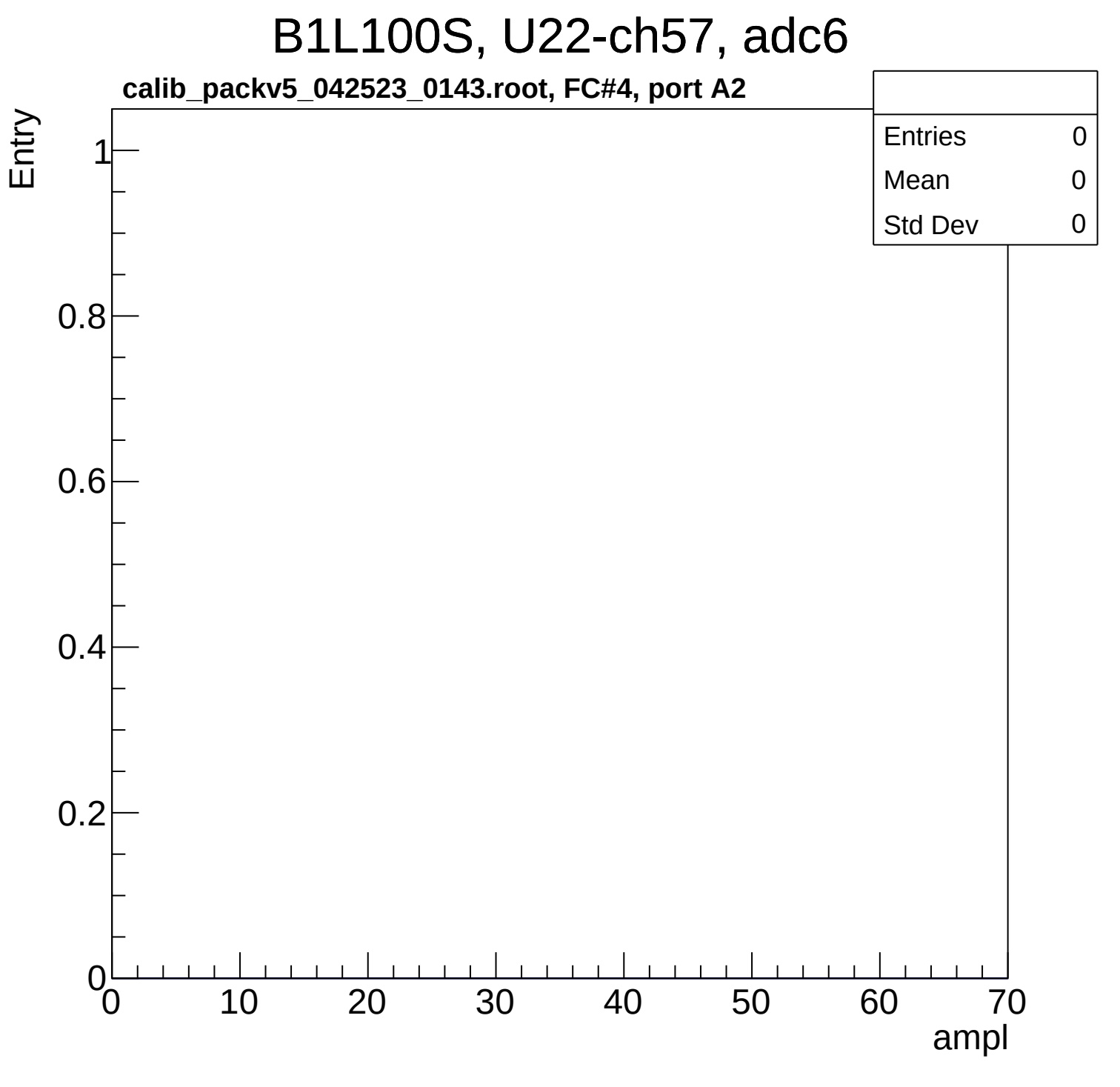
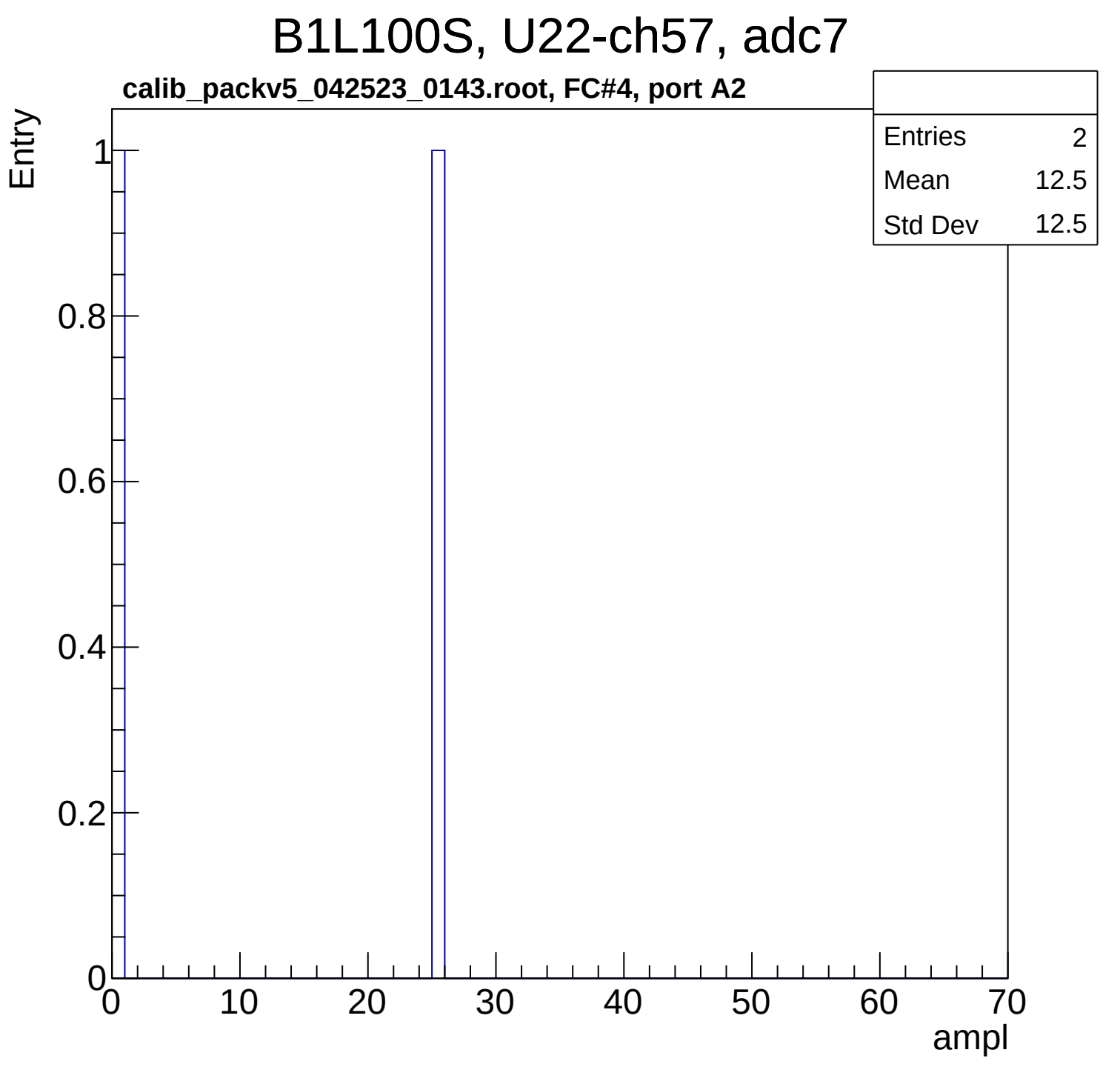
40
50
60
70


B1L100S, U22-ch57, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch57, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 12.5
Std Dev 12.5
Entry
1
0.8
0.6
0.4
0.2
0
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10
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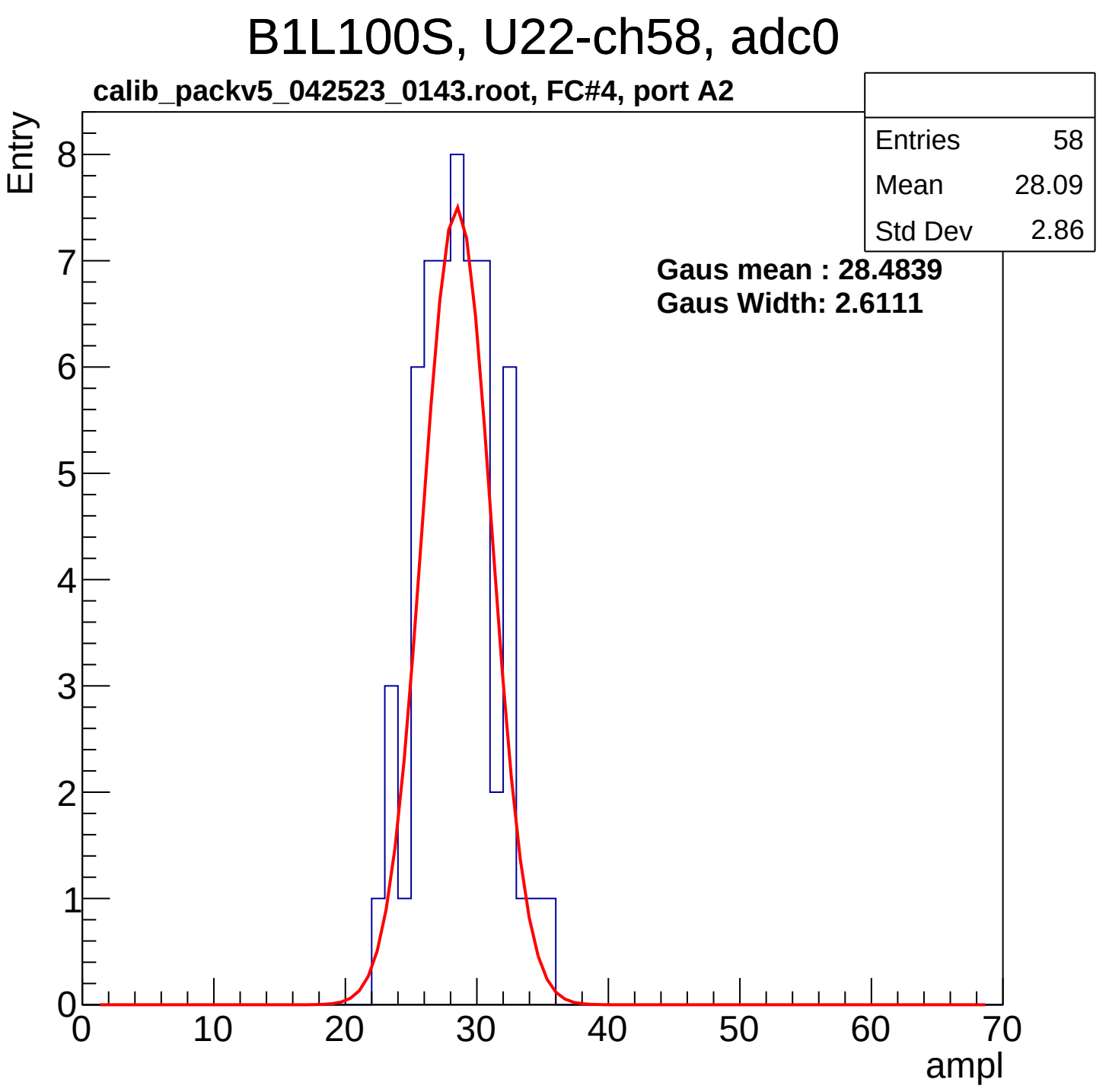
50
60
70
ampl


B1L100S, U22-ch58, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 28.09
Std Dev 2.86
Gaus mean : 28.4839
Gaus Width: 2.6111
Entry
ampl
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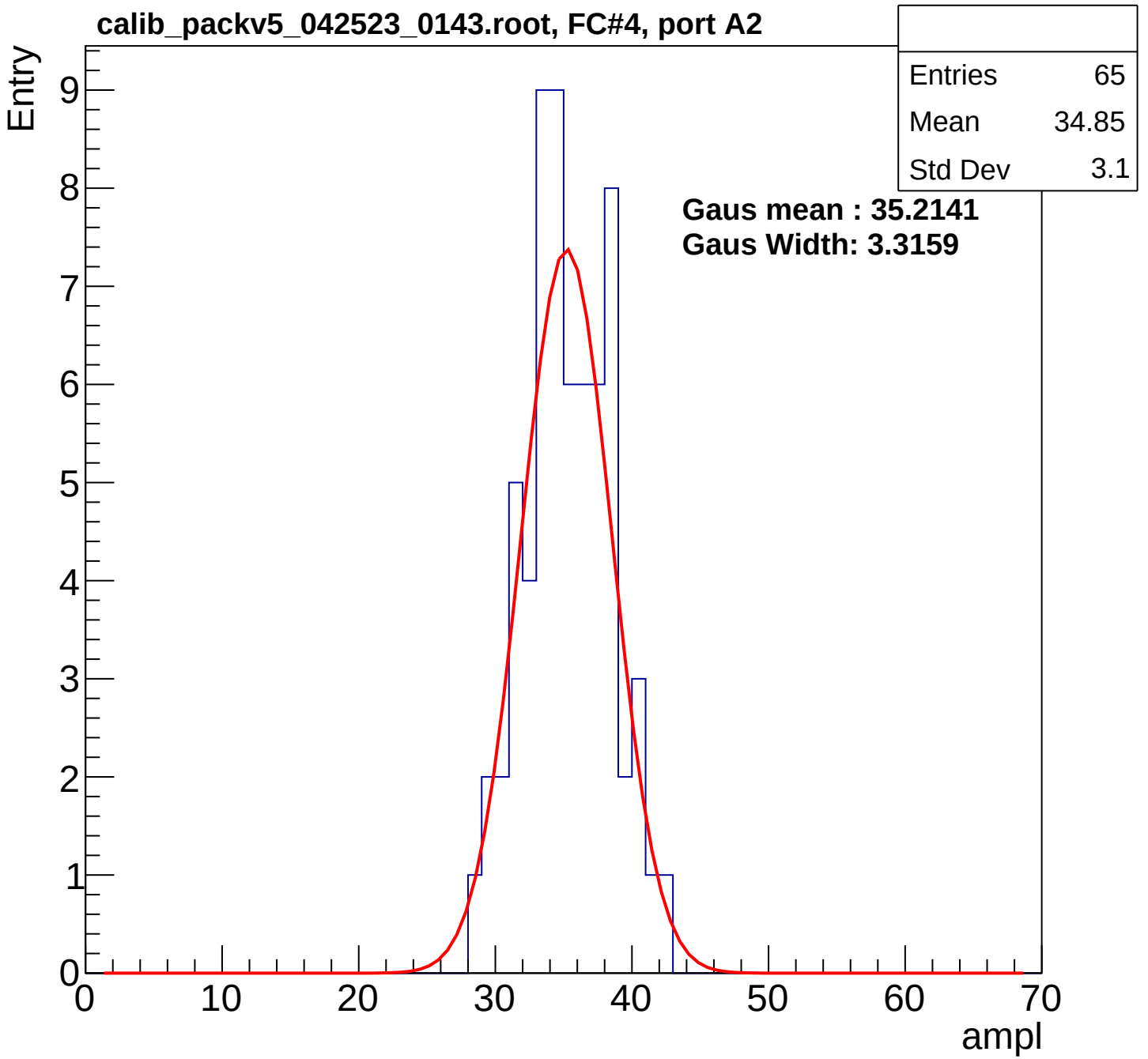
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch58, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 34.85
Std Dev 3.1
Gaus mean : 35.2141
Gaus Width: 3.3159
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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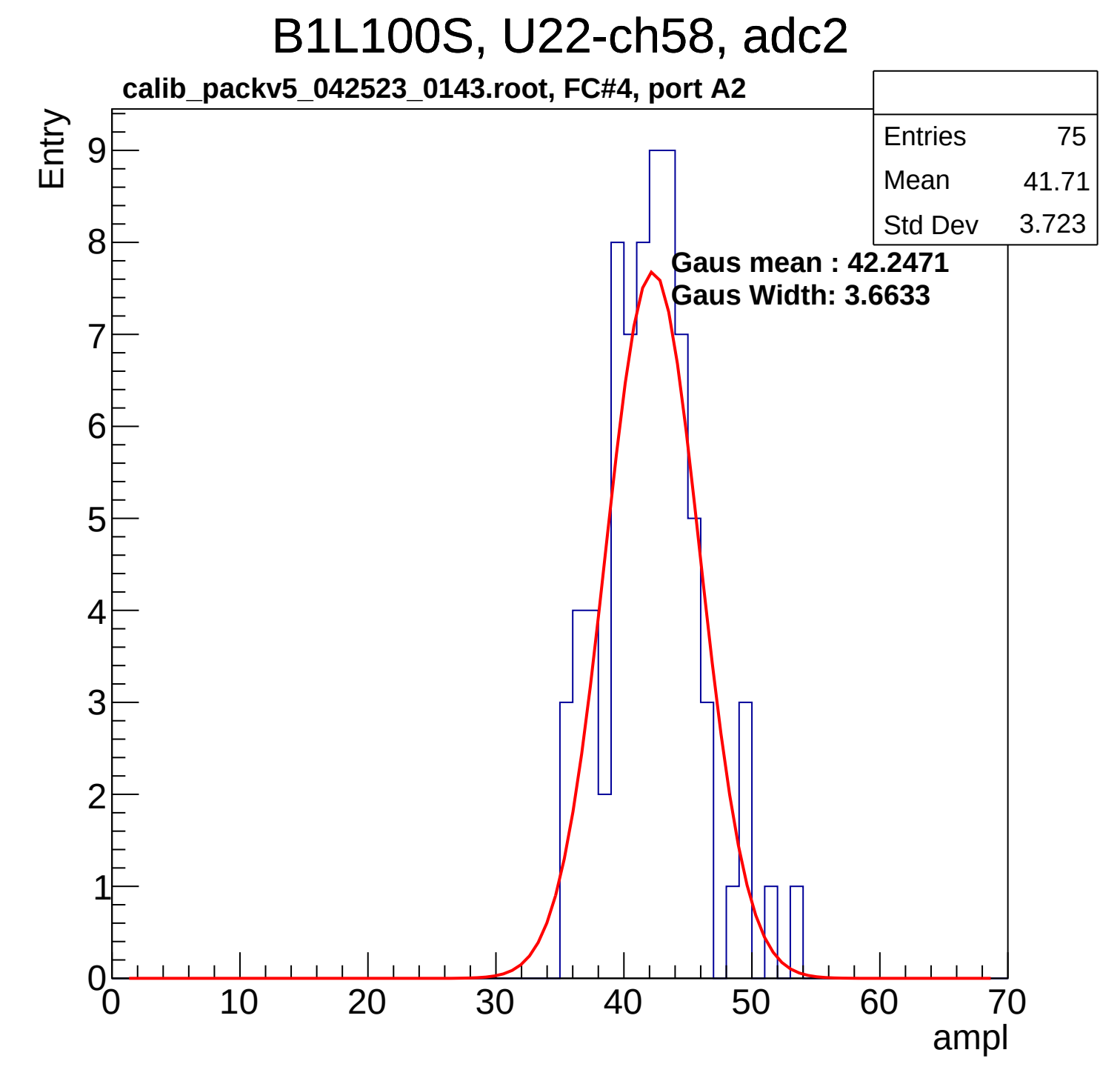
B1L100S, U22-ch58, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 41.71
Std Dev 3.723
Gaus mean : 42.2471
Gaus Width: 3.6633
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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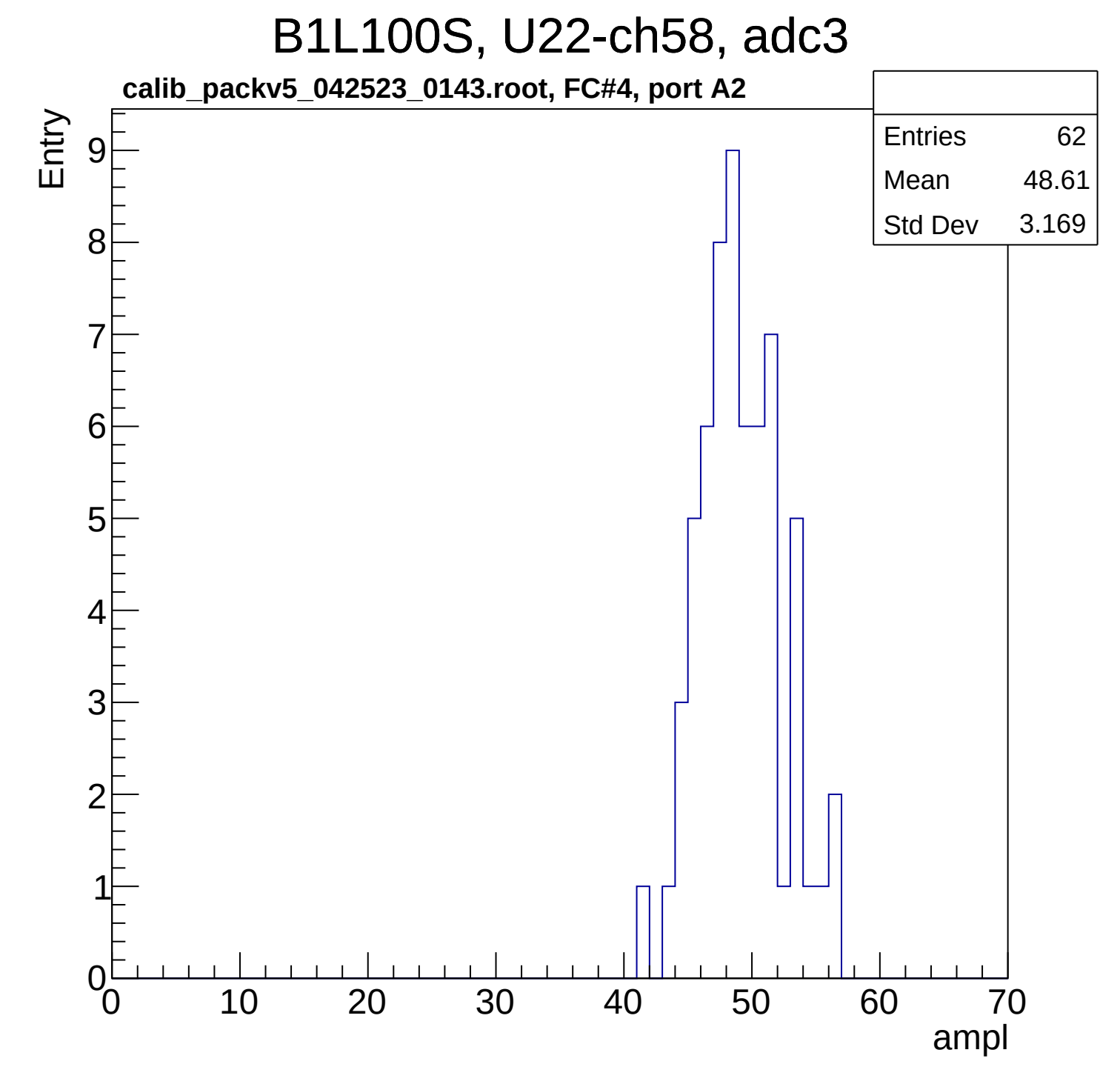
B1L100S, U22-ch58, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 48.61
Std Dev 3.169
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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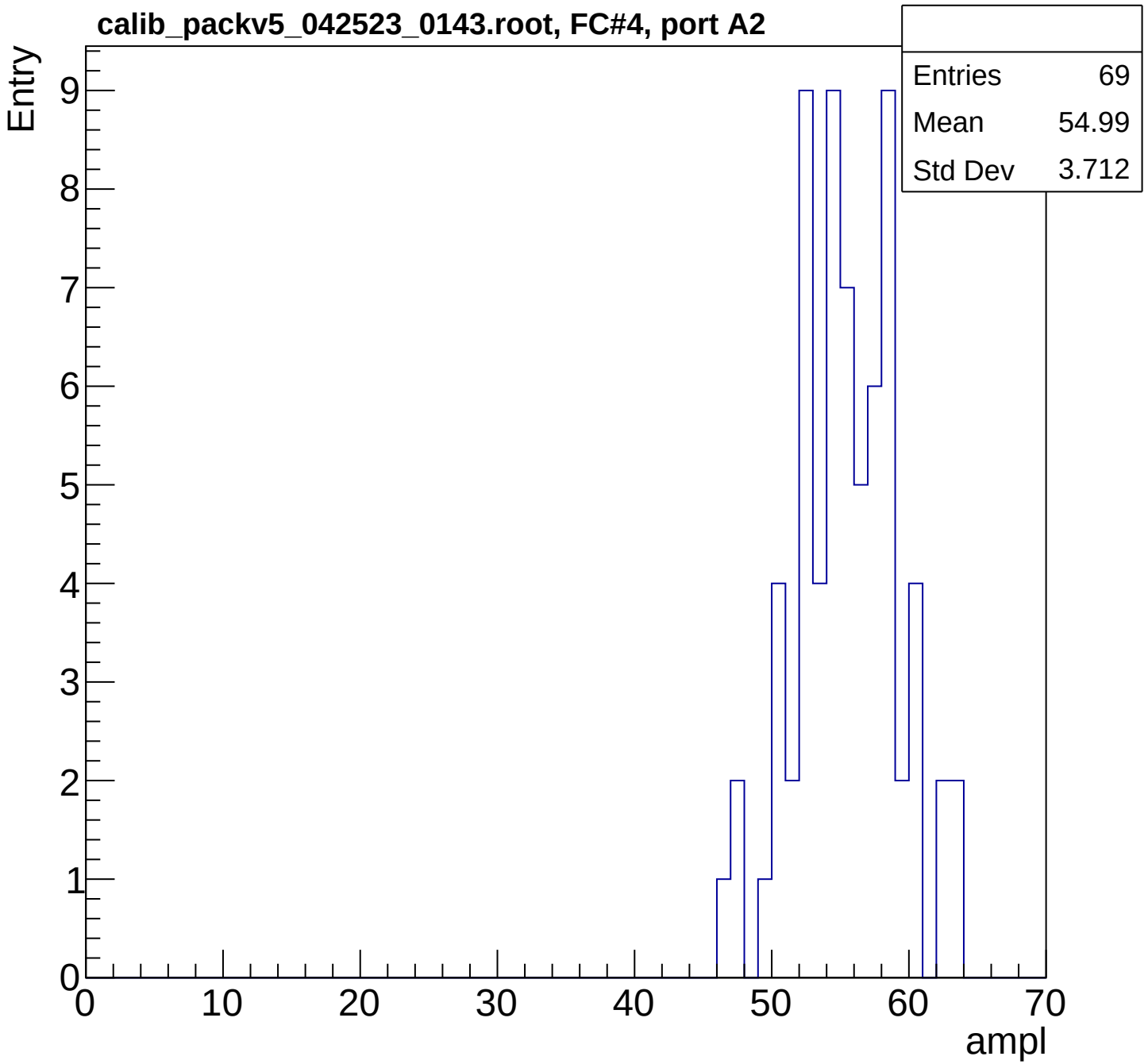
B1L100S, U22-ch58, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 54.99
Std Dev 3.712
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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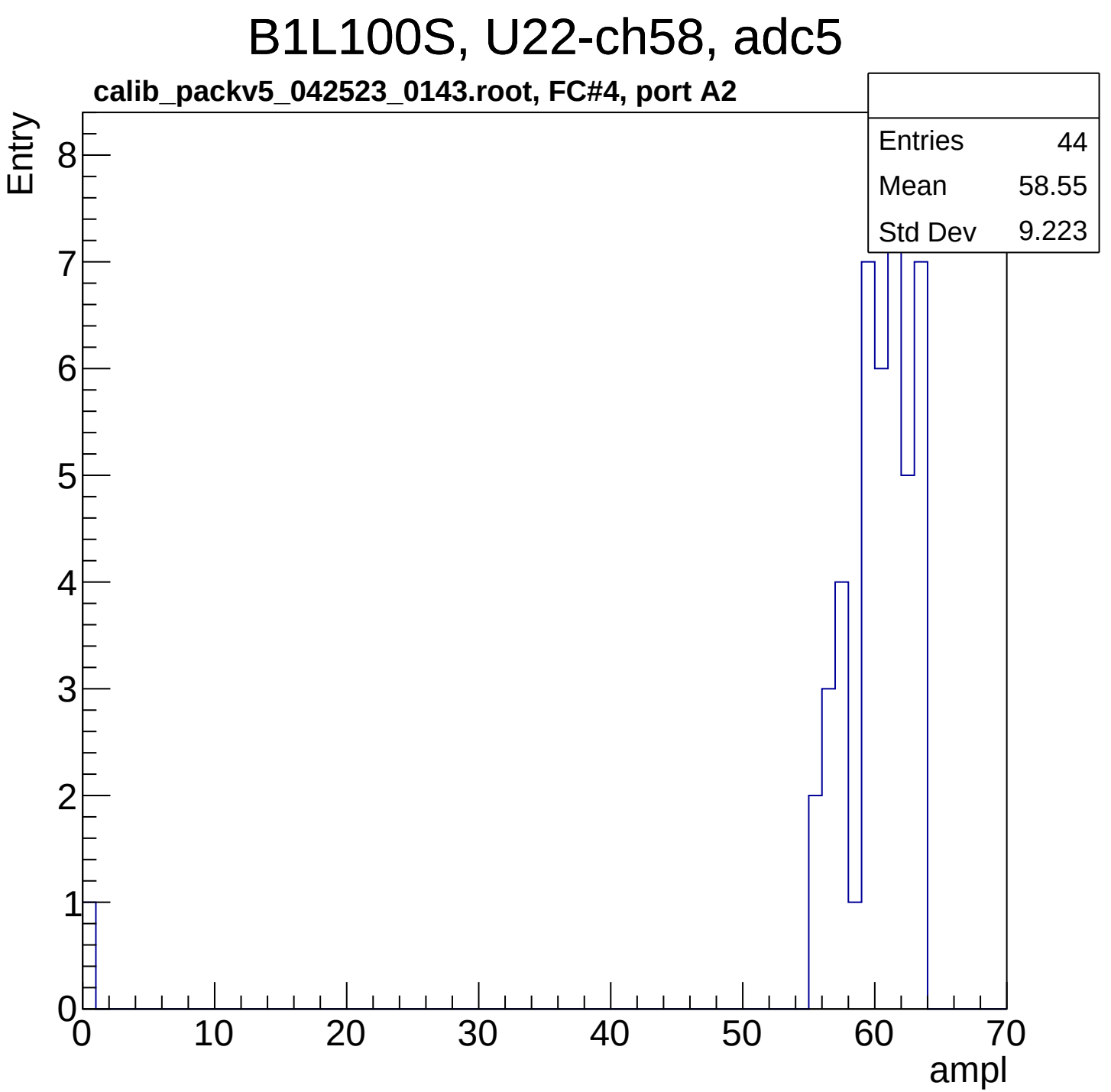
B1L100S, U22-ch58, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.55
Std Dev 9.223
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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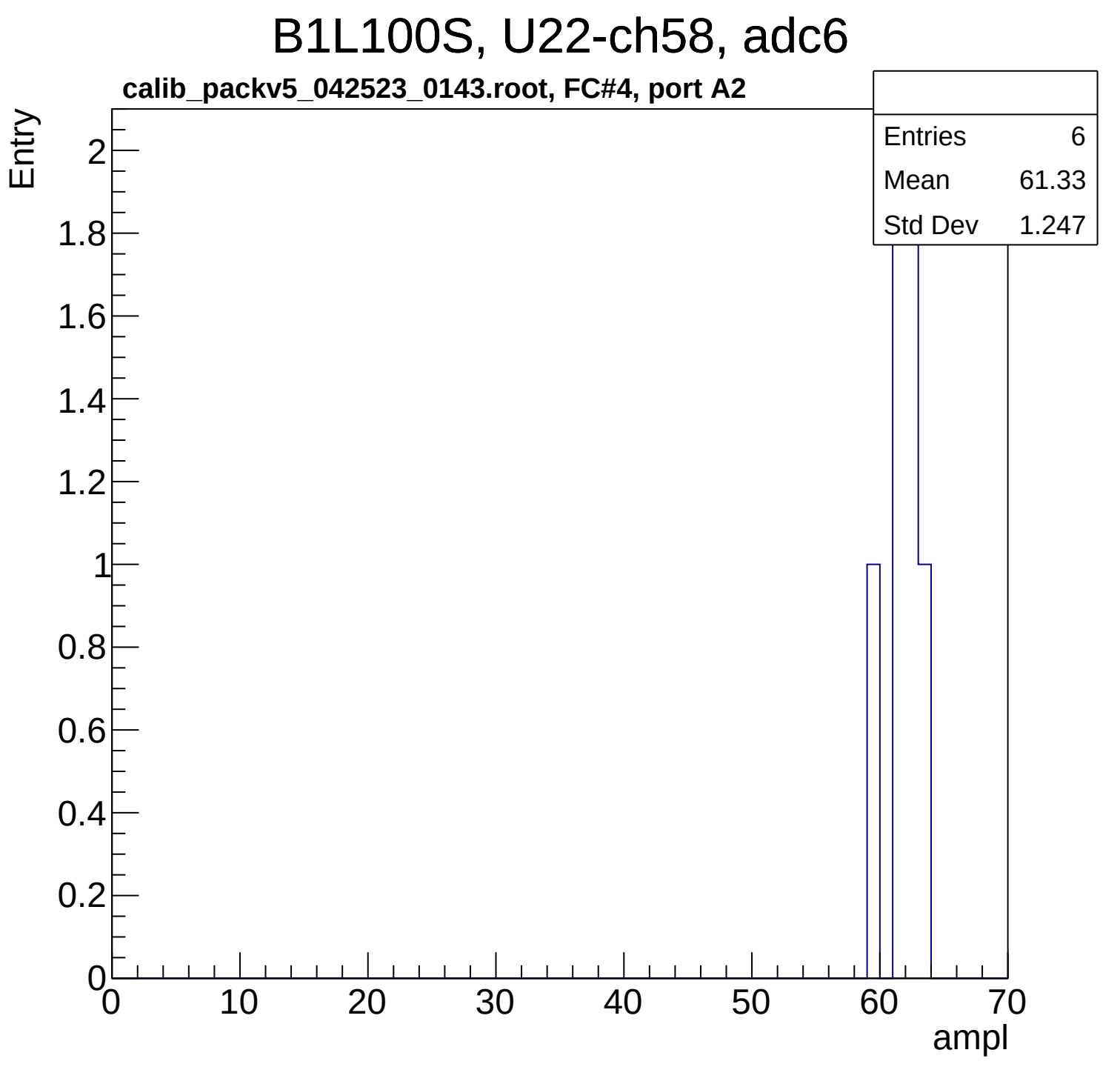
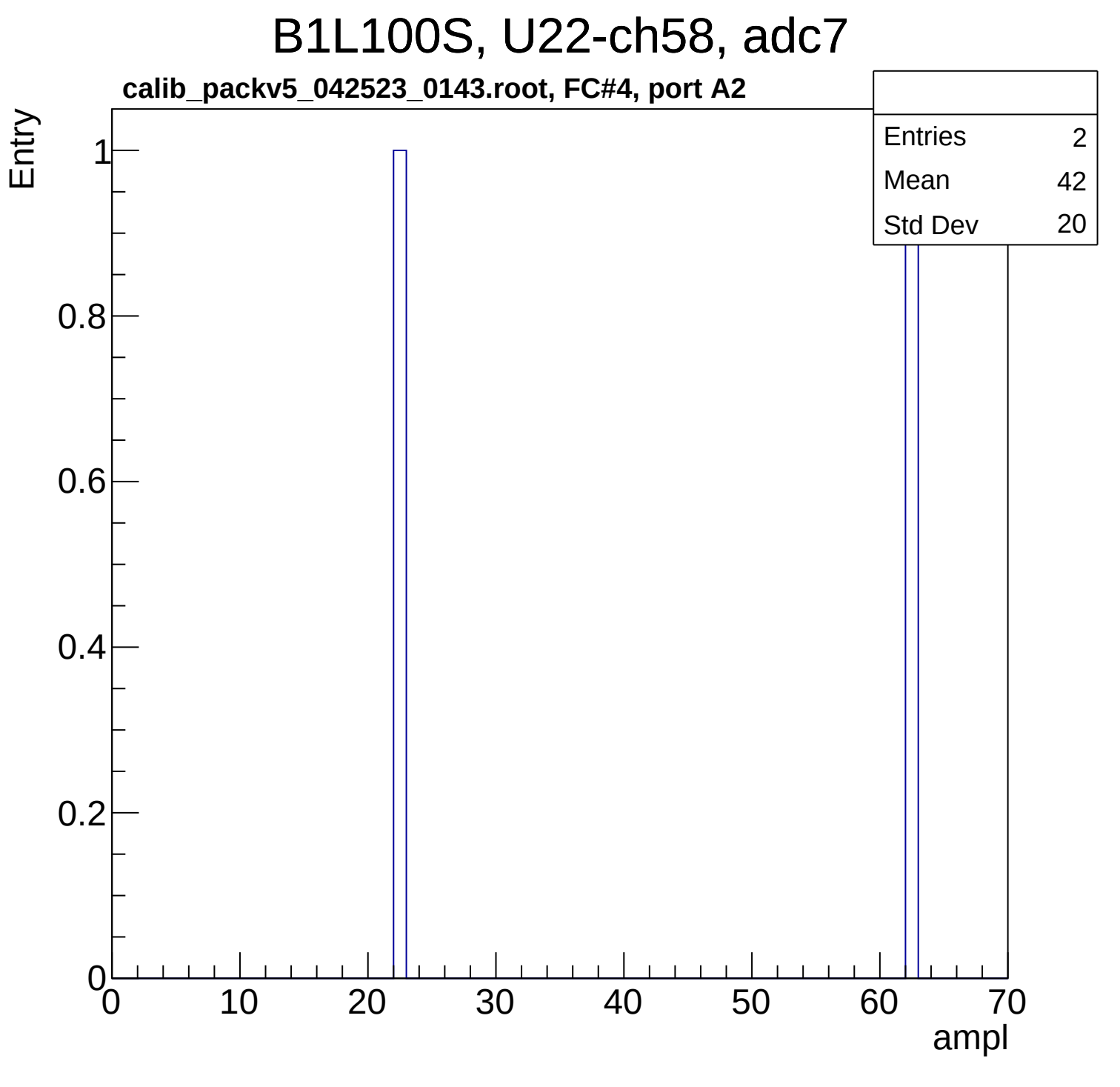
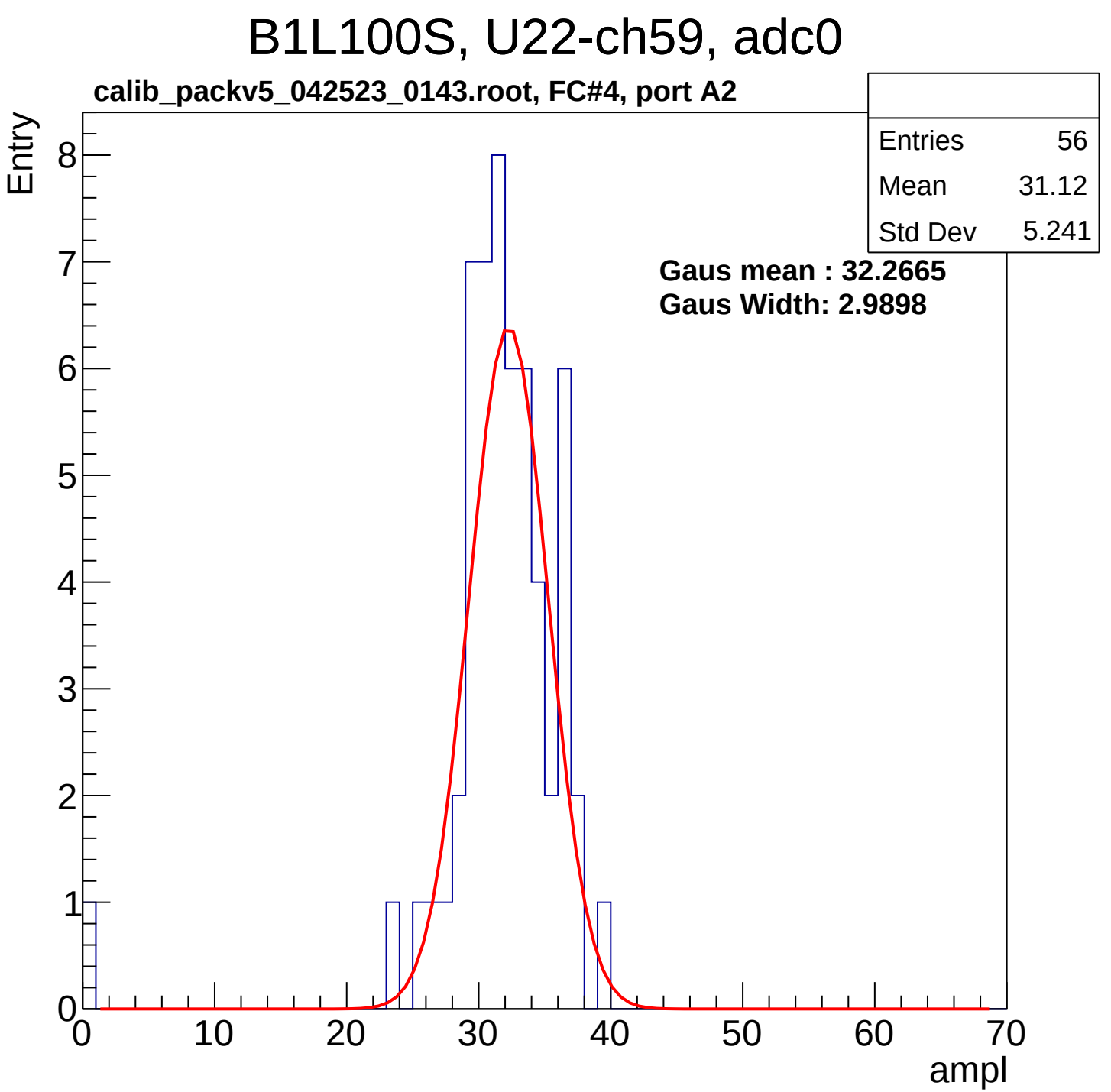
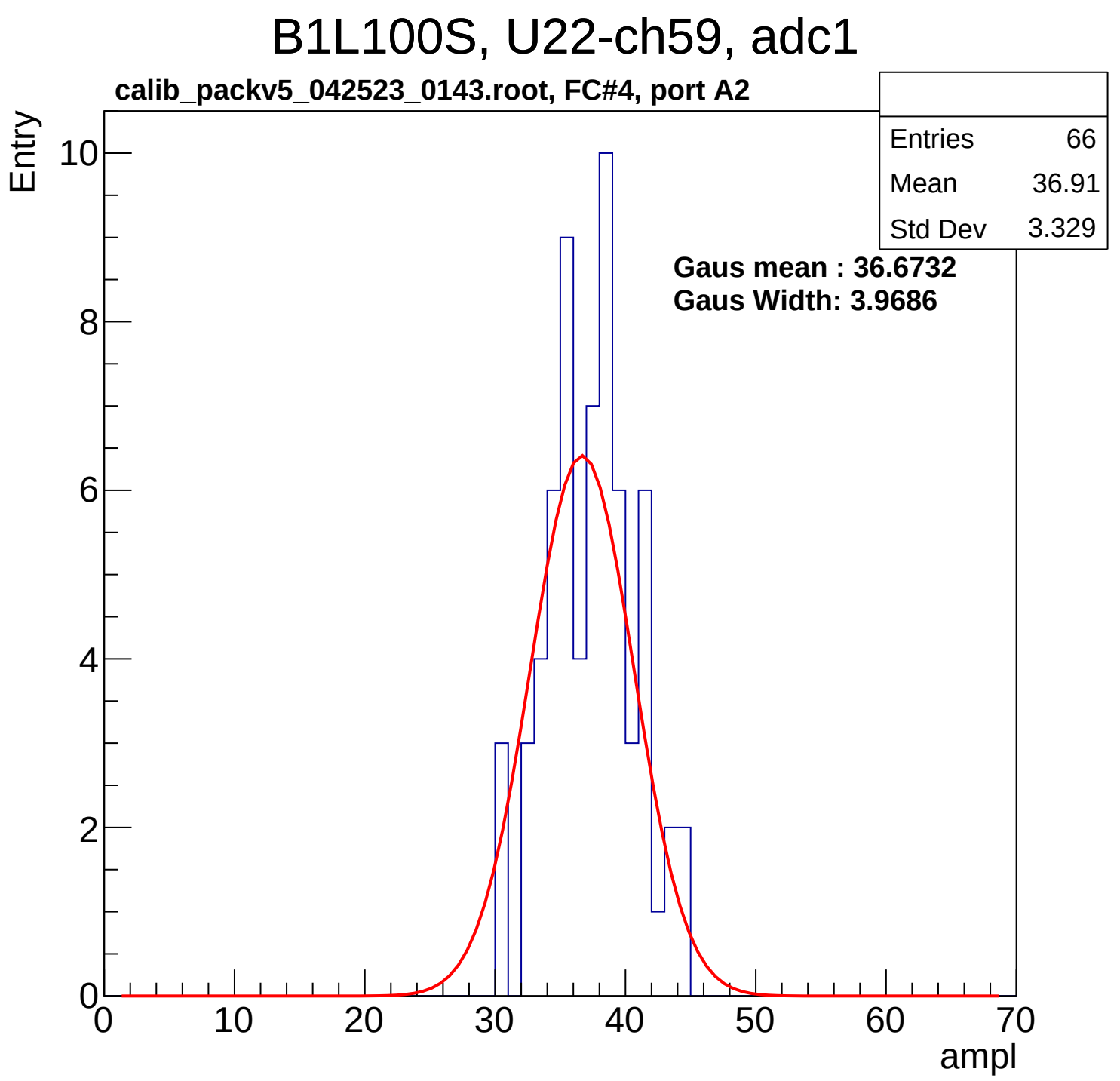

B1L100S, U22-ch58, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch58, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 42
Std Dev 20
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch59, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 31.12
Std Dev 5.241
Gaus mean : 32.2665
Gaus Width: 2.9898
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch59, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 36.91
Std Dev 3.329
Gaus mean : 36.6732
Gaus Width: 3.9686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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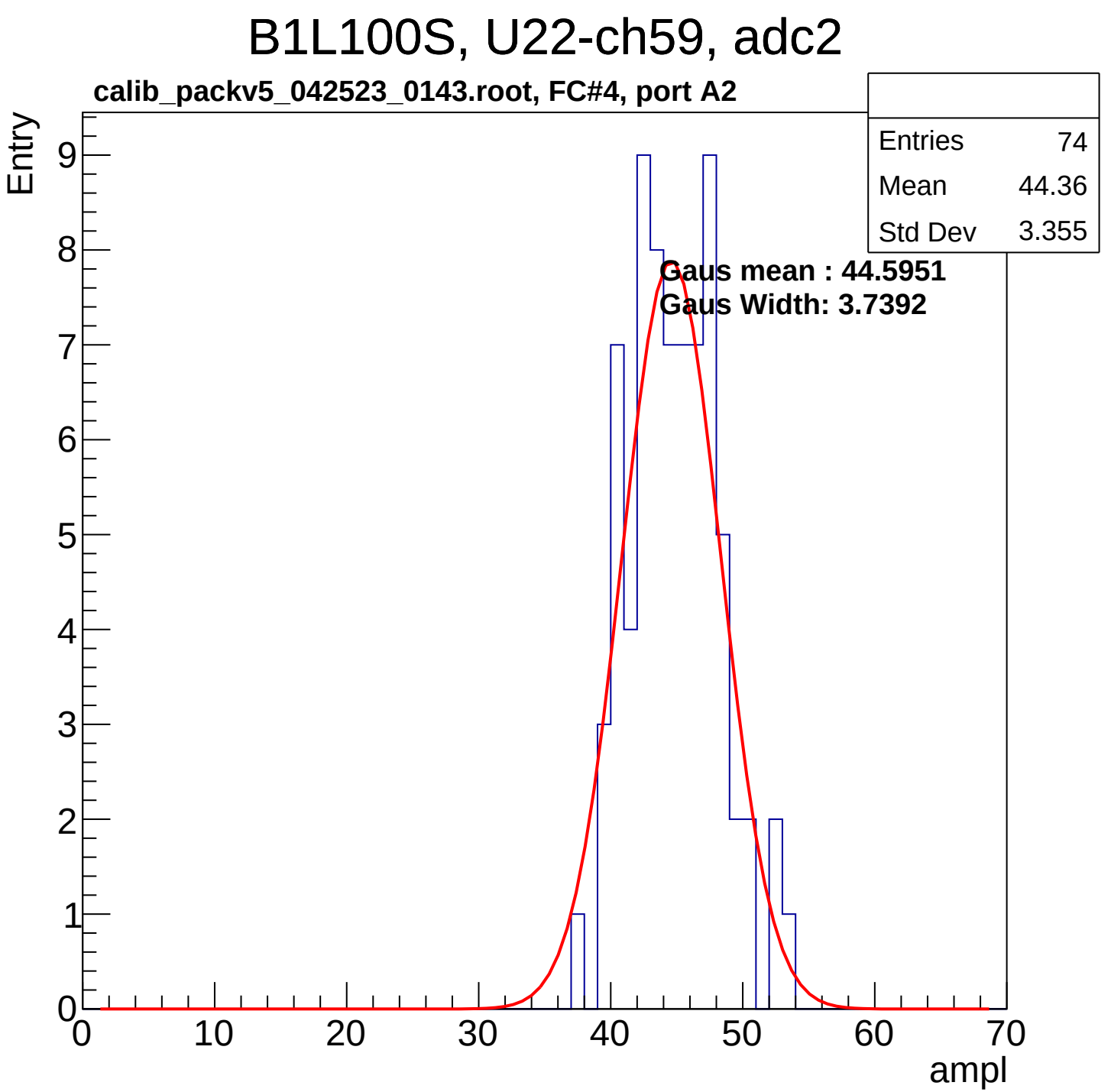
B1L100S, U22-ch59, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 44.36
Std Dev 3.355
Gaus mean : 44.5951
Gaus Width: 3.7392
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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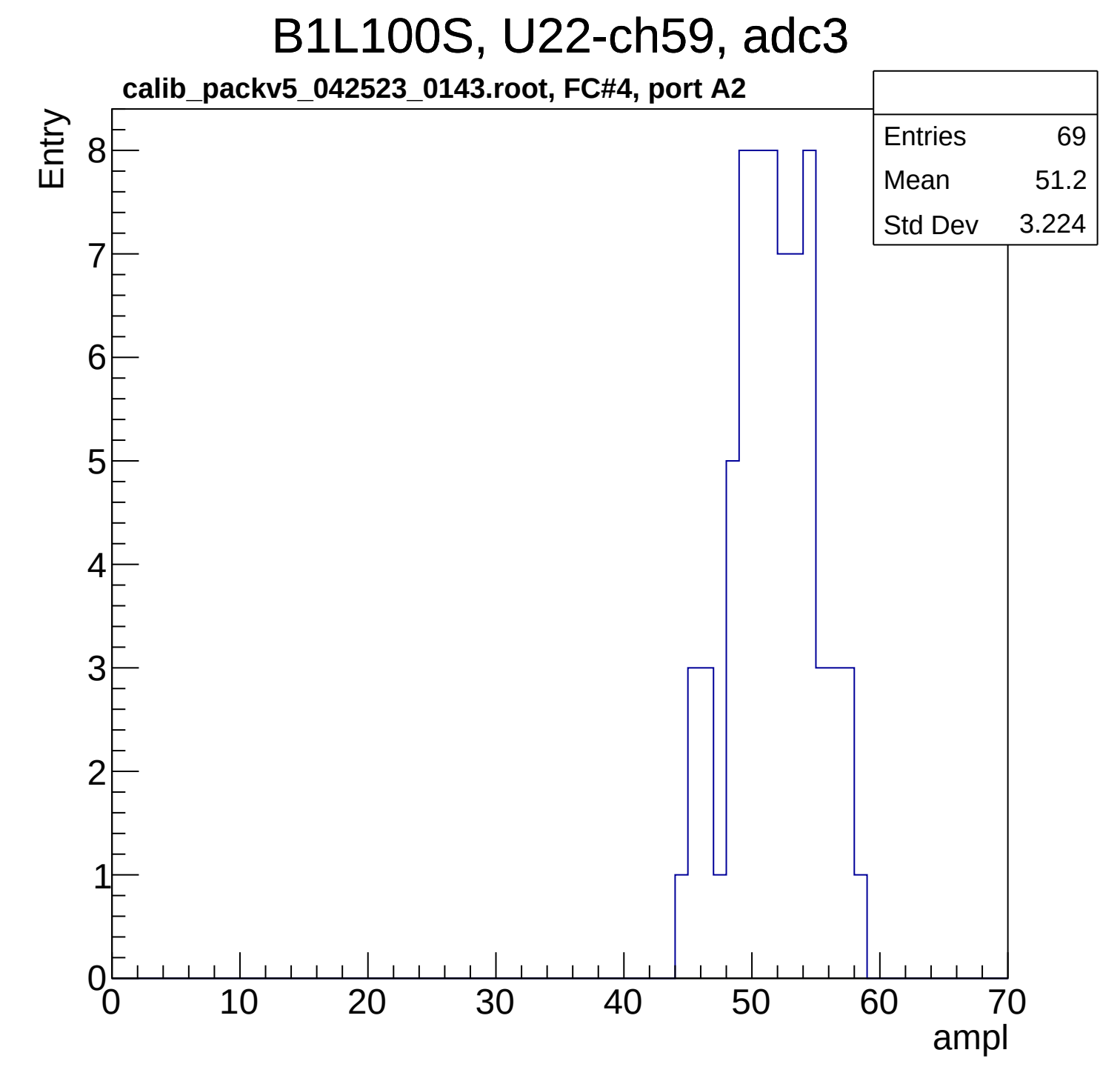
B1L100S, U22-ch59, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 51.2
Std Dev 3.224
Entry
ampl
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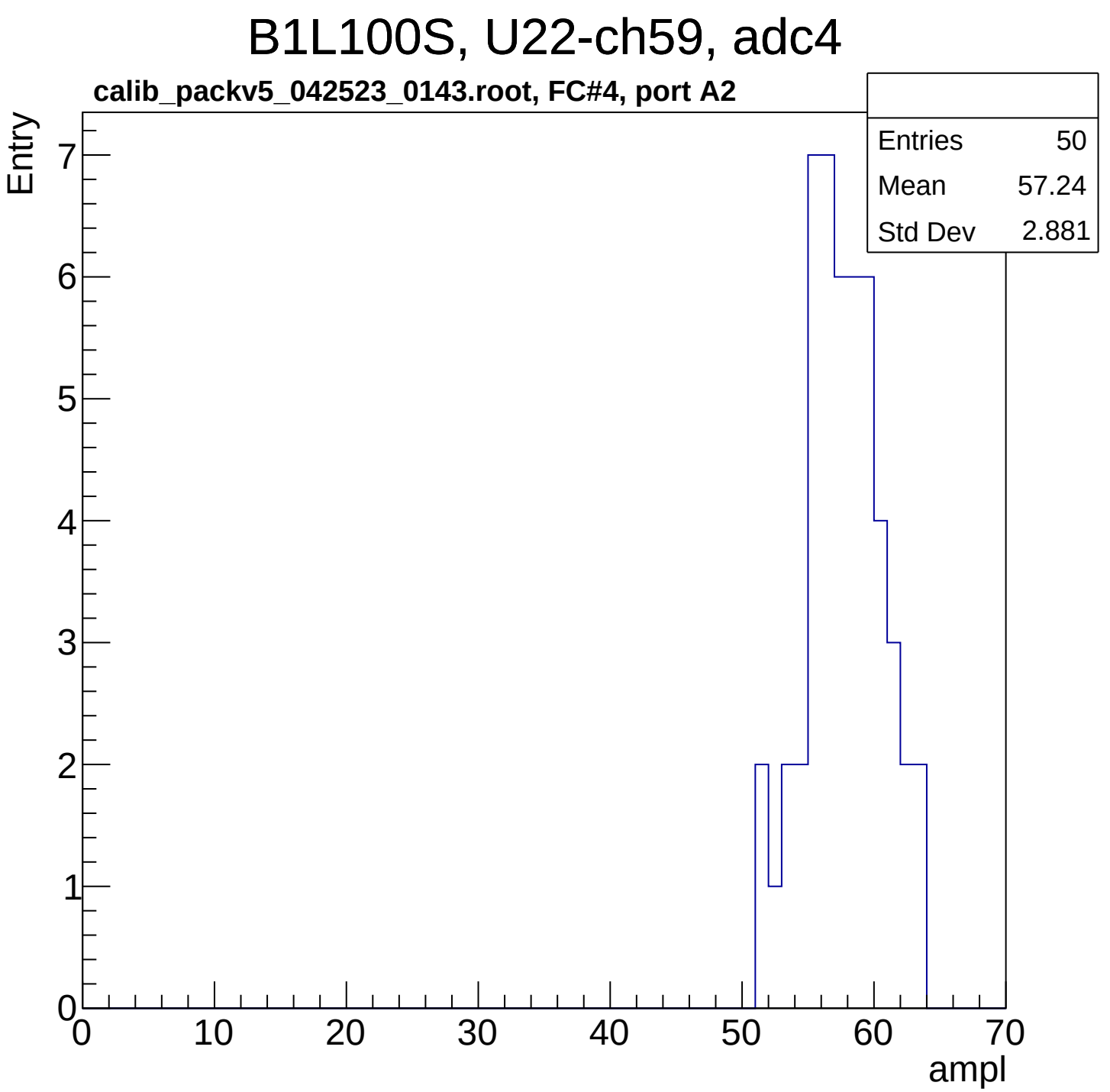
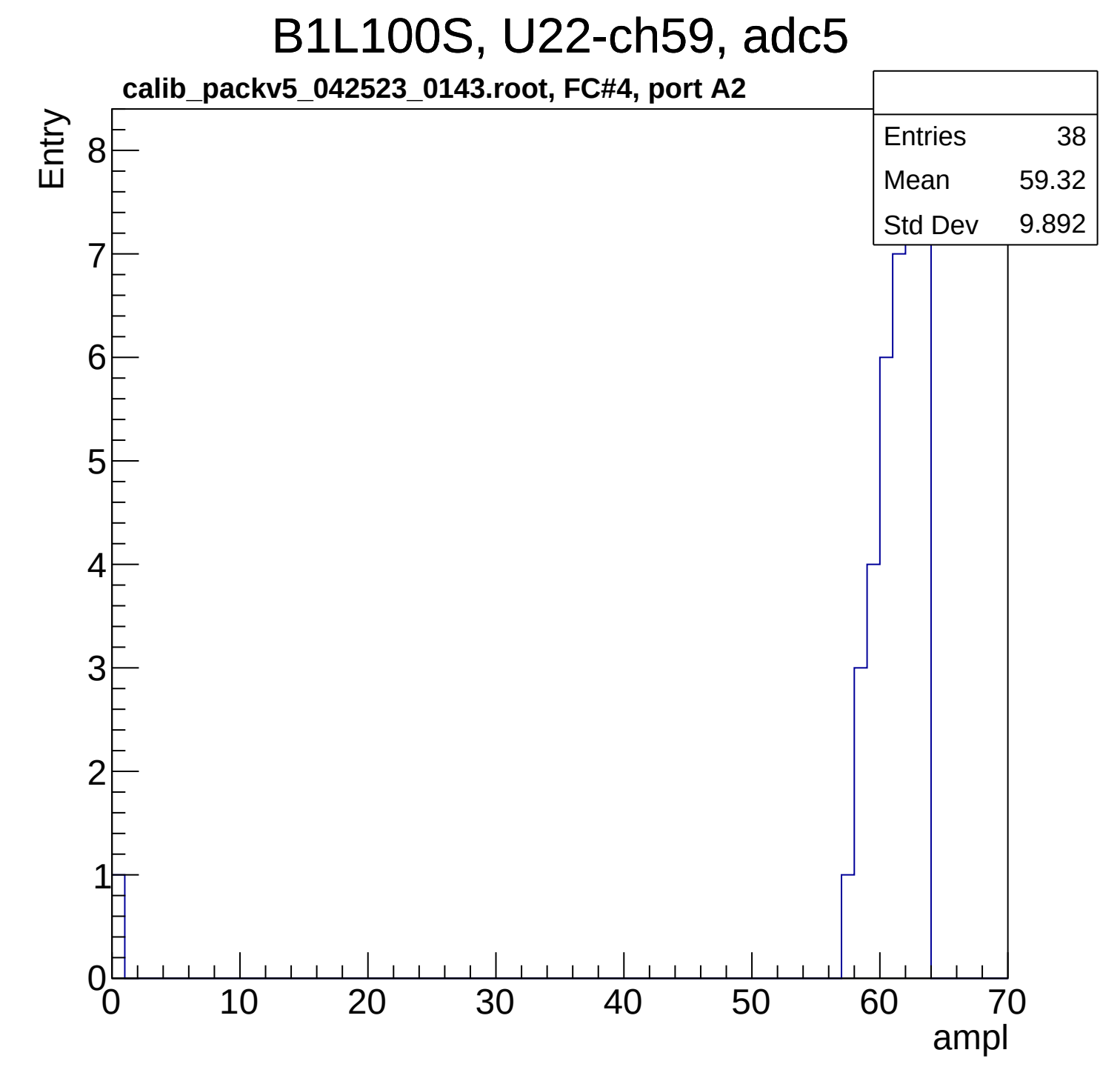
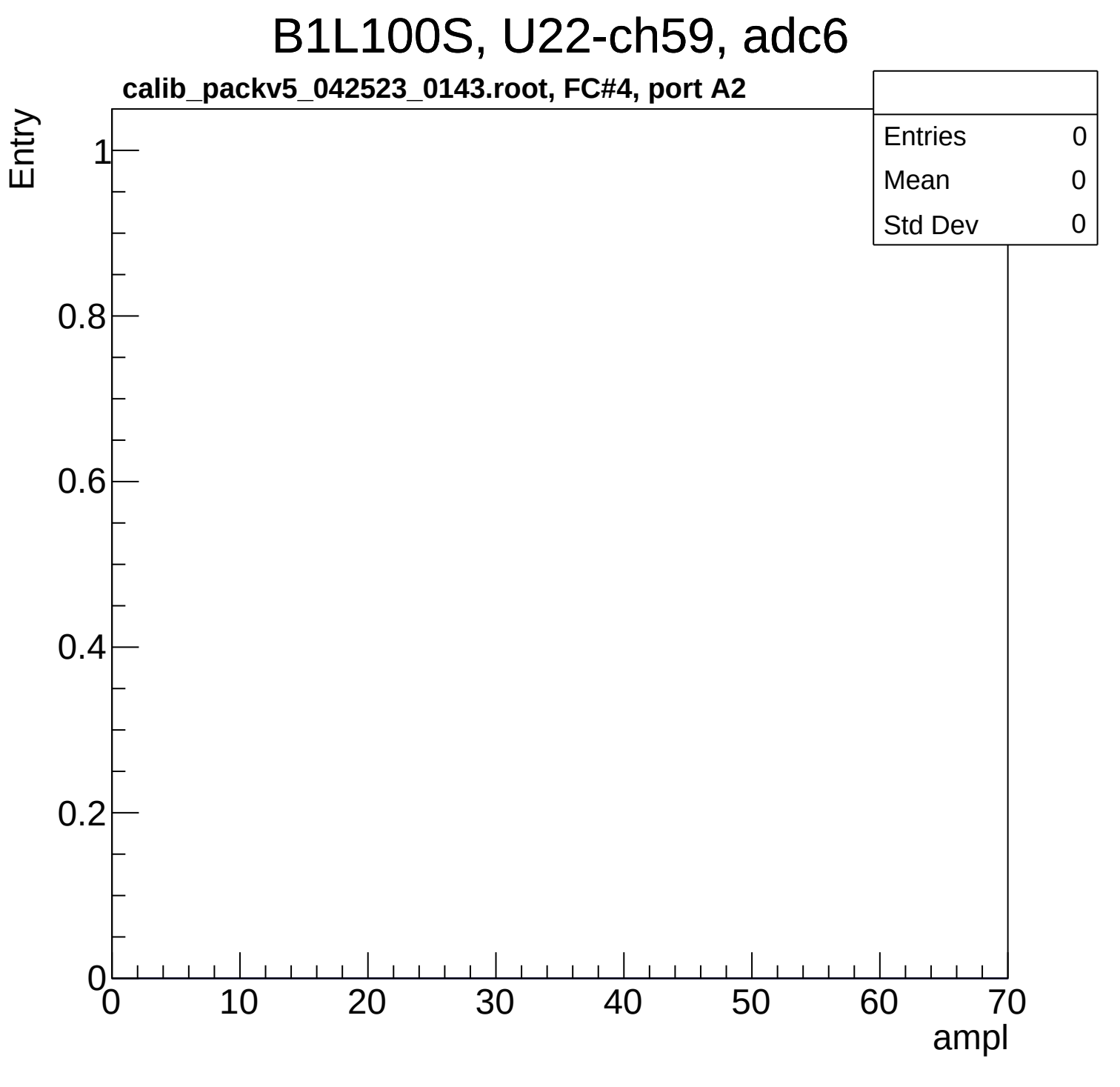
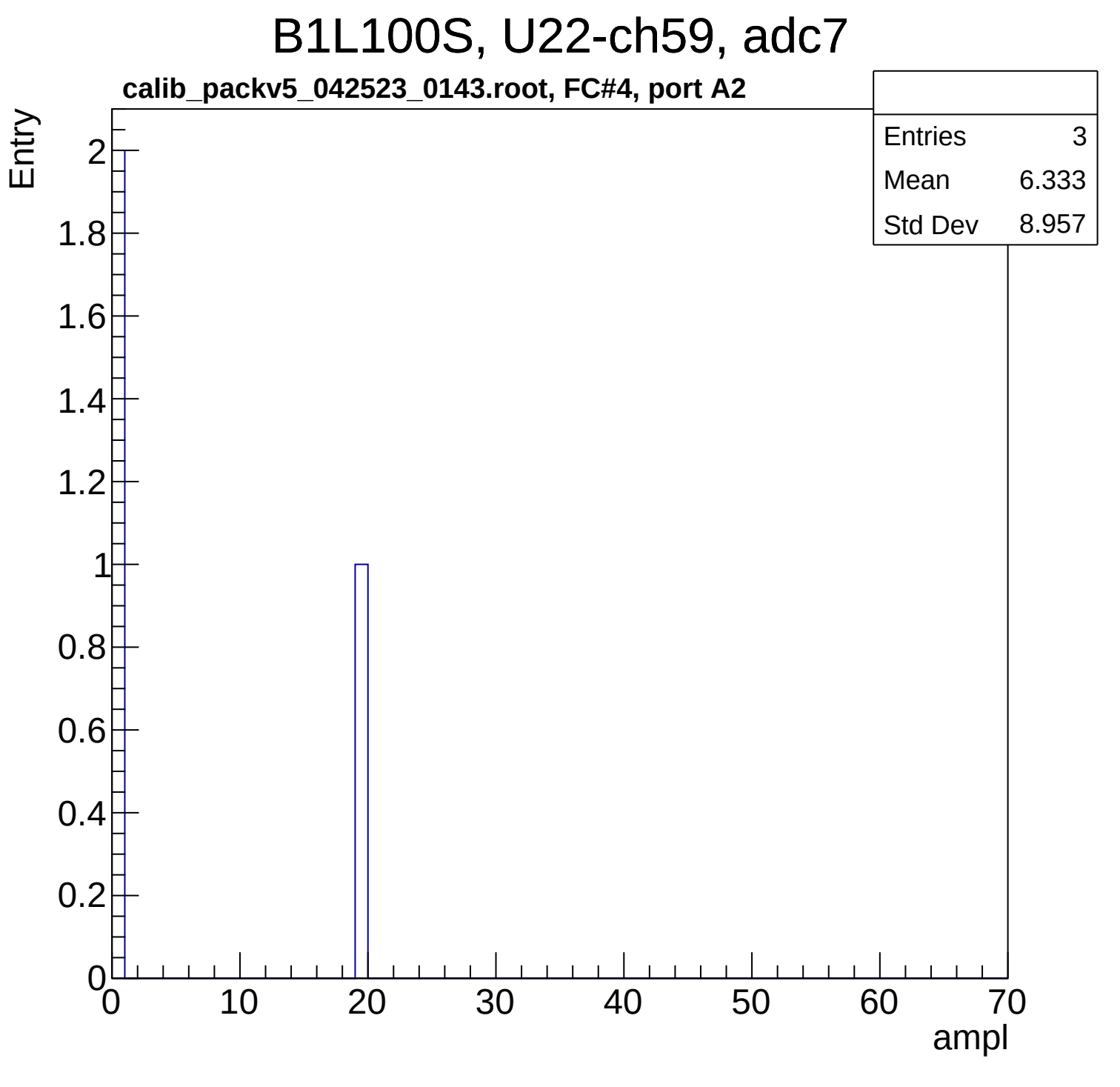
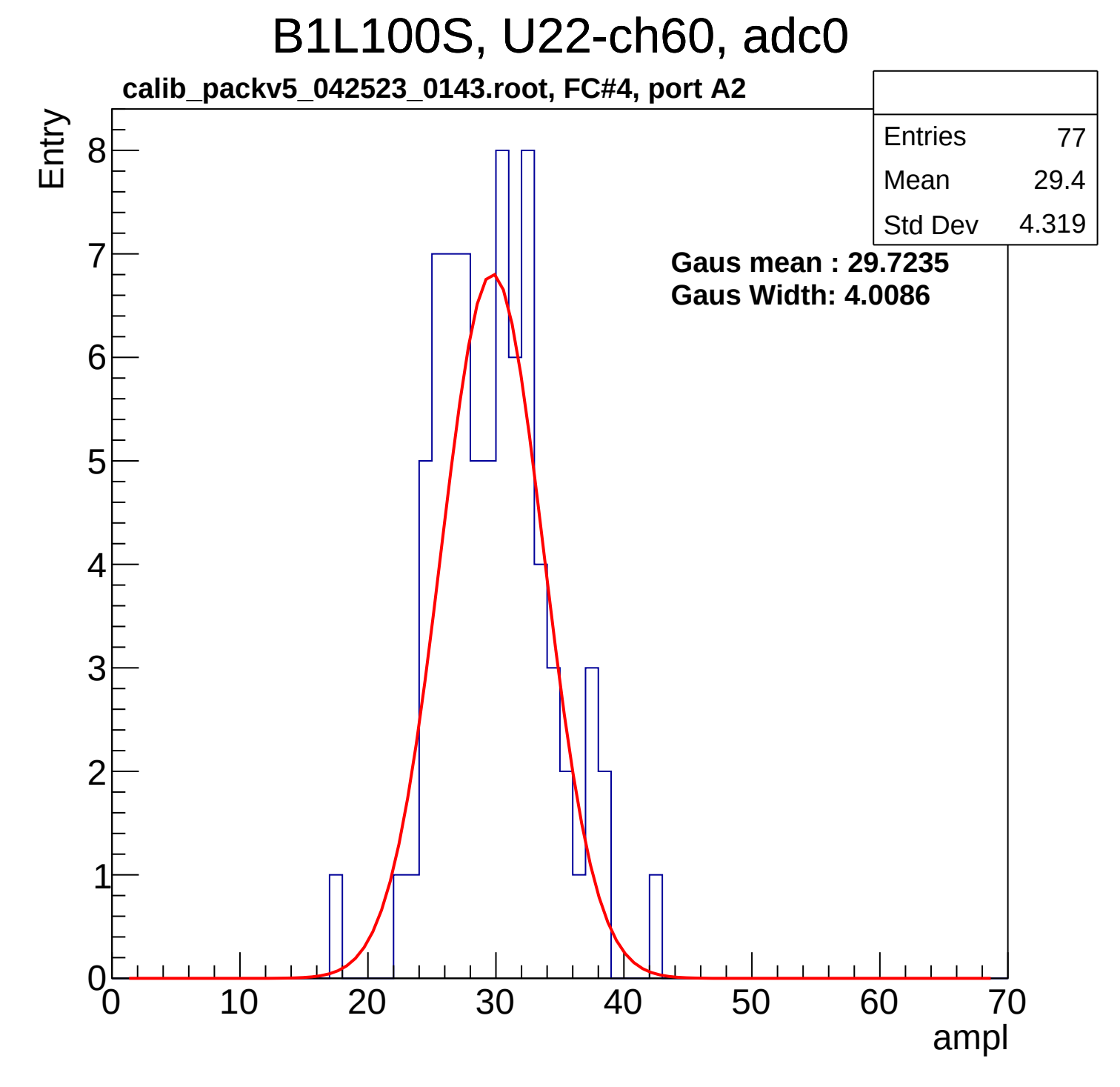
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

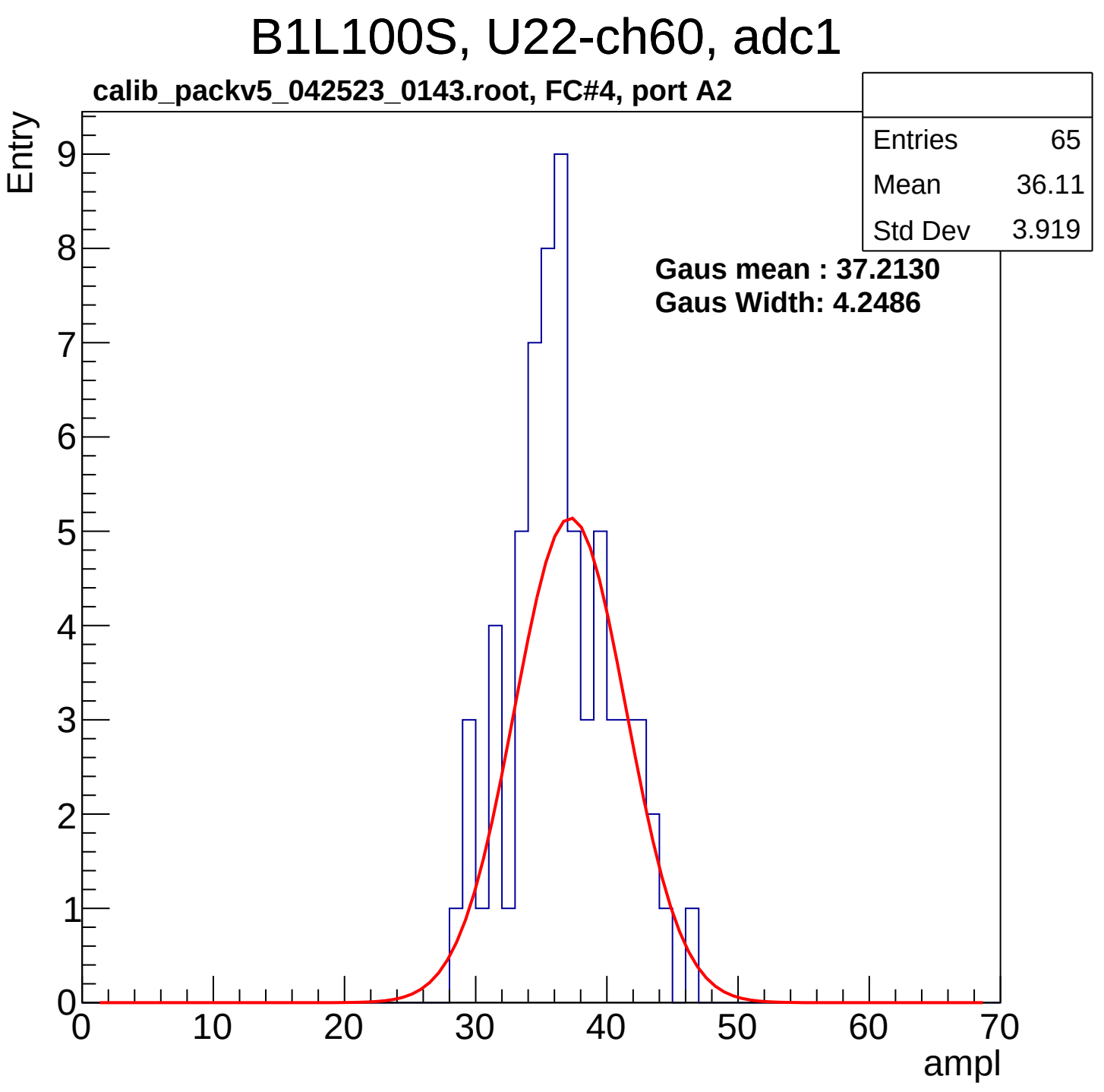

B1L100S, U22-ch59, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 57.24
Std Dev 2.881
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch59, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.32
Std Dev 9.892
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch59, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch59, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 6.333
Std Dev 8.957
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch60, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 29.4
Std Dev 4.319
Gaus mean : 29.7235
Gaus Width: 4.0086
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch60, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 36.11
Std Dev 3.919
Gaus mean : 37.2130
Gaus Width: 4.2486
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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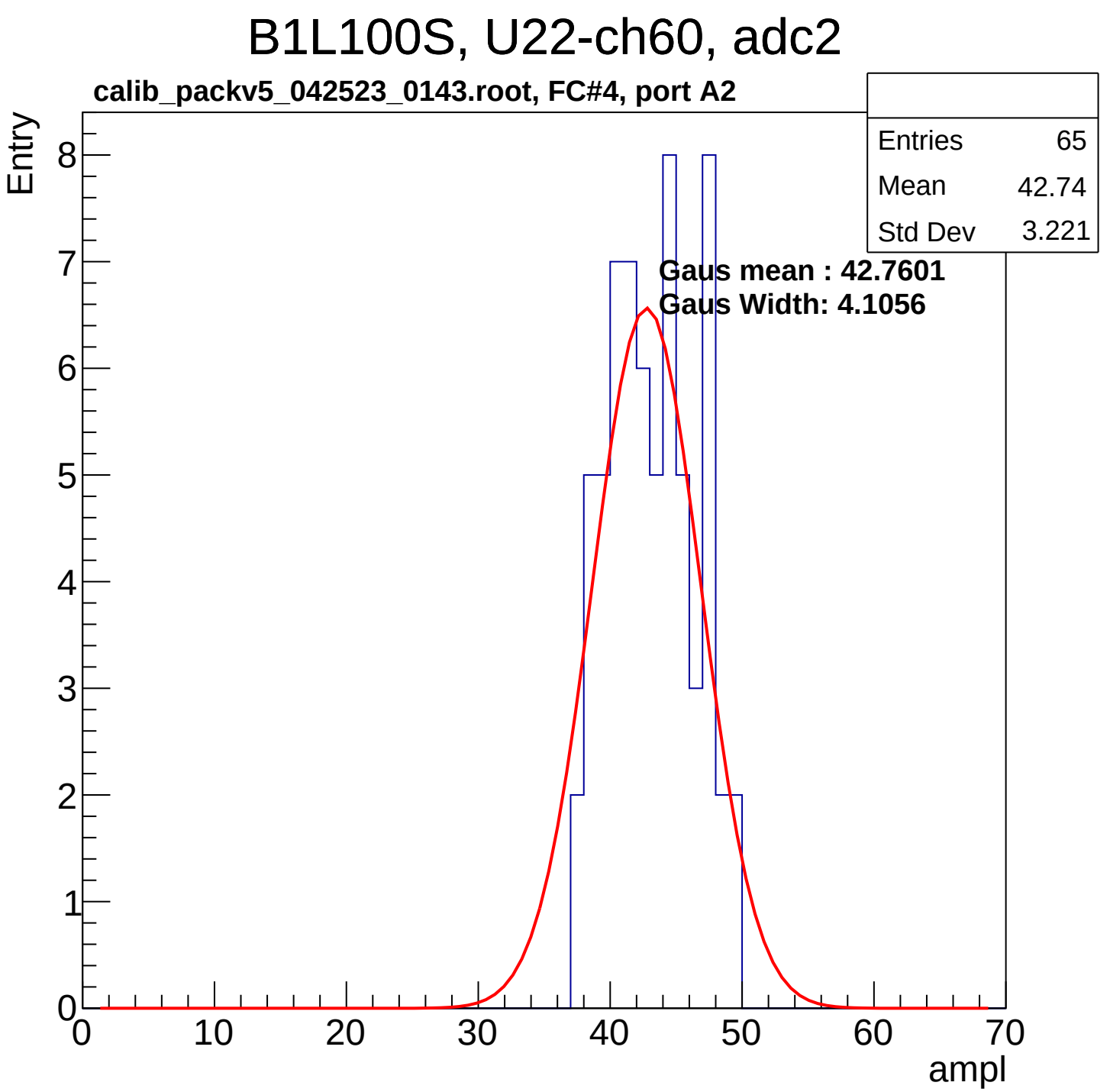
B1L100S, U22-ch60, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.74
Std Dev 3.221
Gaus mean : 42.7601
Gaus Width: 4.1056
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
30
40
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60
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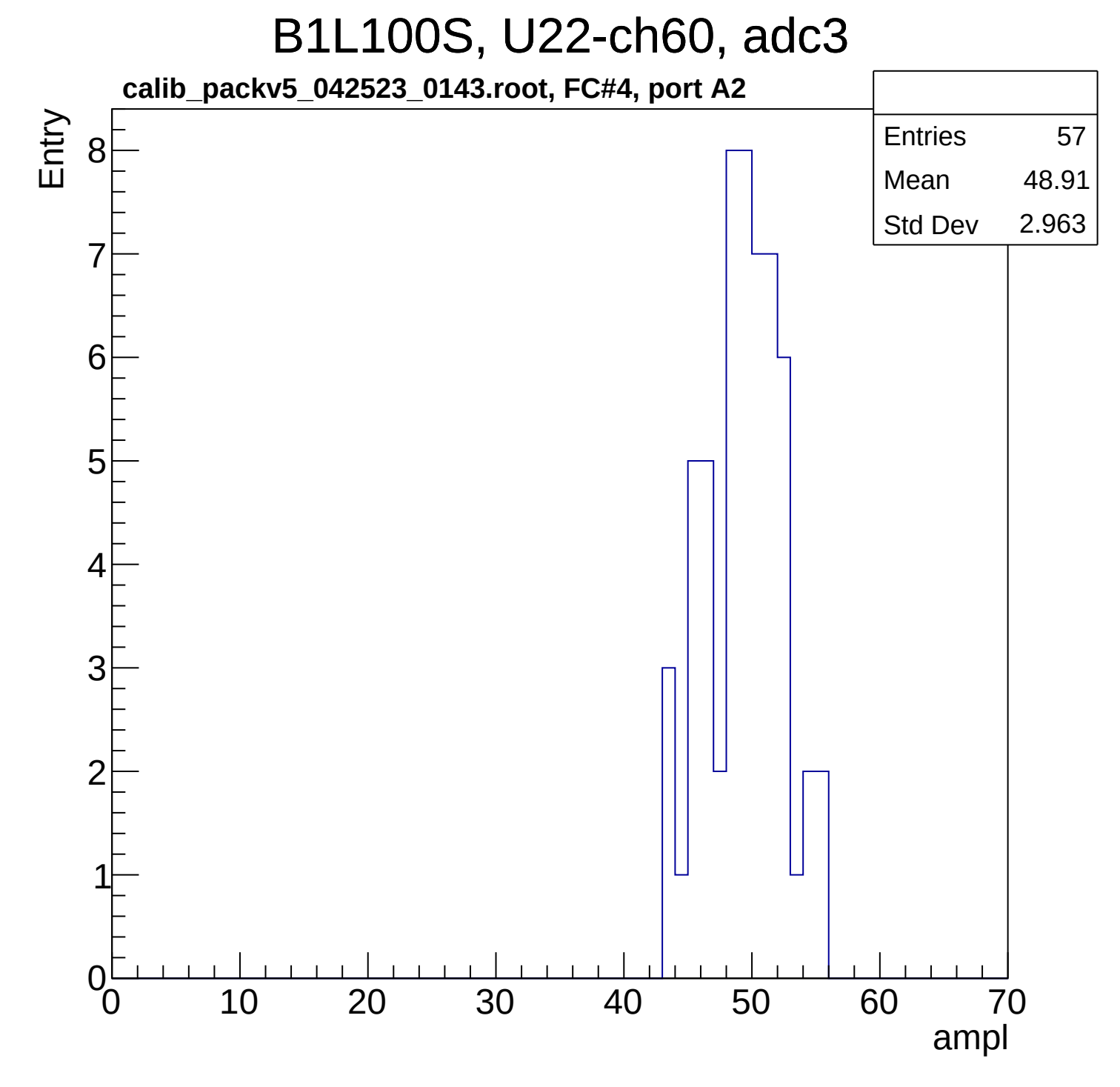
B1L100S, U22-ch60, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 48.91
Std Dev 2.963
Entry
ampl
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1
2
3
4
5
6
7
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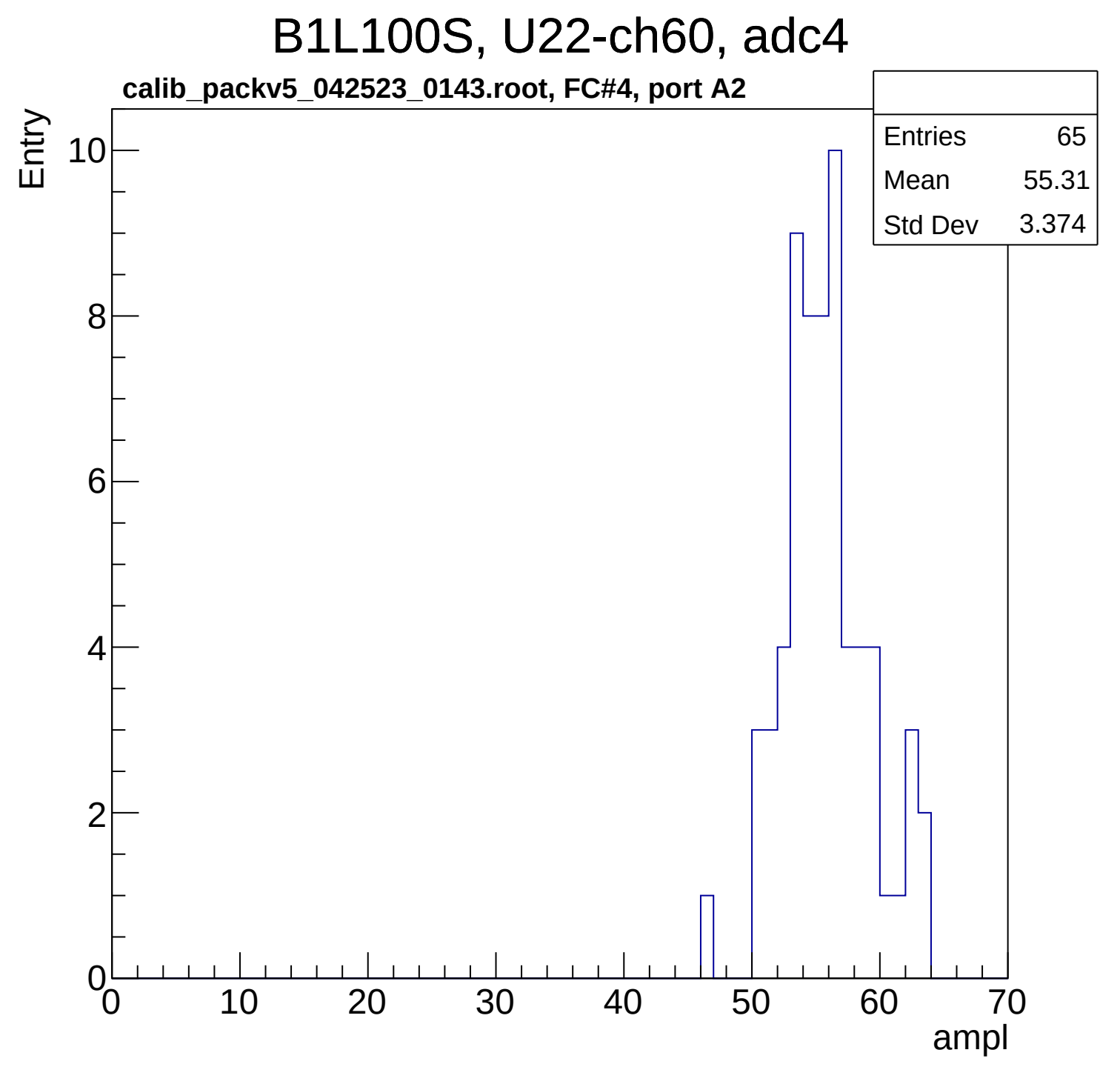
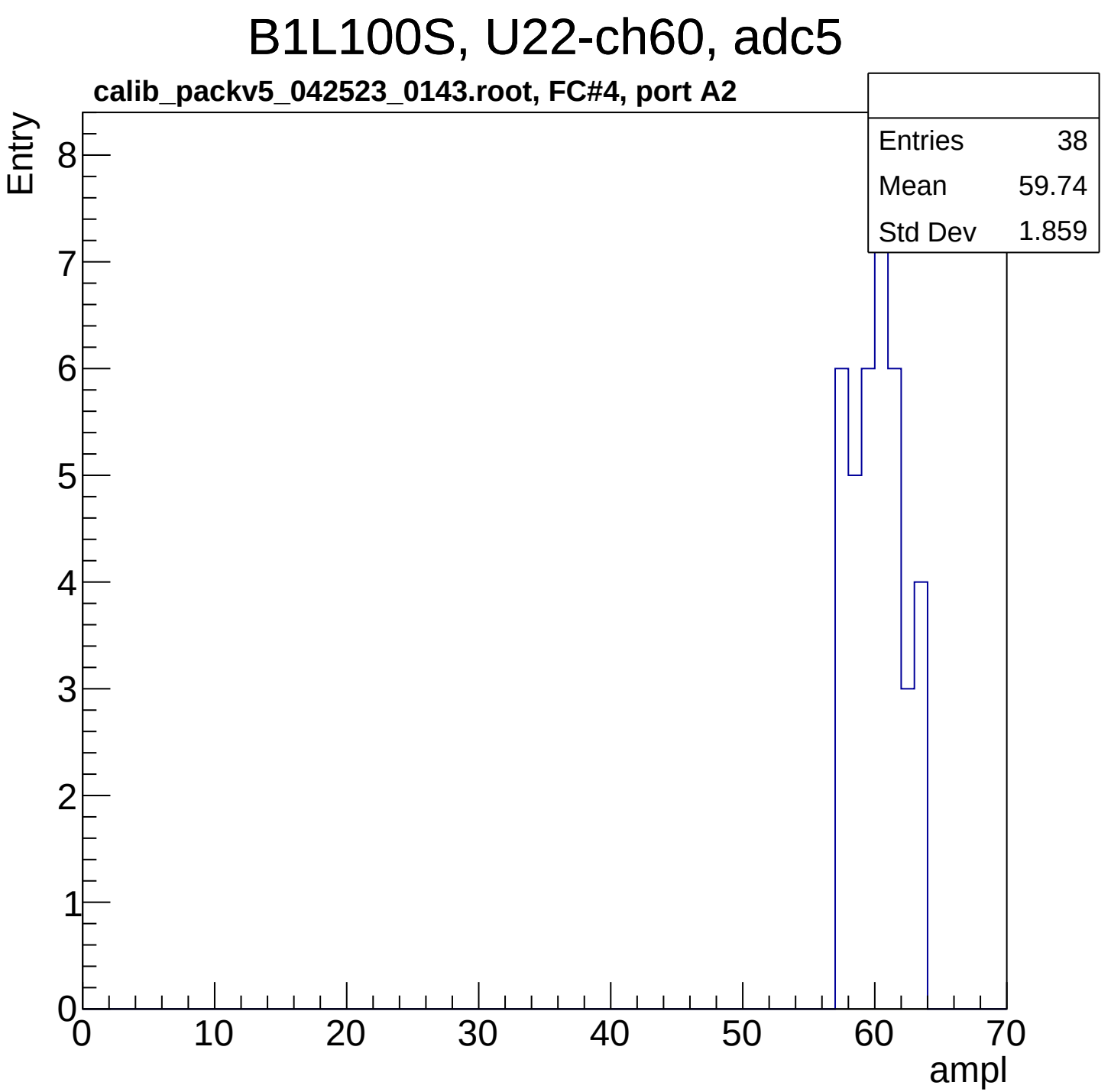
0
10
20
30
40
50
60
70


B1L100S, U22-ch60, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 55.31
Std Dev 3.374
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch60, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.74
Std Dev 1.859
Entry
ampl
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1
2
3
4
5
6
7
8
0
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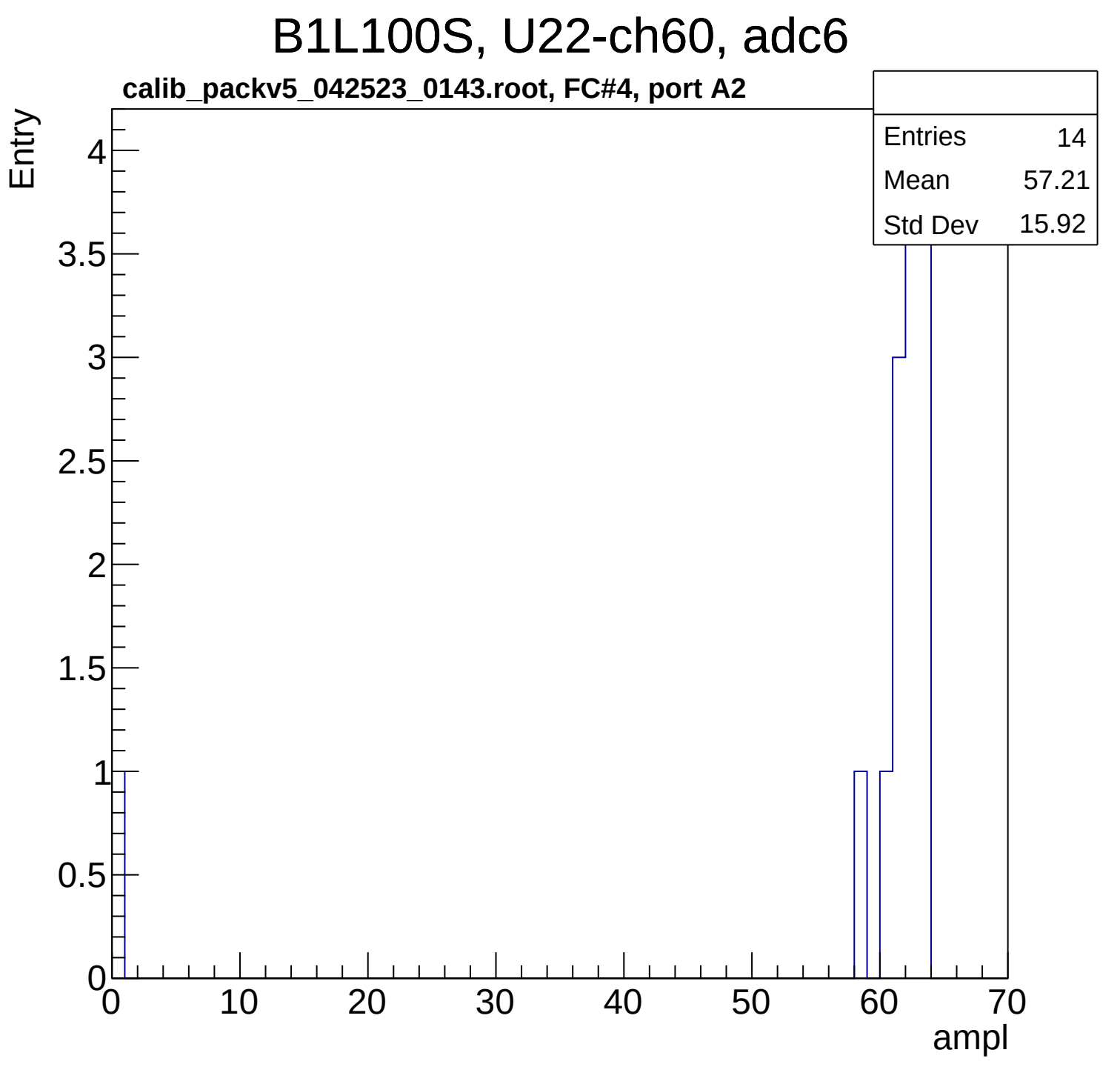
20
30
40
50
60
70


B1L100S, U22-ch60, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 14
Mean 57.21
Std Dev 15.92
Entry
ampl
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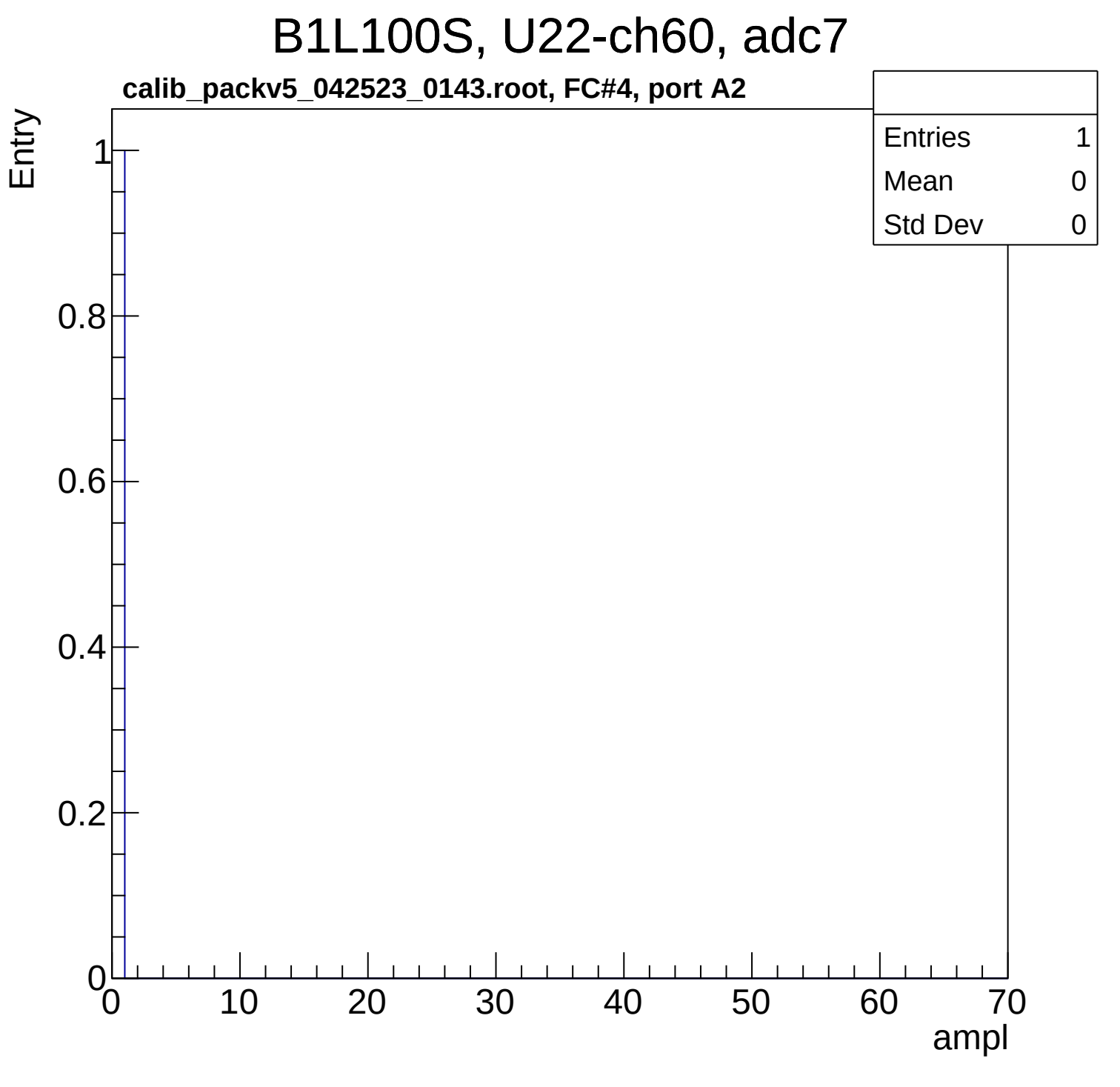
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch60, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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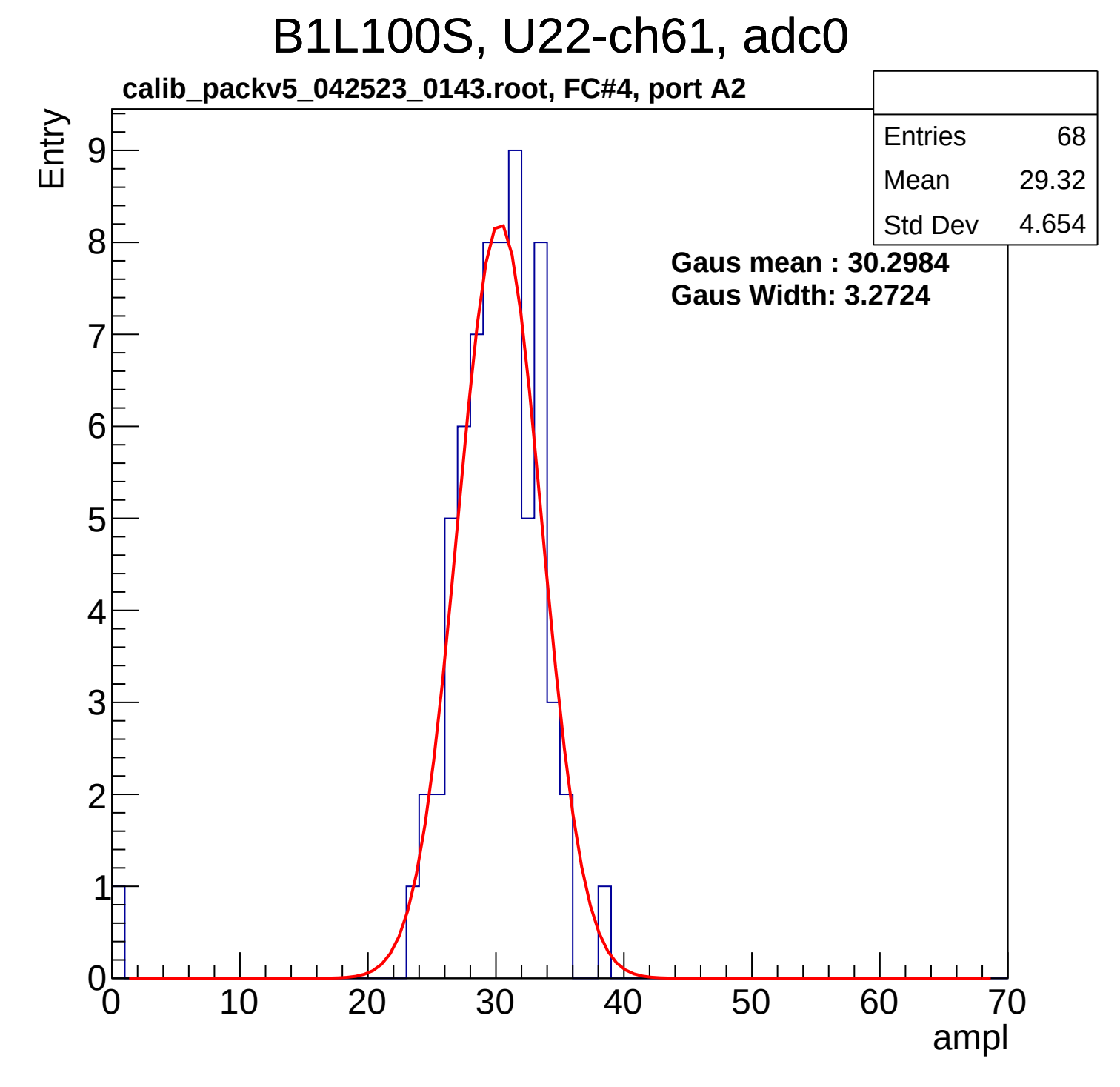
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch61, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 29.32
Std Dev 4.654
Gaus mean : 30.2984
Gaus Width: 3.2724
Entry
ampl
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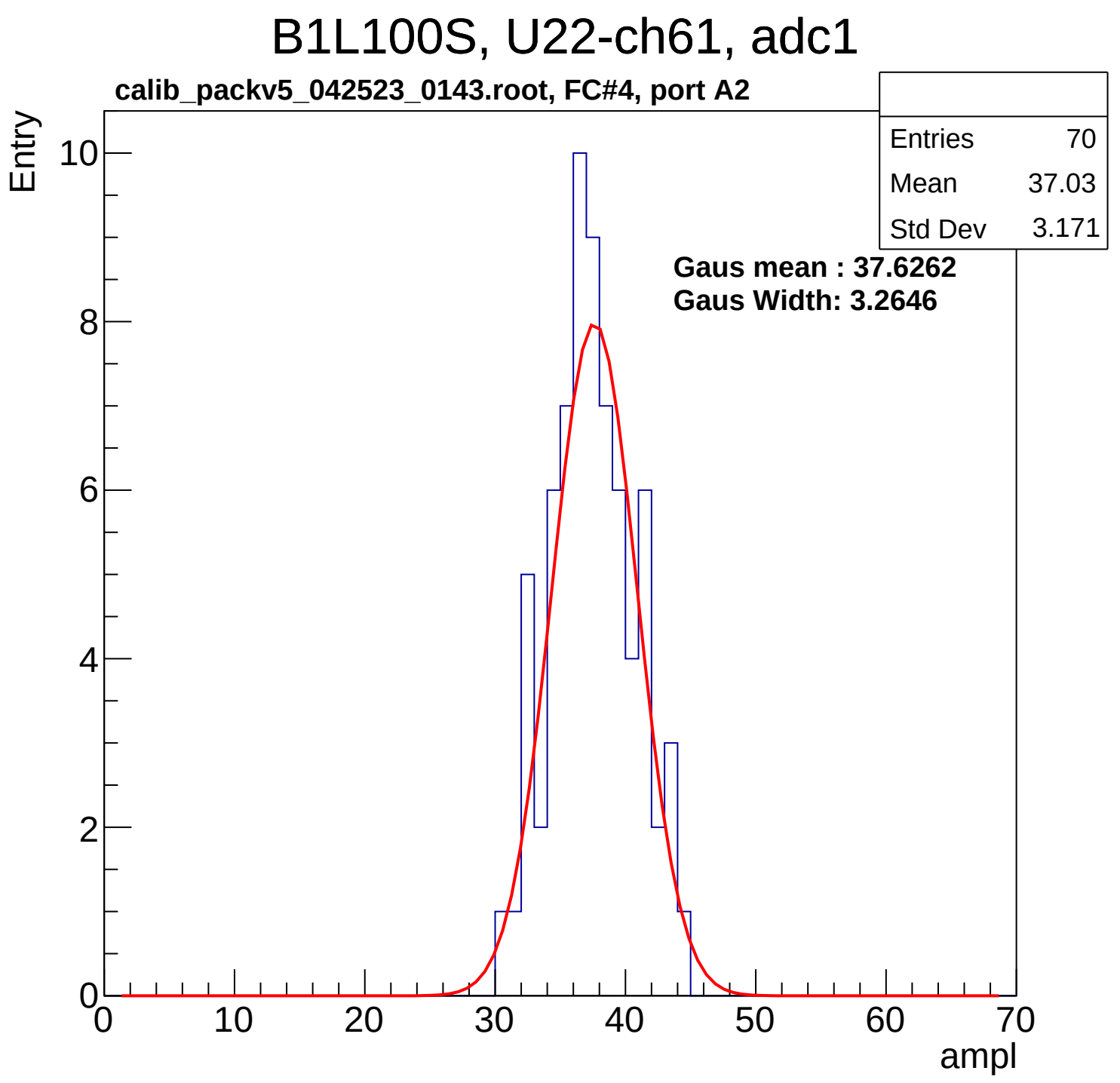
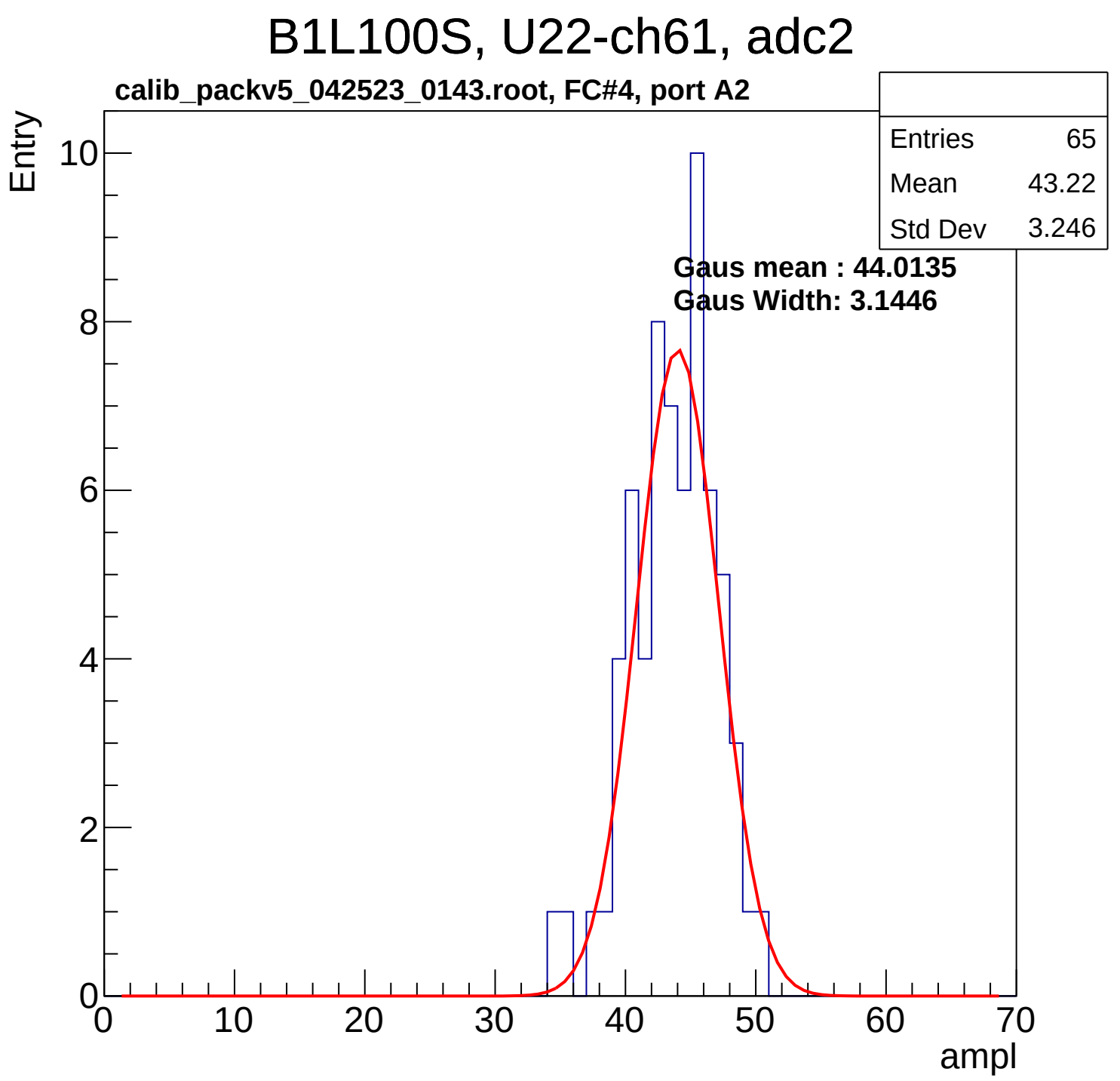
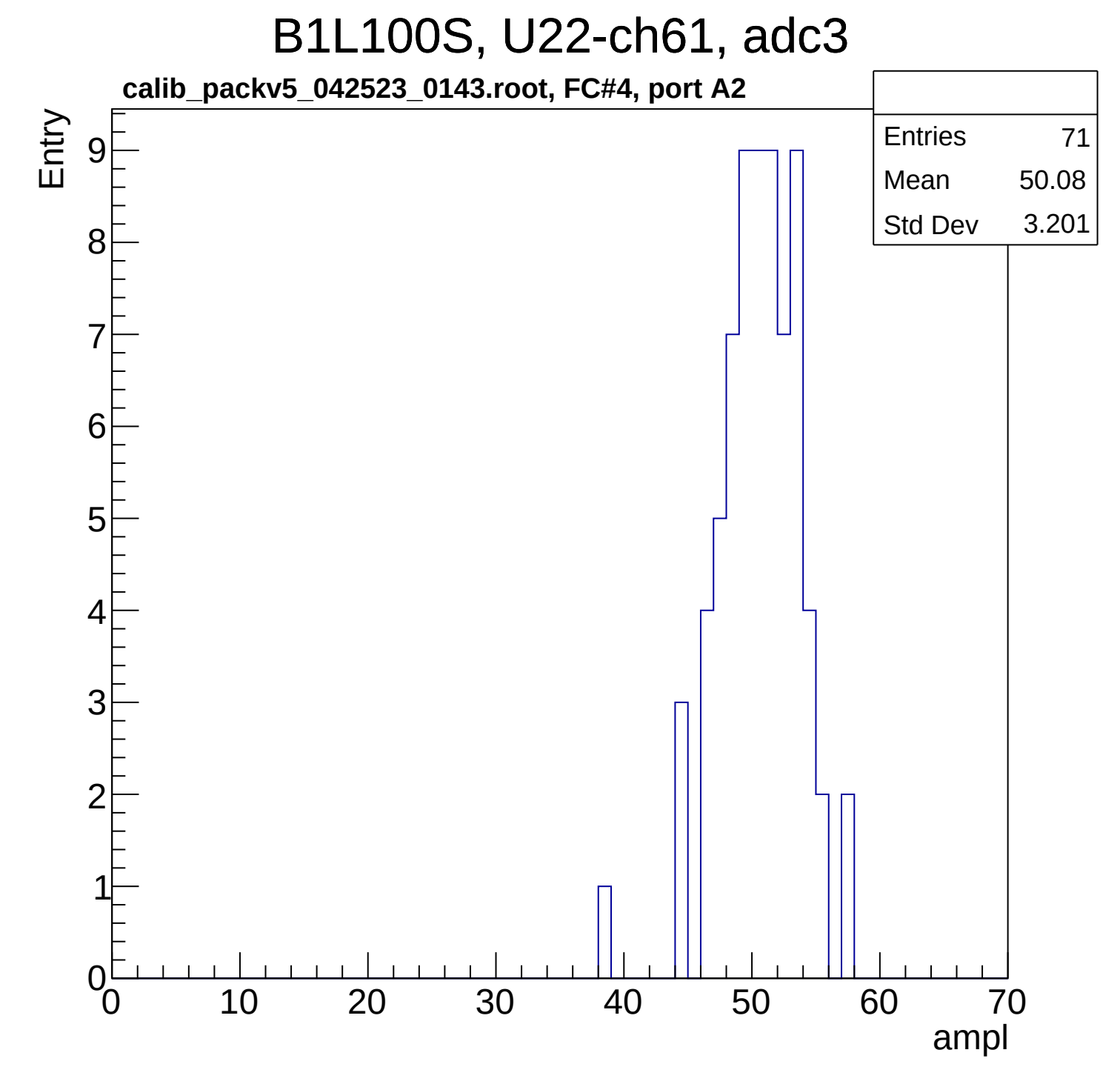
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch61, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 37.03
Std Dev 3.171
Gaus mean : 37.6262
Gaus Width: 3.2646
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch61, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.22
Std Dev 3.246
Gaus mean : 44.0135
Gaus Width: 3.1446
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch61, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 50.08
Std Dev 3.201
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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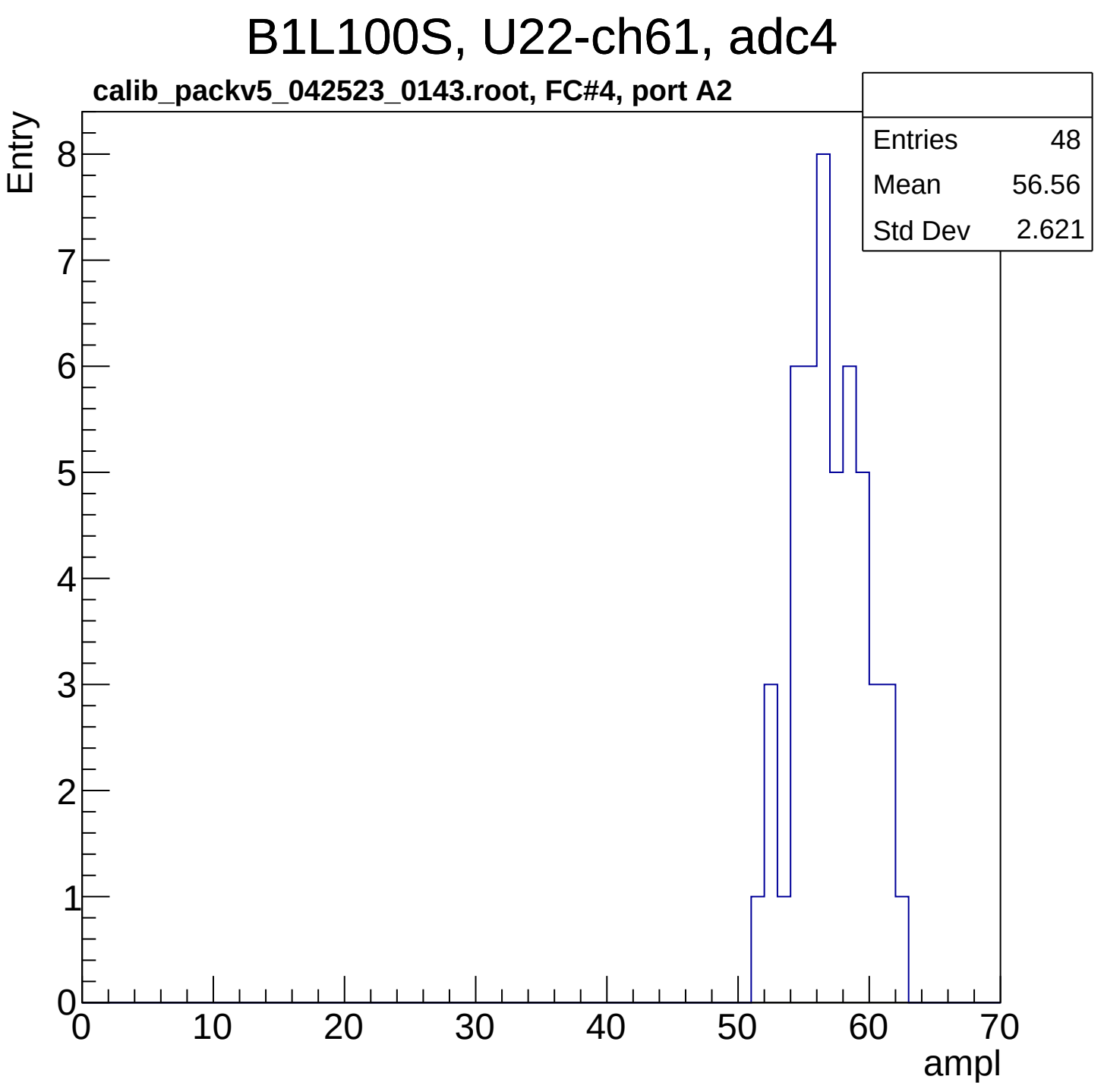
B1L100S, U22-ch61, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.56
Std Dev 2.621
Entry
ampl
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7
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0
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20
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60
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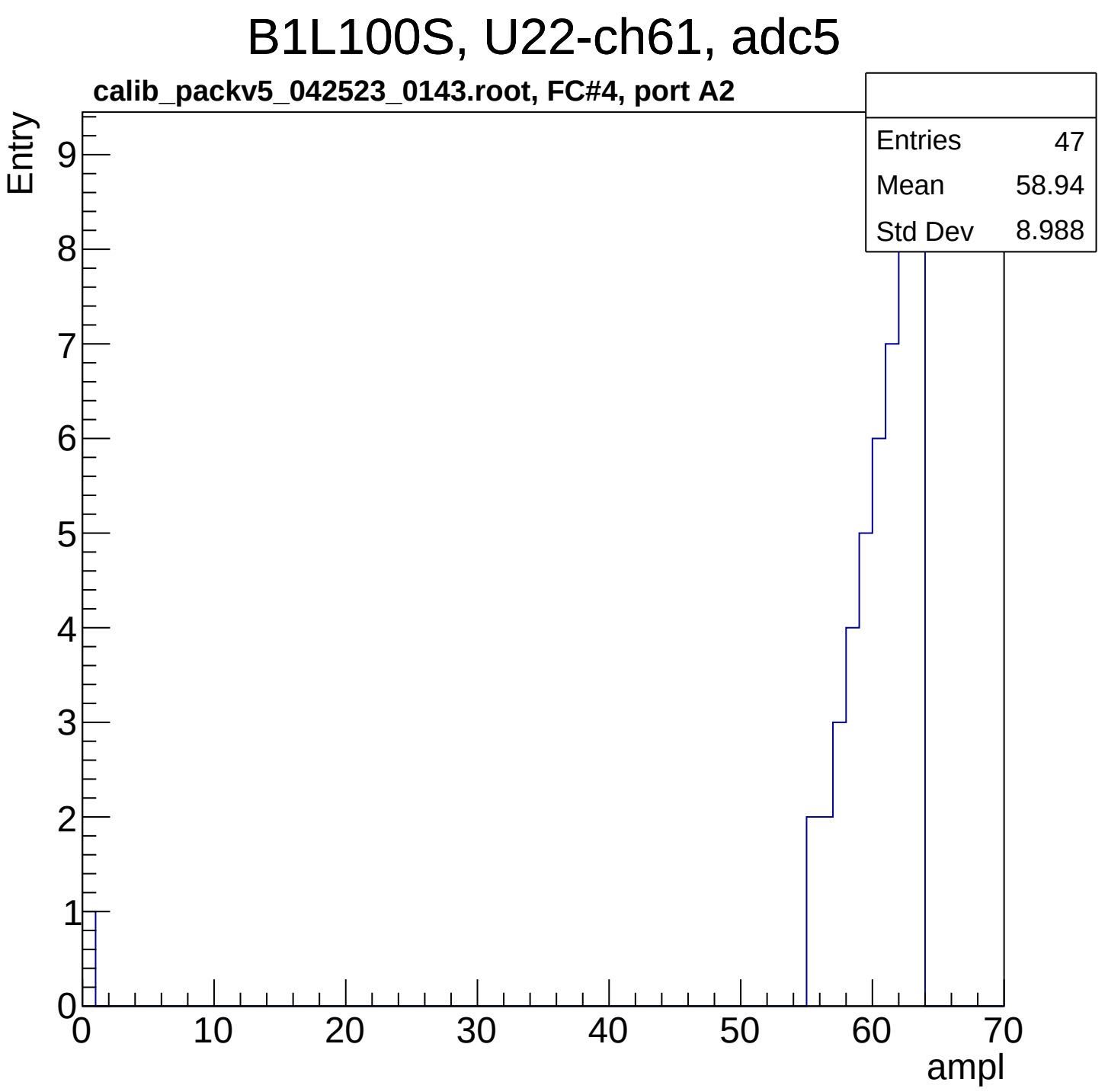
B1L100S, U22-ch61, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.94
Std Dev 8.988
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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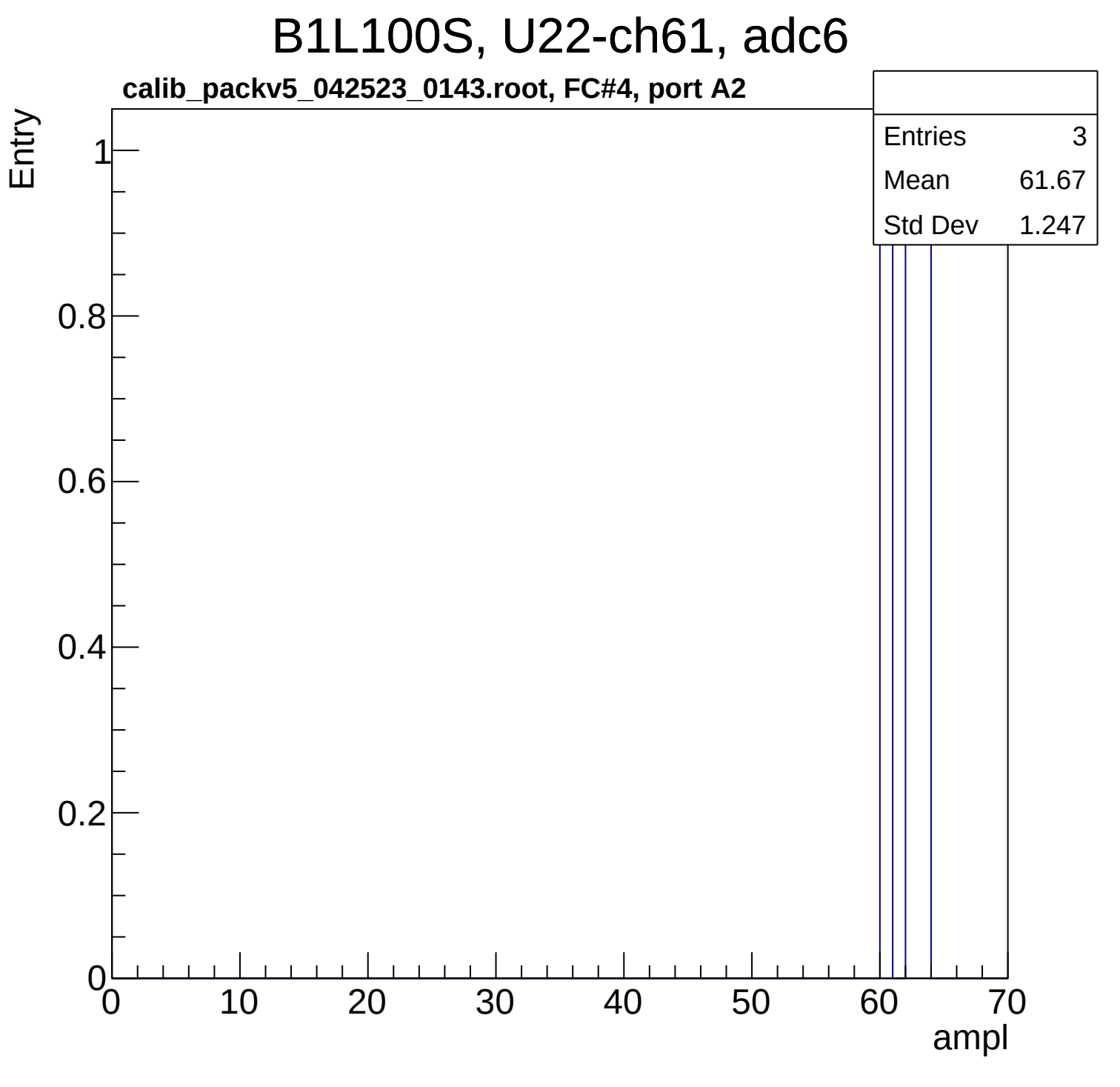
60
70


B1L100S, U22-ch61, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.67
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
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40
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60
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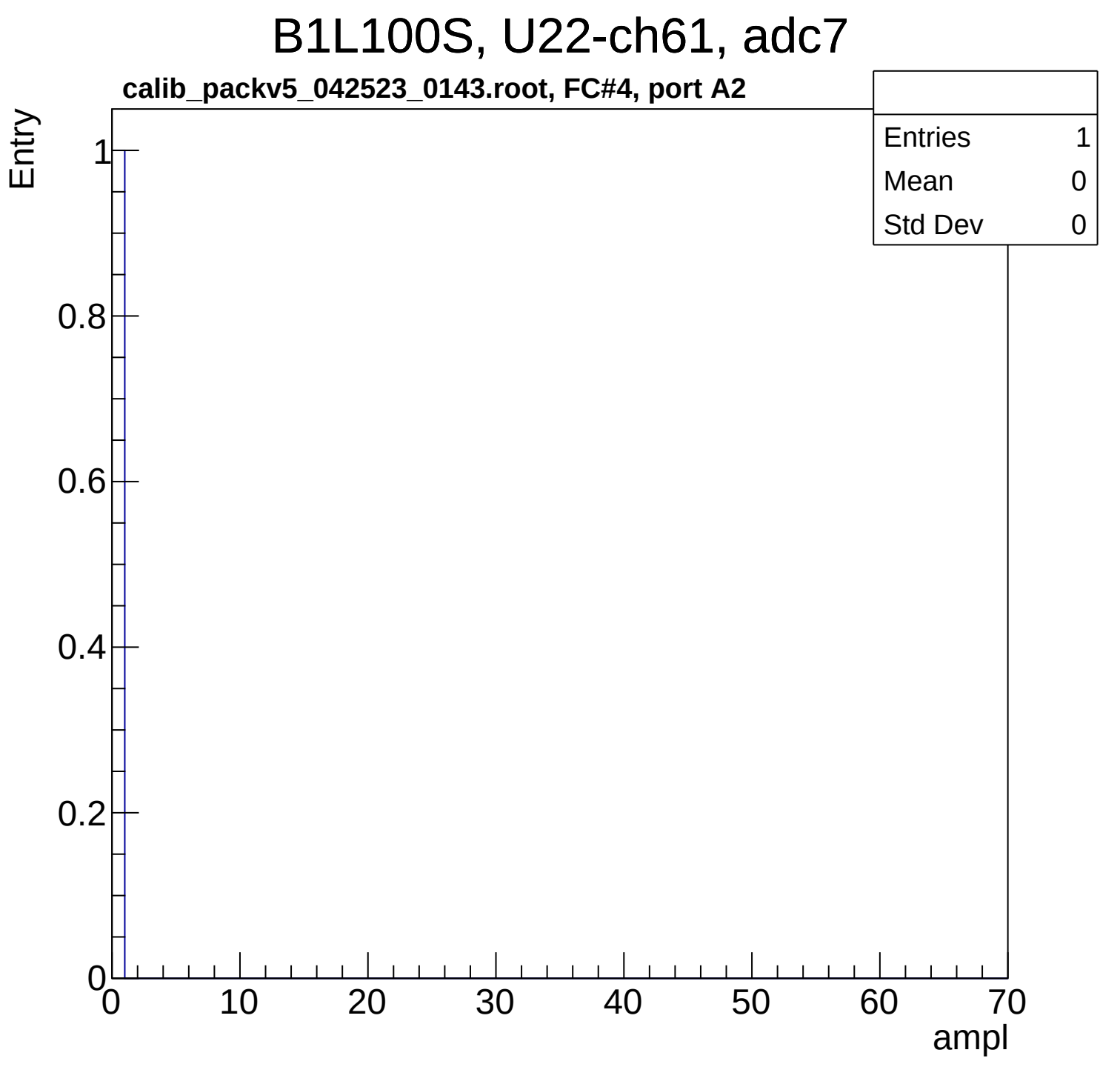
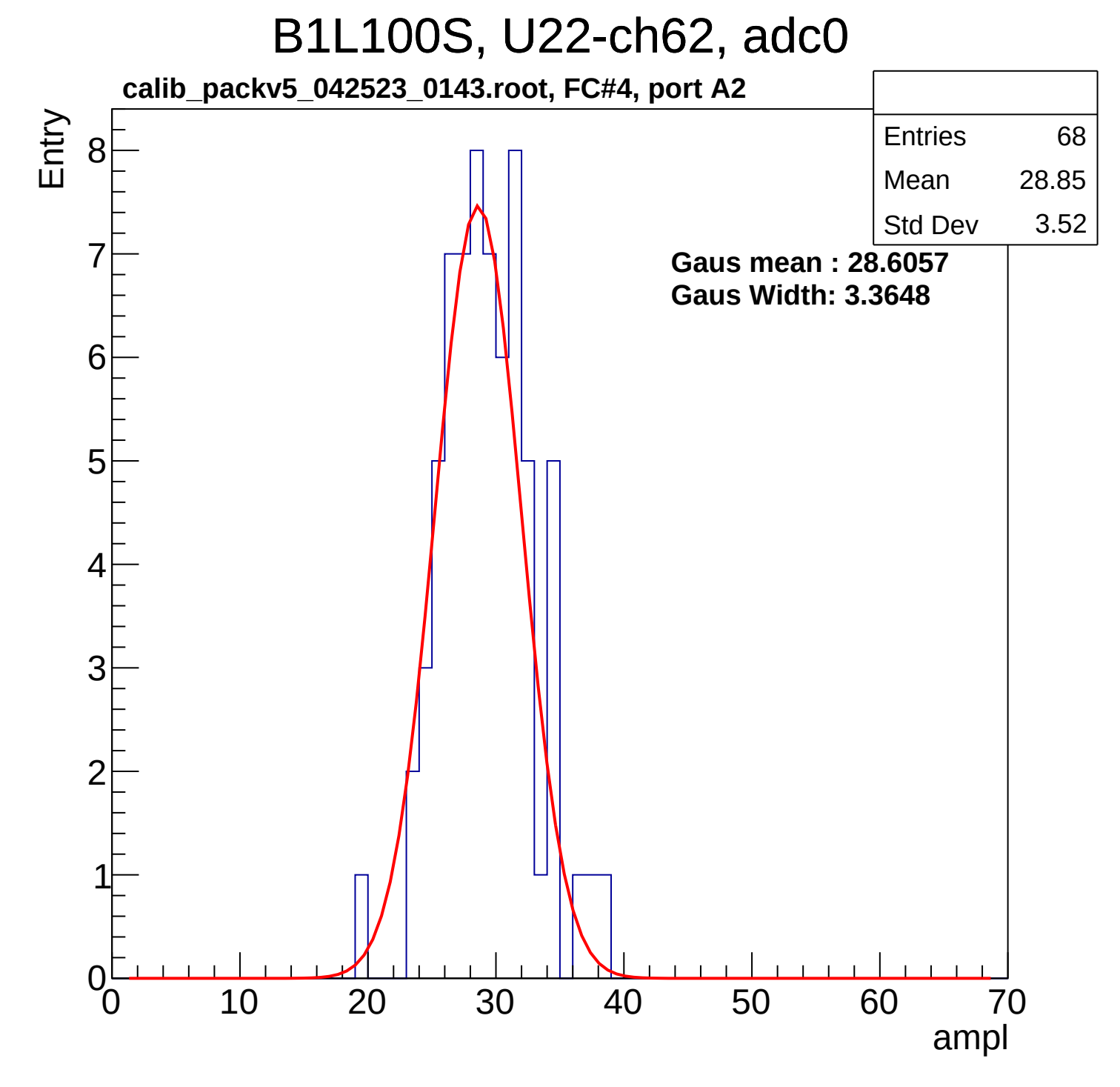

B1L100S, U22-ch61, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch62, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 28.85
Std Dev 3.52
Gaus mean : 28.6057
Gaus Width: 3.3648
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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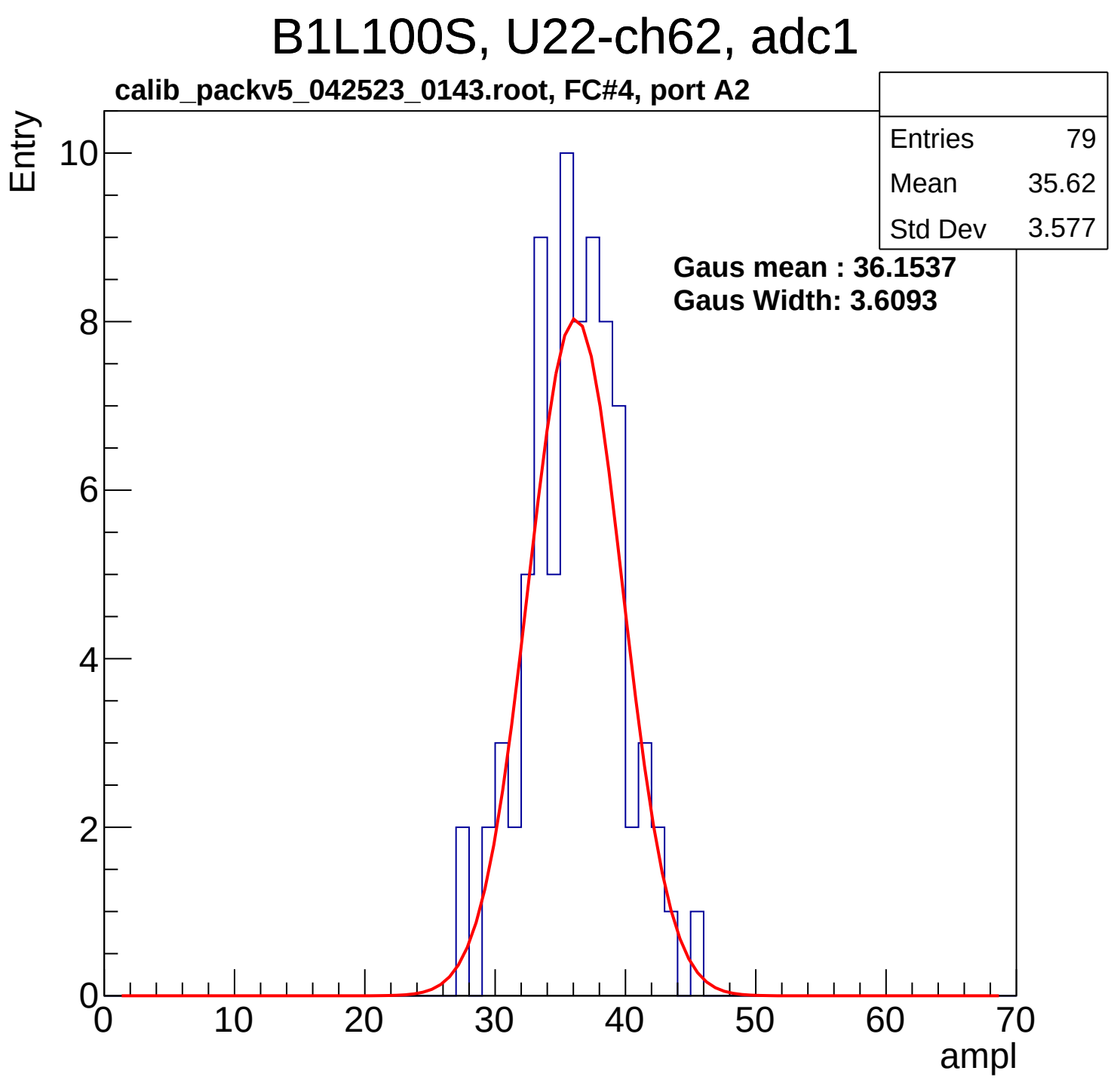
B1L100S, U22-ch62, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.62
Std Dev 3.577
Gaus mean : 36.1537
Gaus Width: 3.6093
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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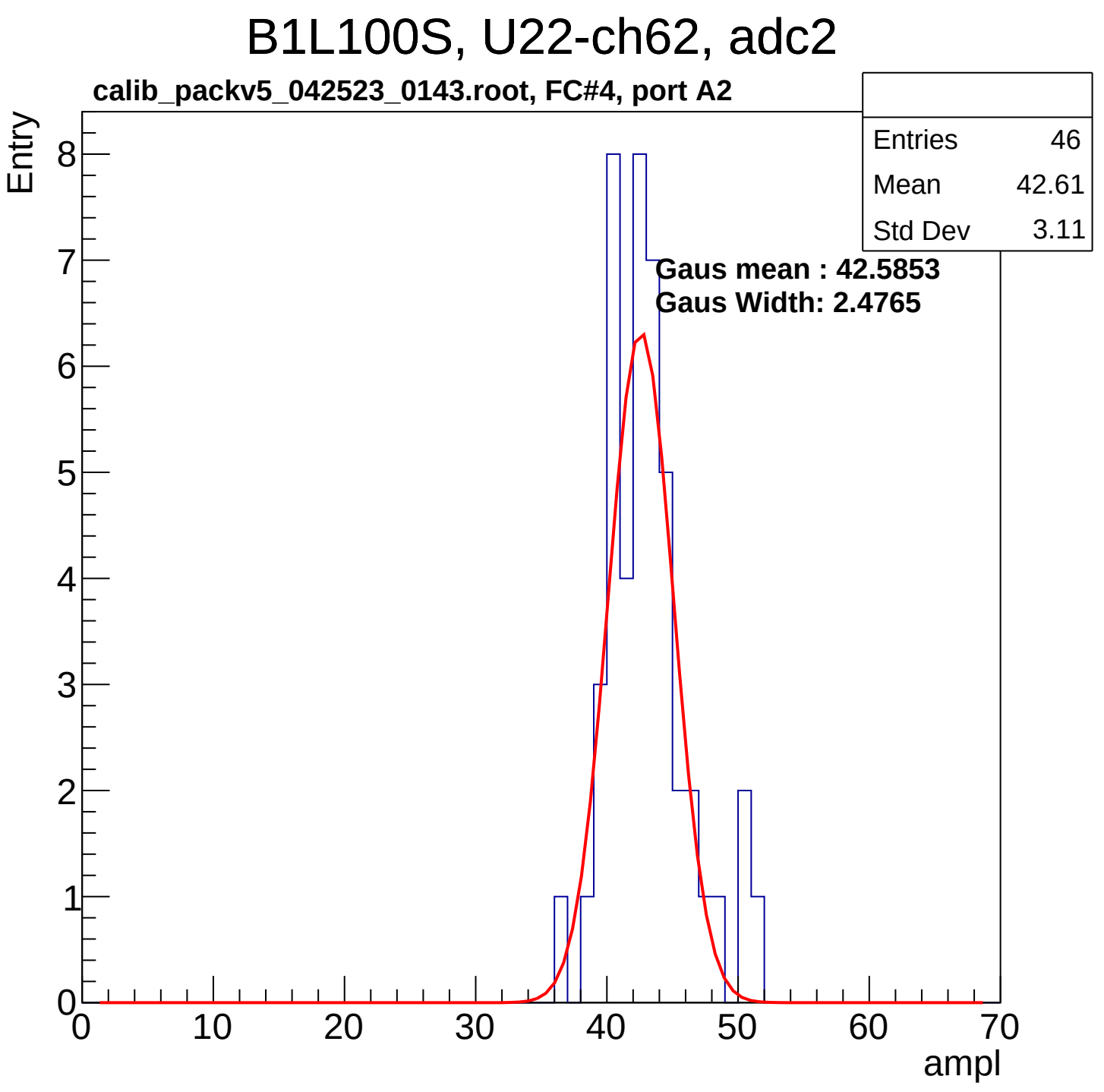
B1L100S, U22-ch62, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 42.61
Std Dev 3.11
Gaus mean : 42.5853
Gaus Width: 2.4765
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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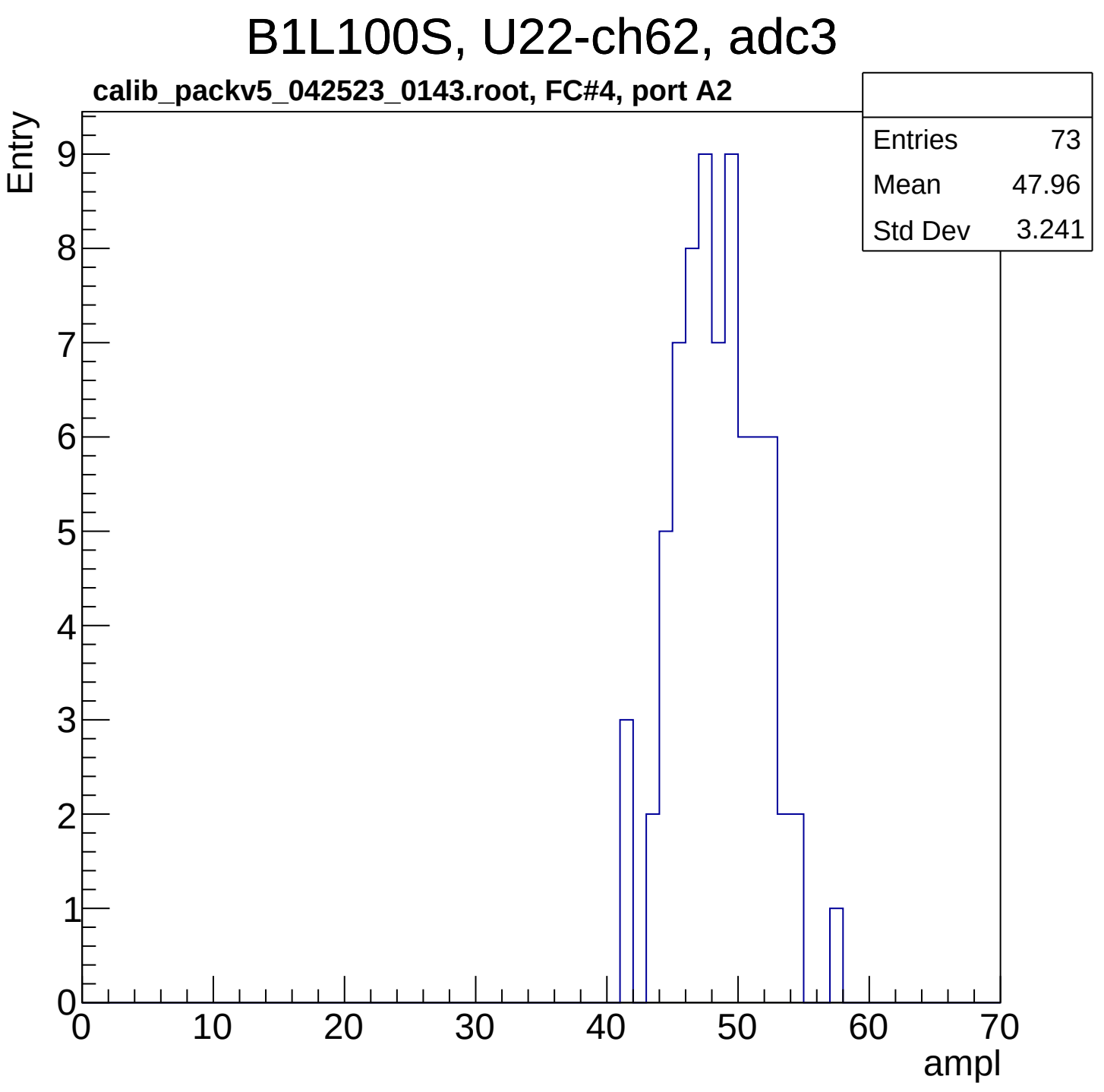
B1L100S, U22-ch62, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 47.96
Std Dev 3.241
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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60
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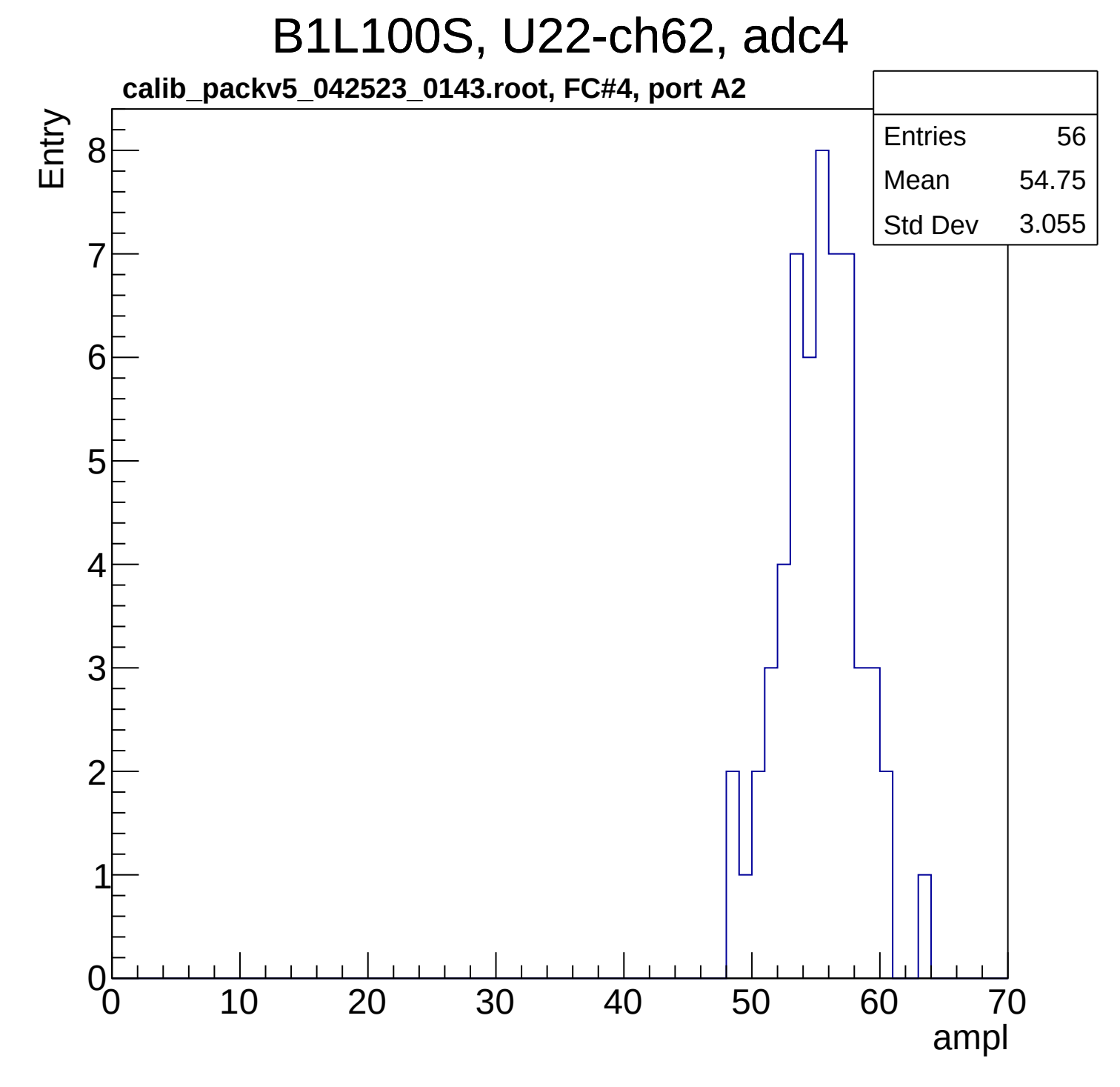
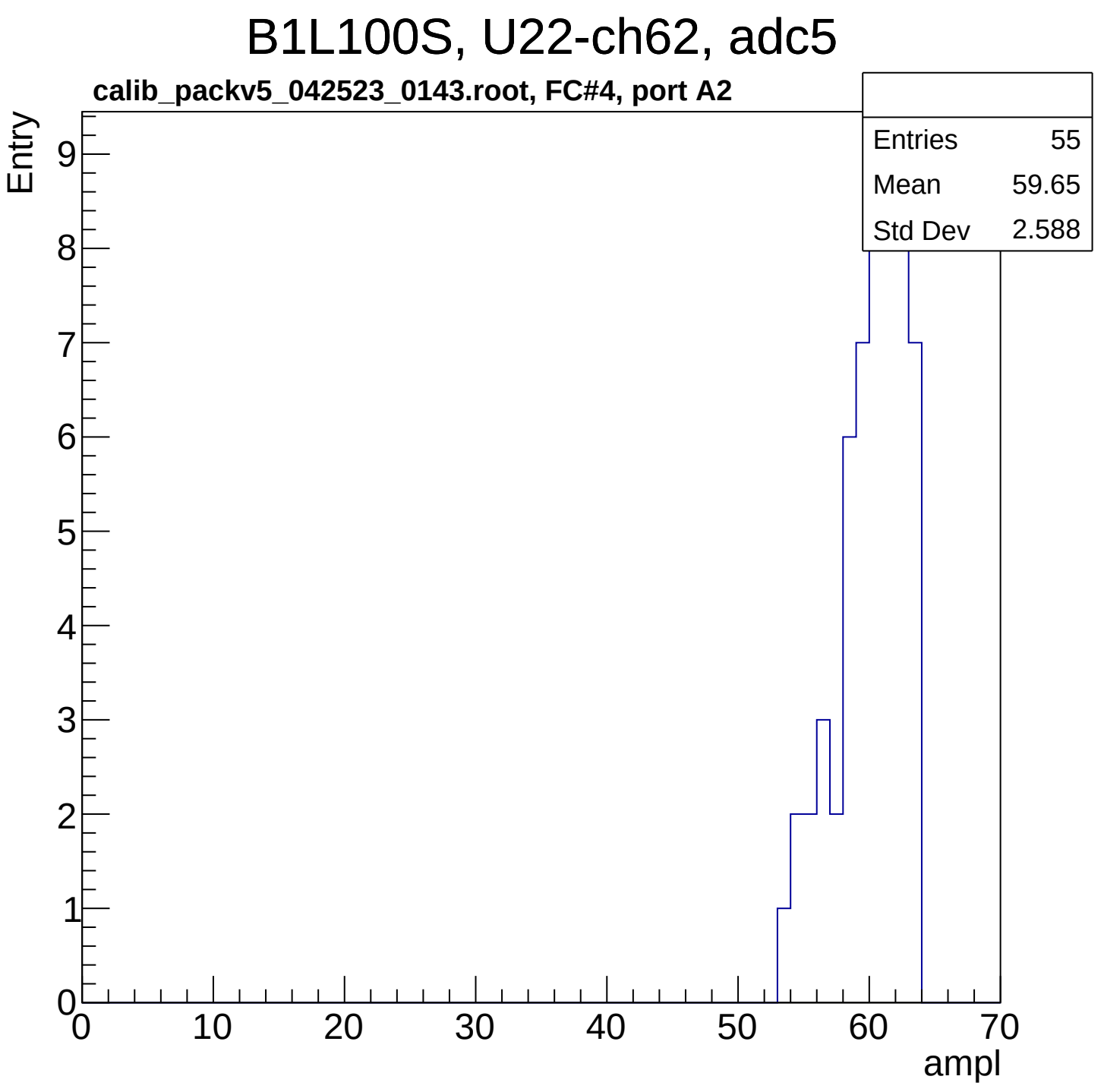

B1L100S, U22-ch62, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 54.75
Std Dev 3.055
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch62, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 59.65
Std Dev 2.588
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
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60
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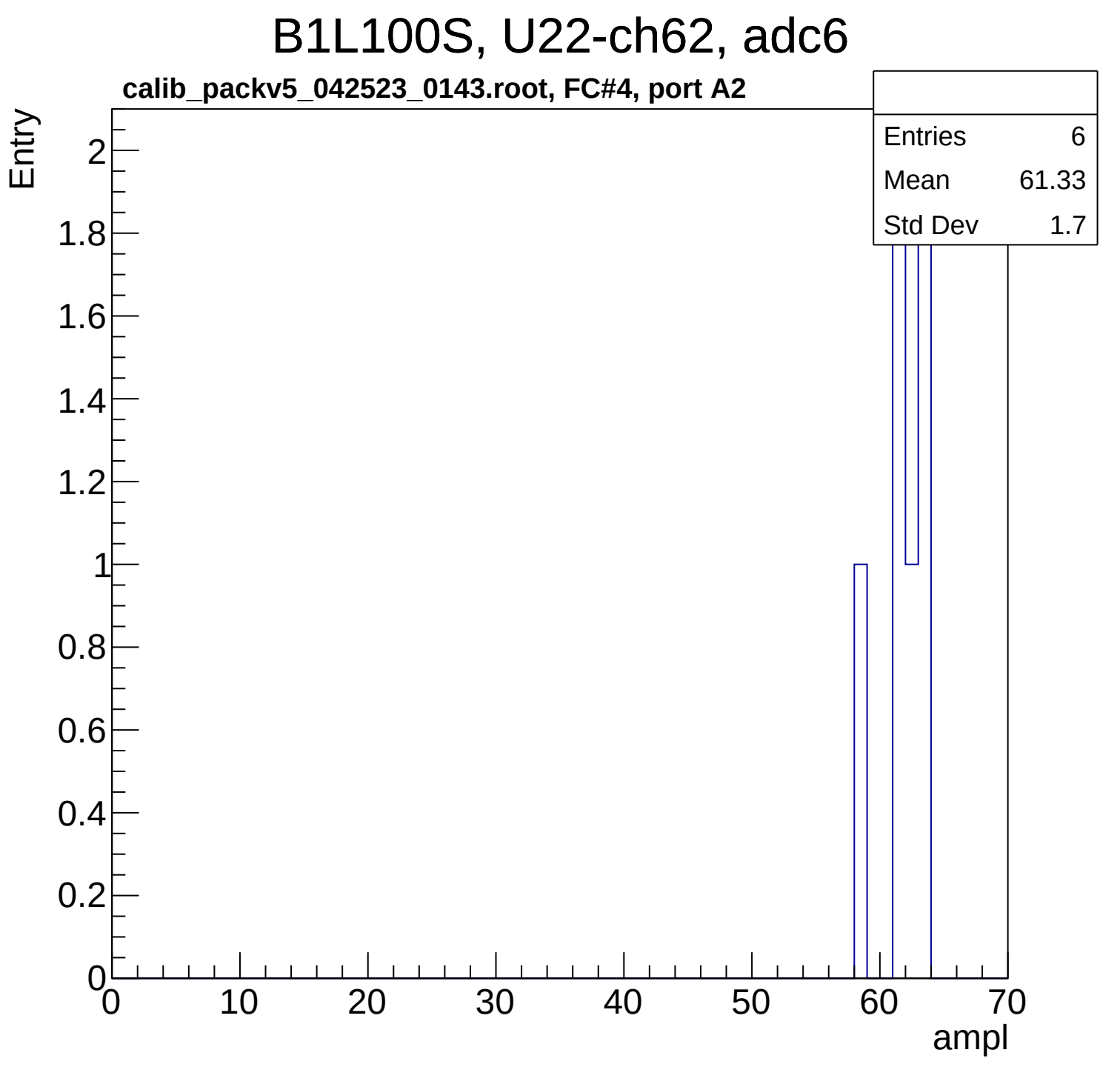
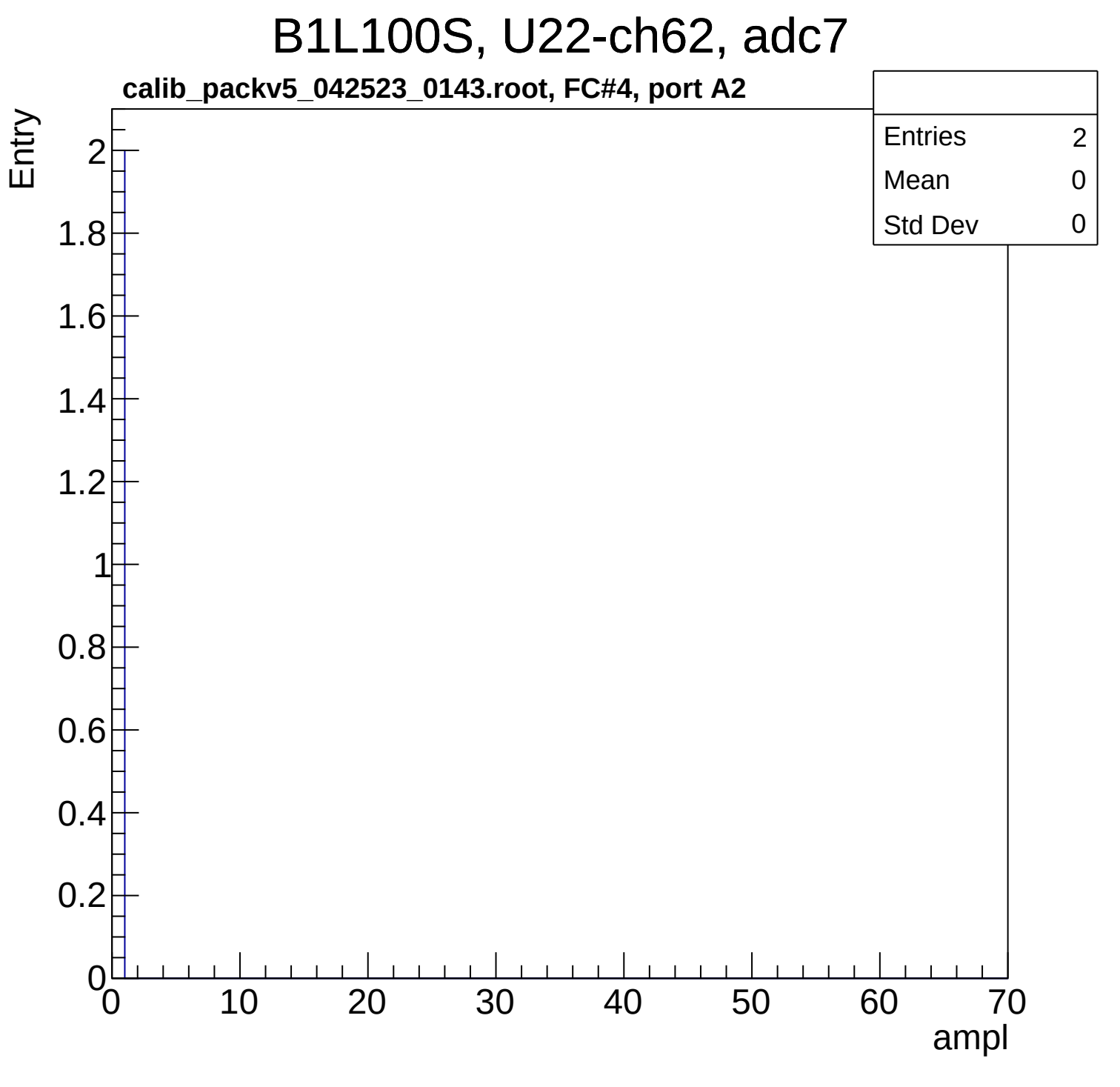
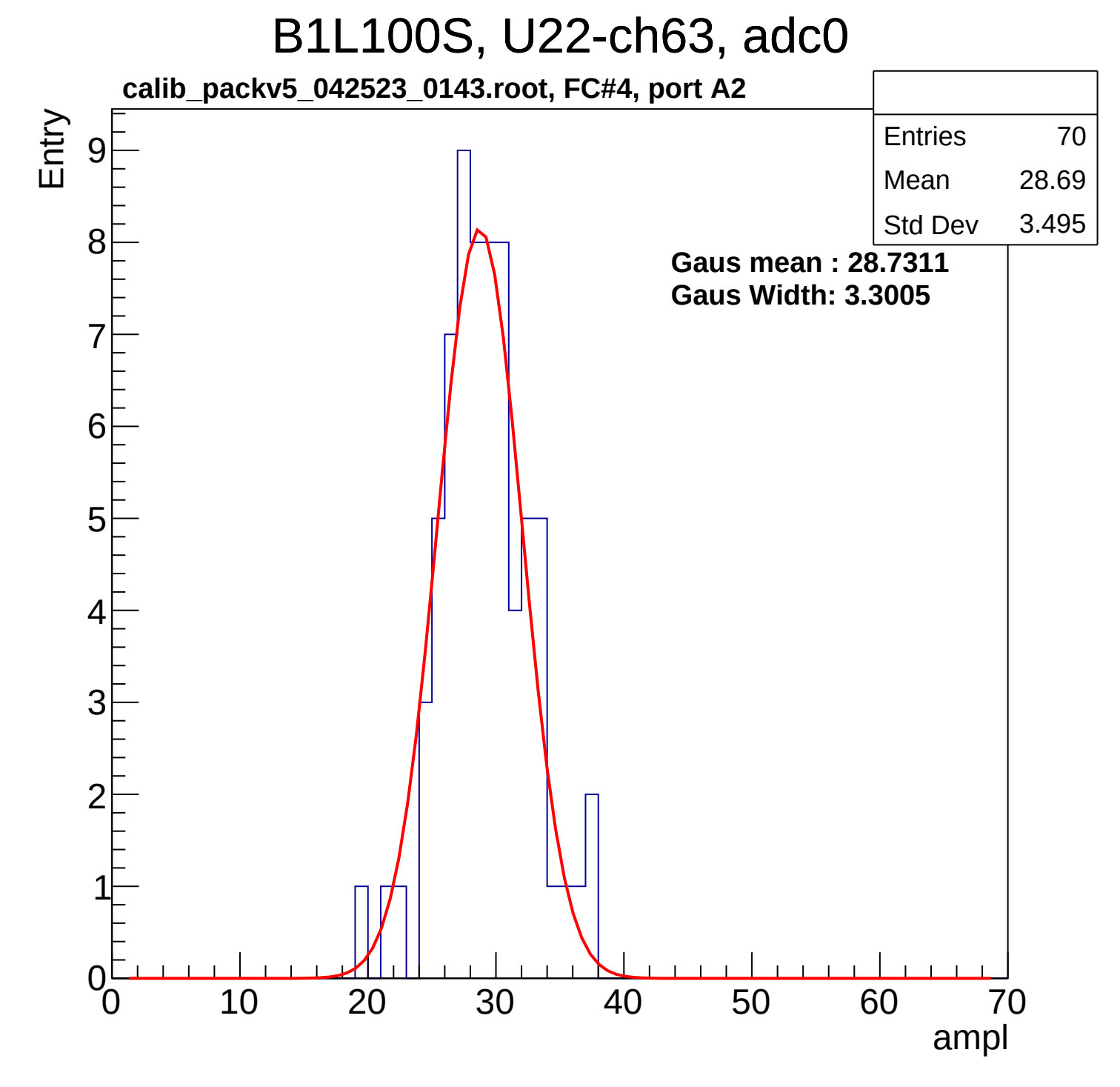
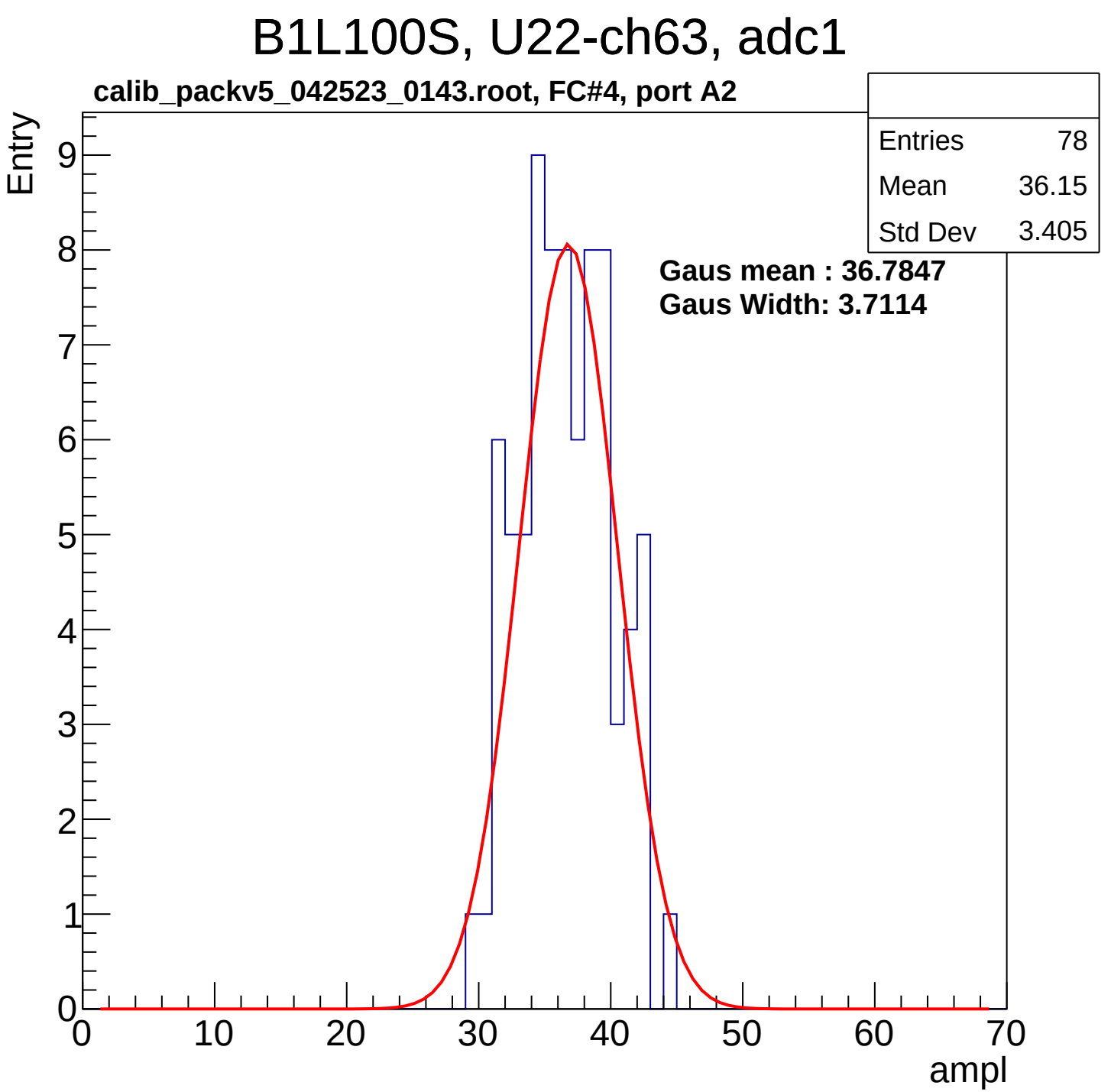

B1L100S, U22-ch62, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 61.33
Std Dev 1.7
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch62, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch63, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.69
Std Dev 3.495
Gaus mean : 28.7311
Gaus Width: 3.3005
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch63, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.15
Std Dev 3.405
Gaus mean : 36.7847
Gaus Width: 3.7114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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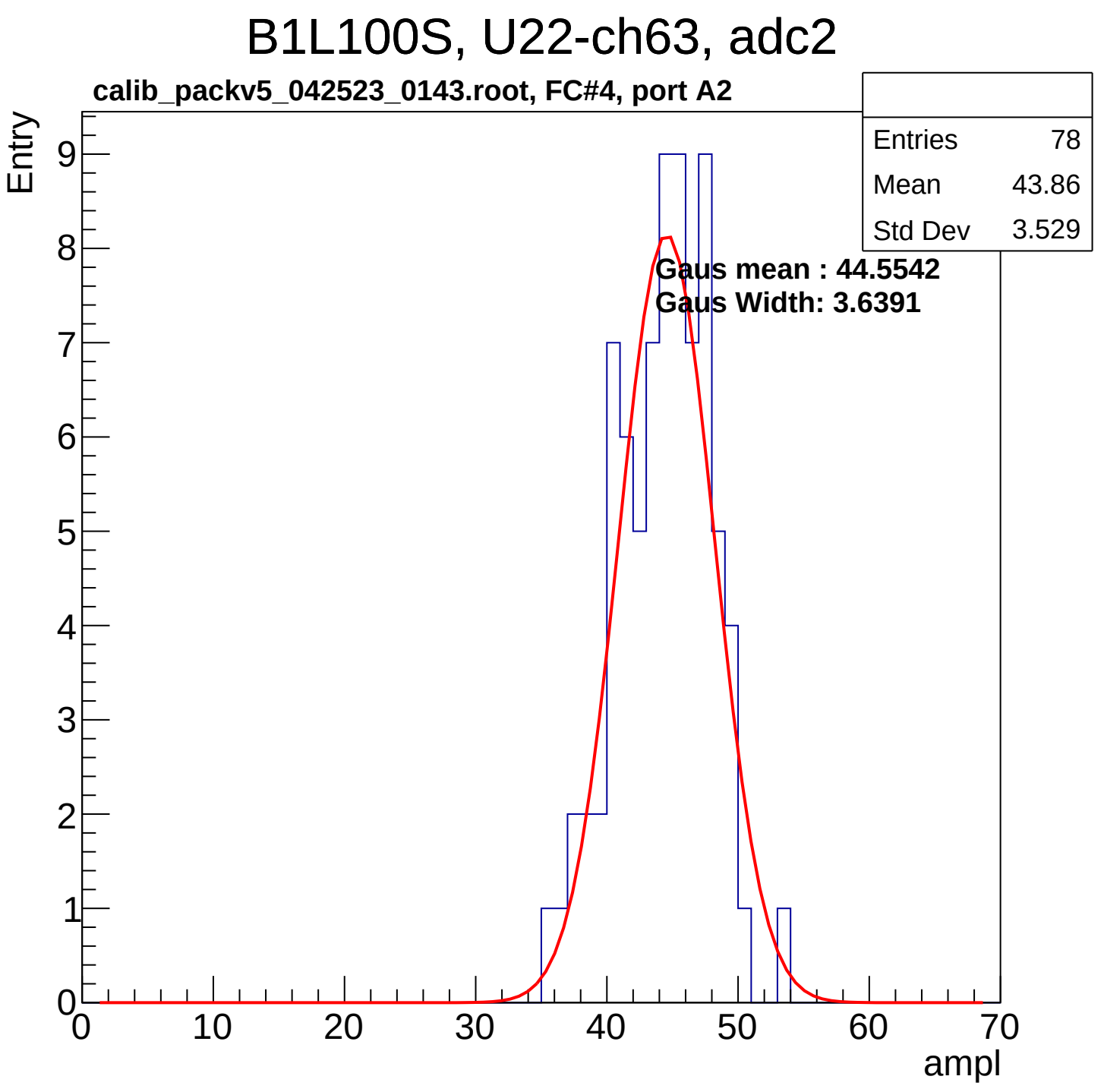
B1L100S, U22-ch63, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 43.86
Std Dev 3.529
Gaus mean : 44.5542
Gaus Width: 3.6391
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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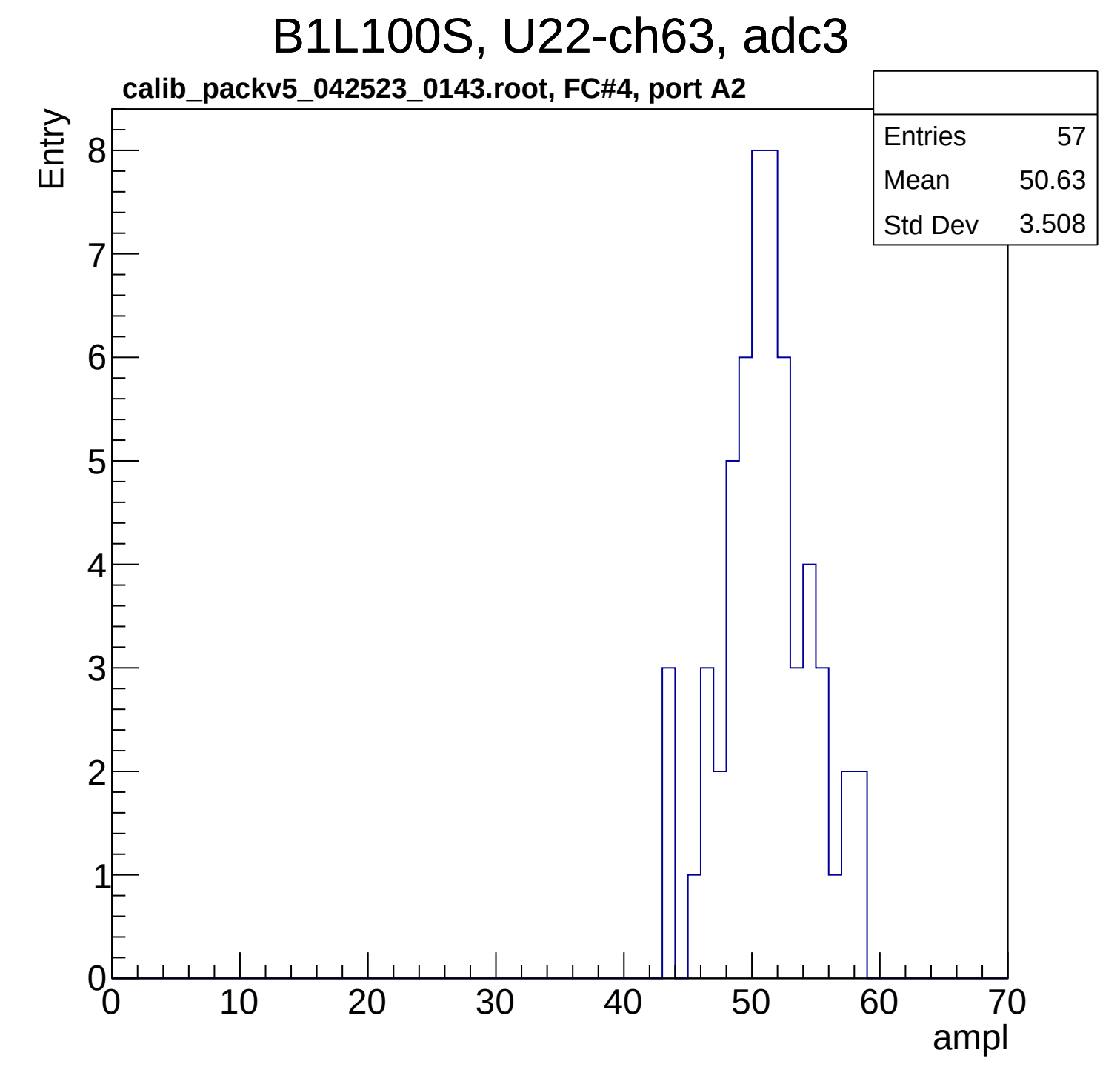
B1L100S, U22-ch63, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50.63
Std Dev 3.508
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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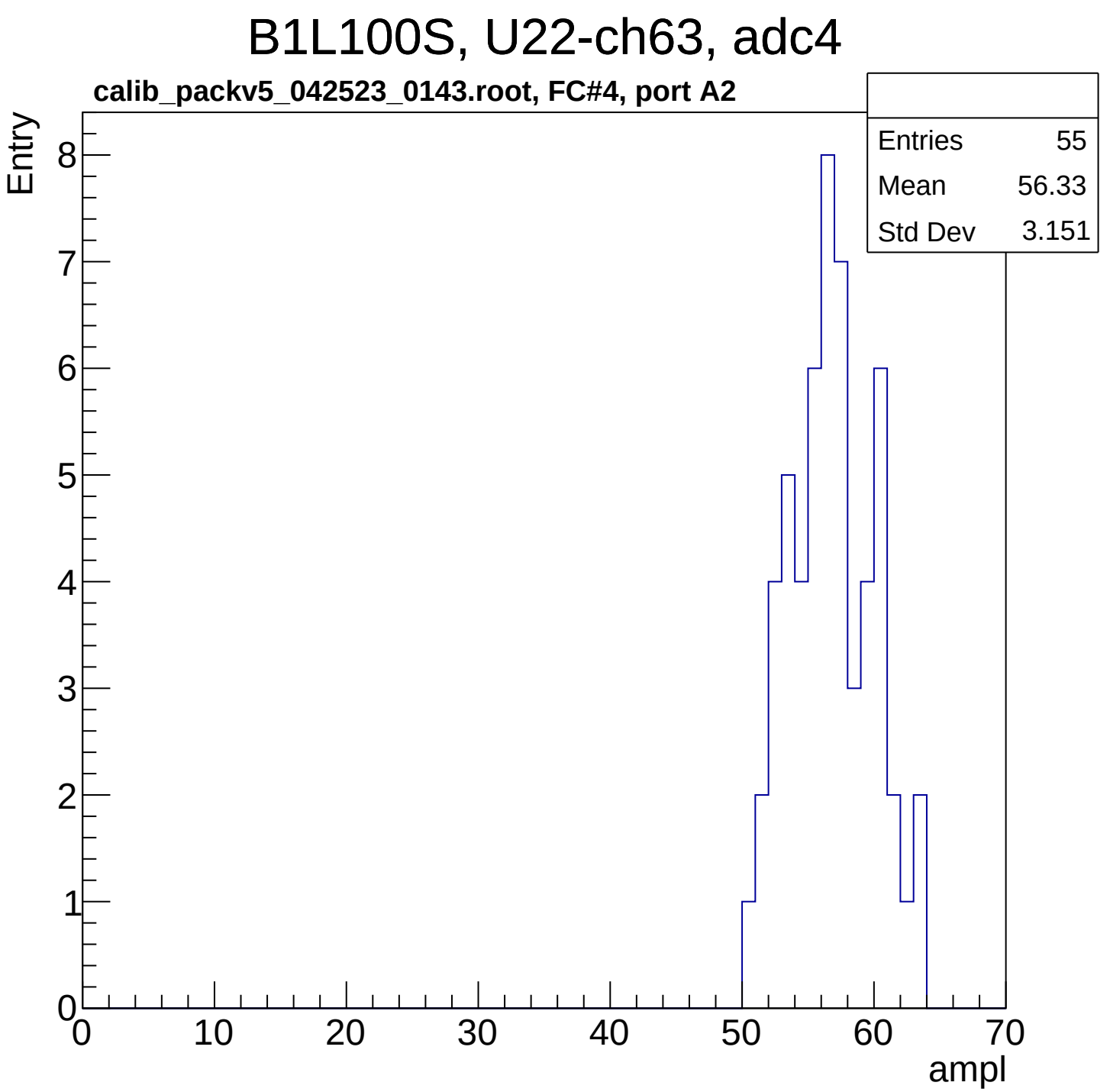
B1L100S, U22-ch63, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 56.33
Std Dev 3.151
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
50
60
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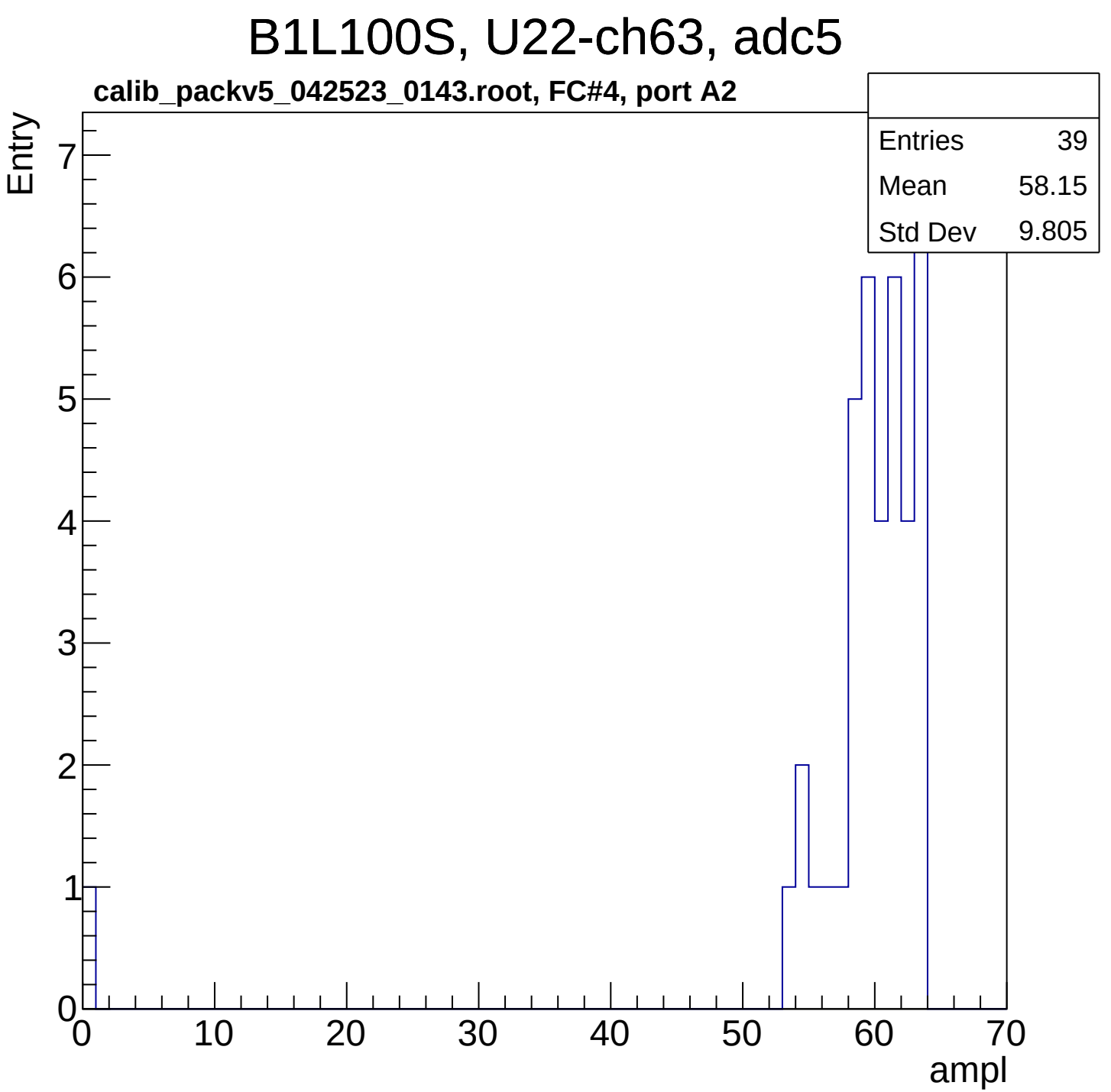
B1L100S, U22-ch63, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 58.15
Std Dev 9.805
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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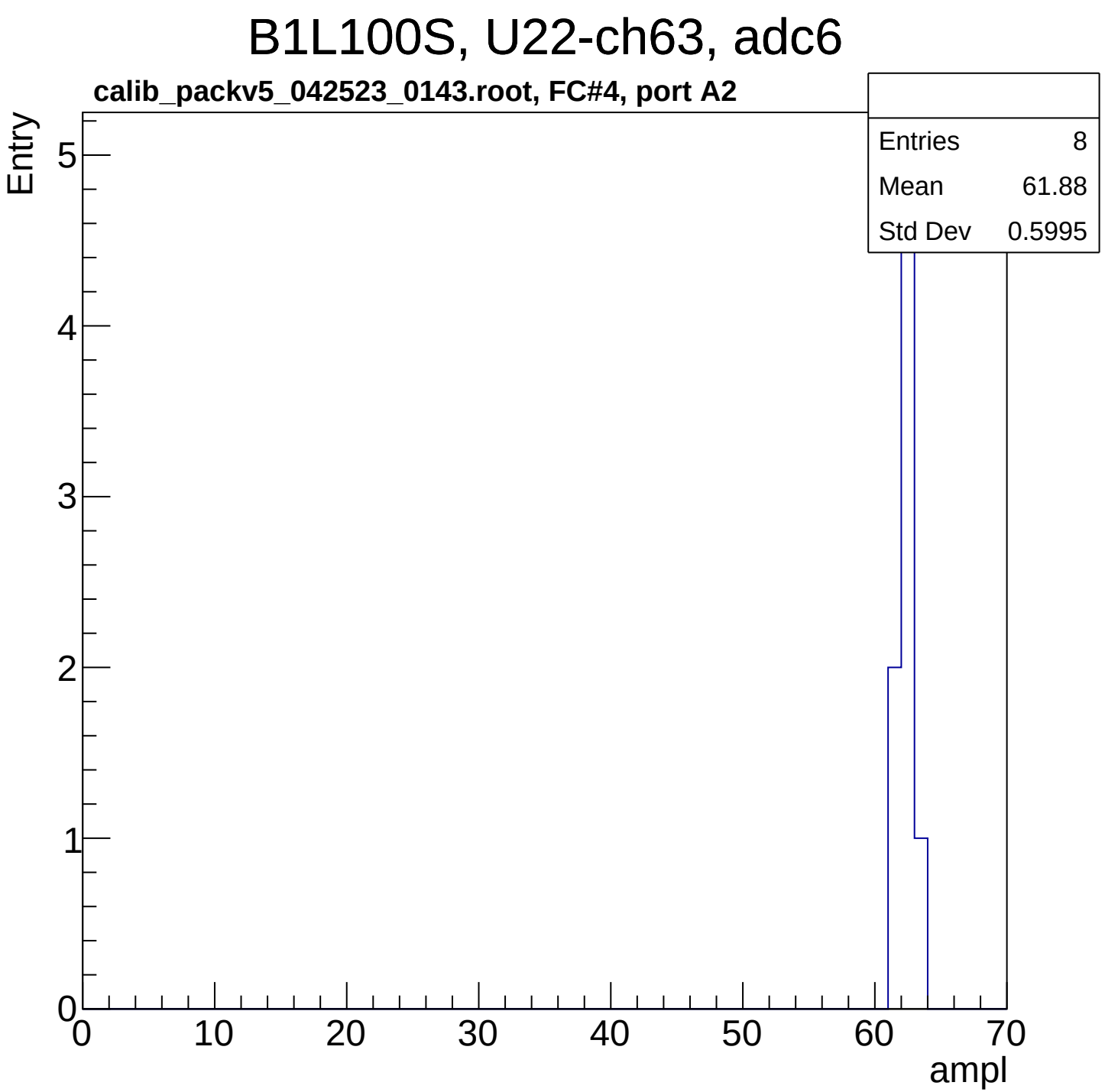
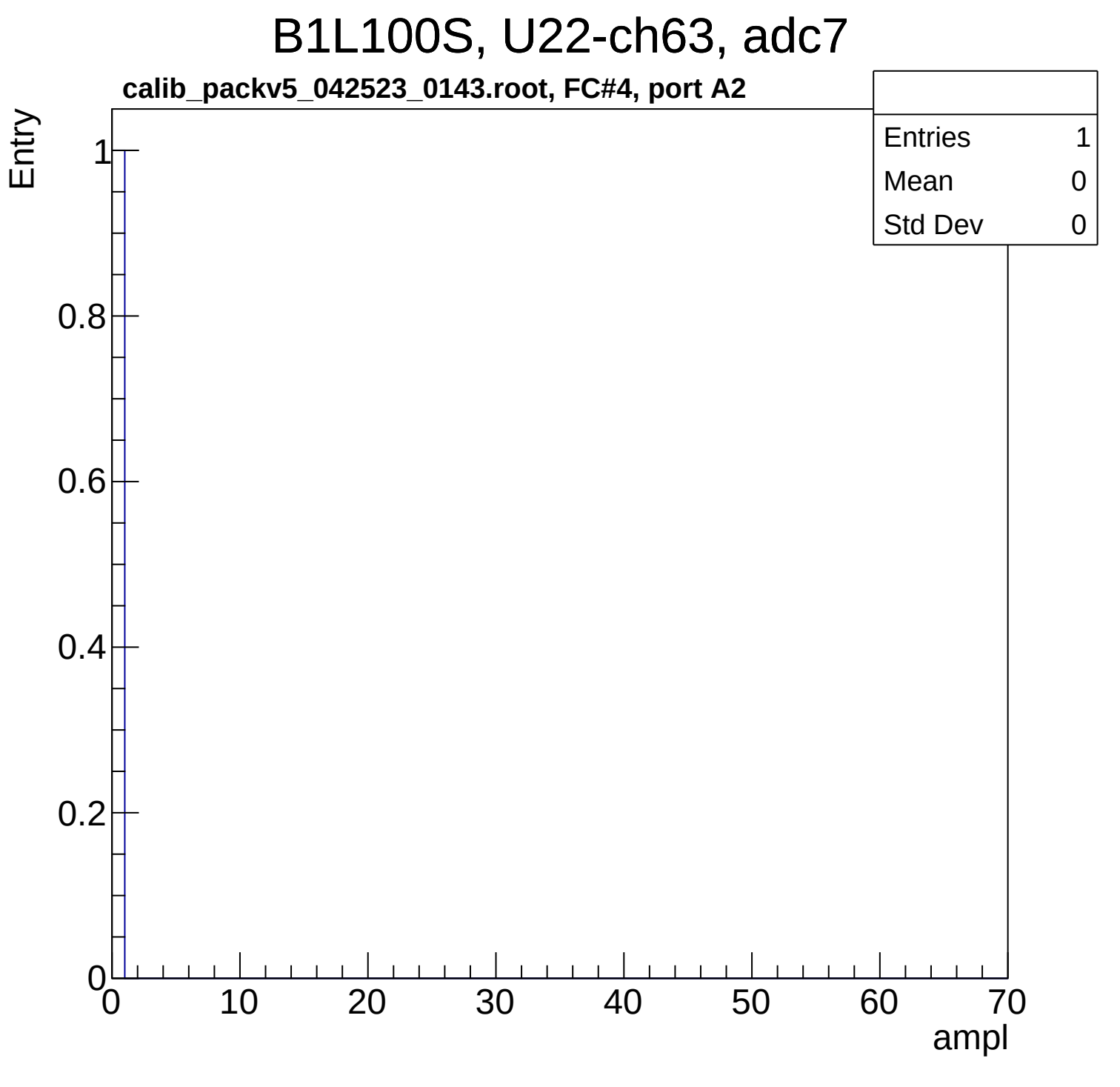
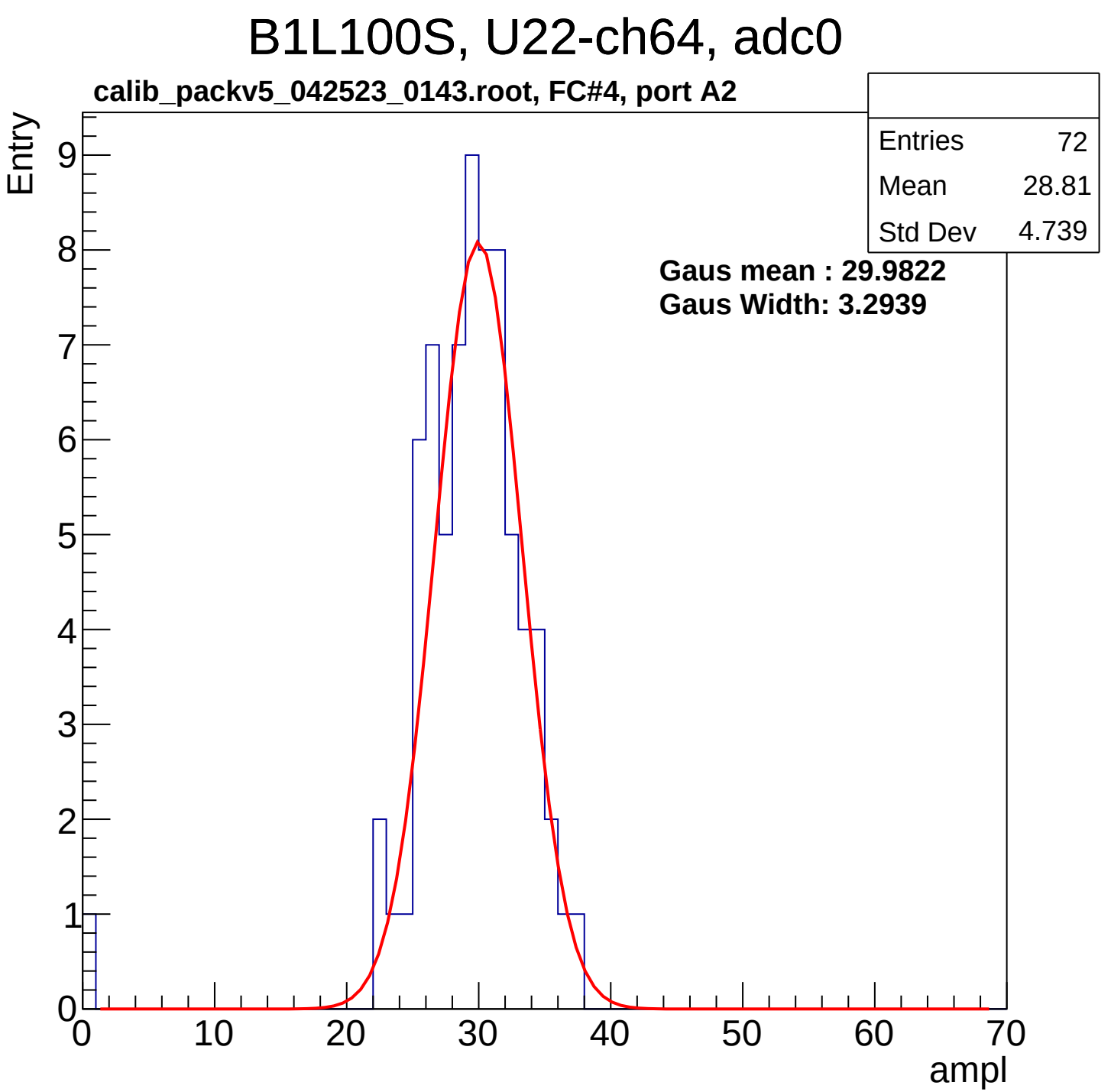
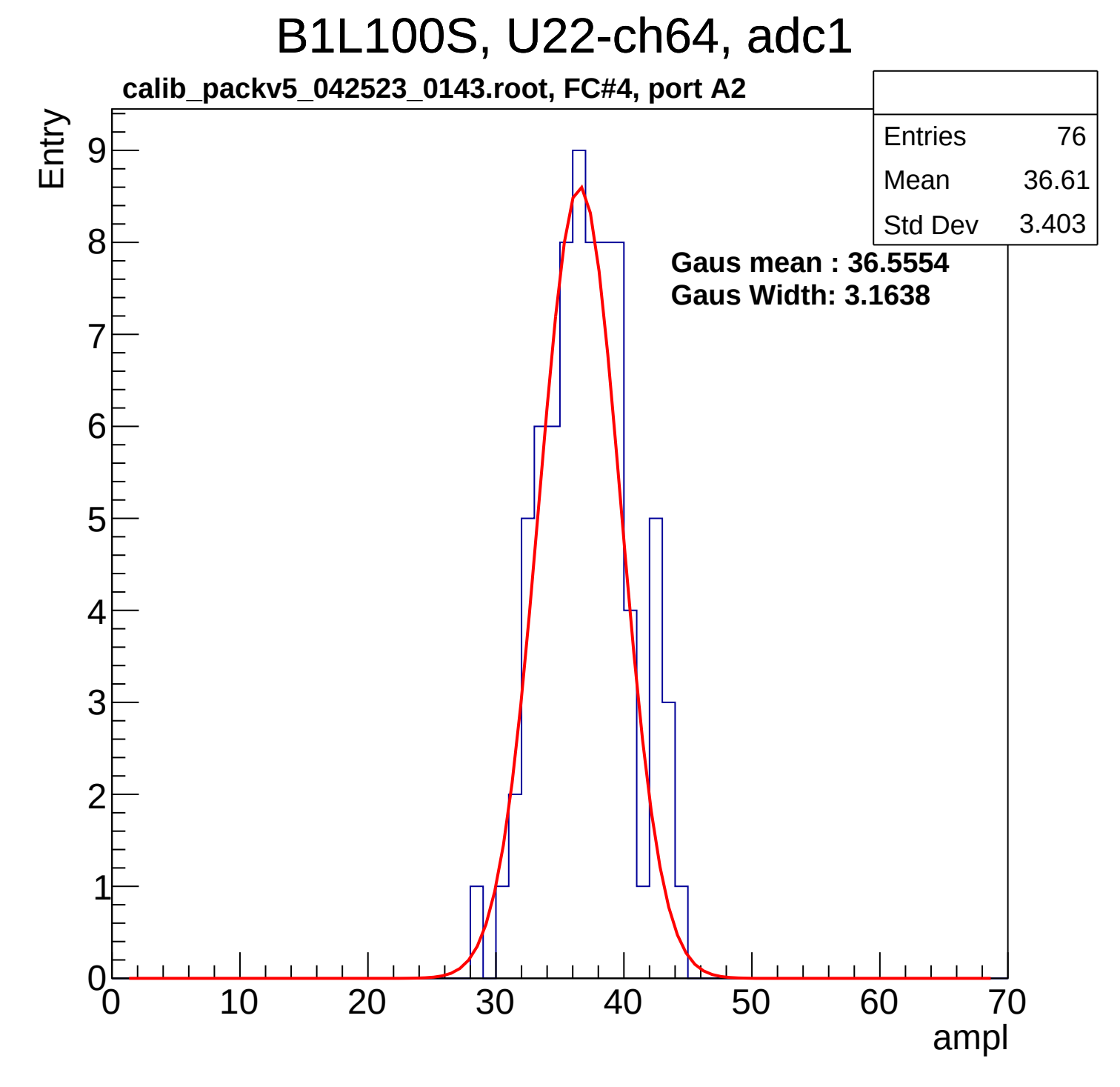

B1L100S, U22-ch63, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.88
Std Dev 0.5995
Entry
0
1
2
3
4
5
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch63, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch64, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 28.81
Std Dev 4.739
Gaus mean : 29.9822
Gaus Width: 3.2939
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch64, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.61
Std Dev 3.403
Gaus mean : 36.5554
Gaus Width: 3.1638
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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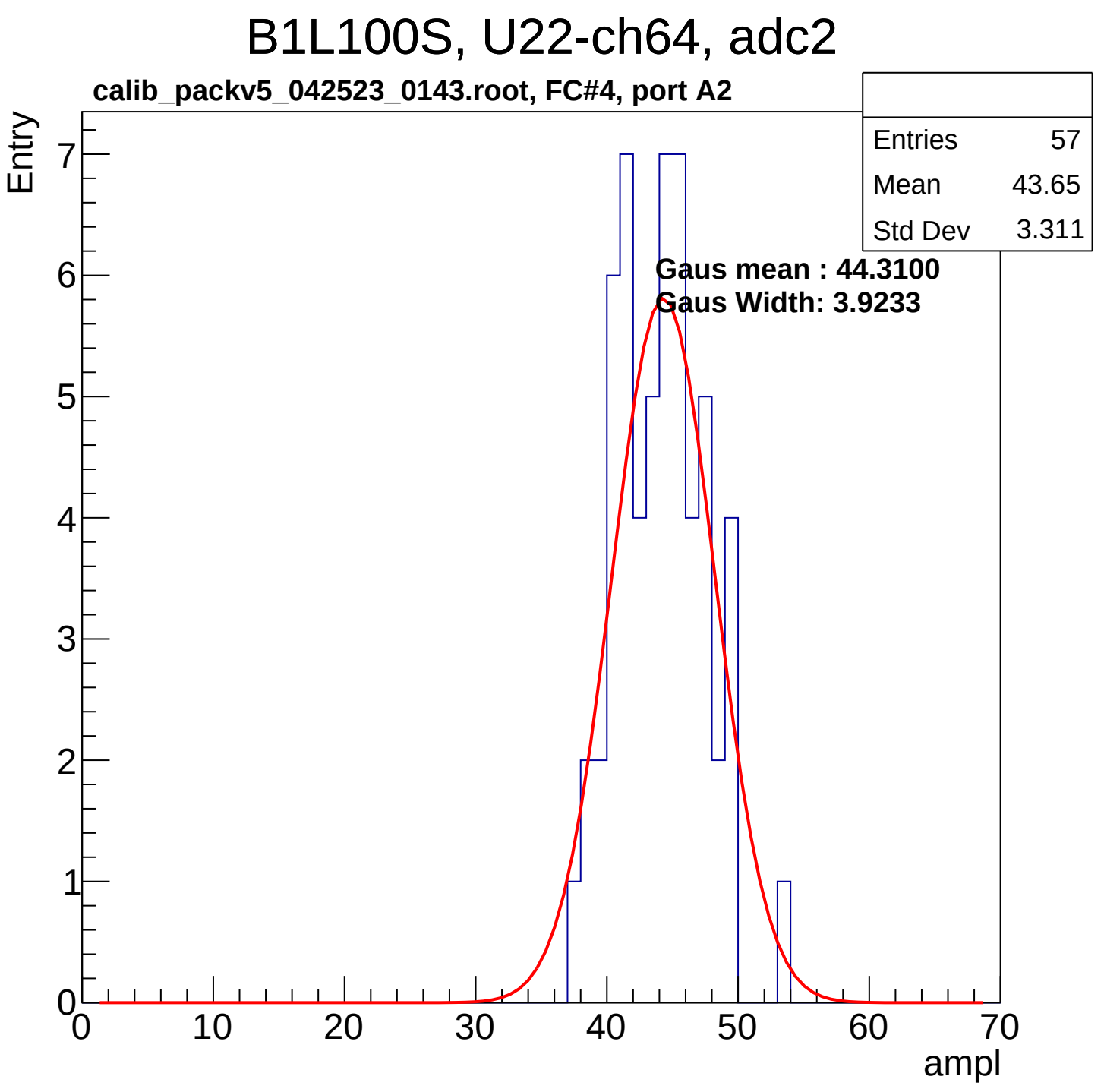
20
30
40
50
60
70


B1L100S, U22-ch64, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 43.65
Std Dev 3.311
Gaus mean : 44.3100
Gaus Width: 3.9233
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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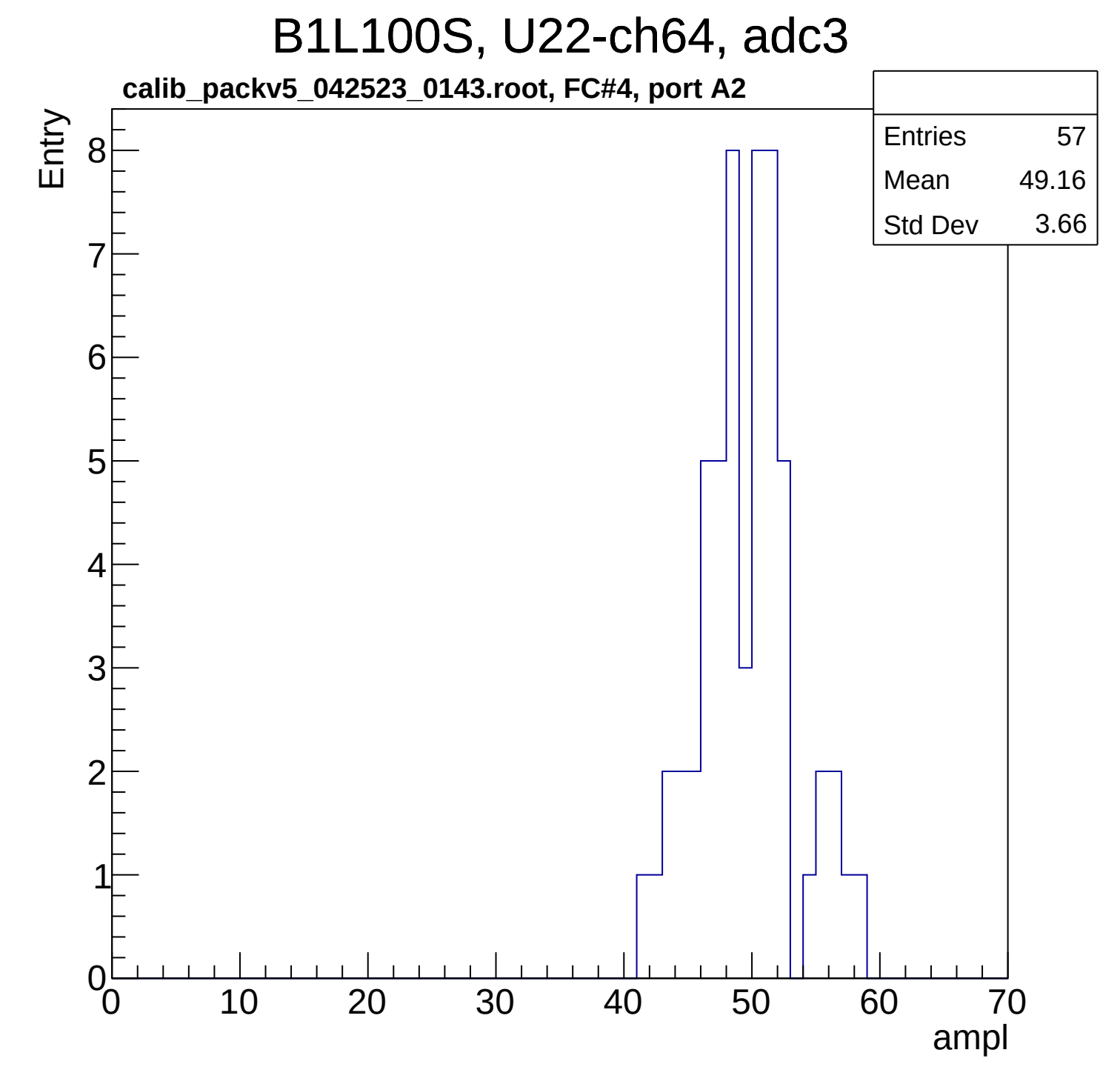
60
70


B1L100S, U22-ch64, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.16
Std Dev 3.66
Entry
ampl
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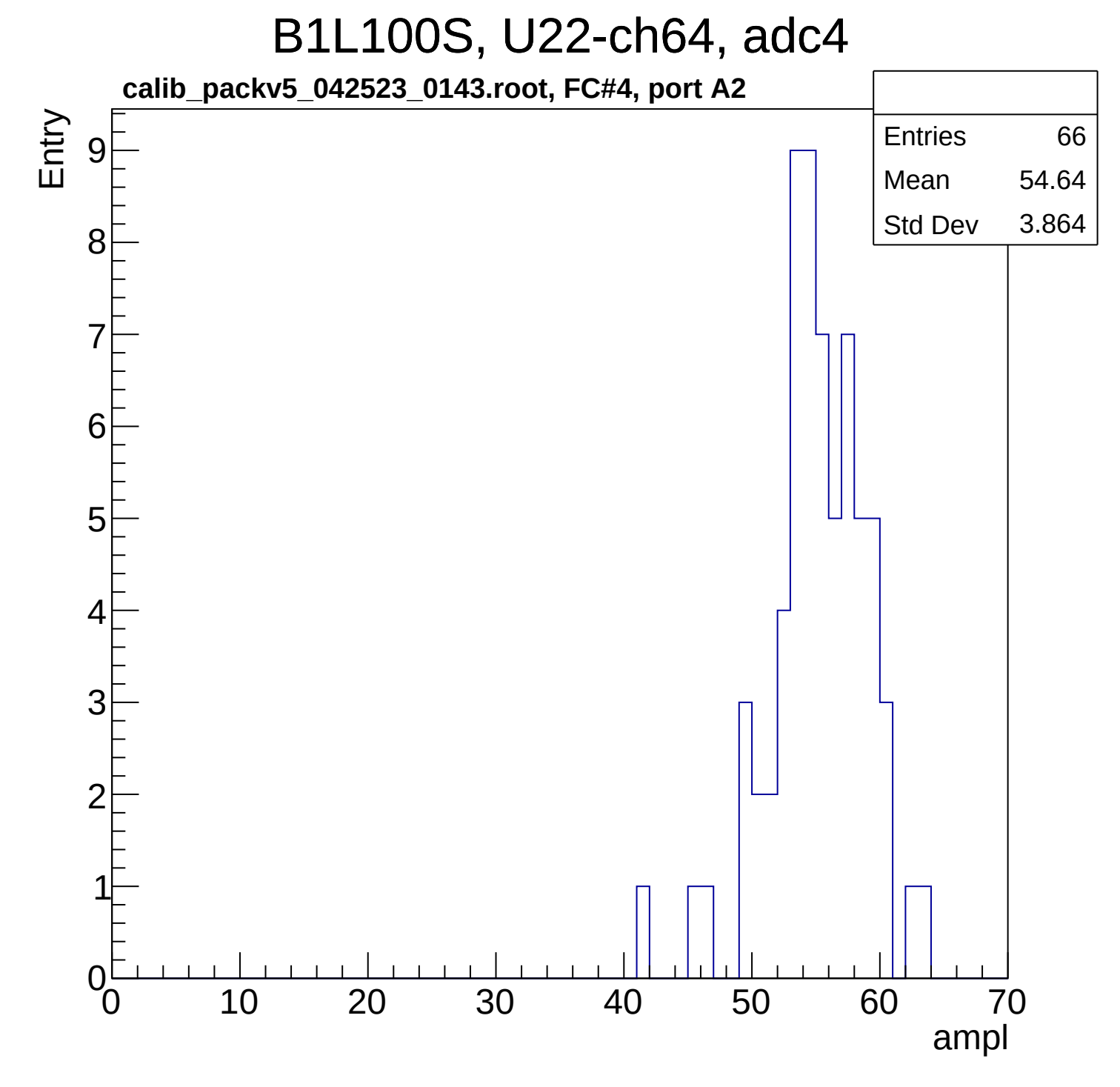
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch64, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 54.64
Std Dev 3.864
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
50
60
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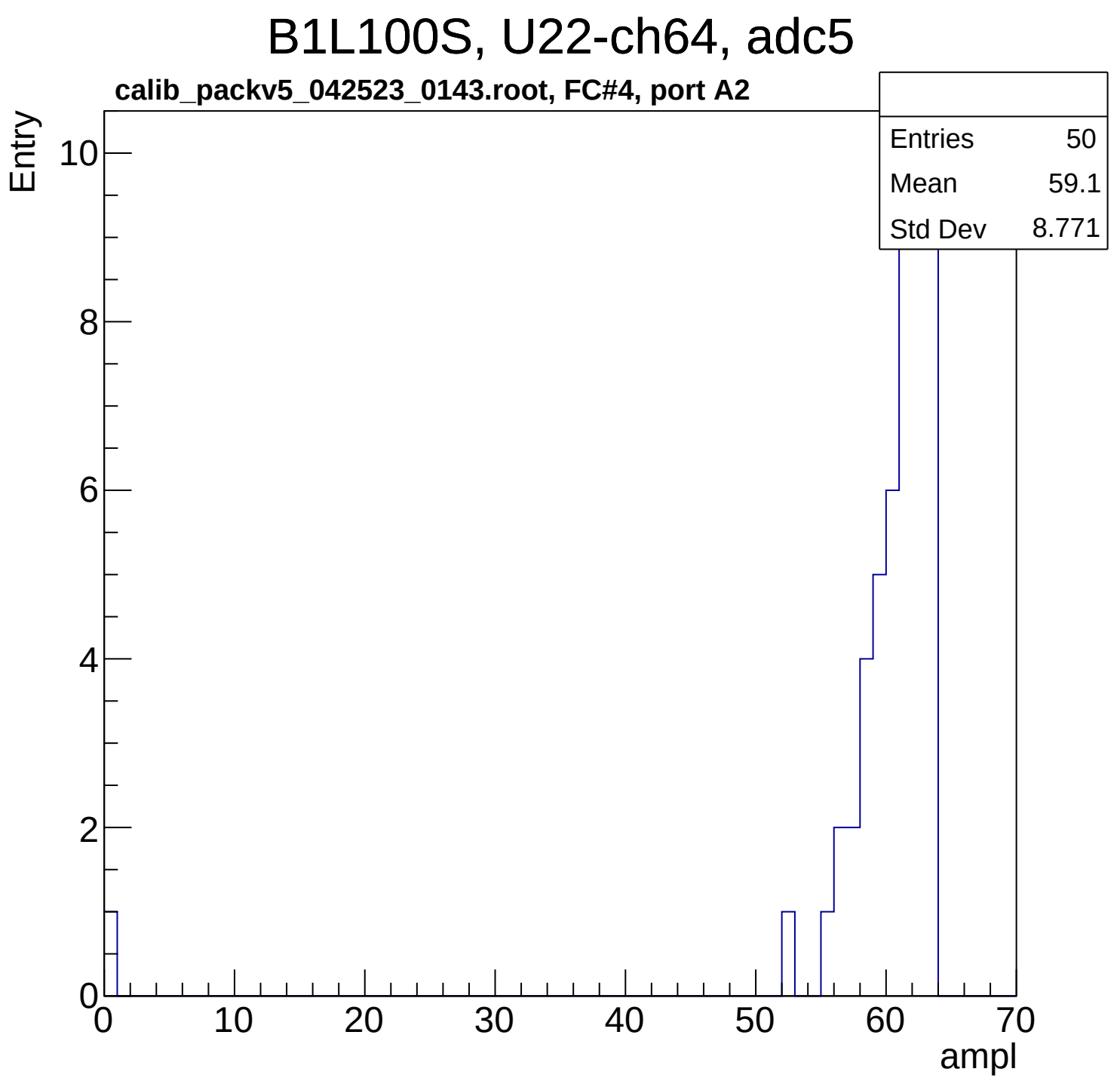
B1L100S, U22-ch64, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 59.1
Std Dev 8.771
Entry
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10
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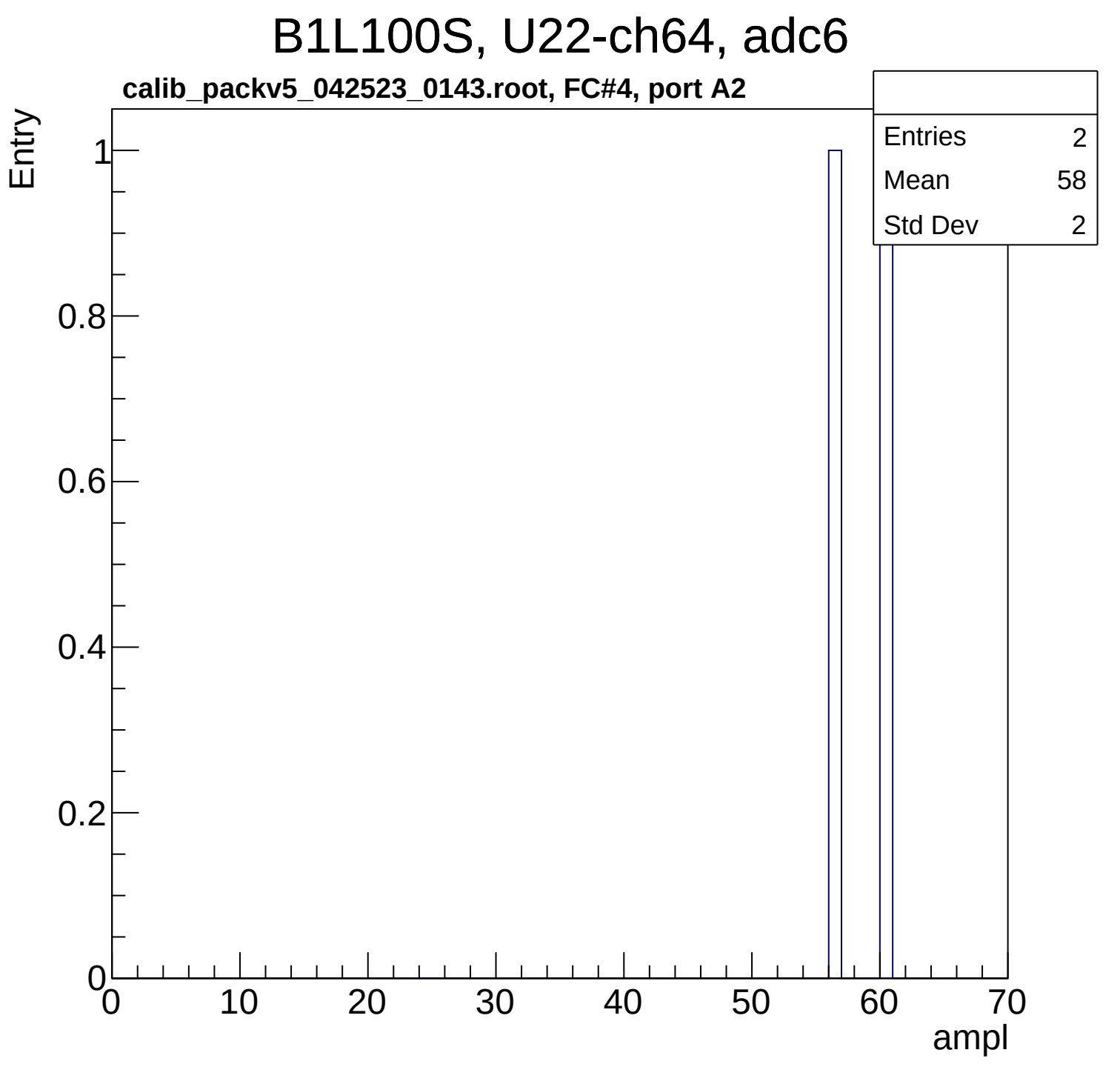
20
30
40
50
60
70
ampl


B1L100S, U22-ch64, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 58
Std Dev 2
Entry
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0.6
0.4
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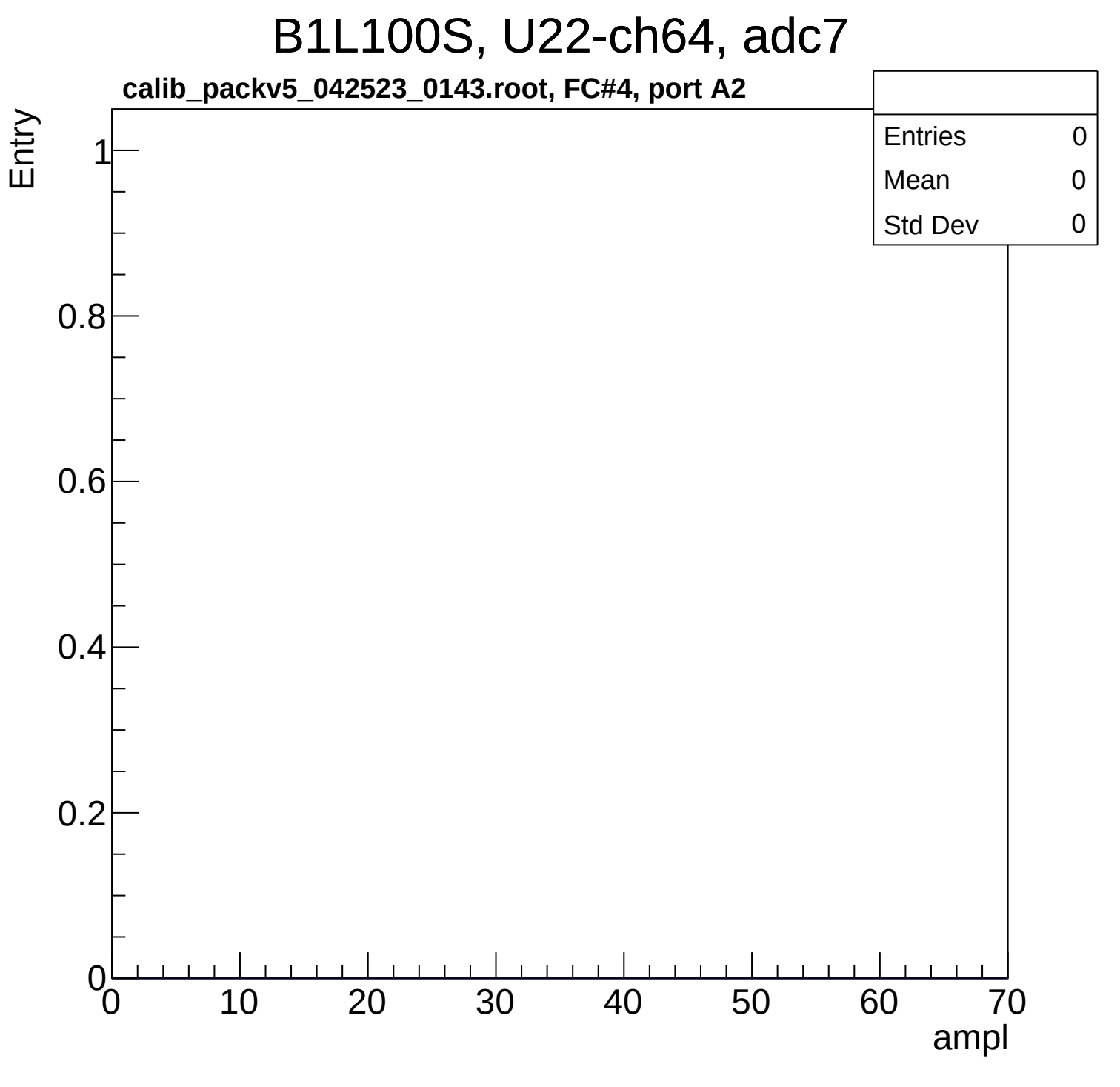
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch64, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
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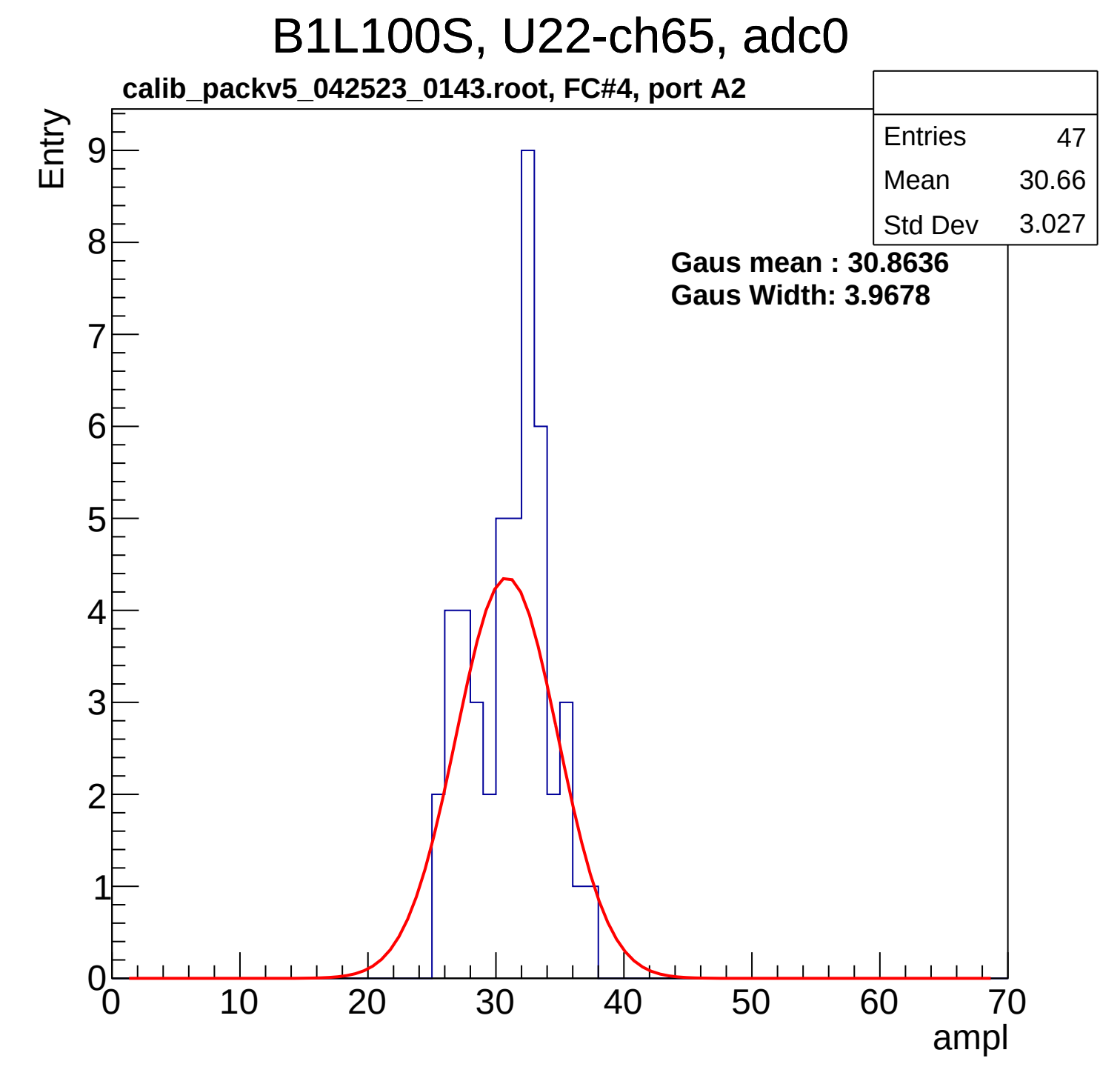
30
40
50
60
70
ampl


B1L100S, U22-ch65, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 30.66
Std Dev 3.027
Gaus mean : 30.8636
Gaus Width: 3.9678
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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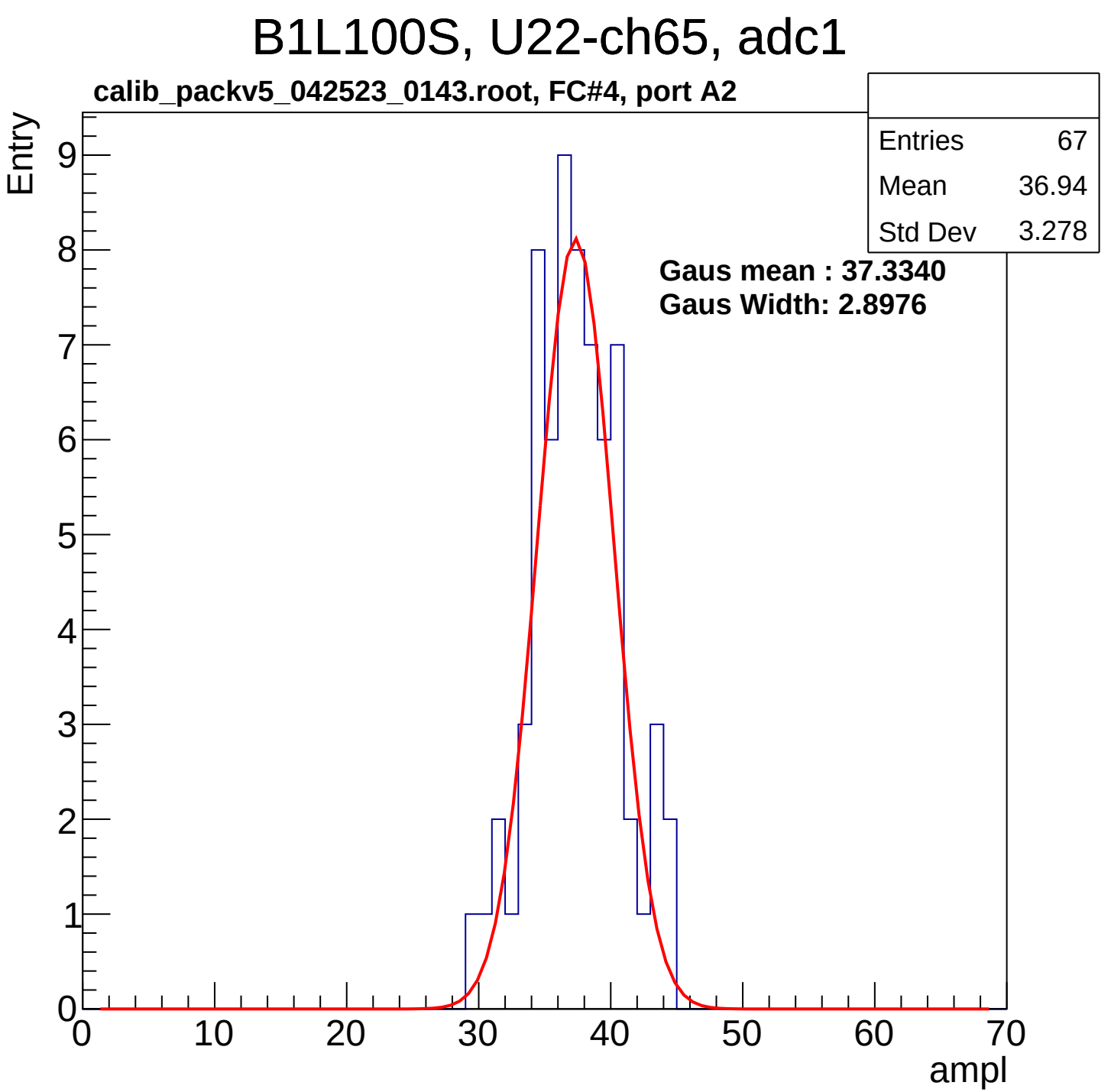
B1L100S, U22-ch65, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.94
Std Dev 3.278
Gaus mean : 37.3340
Gaus Width: 2.8976
Entry
ampl
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3
4
5
6
7
8
9
0
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60
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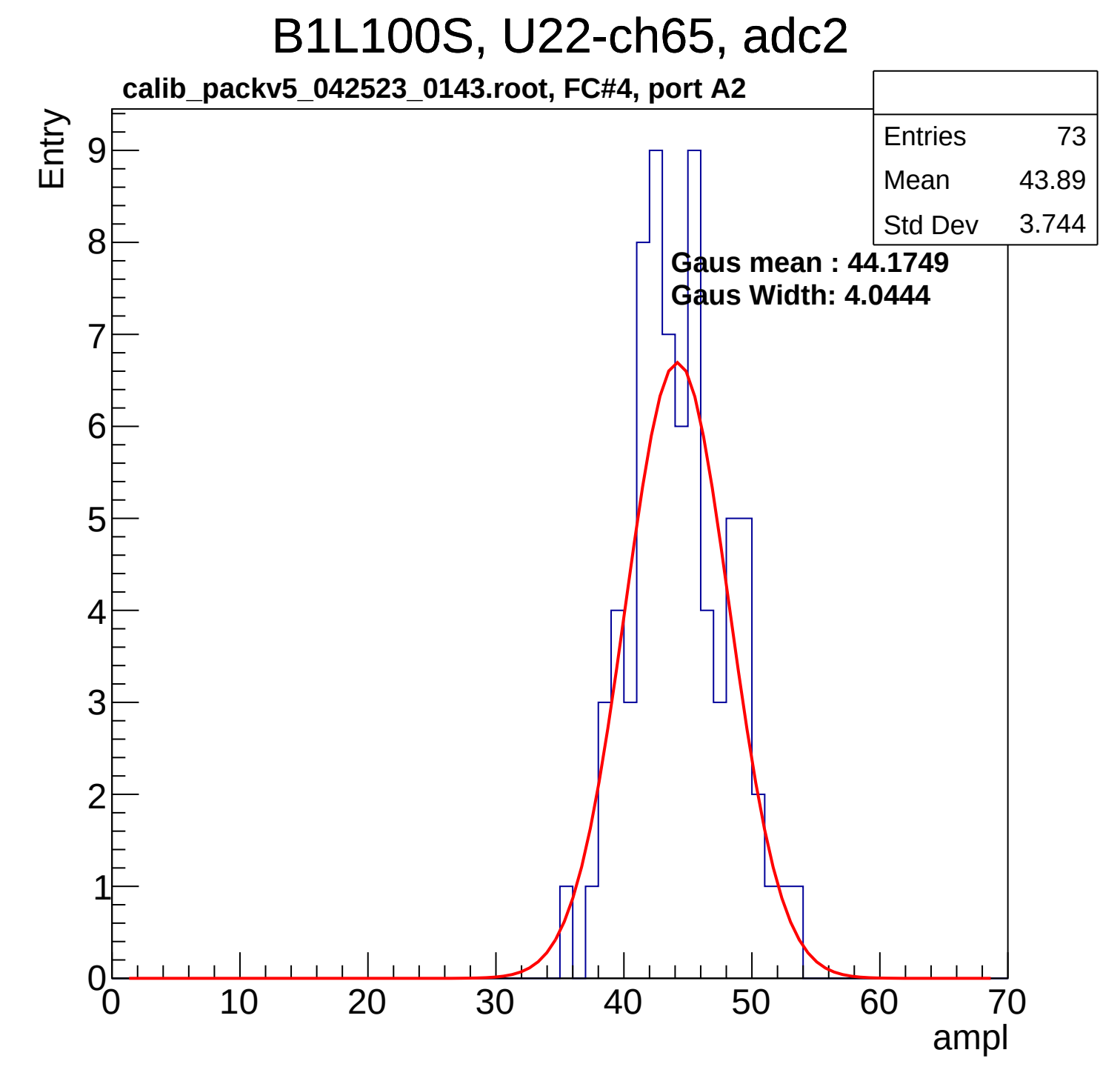
B1L100S, U22-ch65, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.89
Std Dev 3.744
Gaus mean : 44.1749
Gaus Width: 4.0444
Entry
ampl
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2
3
4
5
6
7
8
9
0
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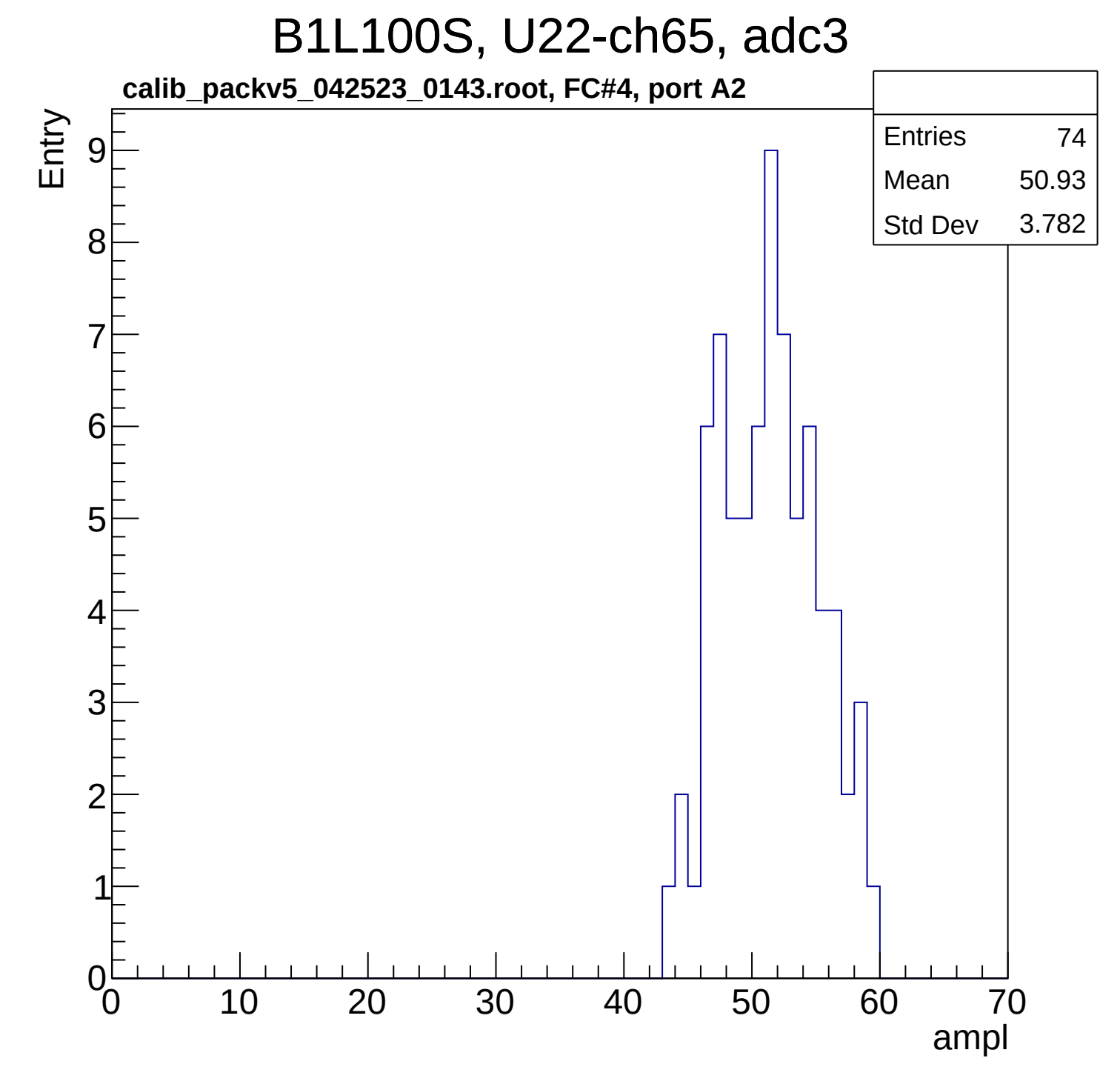
B1L100S, U22-ch65, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 50.93
Std Dev 3.782
Entry
ampl
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3
4
5
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7
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10
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60
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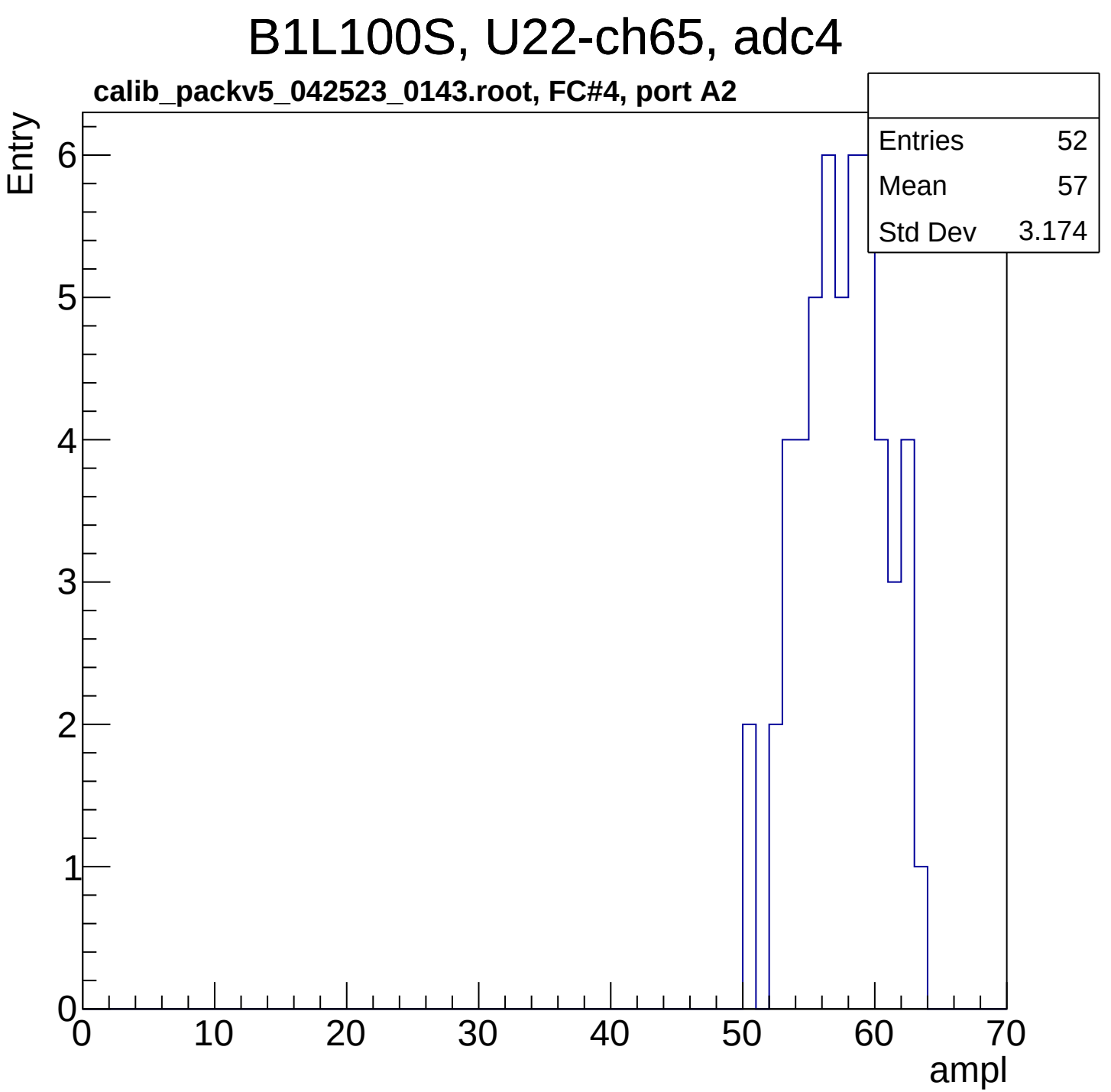
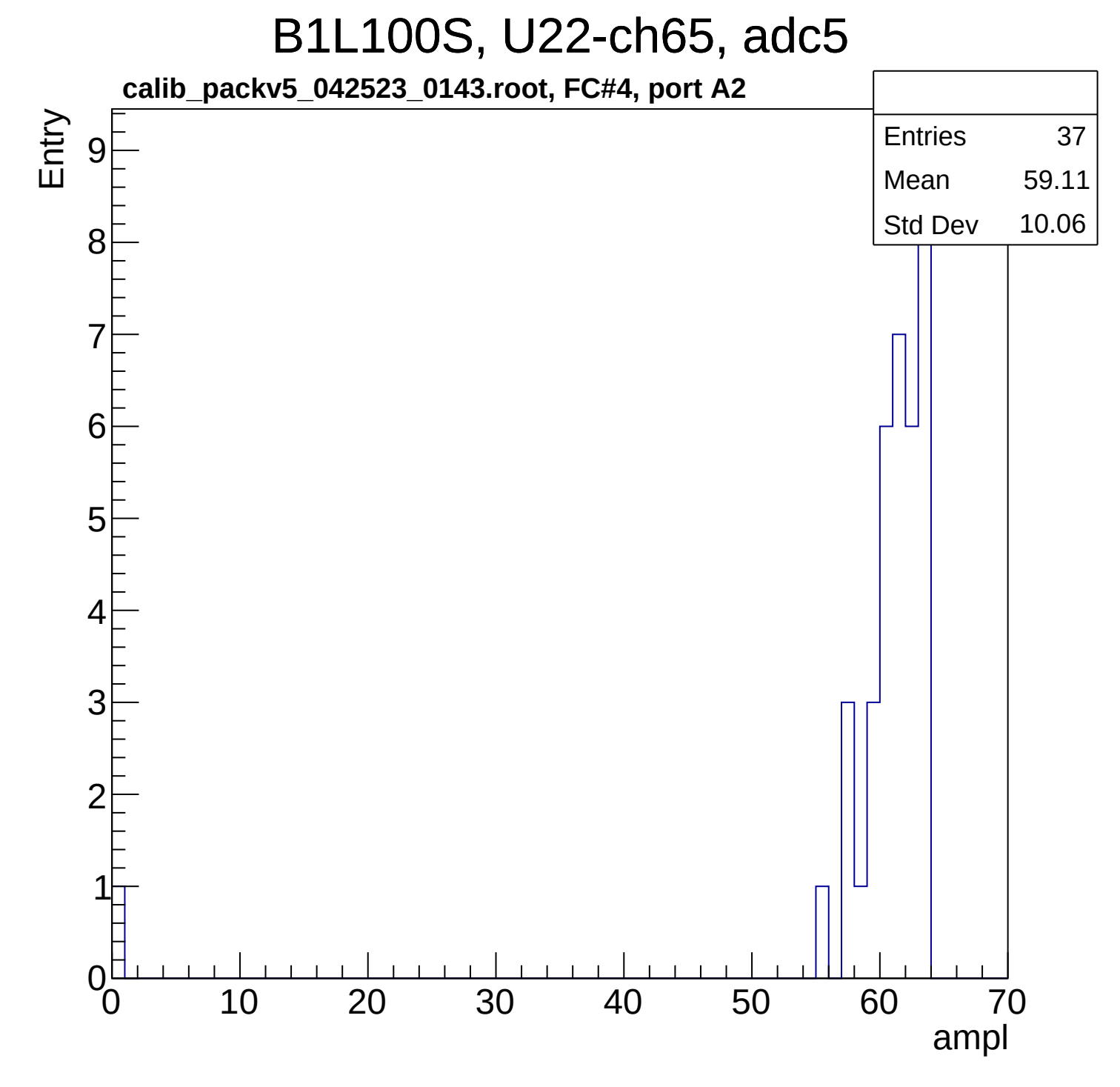

B1L100S, U22-ch65, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 57
Std Dev 3.174
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch65, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 59.11
Std Dev 10.06
Entry
ampl
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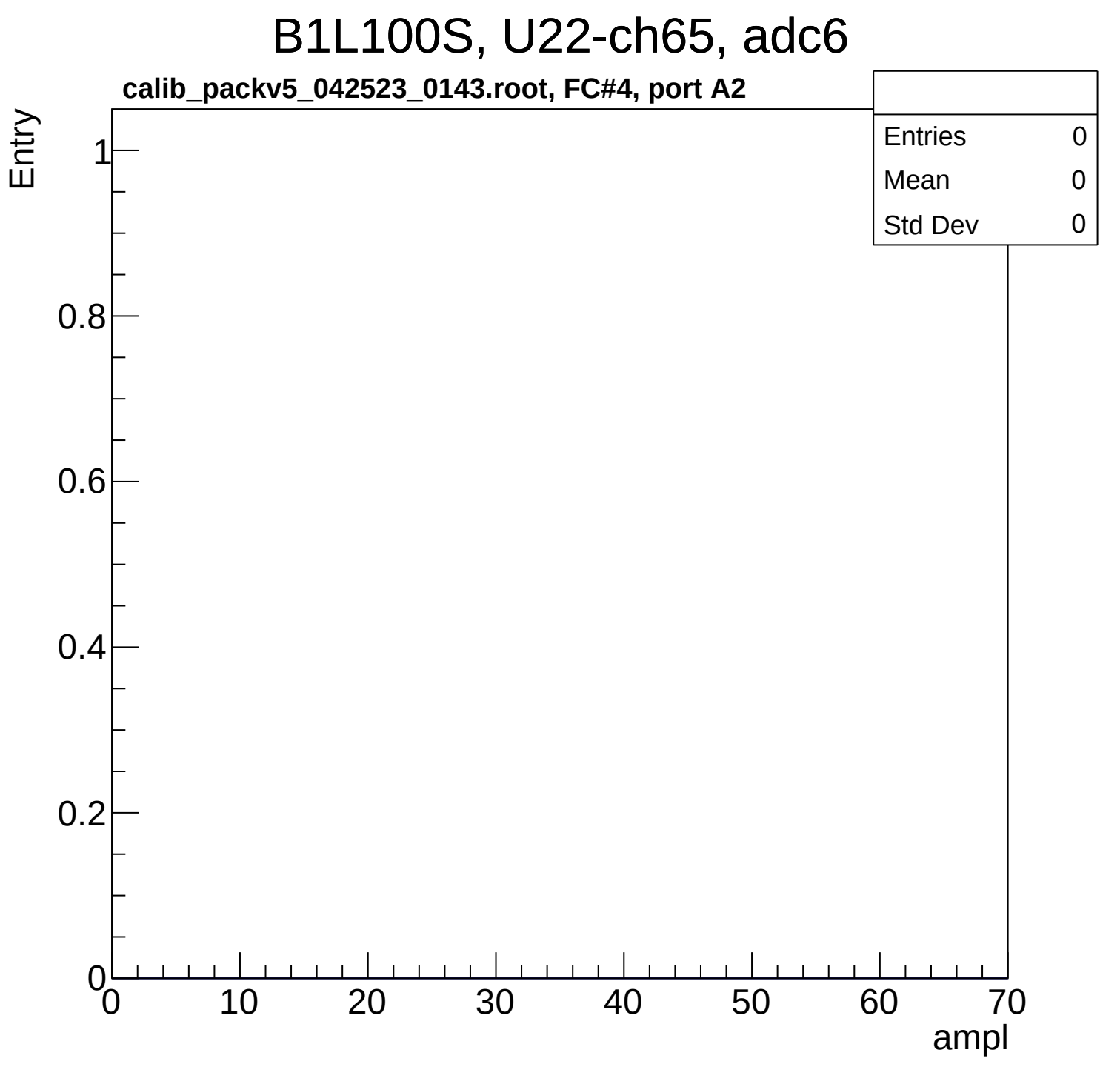
B1L100S, U22-ch65, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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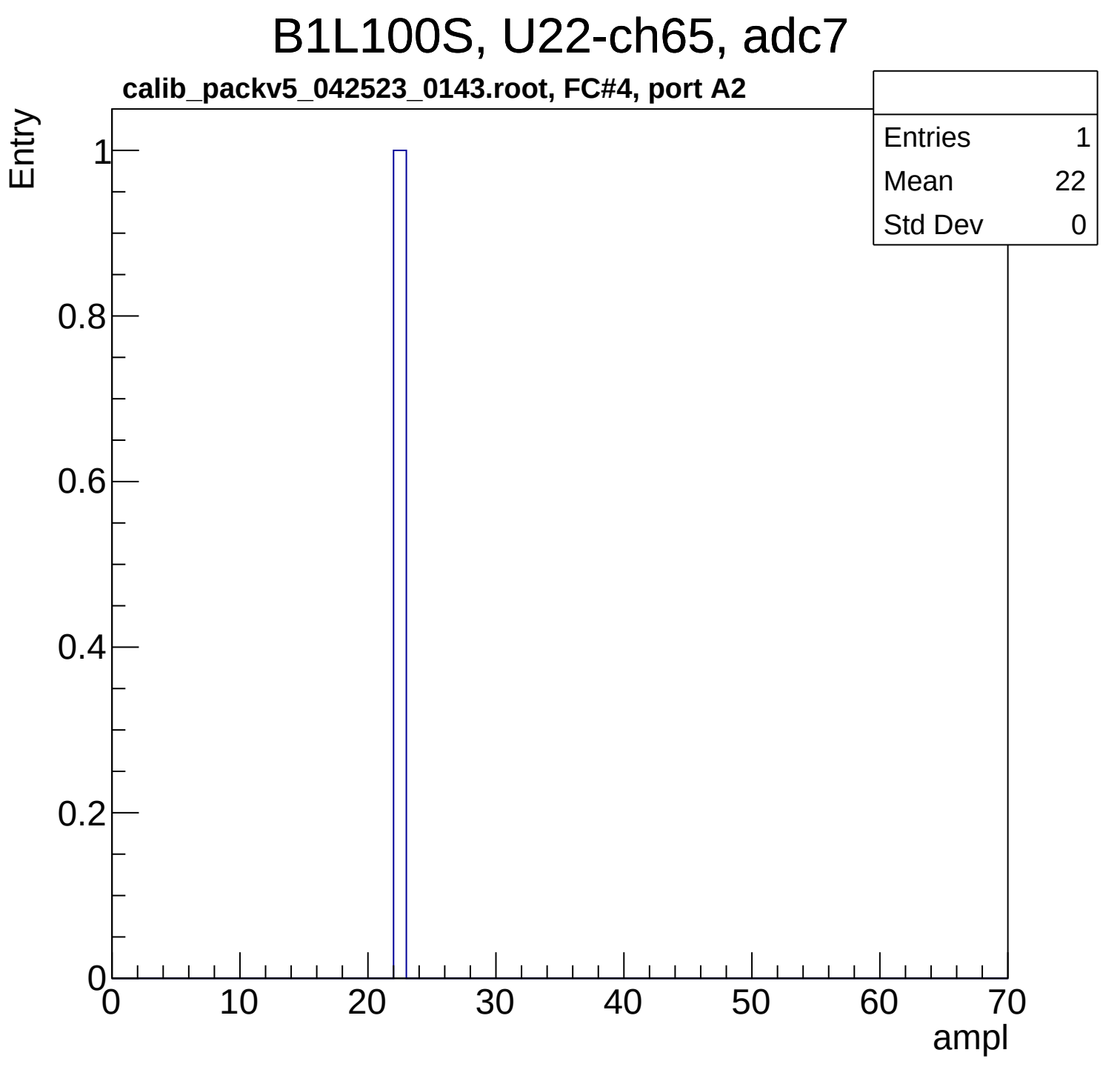
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch65, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 22
Std Dev 0
Entry
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0.8
0.6
0.4
0.2
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10
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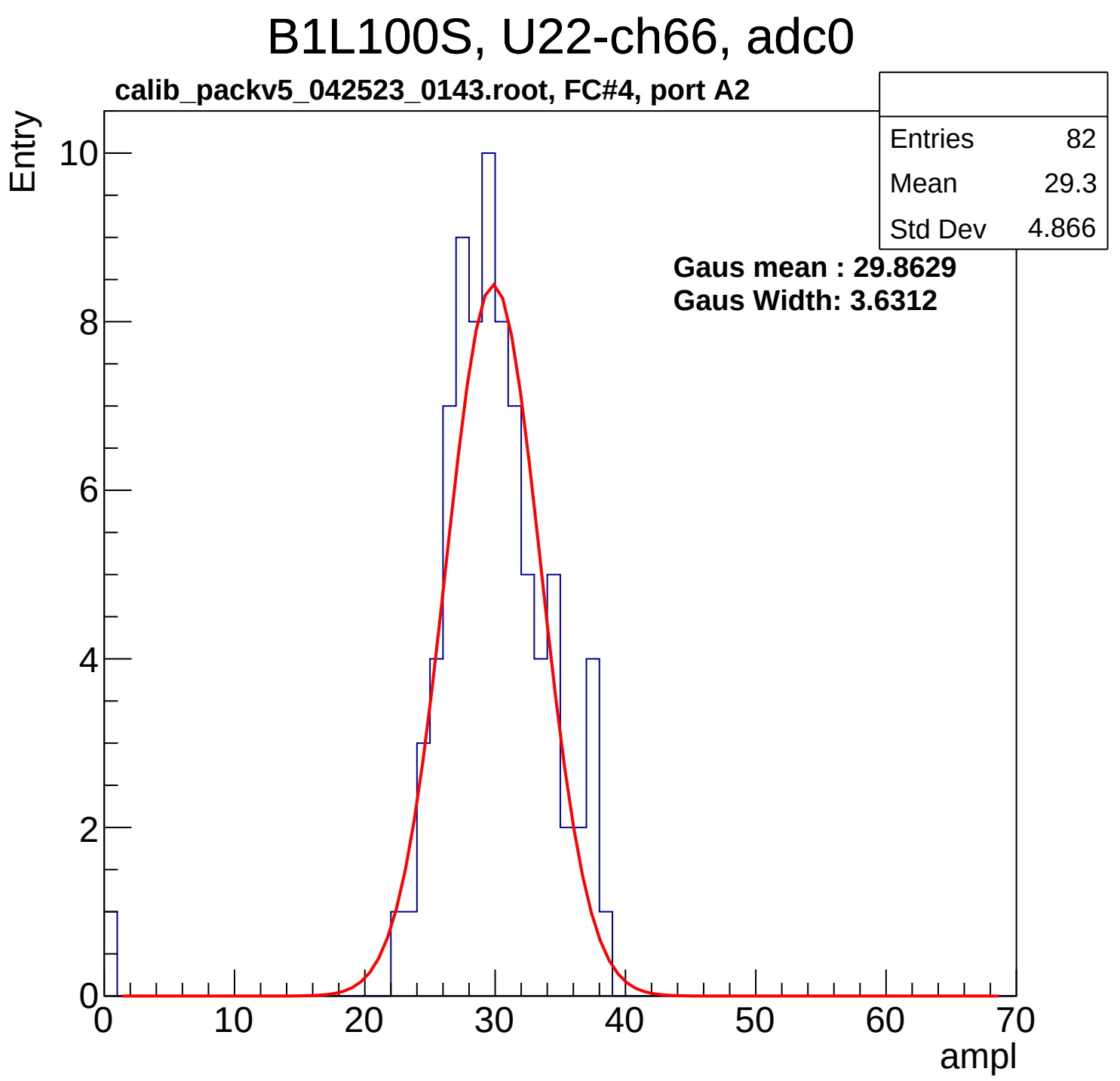
50
60
70
ampl


B1L100S, U22-ch66, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 29.3
Std Dev 4.866
Gaus mean : 29.8629
Gaus Width: 3.6312
Entry
ampl
0
2
4
6
8
10
0
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40
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60
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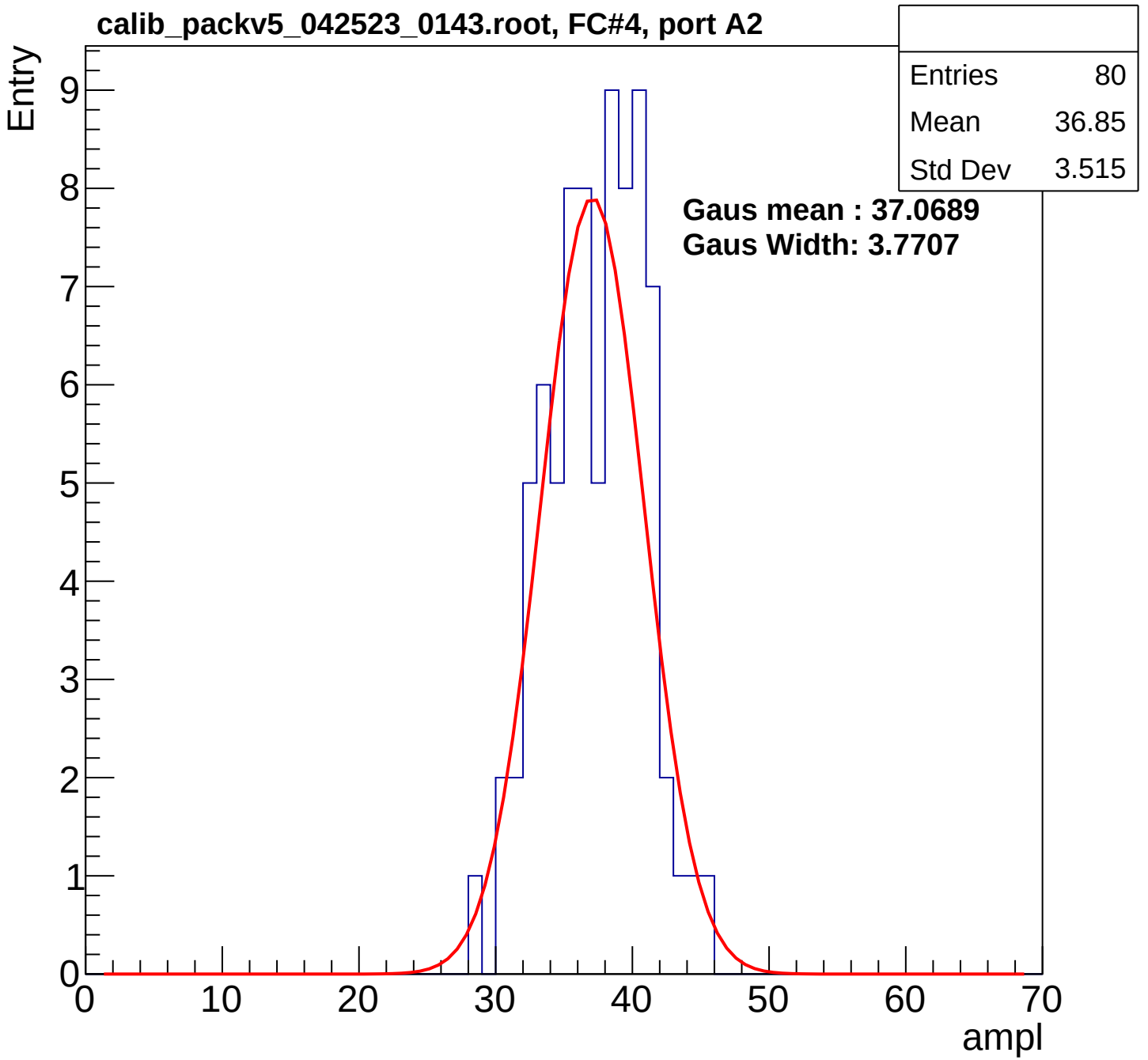
B1L100S, U22-ch66, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.85
Std Dev 3.515
Gaus mean : 37.0689
Gaus Width: 3.7707
Entry
ampl
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2
3
4
5
6
7
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0
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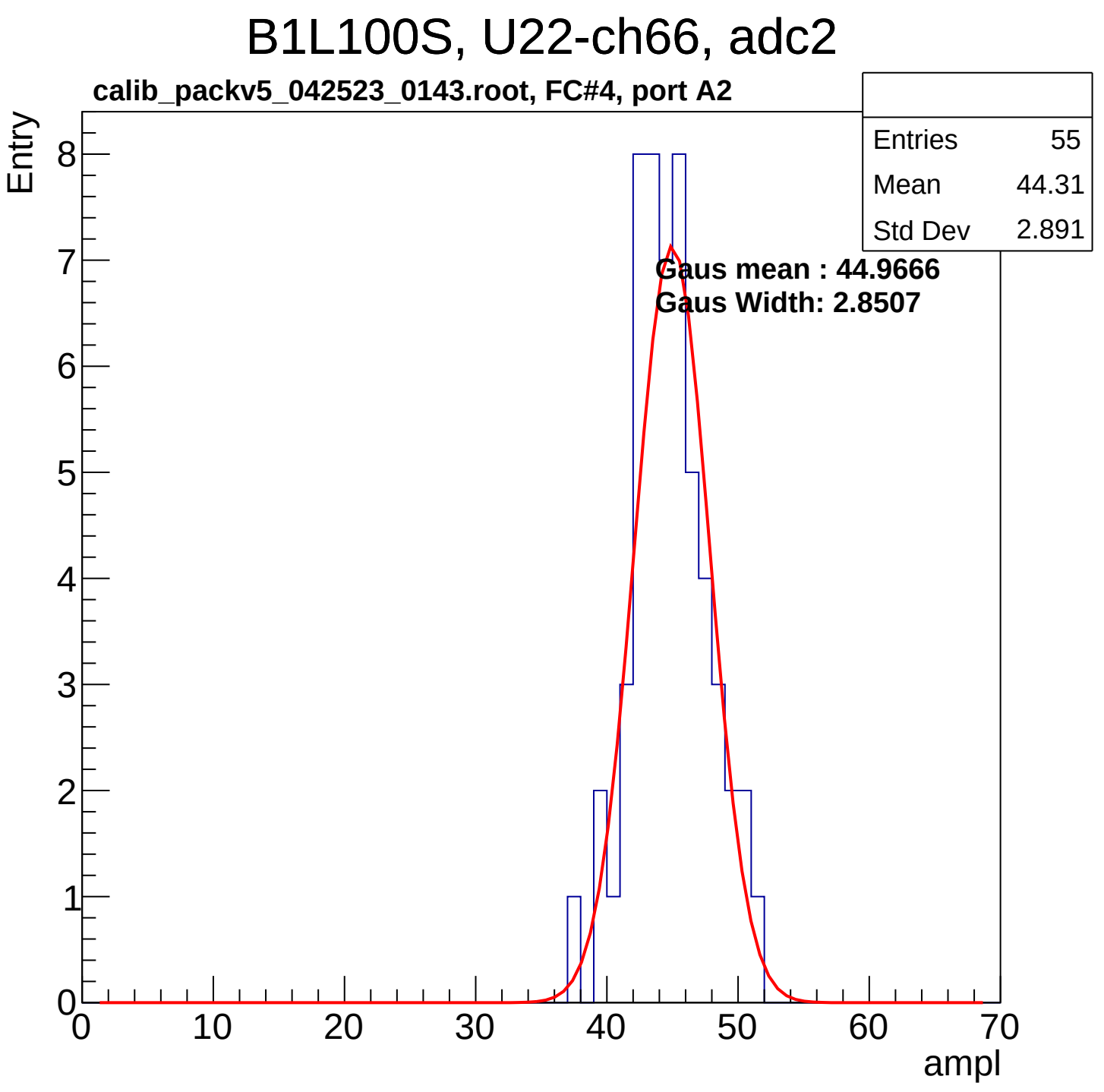
B1L100S, U22-ch66, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 44.31
Std Dev 2.891
Gaus mean : 44.9666
Gaus Width: 2.8507
Entry
ampl
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1
2
3
4
5
6
7
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0
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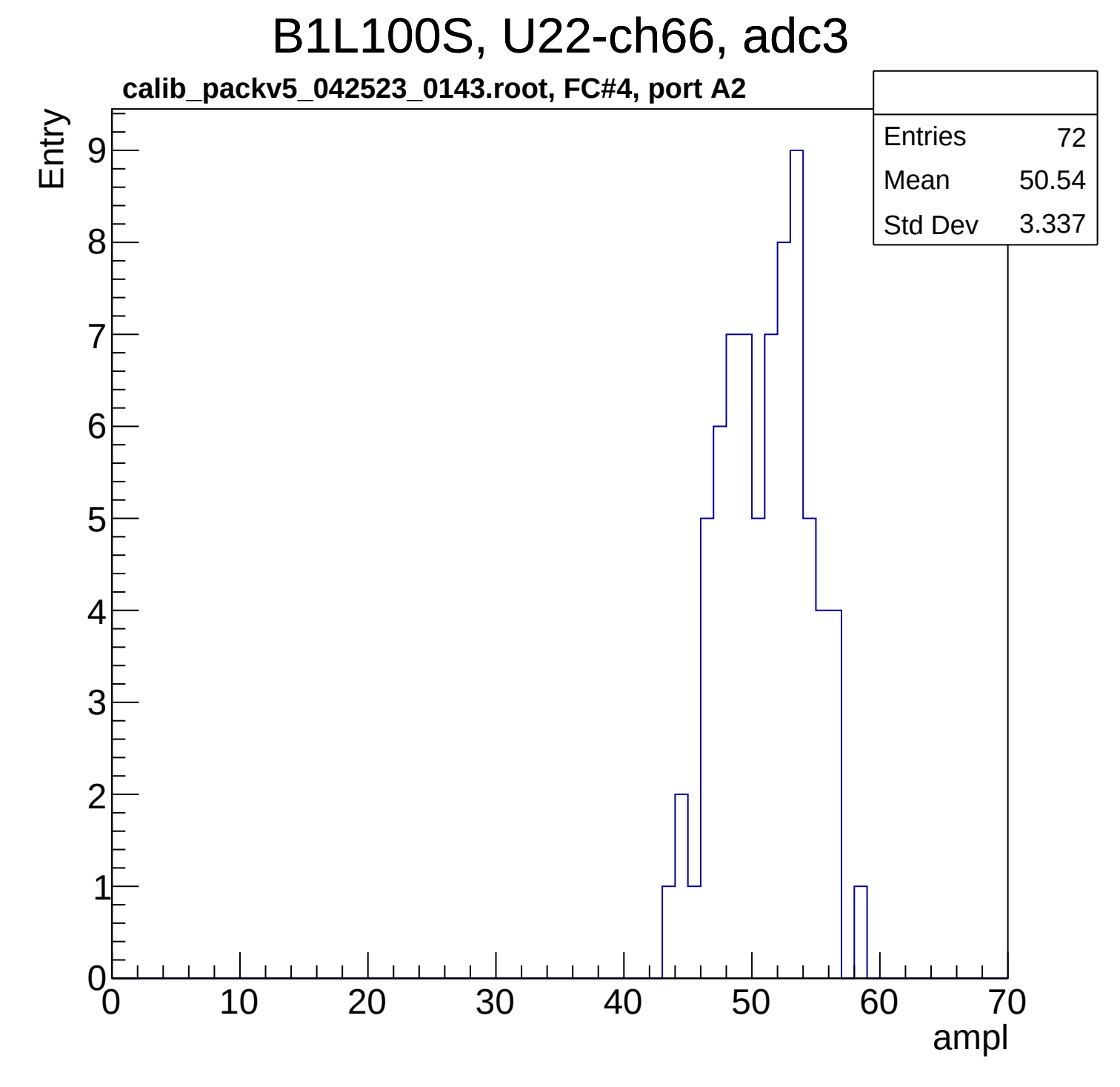
B1L100S, U22-ch66, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 50.54
Std Dev 3.337
Entry
ampl
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2
3
4
5
6
7
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0
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60
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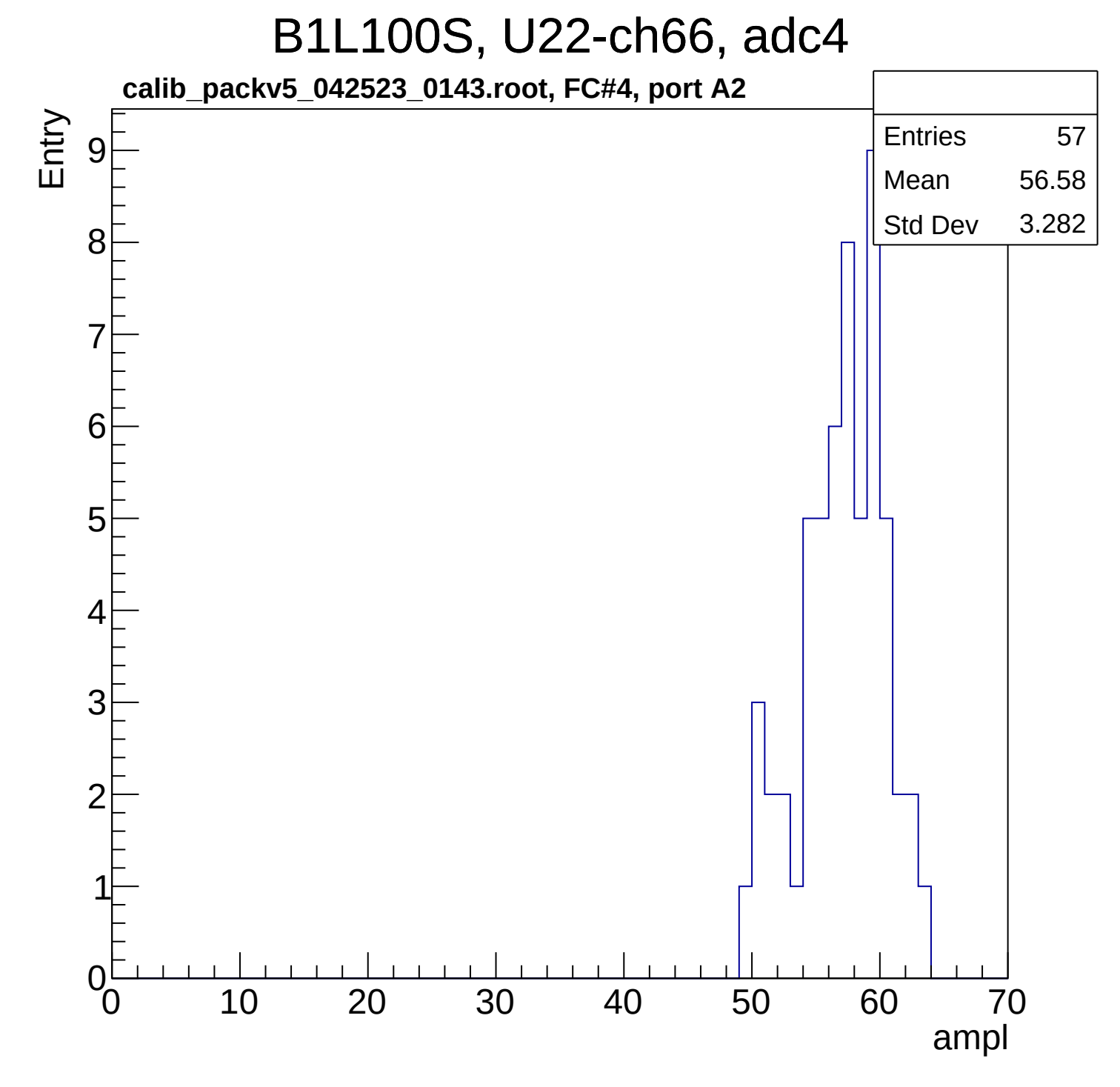
B1L100S, U22-ch66, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 56.58
Std Dev 3.282
Entry
ampl
0
1
2
3
4
5
6
7
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0
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60
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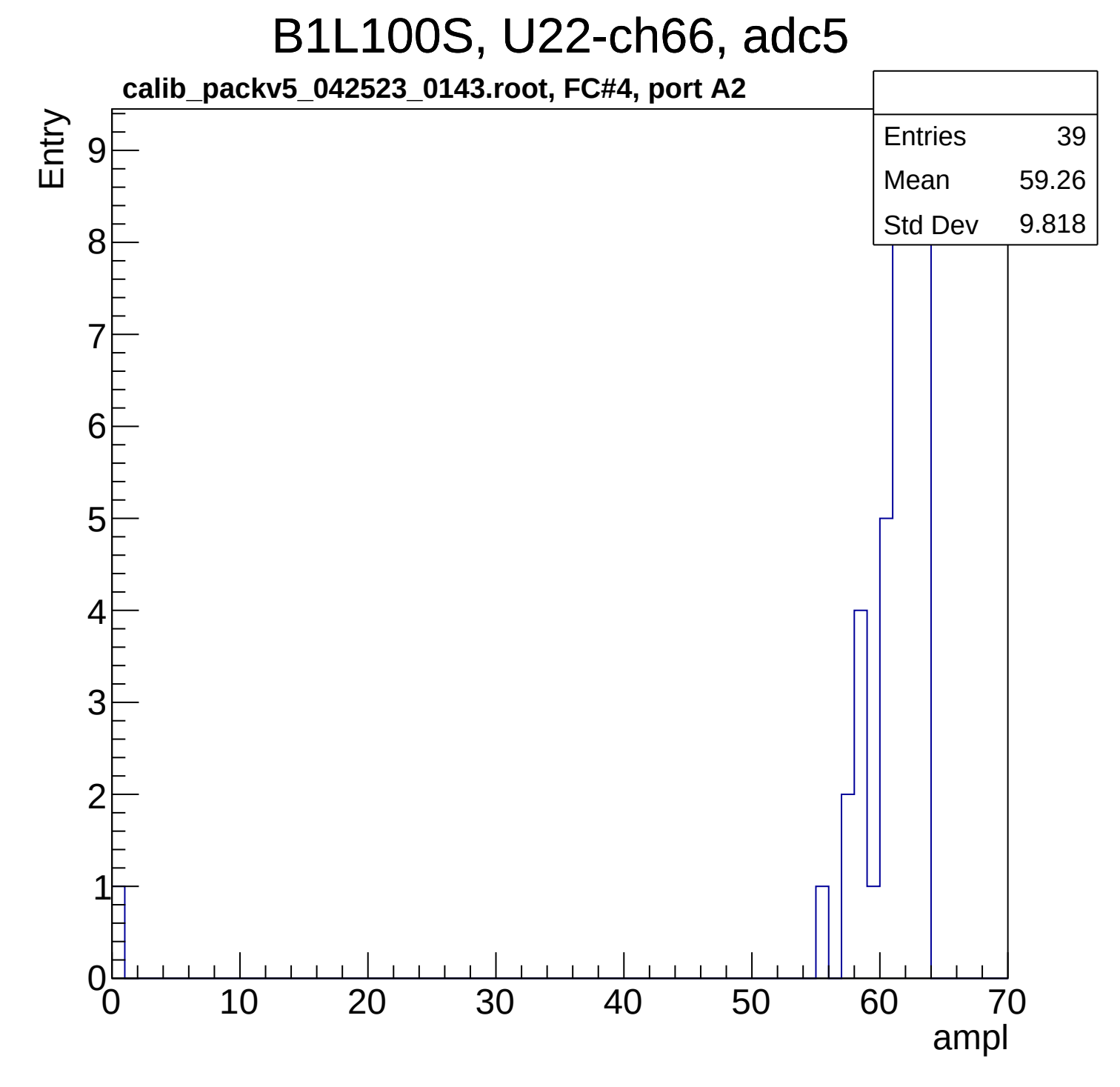
B1L100S, U22-ch66, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 39
Mean 59.26
Std Dev 9.818
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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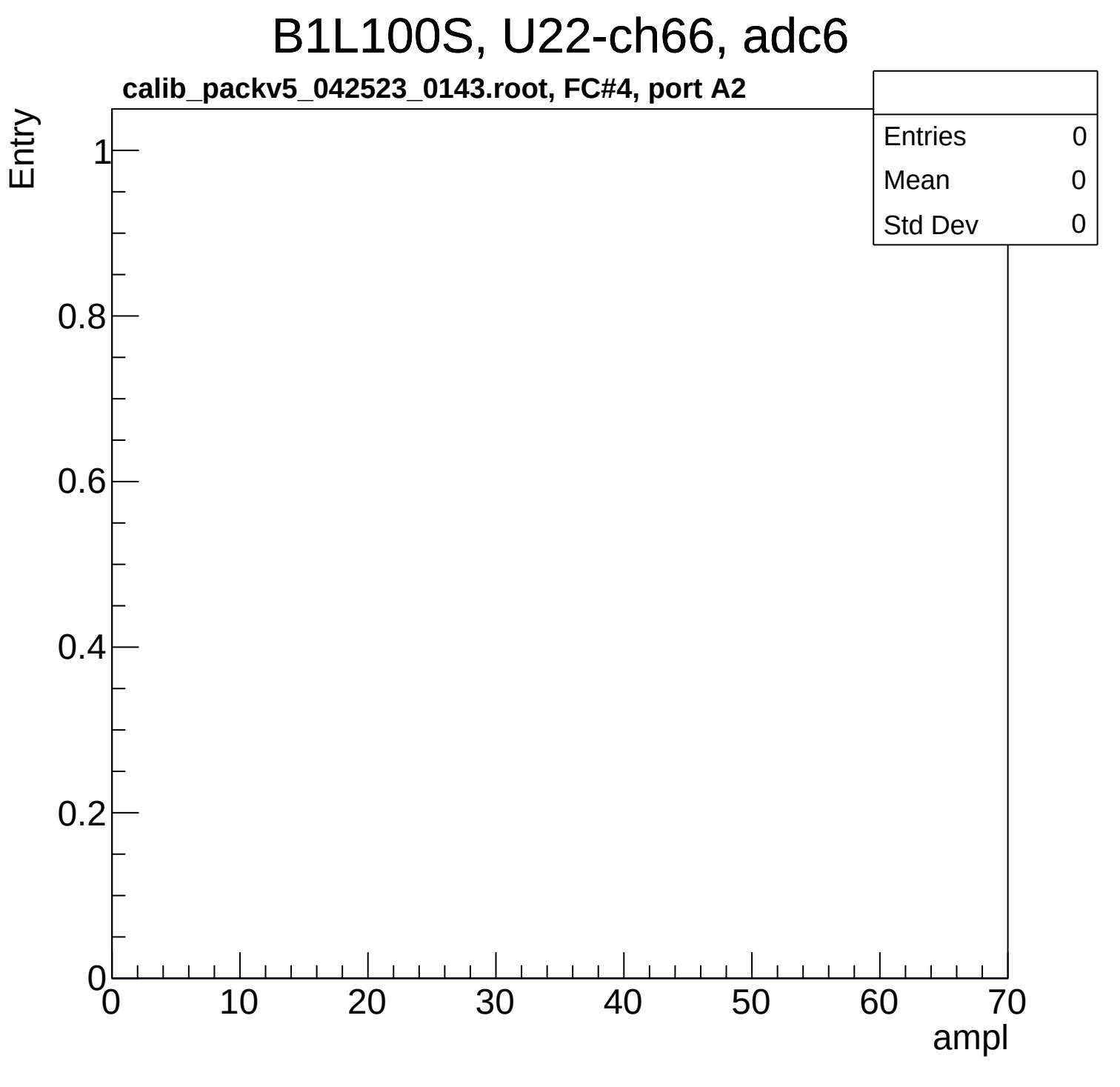
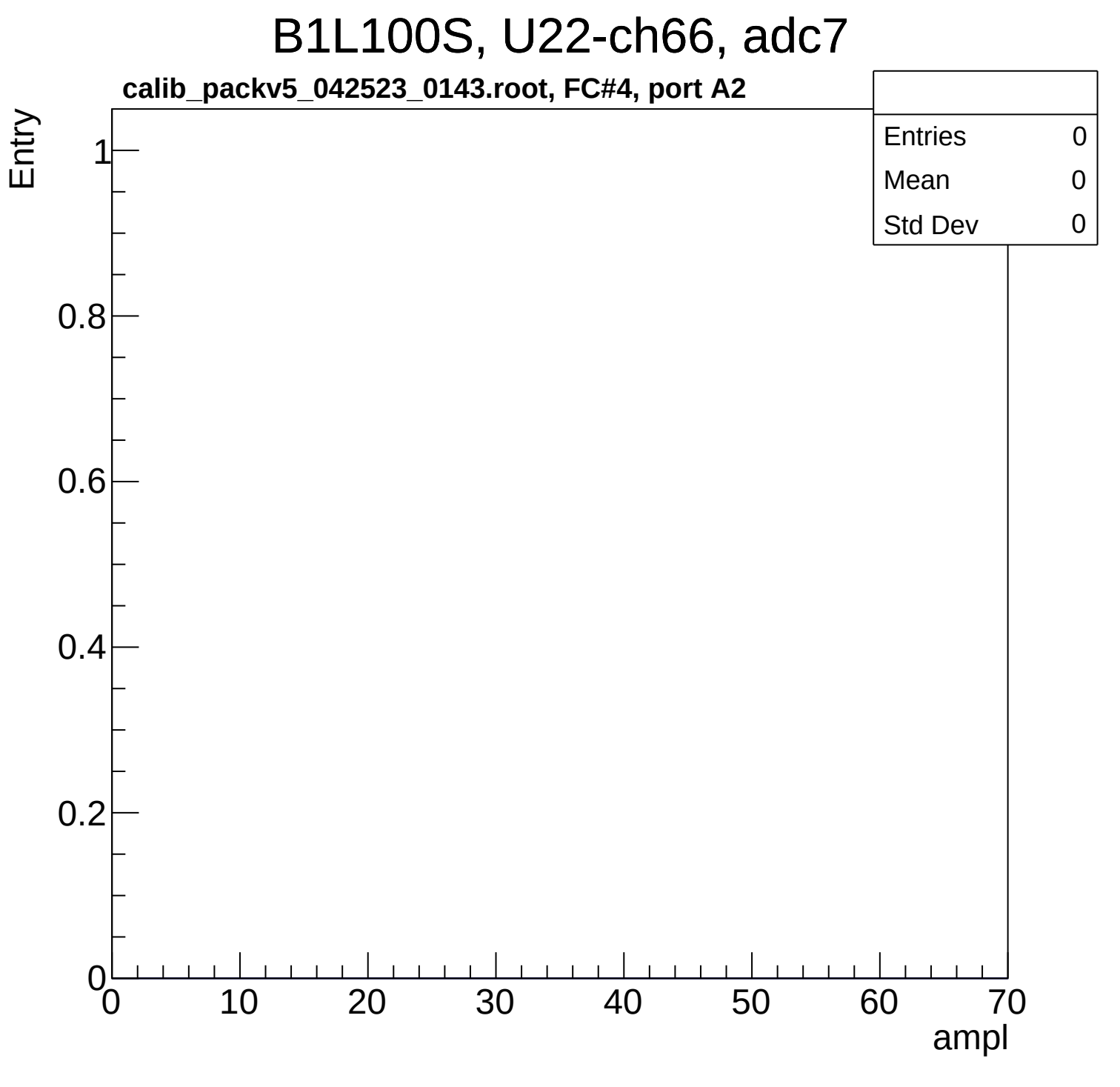
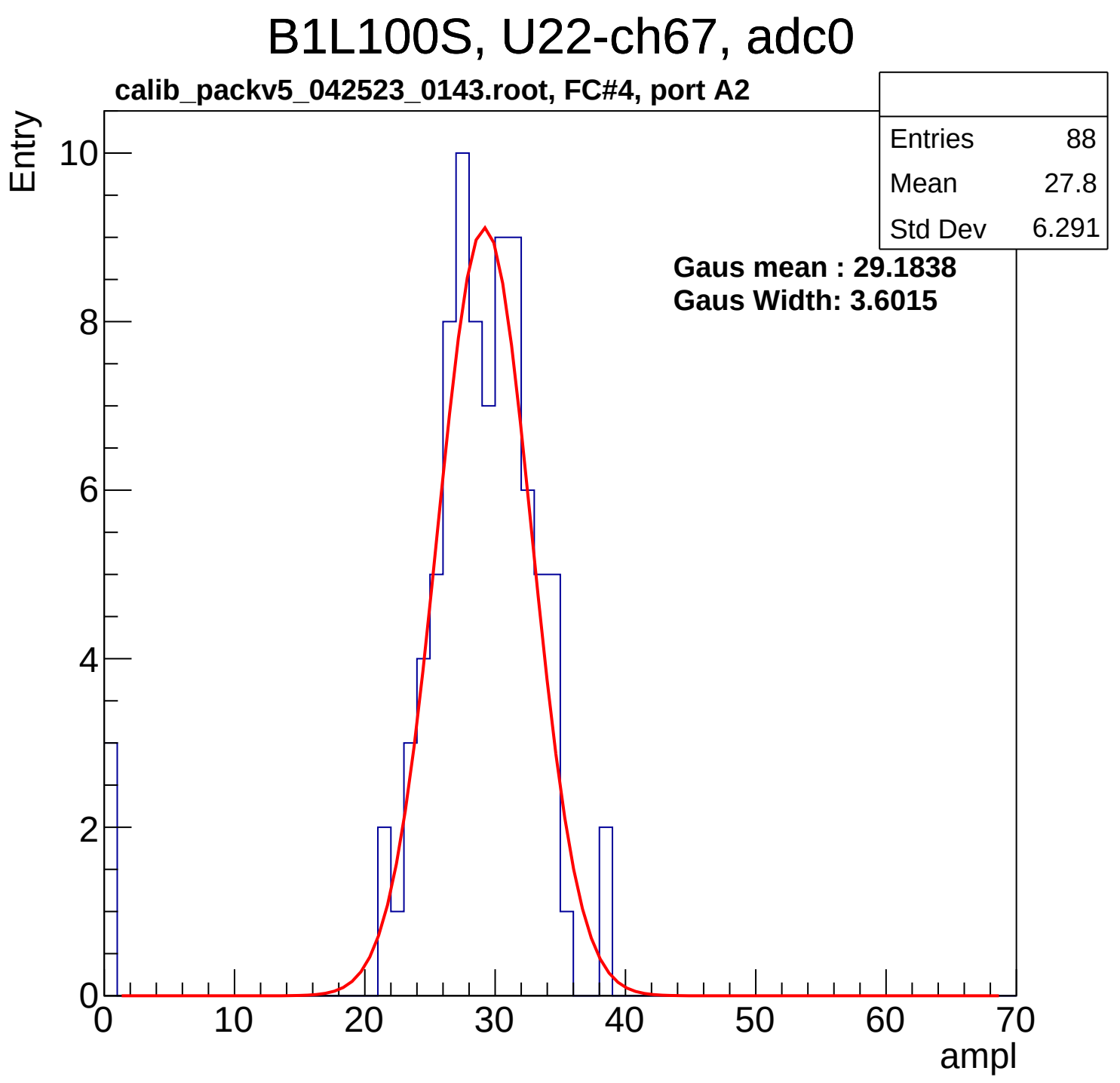

B1L100S, U22-ch66, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch66, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch67, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 27.8
Std Dev 6.291
Gaus mean : 29.1838
Gaus Width: 3.6015
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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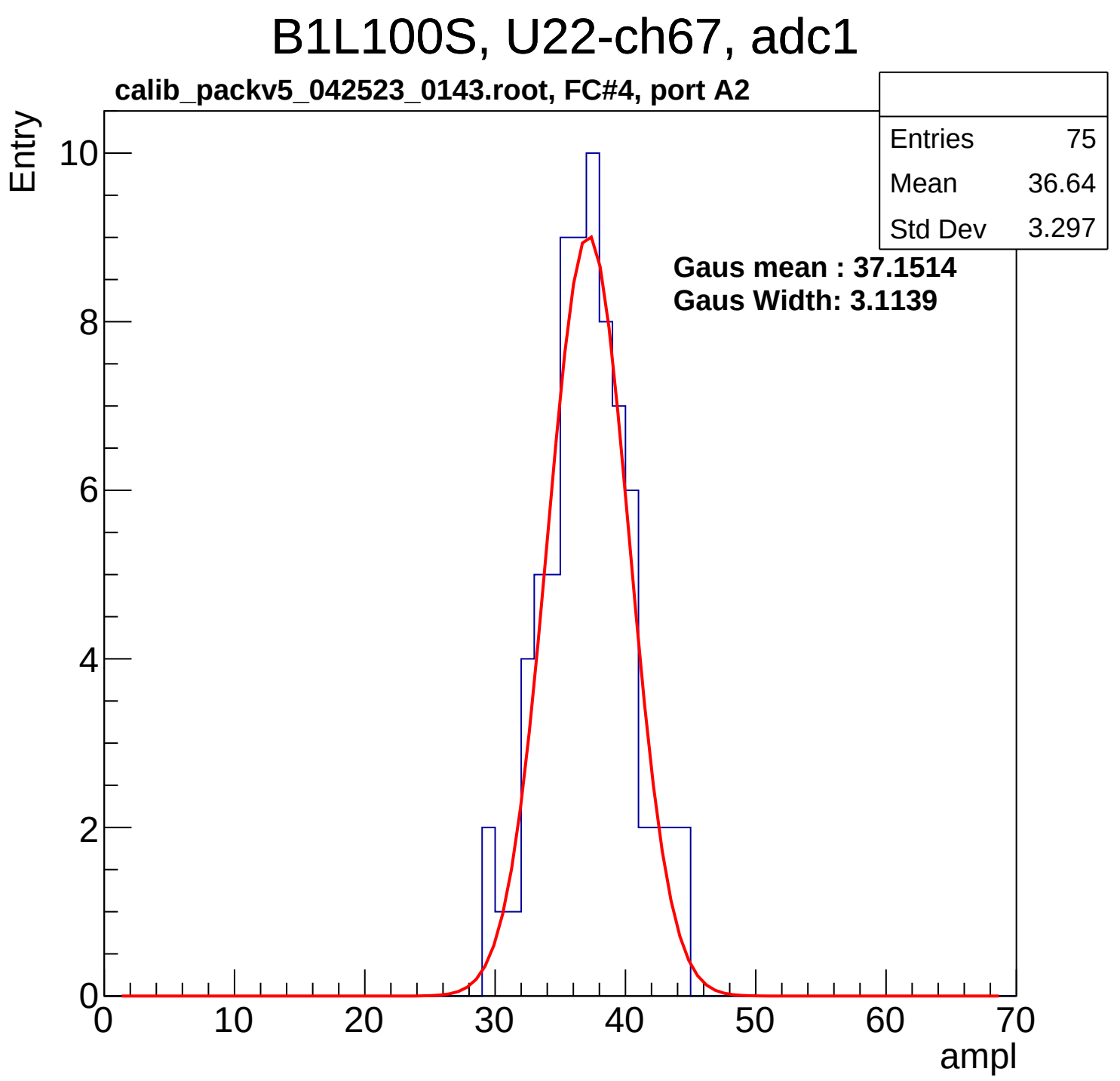
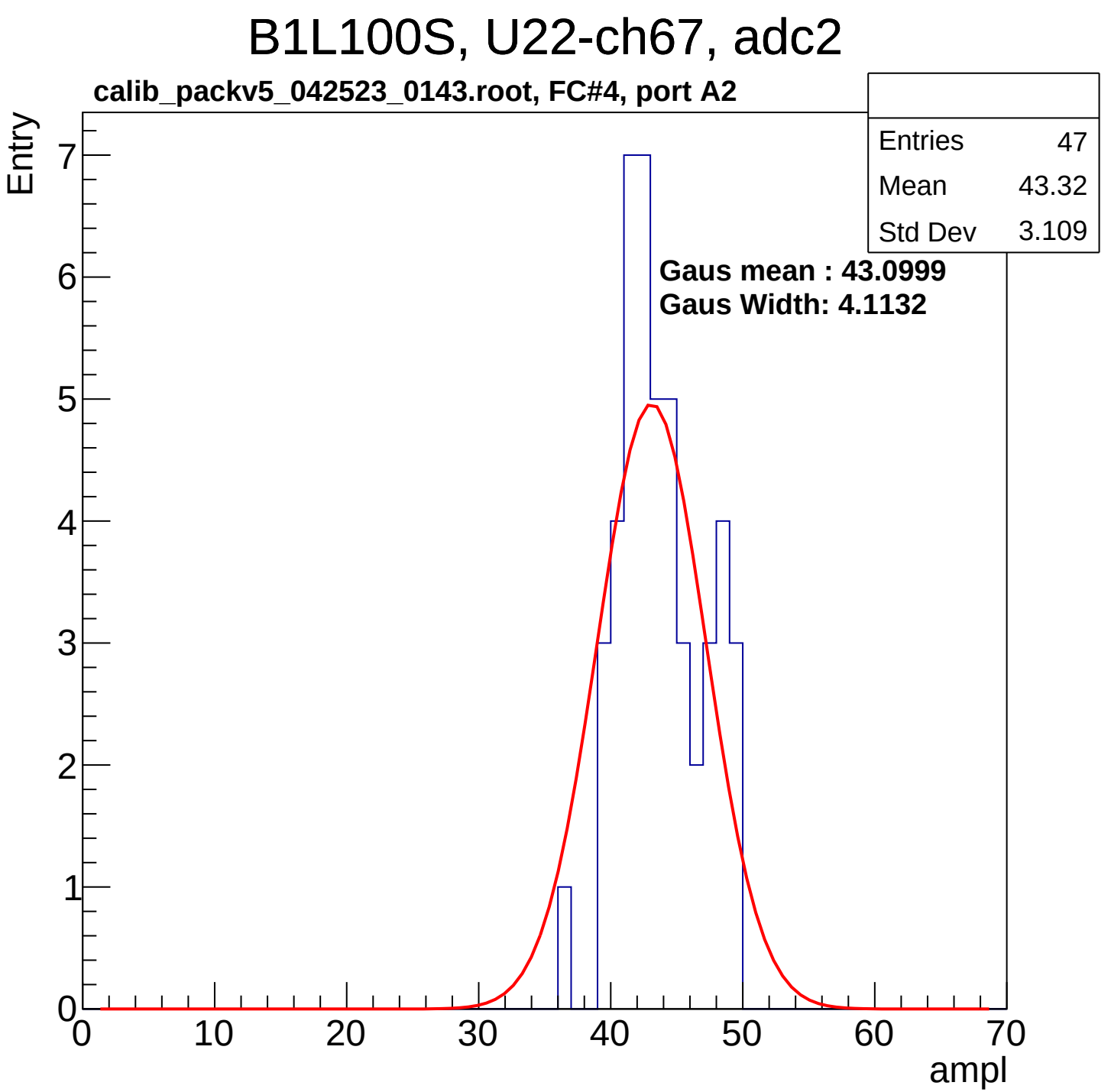
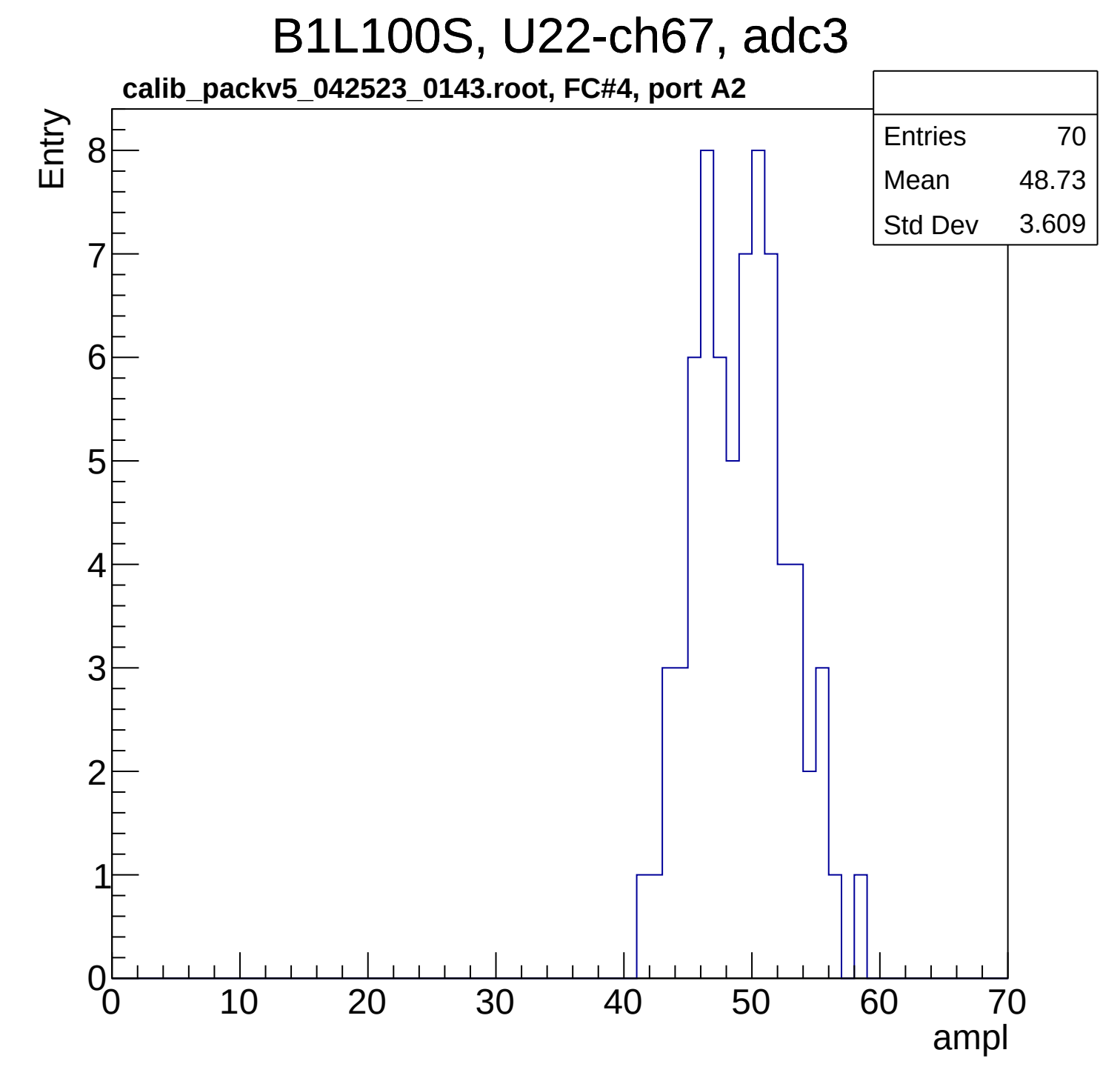
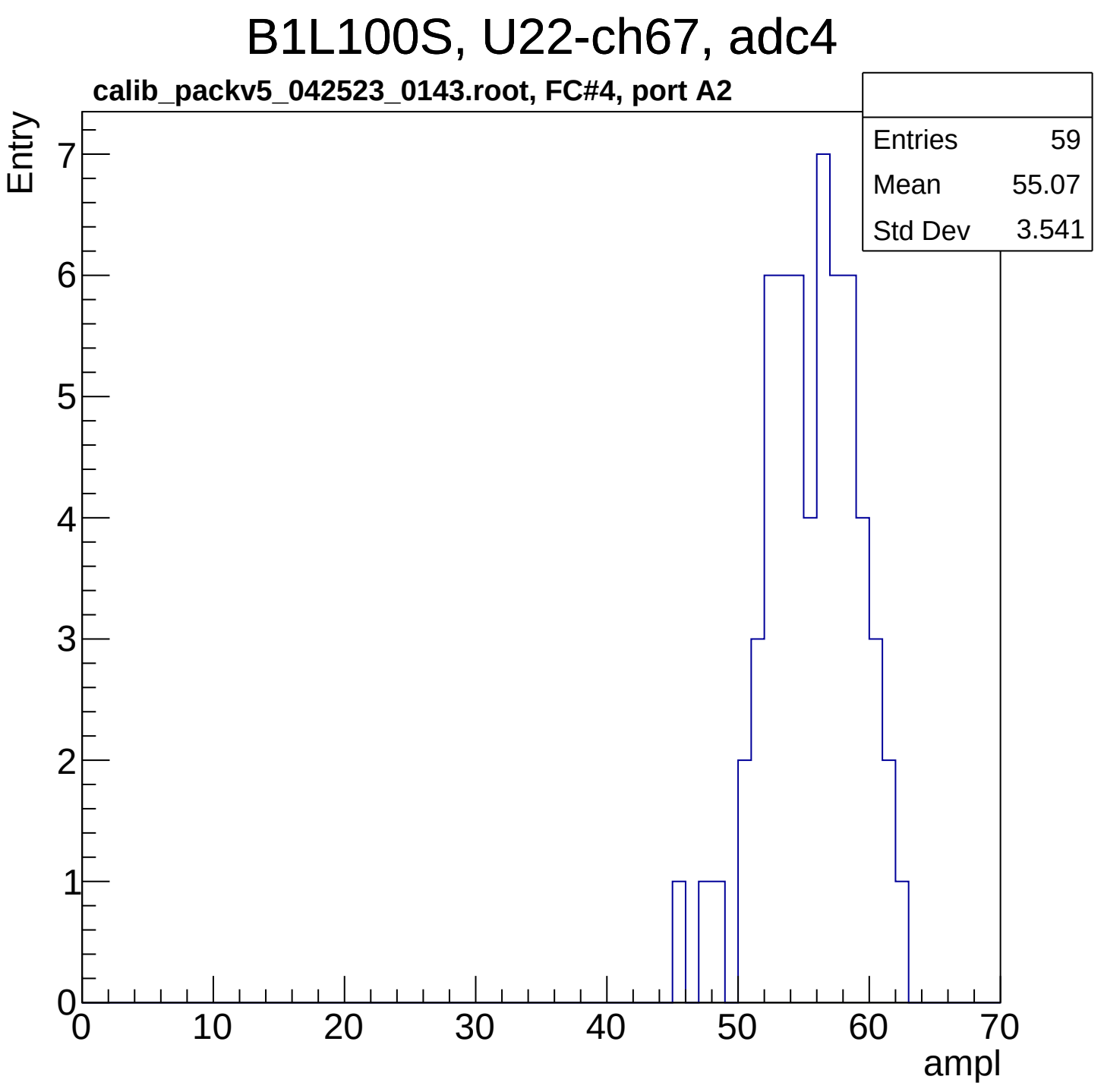
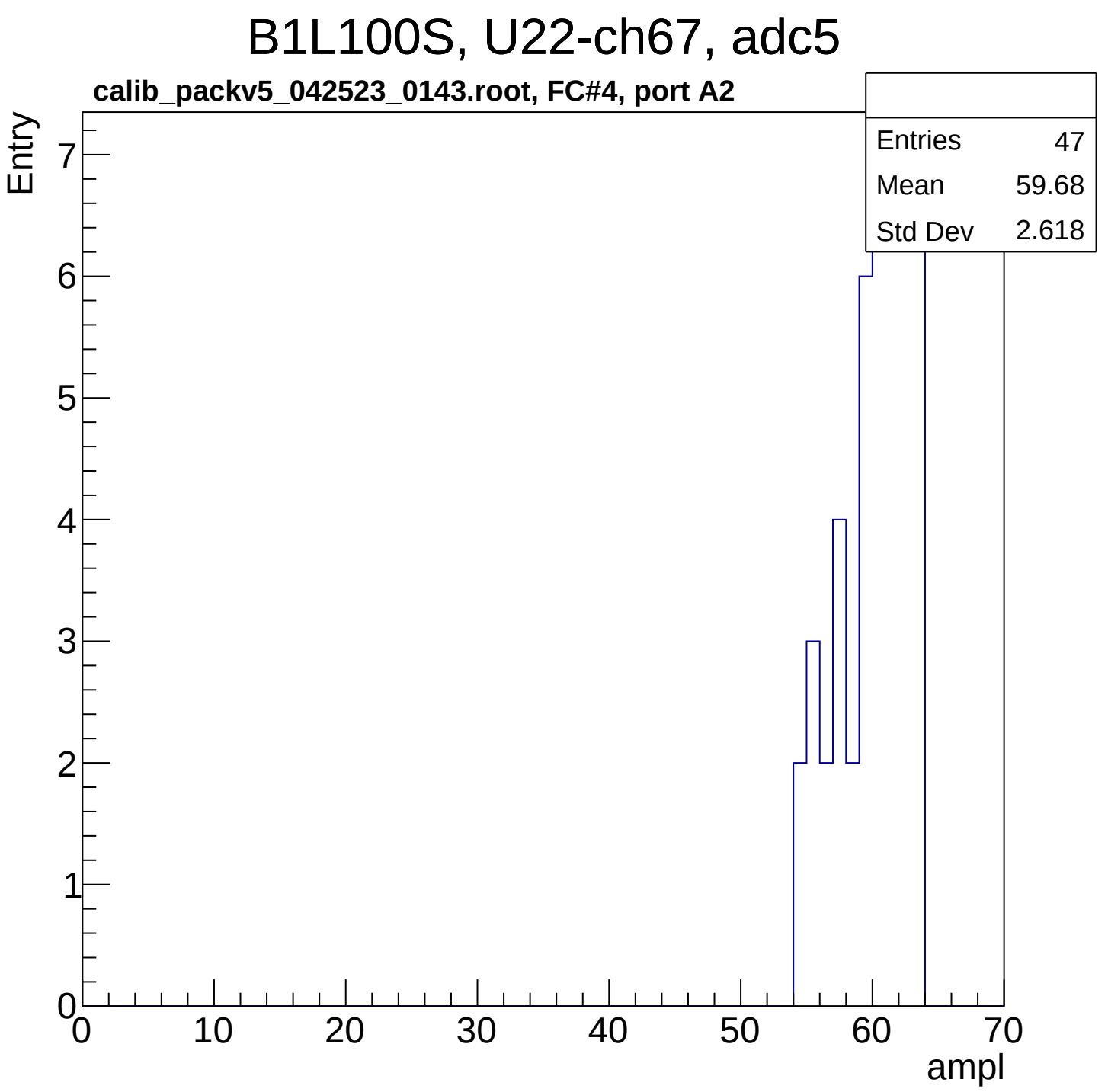
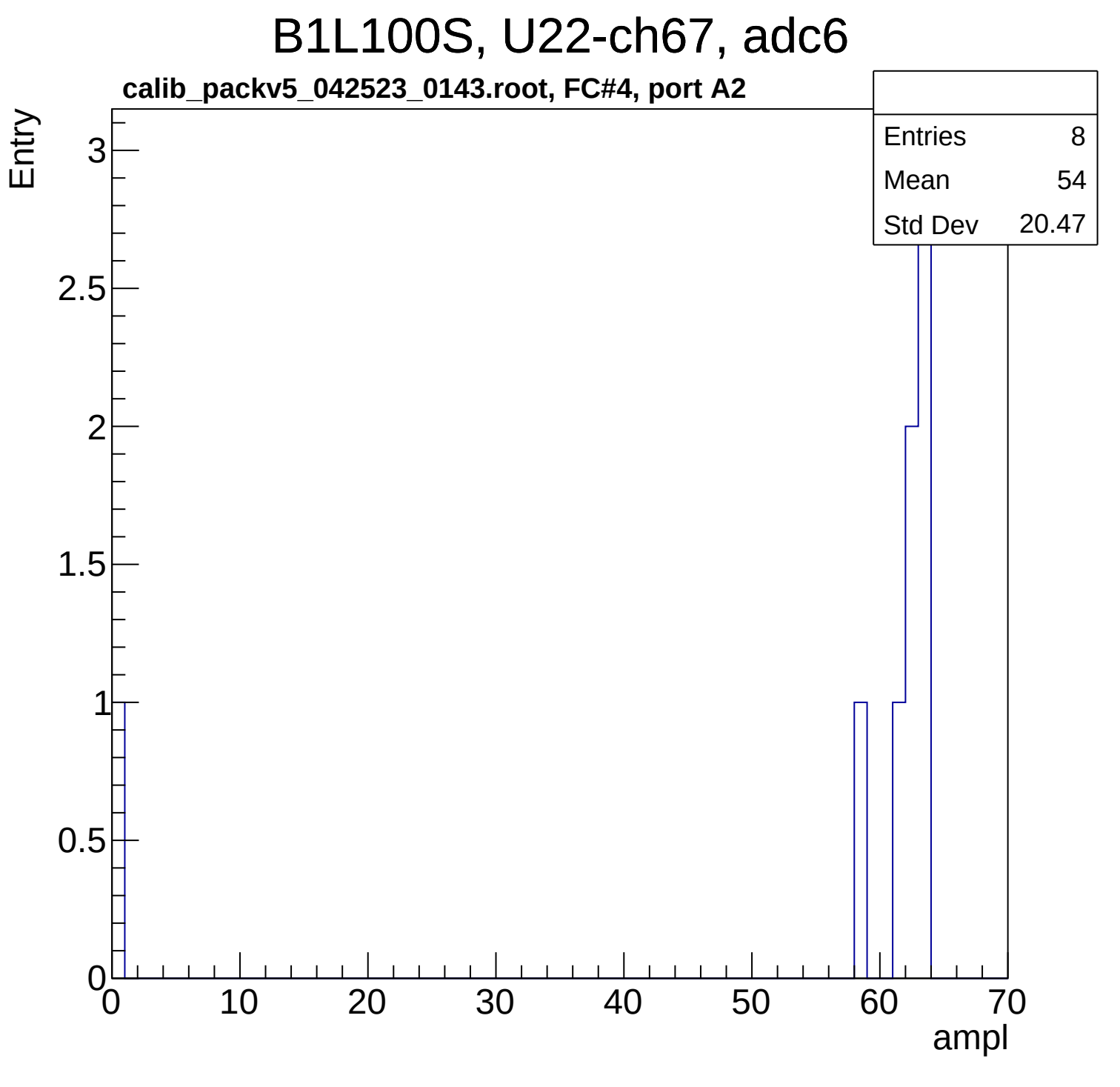

B1L100S, U22-ch67, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.64
Std Dev 3.297
Gaus mean : 37.1514
Gaus Width: 3.1139
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch67, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 43.32
Std Dev 3.109
Gaus mean : 43.0999
Gaus Width: 4.1132
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch67, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 48.73
Std Dev 3.609
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch67, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.07
Std Dev 3.541
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch67, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 59.68
Std Dev 2.618
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch67, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 54
Std Dev 20.47
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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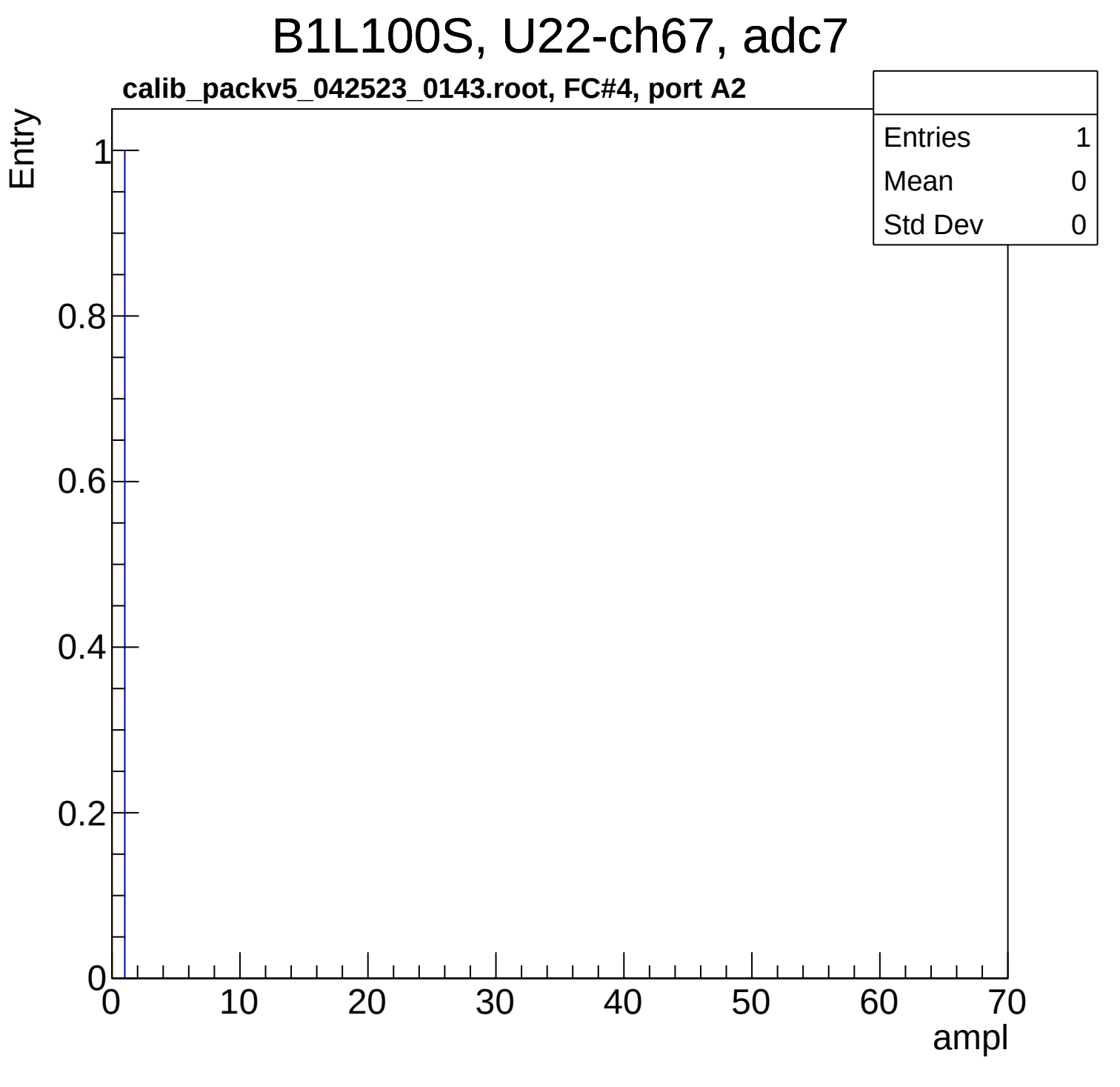
B1L100S, U22-ch67, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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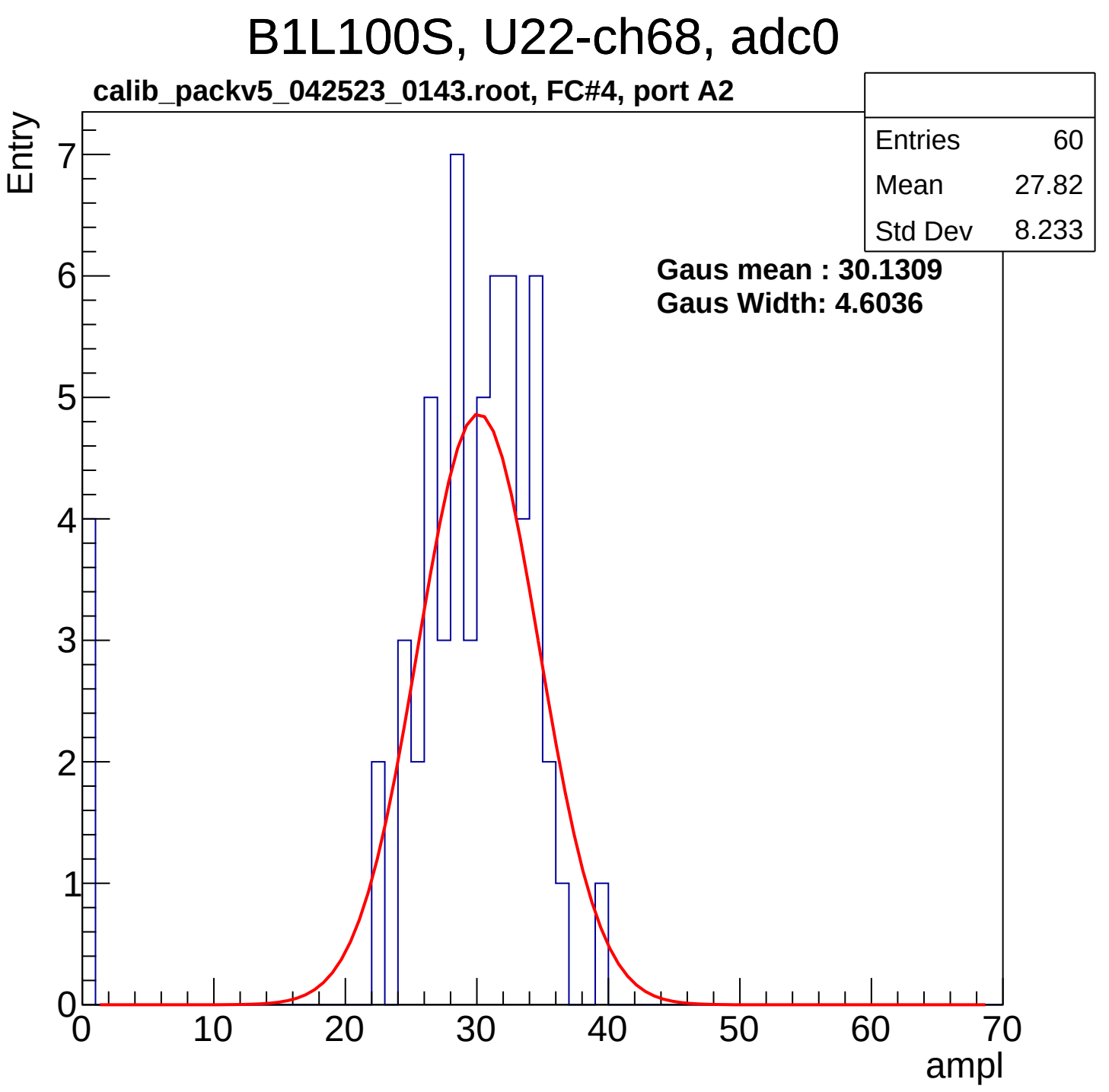
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch68, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 27.82
Std Dev 8.233
Gaus mean : 30.1309
Gaus Width: 4.6036
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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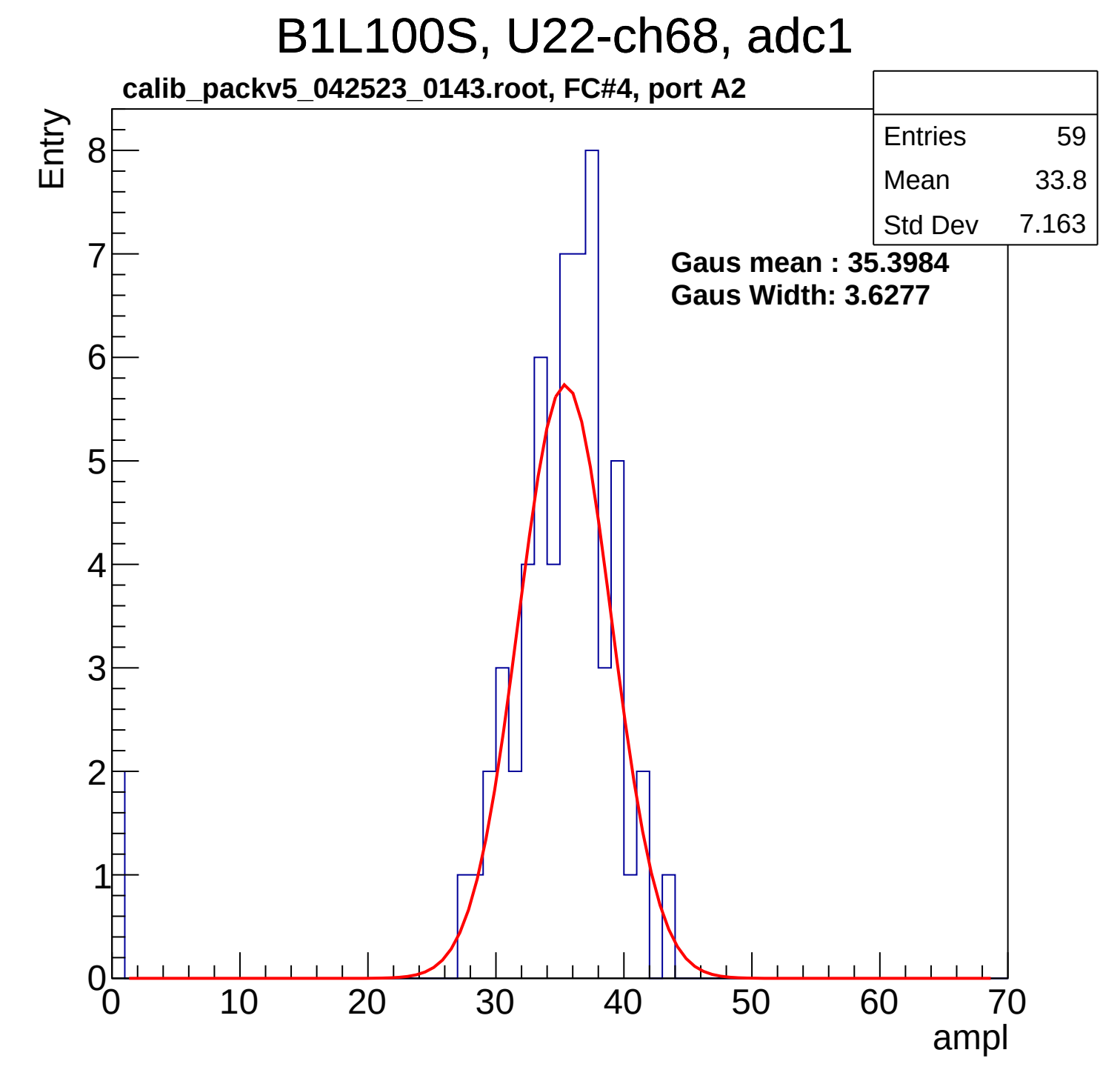
B1L100S, U22-ch68, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 33.8
Std Dev 7.163
Gaus mean : 35.3984
Gaus Width: 3.6277
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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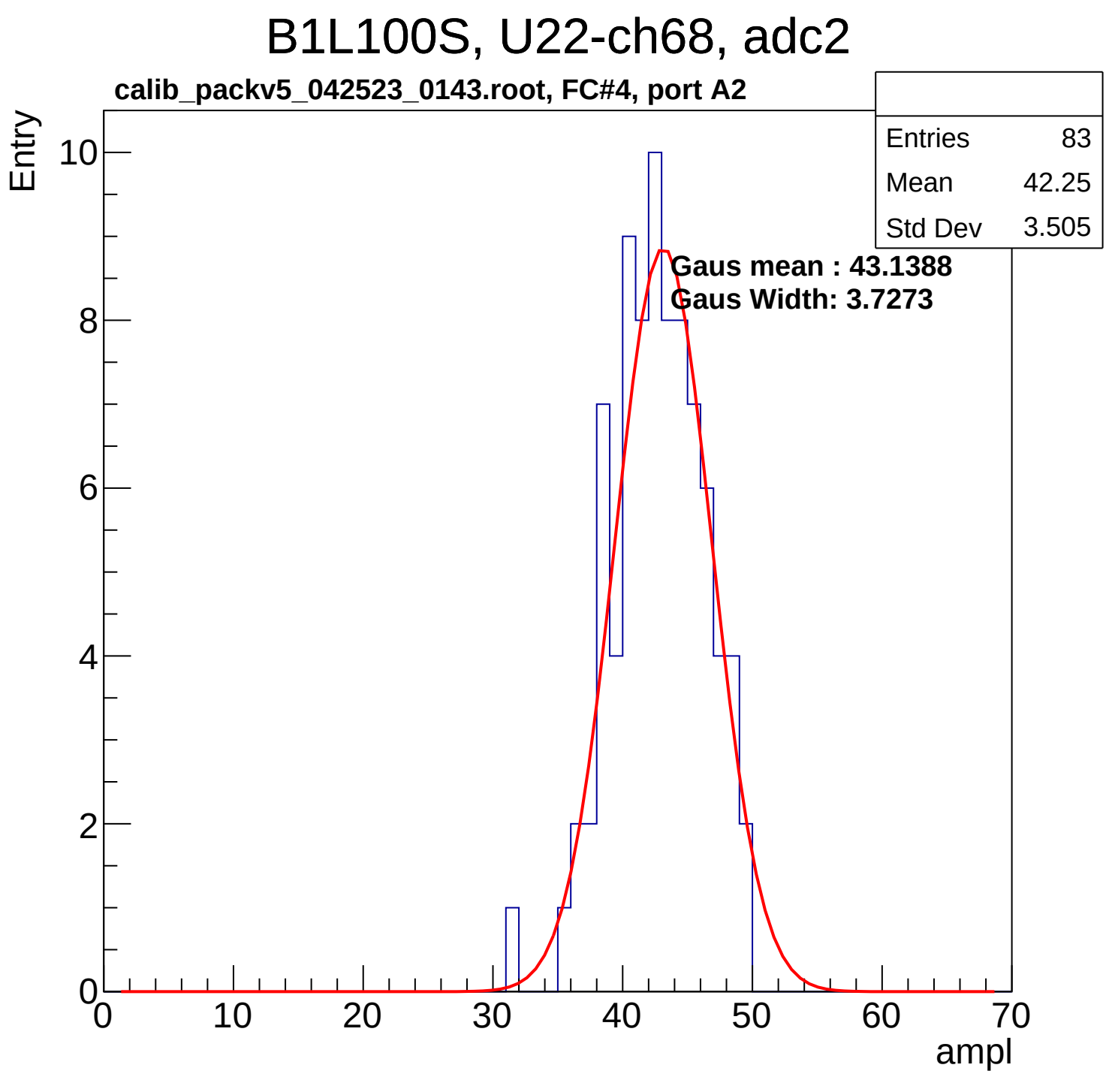
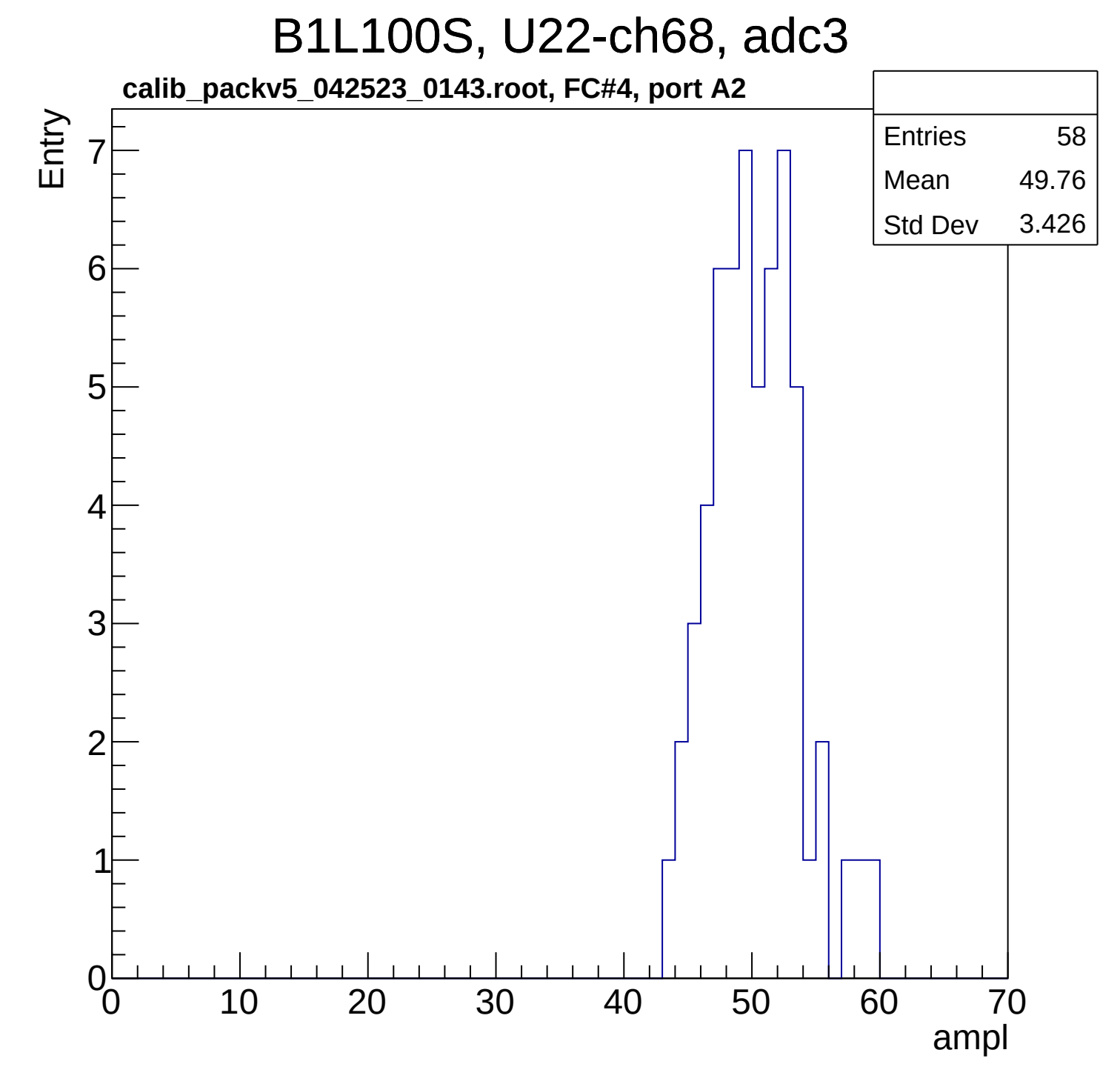

B1L100S, U22-ch68, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 42.25
Std Dev 3.505
Gaus mean : 43.1388
Gaus Width: 3.7273
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch68, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.76
Std Dev 3.426
Entry
ampl
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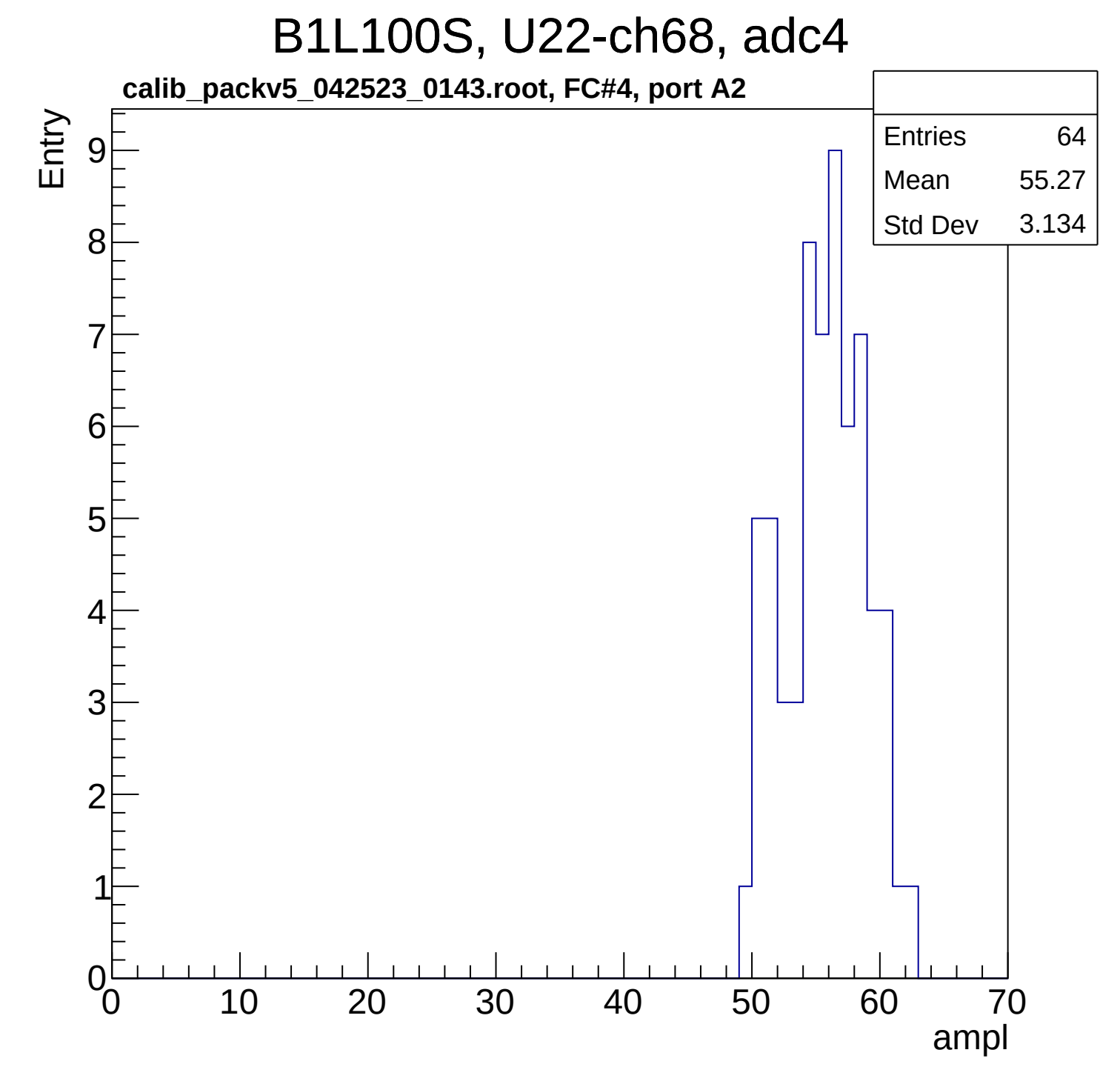
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch68, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.27
Std Dev 3.134
Entry
ampl
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2
3
4
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6
7
8
9
0
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60
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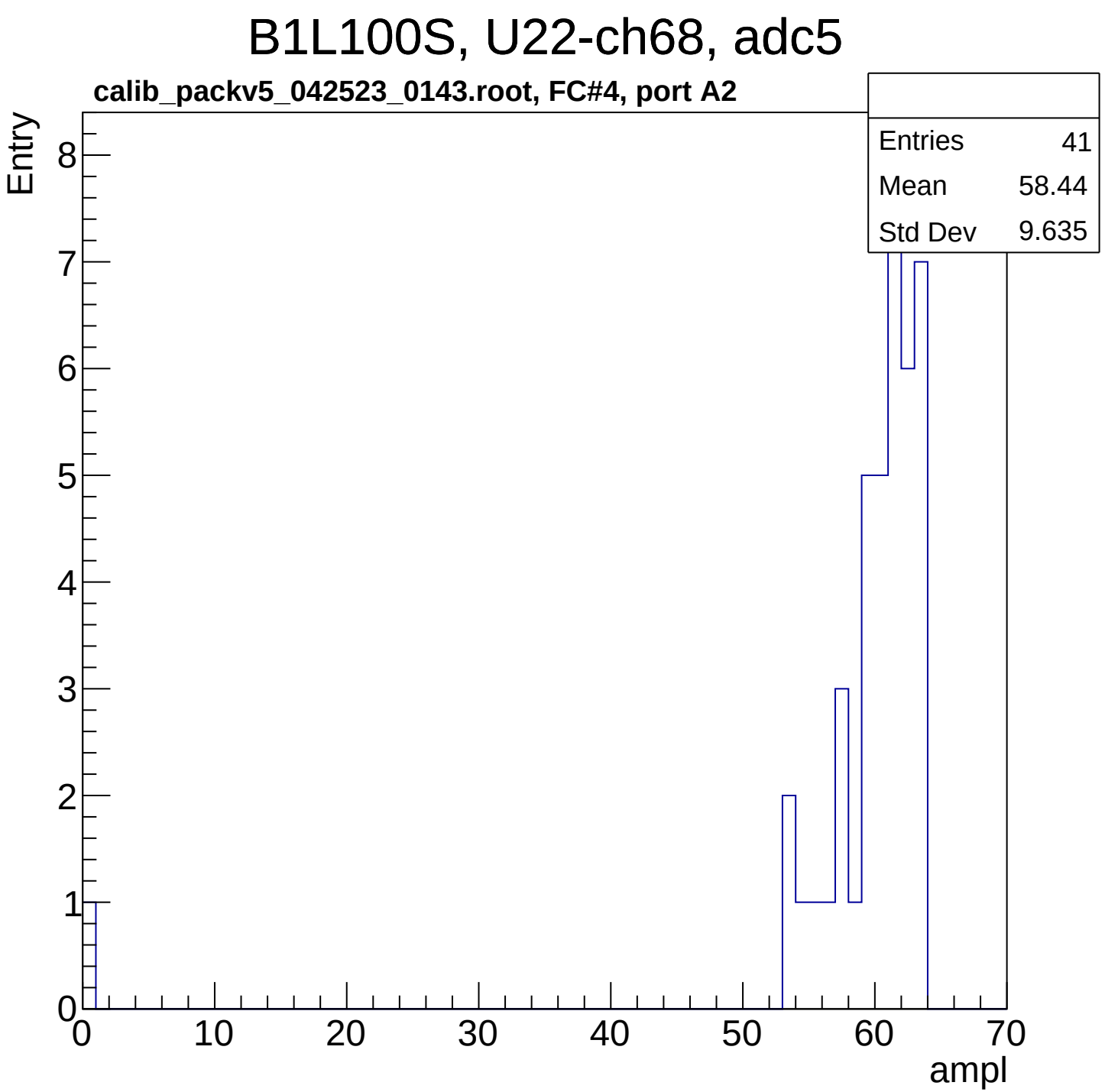
B1L100S, U22-ch68, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 58.44
Std Dev 9.635
Entry
ampl
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4
5
6
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0
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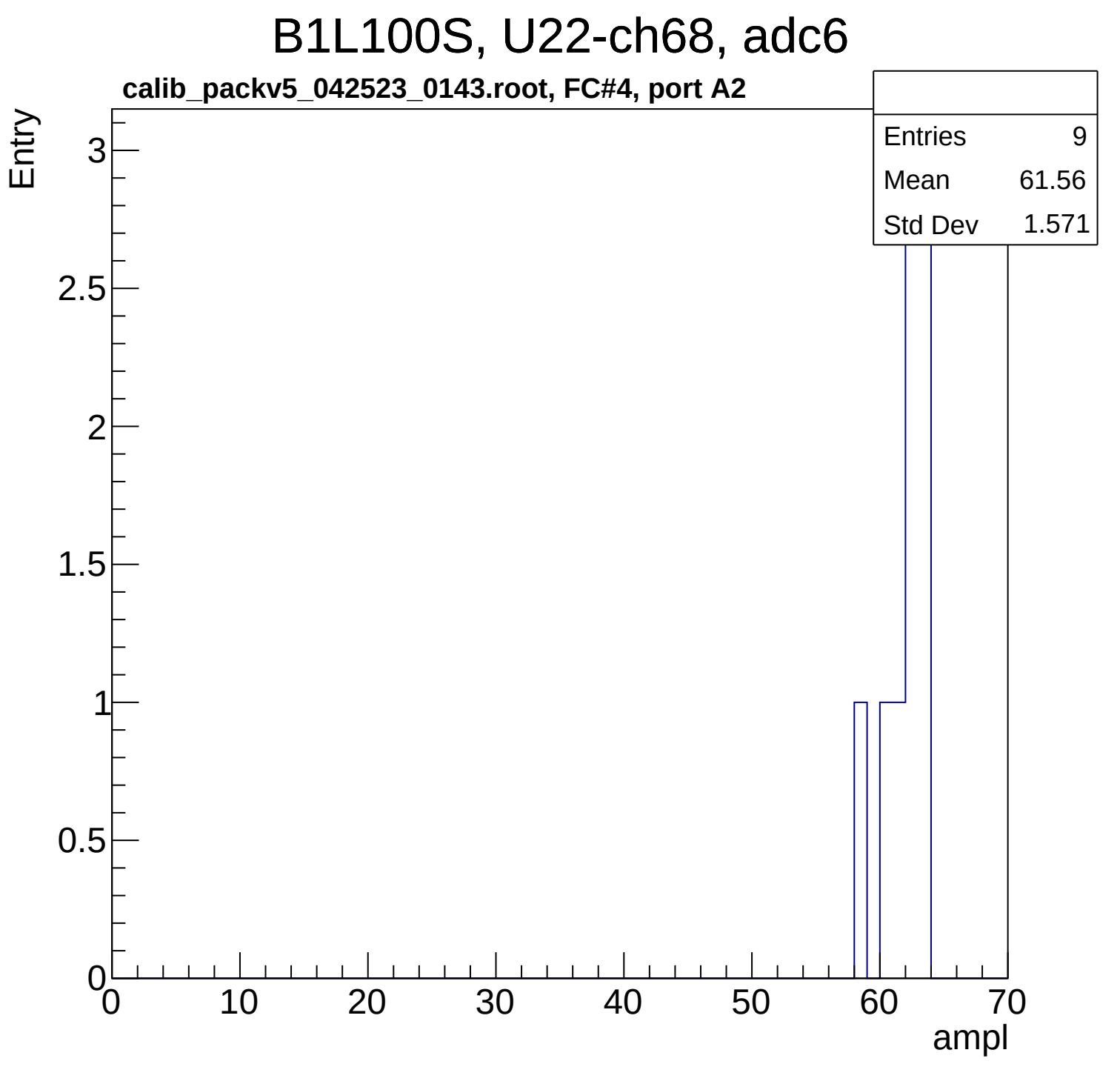
B1L100S, U22-ch68, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.56
Std Dev 1.571
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
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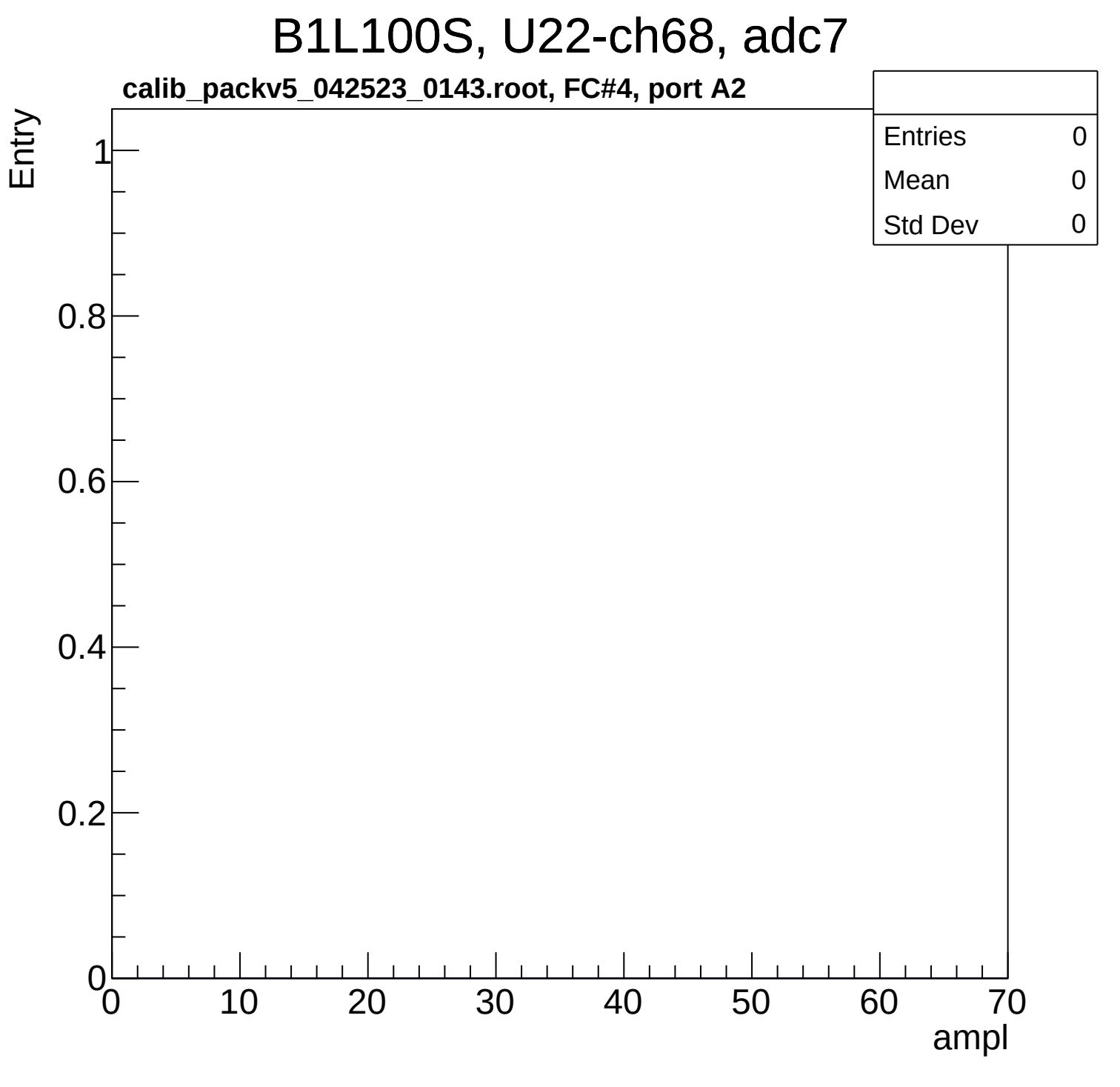
B1L100S, U22-ch68, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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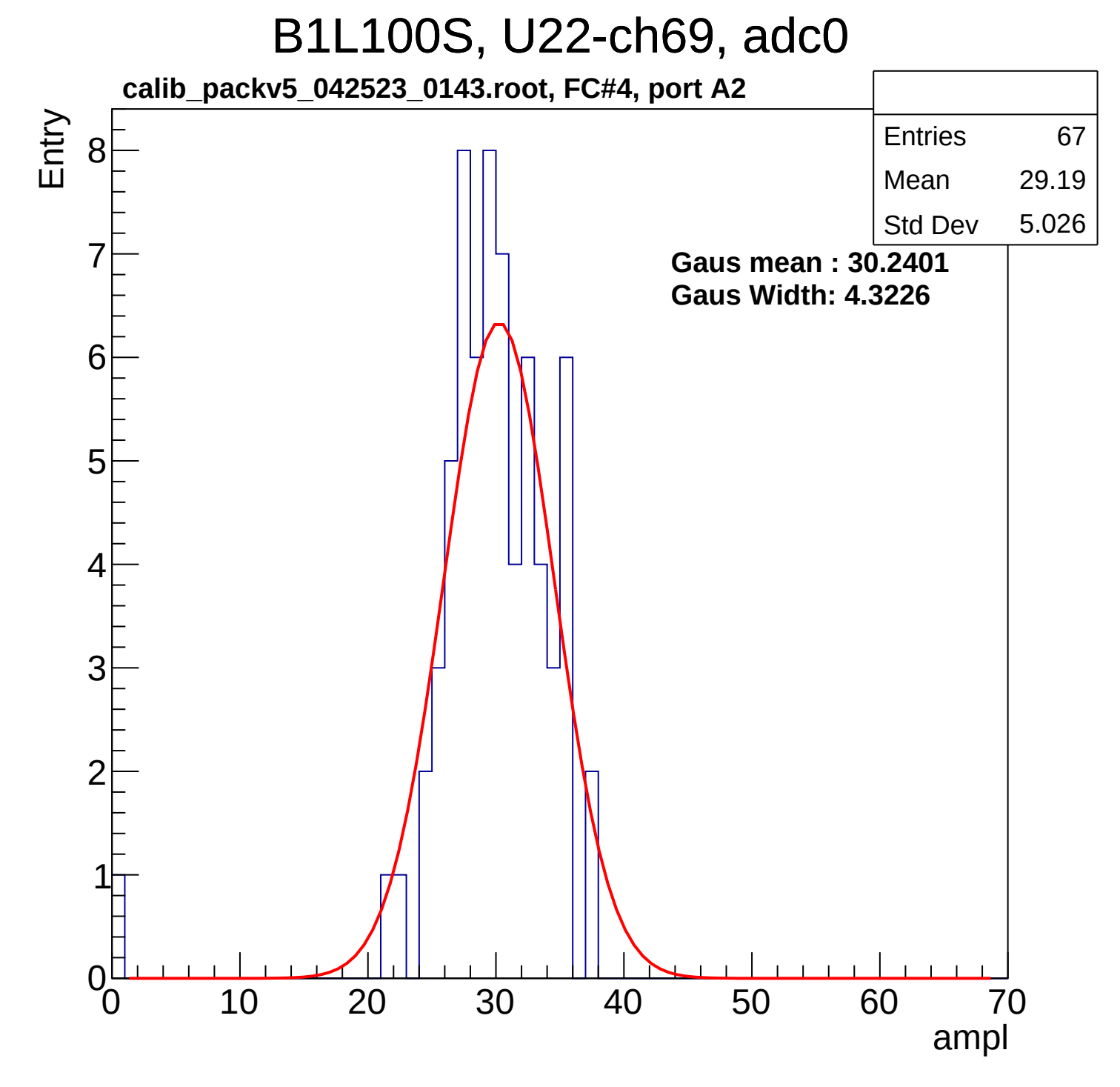
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch69, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.19
Std Dev 5.026
Gaus mean : 30.2401
Gaus Width: 4.3226
Entry
ampl
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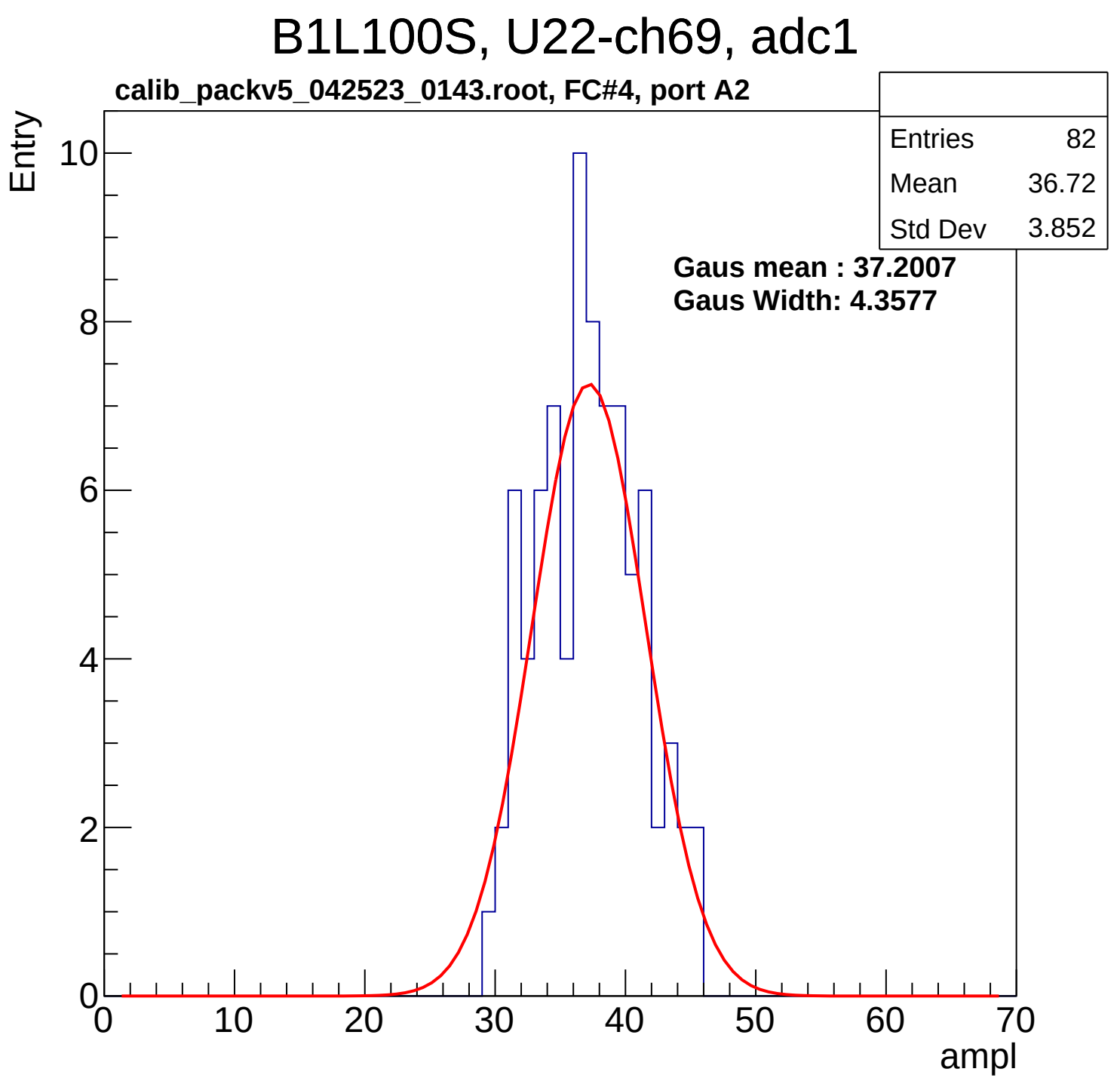
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch69, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 36.72
Std Dev 3.852
Gaus mean : 37.2007
Gaus Width: 4.3577
Entry
ampl
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6
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0
10
20
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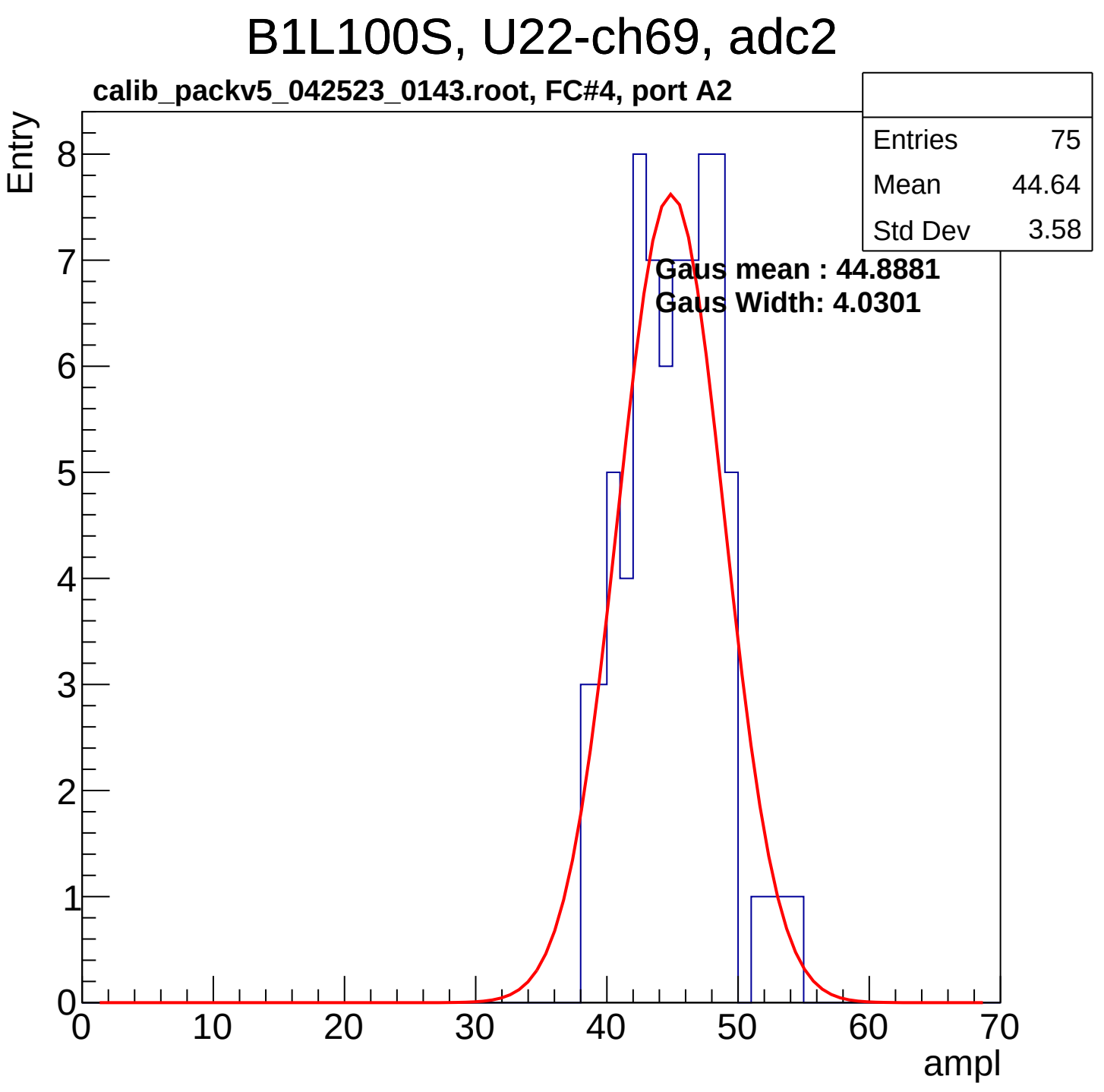
B1L100S, U22-ch69, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.64
Std Dev 3.58
Gaus mean : 44.8881
Gaus Width: 4.0301
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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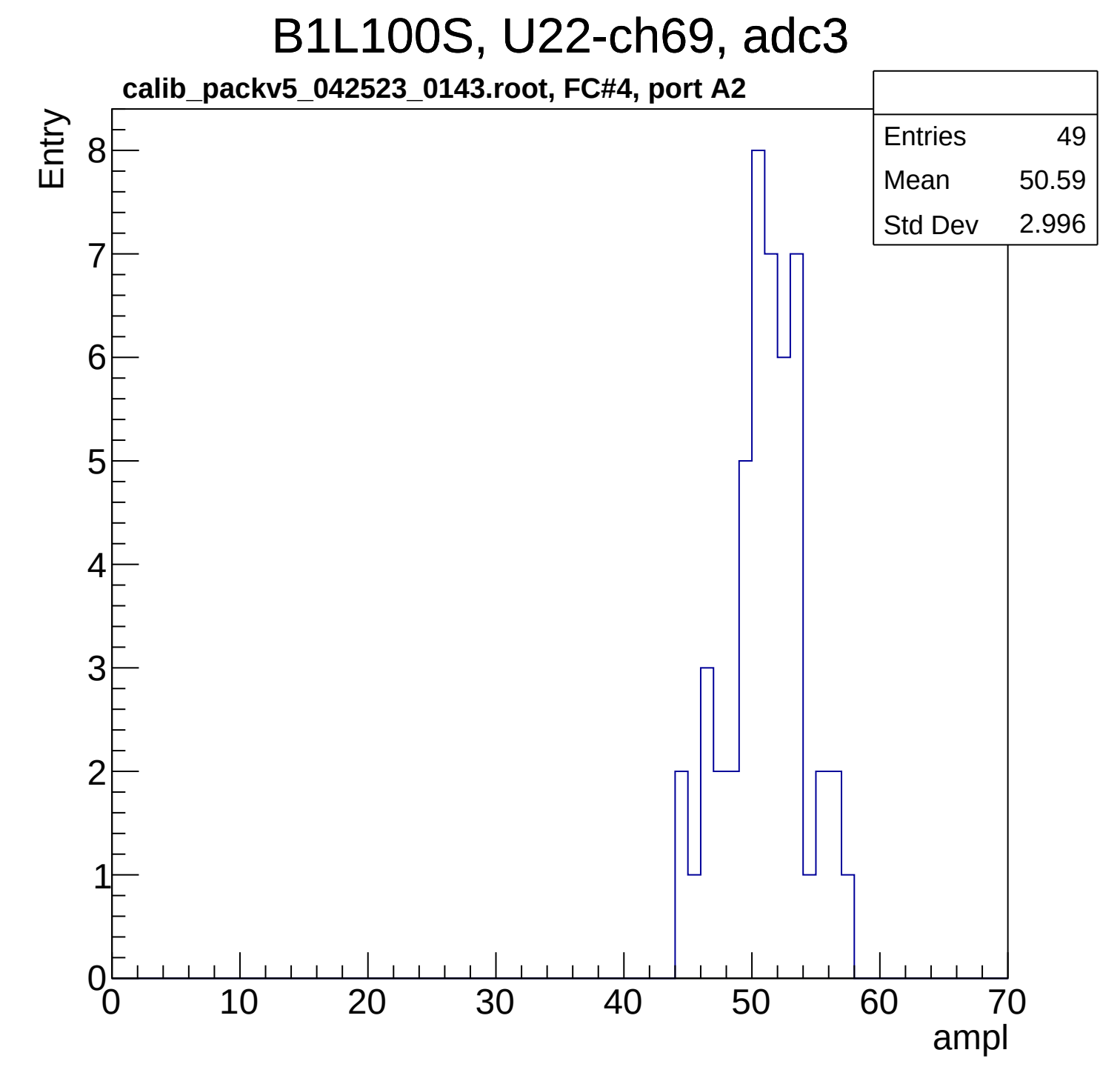
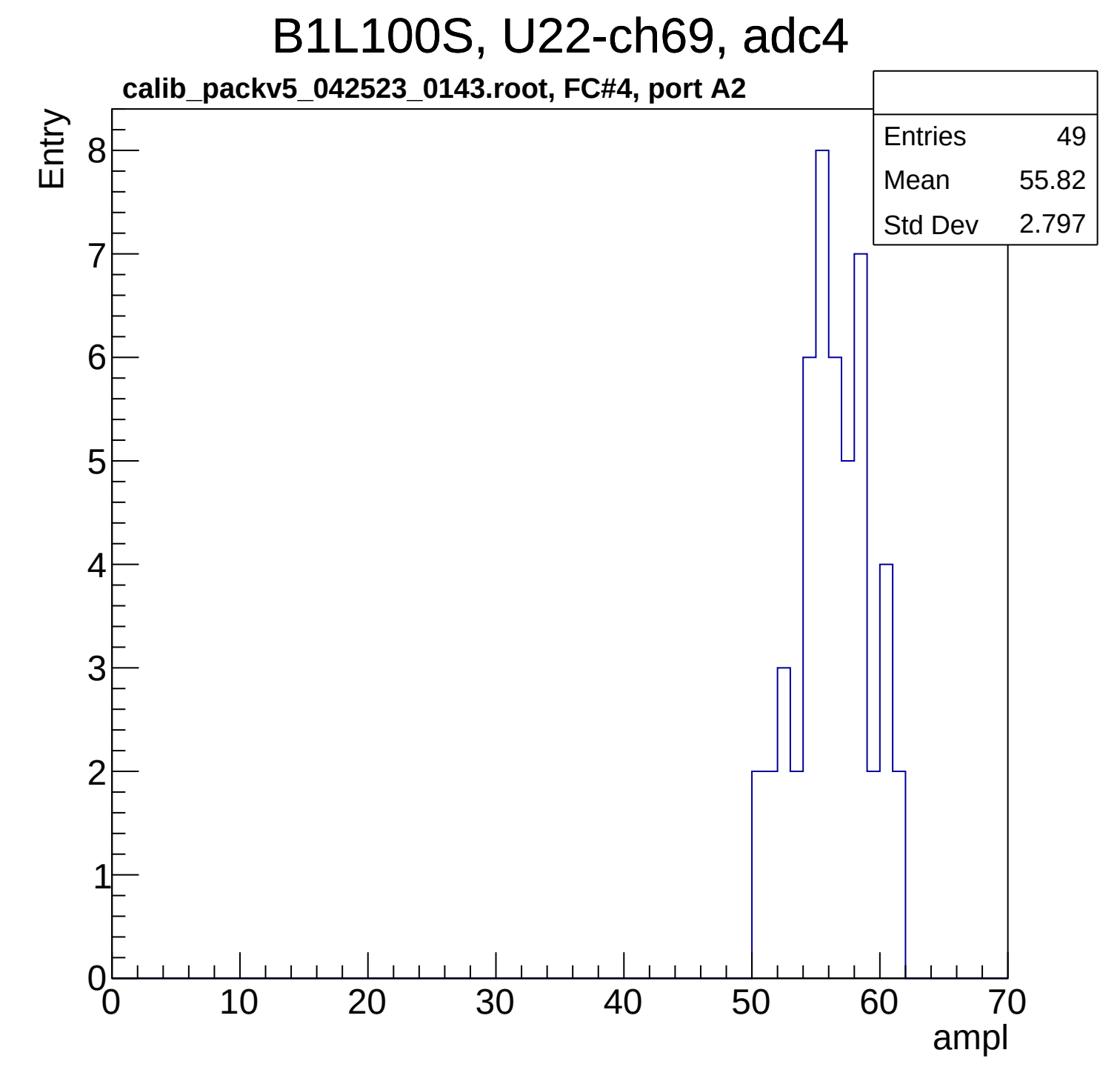
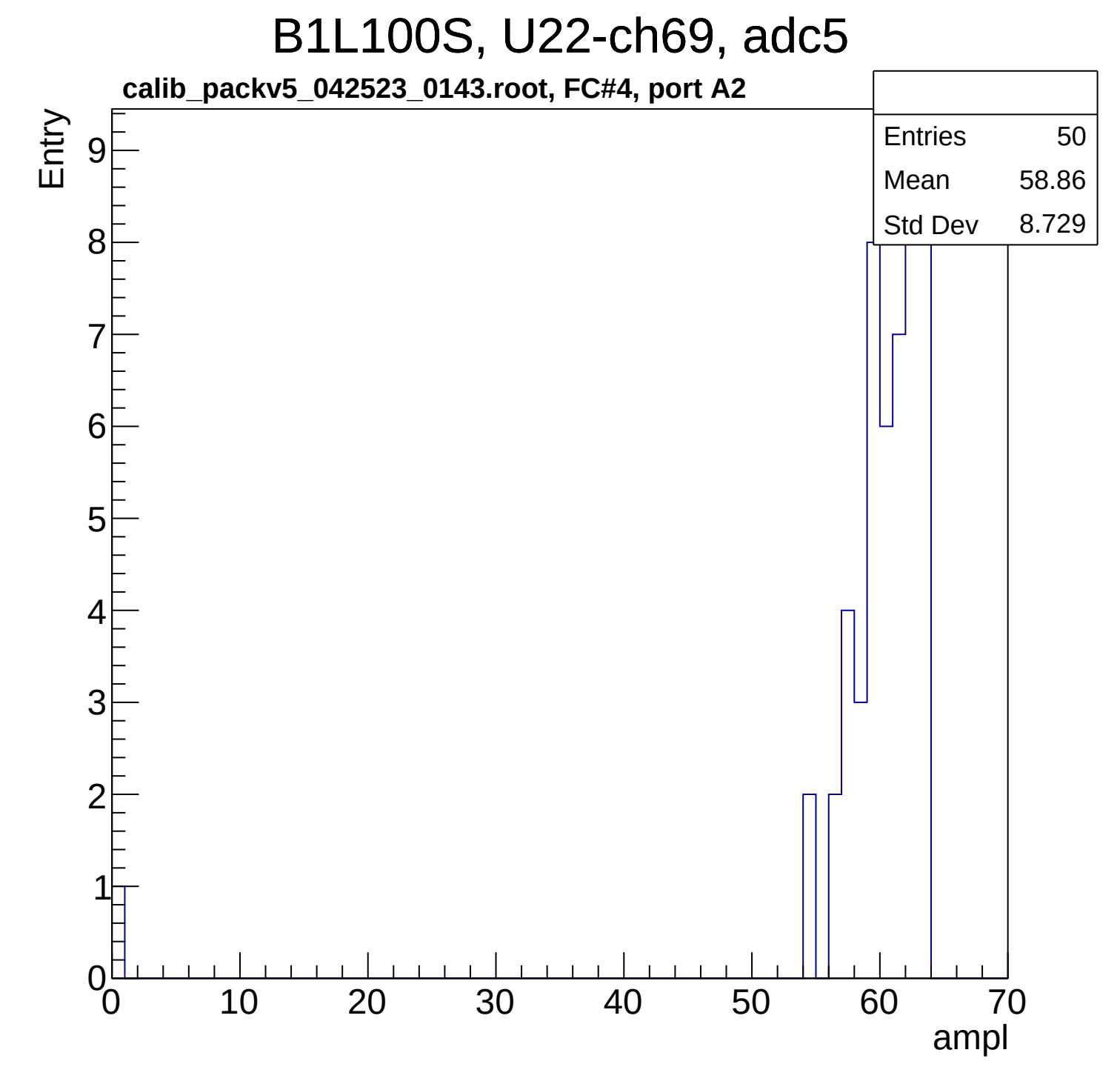
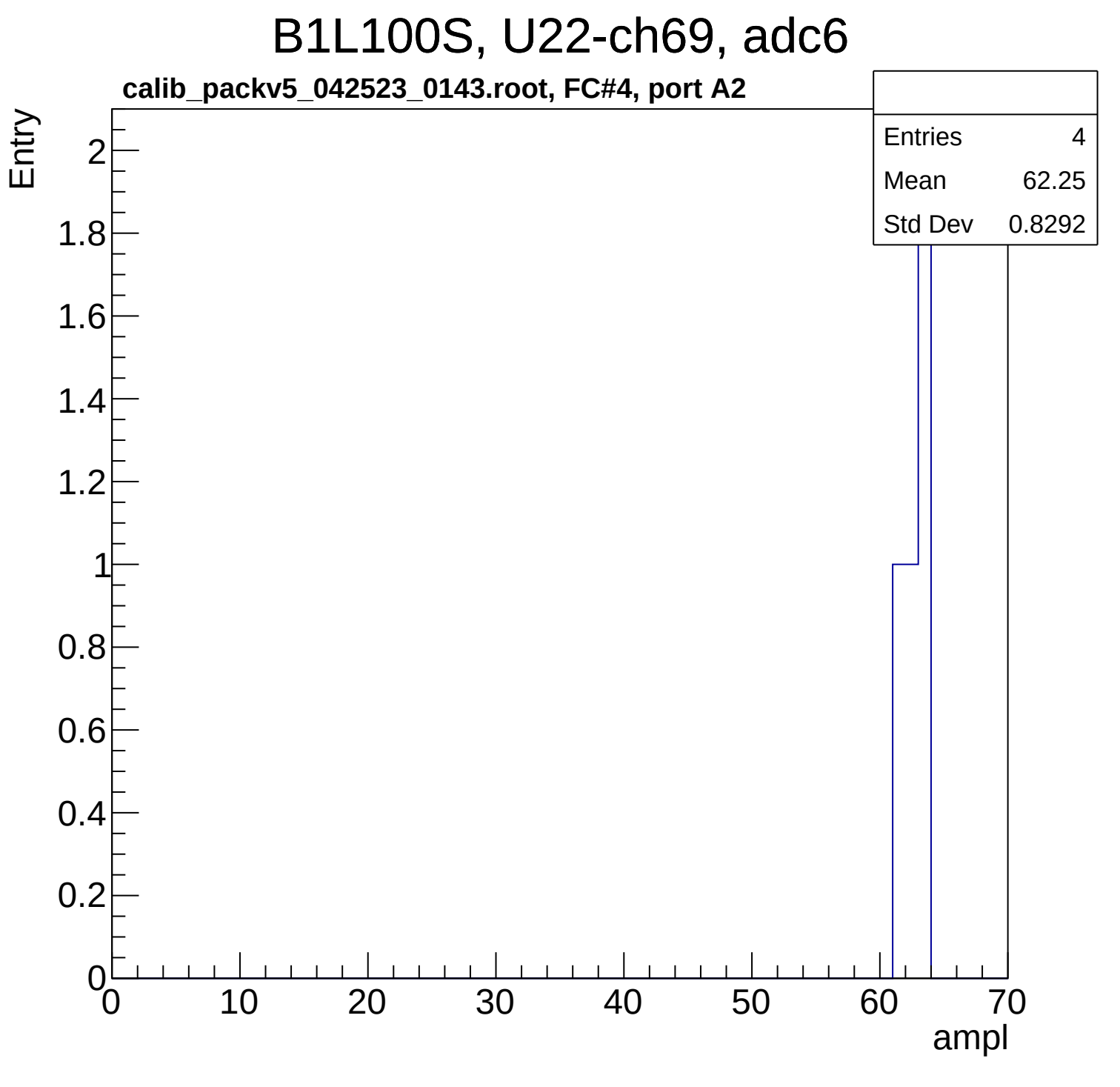
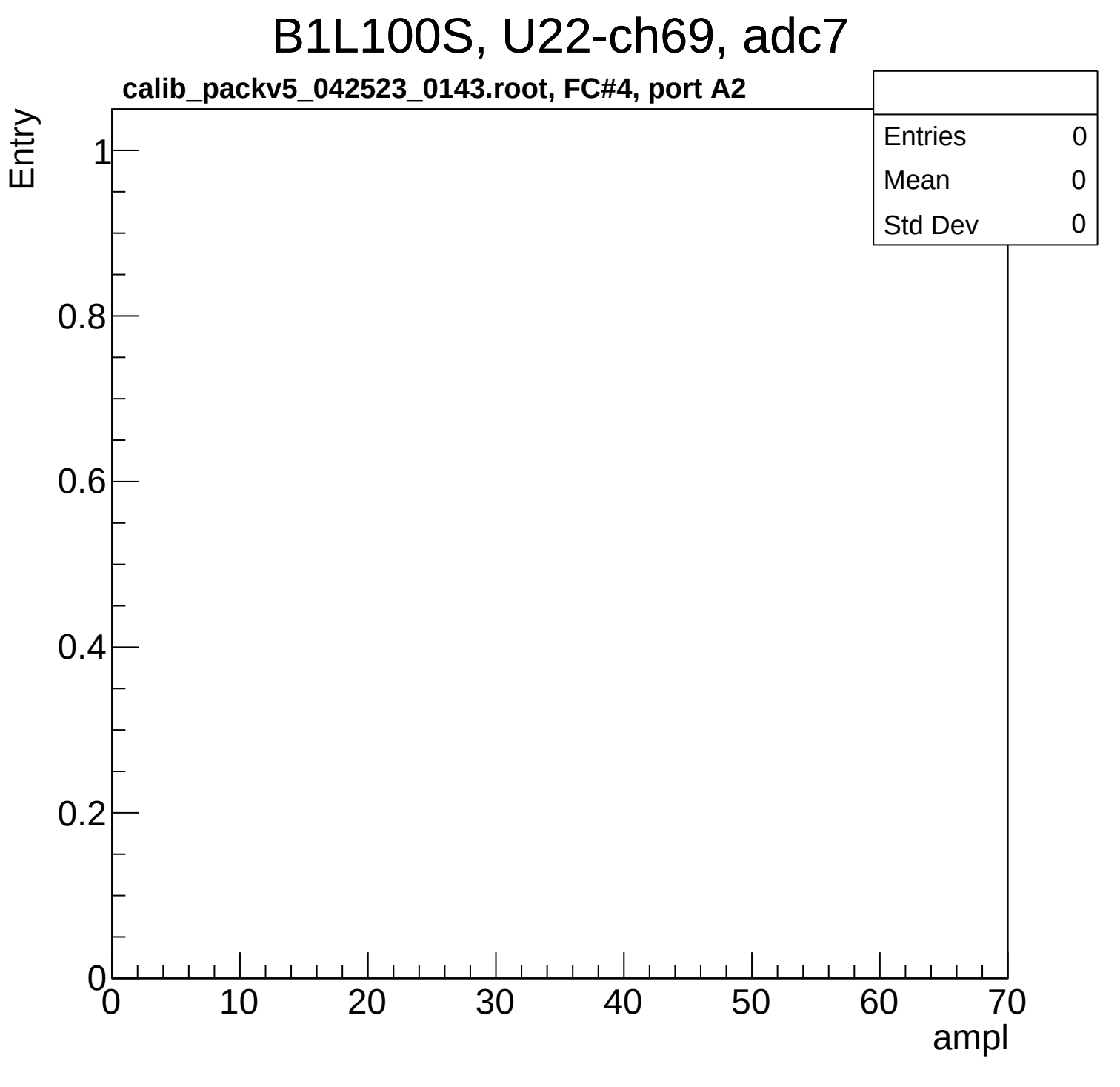
40
50
60
70

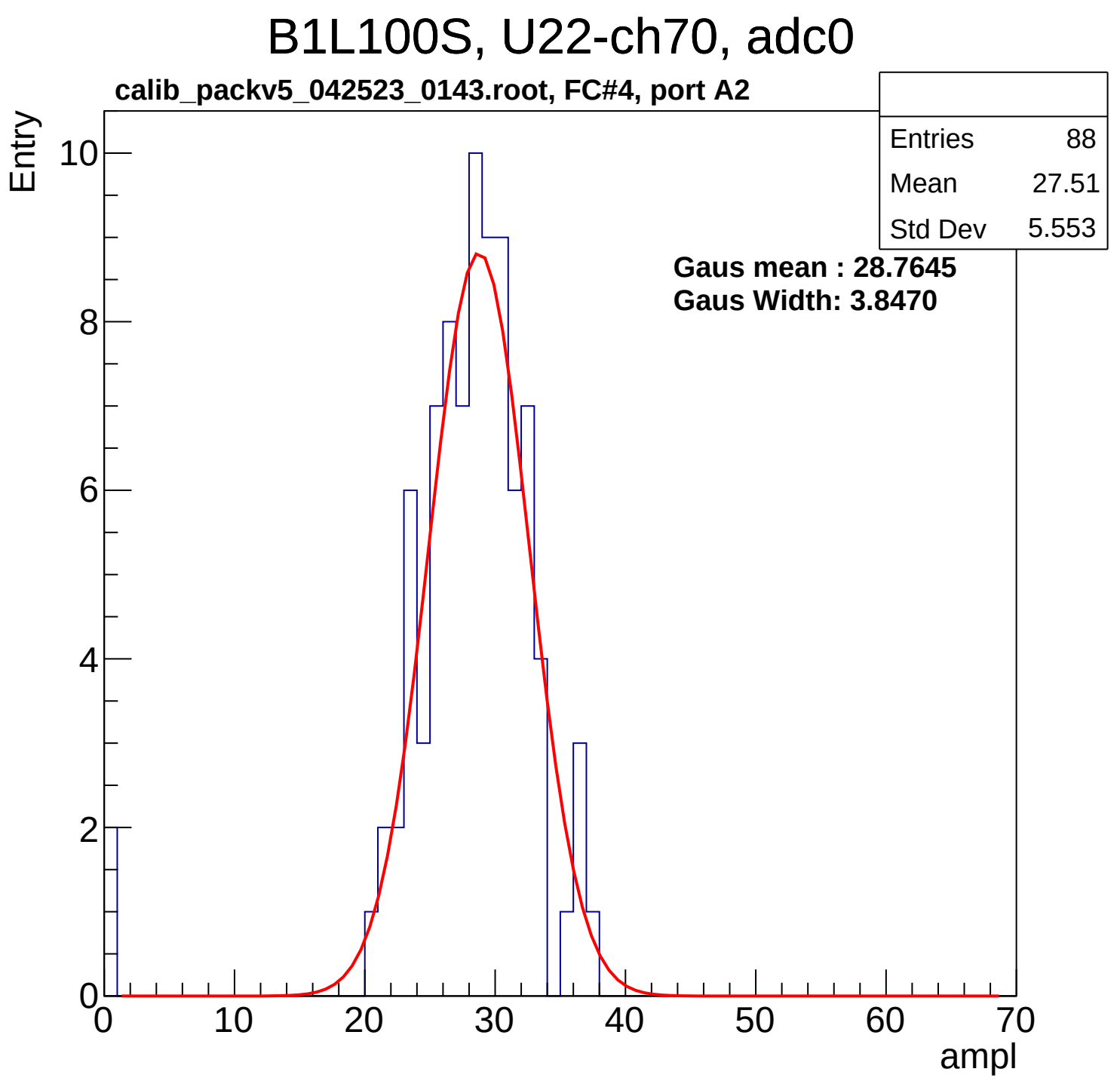

B1L100S, U22-ch69, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 50.59
Std Dev 2.996
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

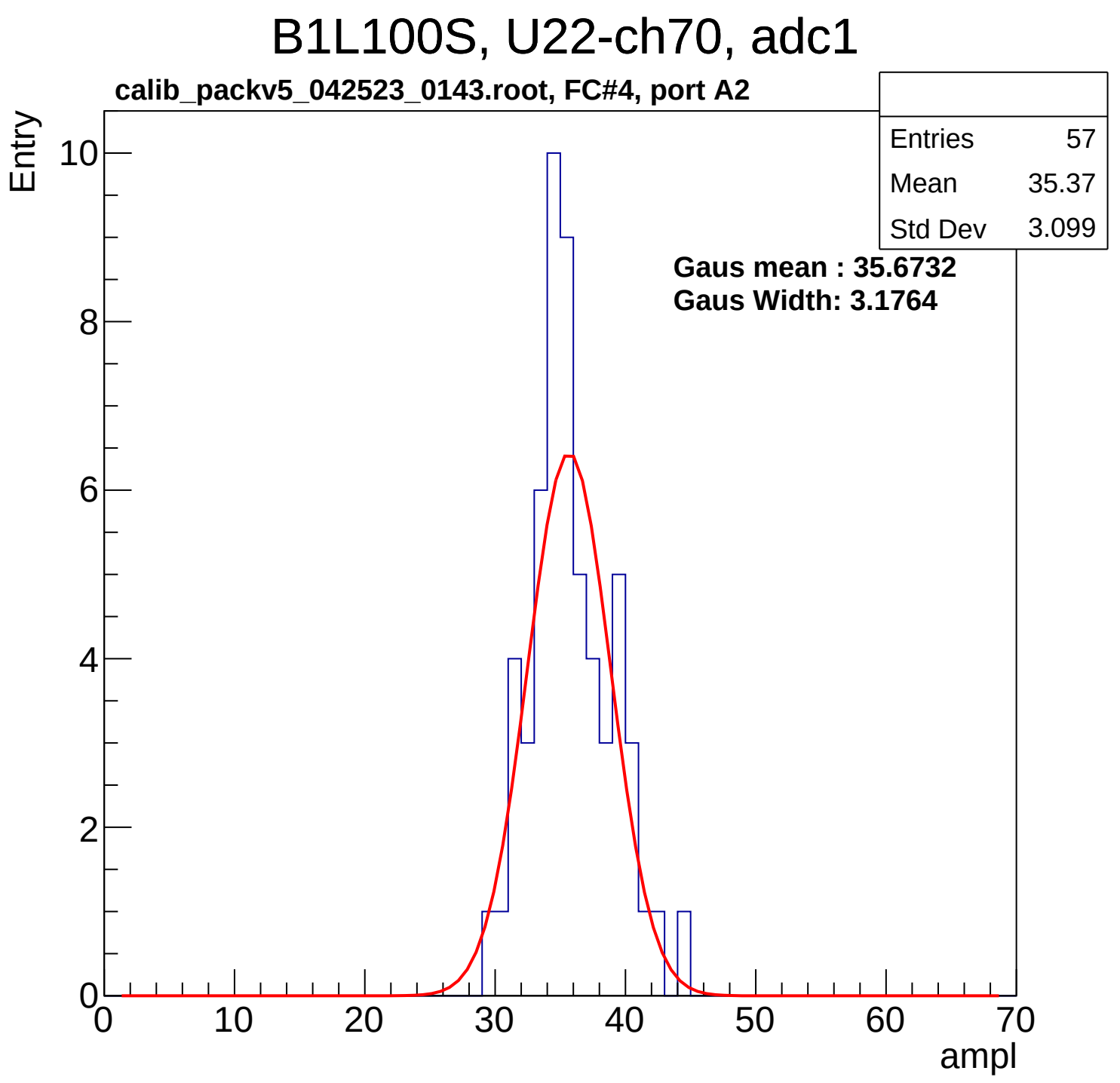

B1L100S, U22-ch69, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.82
Std Dev 2.797
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

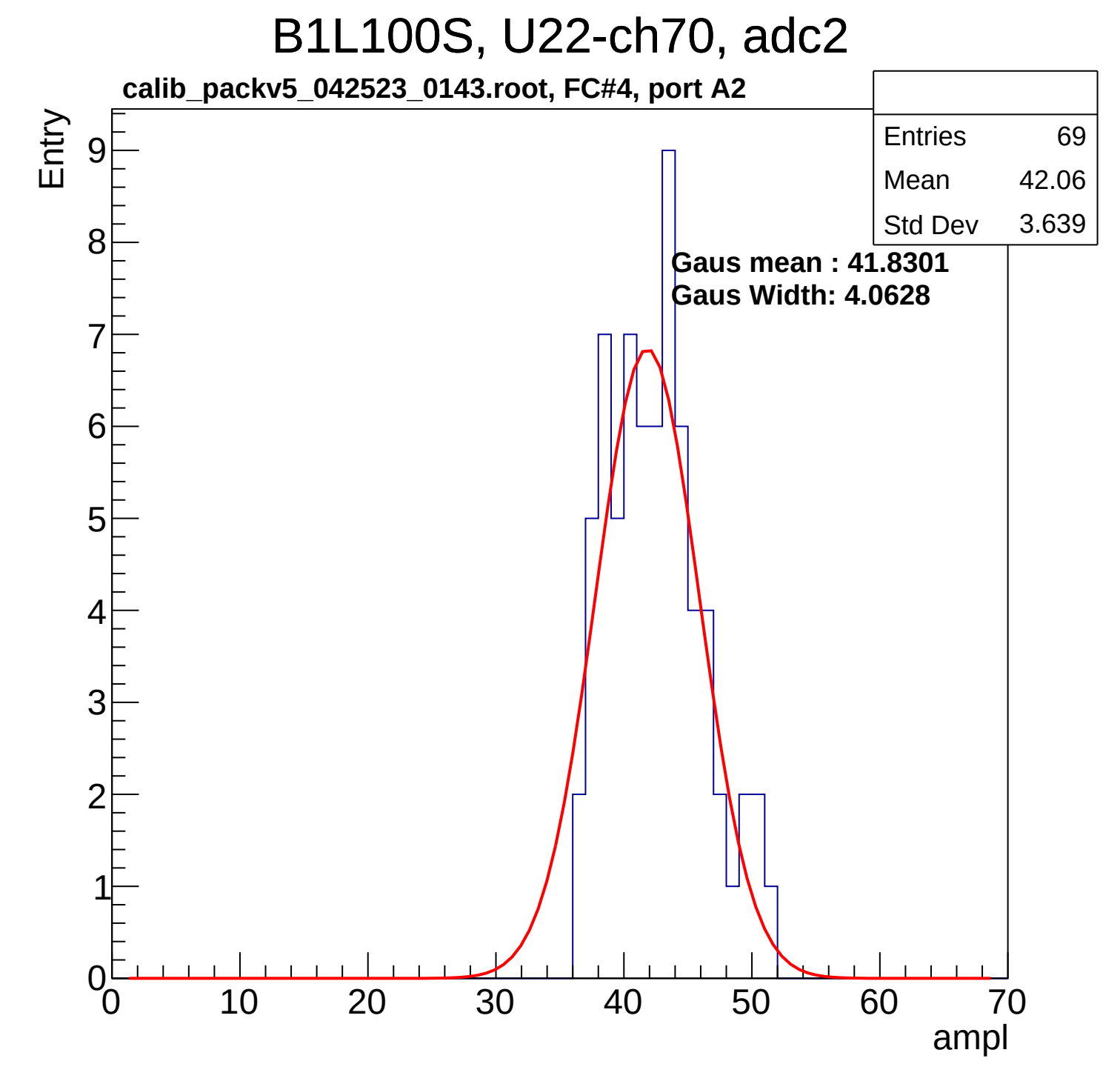

B1L100S, U22-ch69, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 58.86
Std Dev 8.729
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

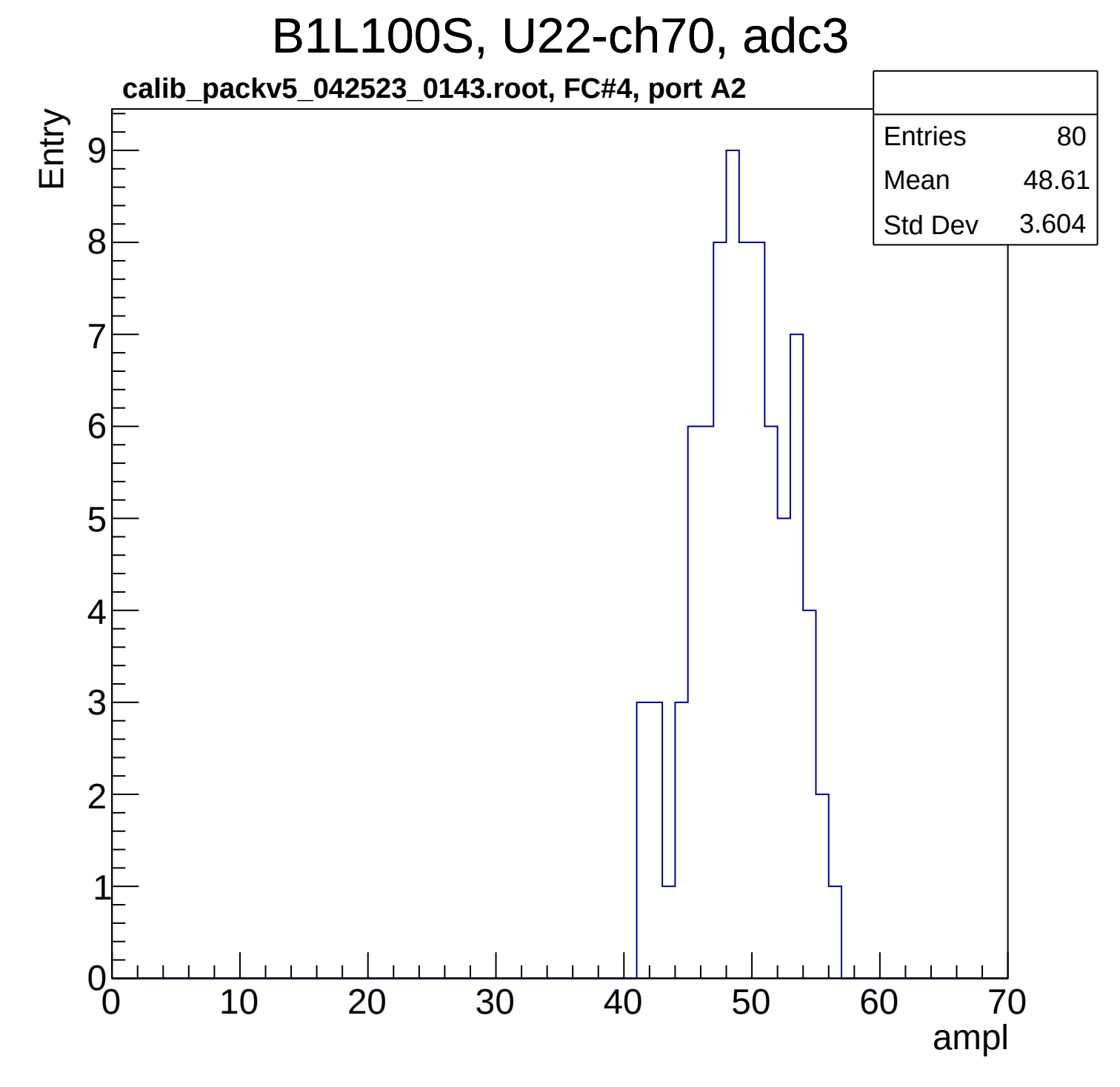

B1L100S, U22-ch69, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 0.8292
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch69, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch70, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 88
Mean 27.51
Std Dev 5.553
Gaus mean : 28.7645
Gaus Width: 3.8470
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch70, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 35.37
Std Dev 3.099
Gaus mean : 35.6732
Gaus Width: 3.1764
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch70, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 42.06
Std Dev 3.639
Gaus mean : 41.8301
Gaus Width: 4.0628
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch70, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 48.61
Std Dev 3.604
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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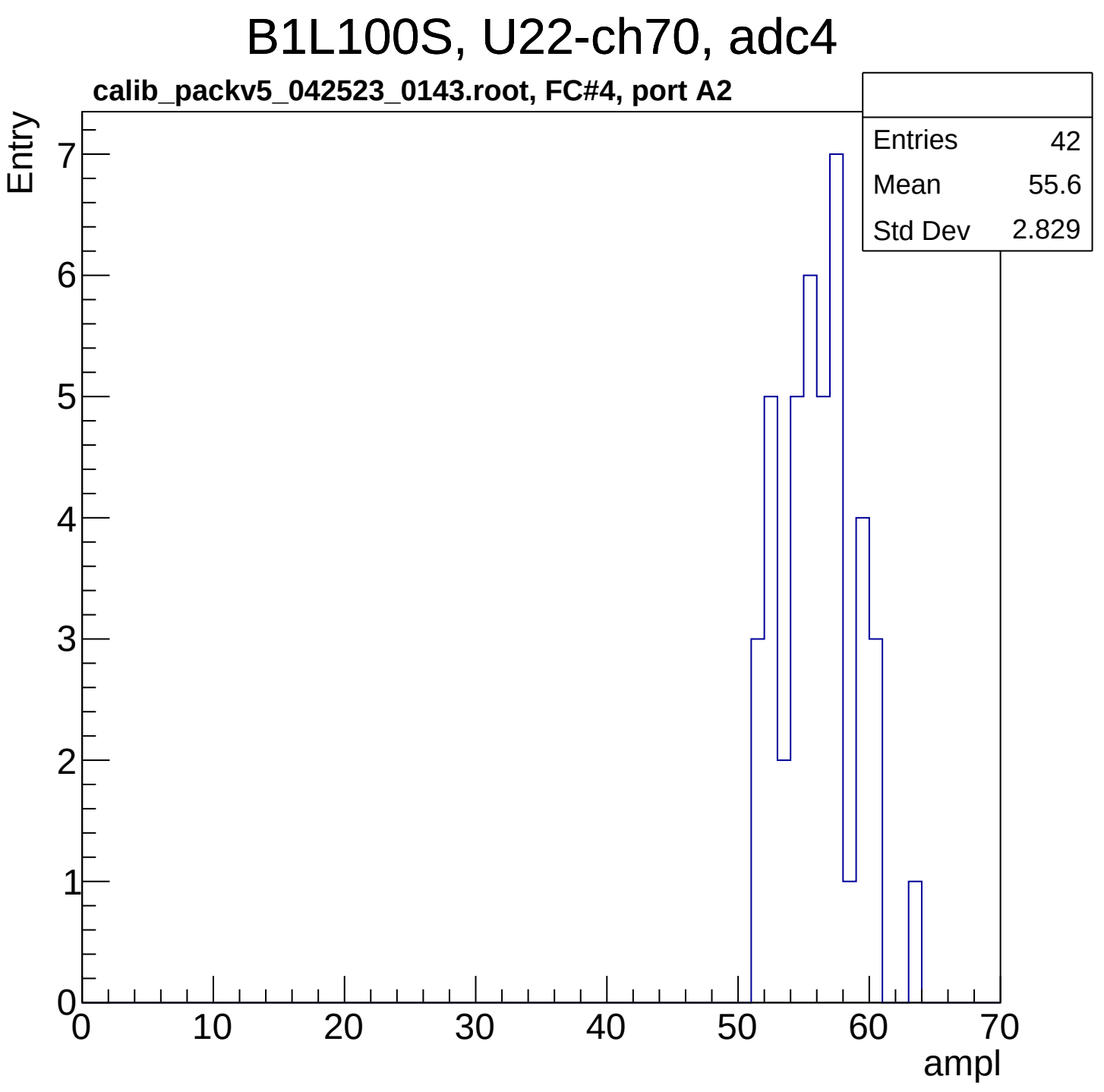
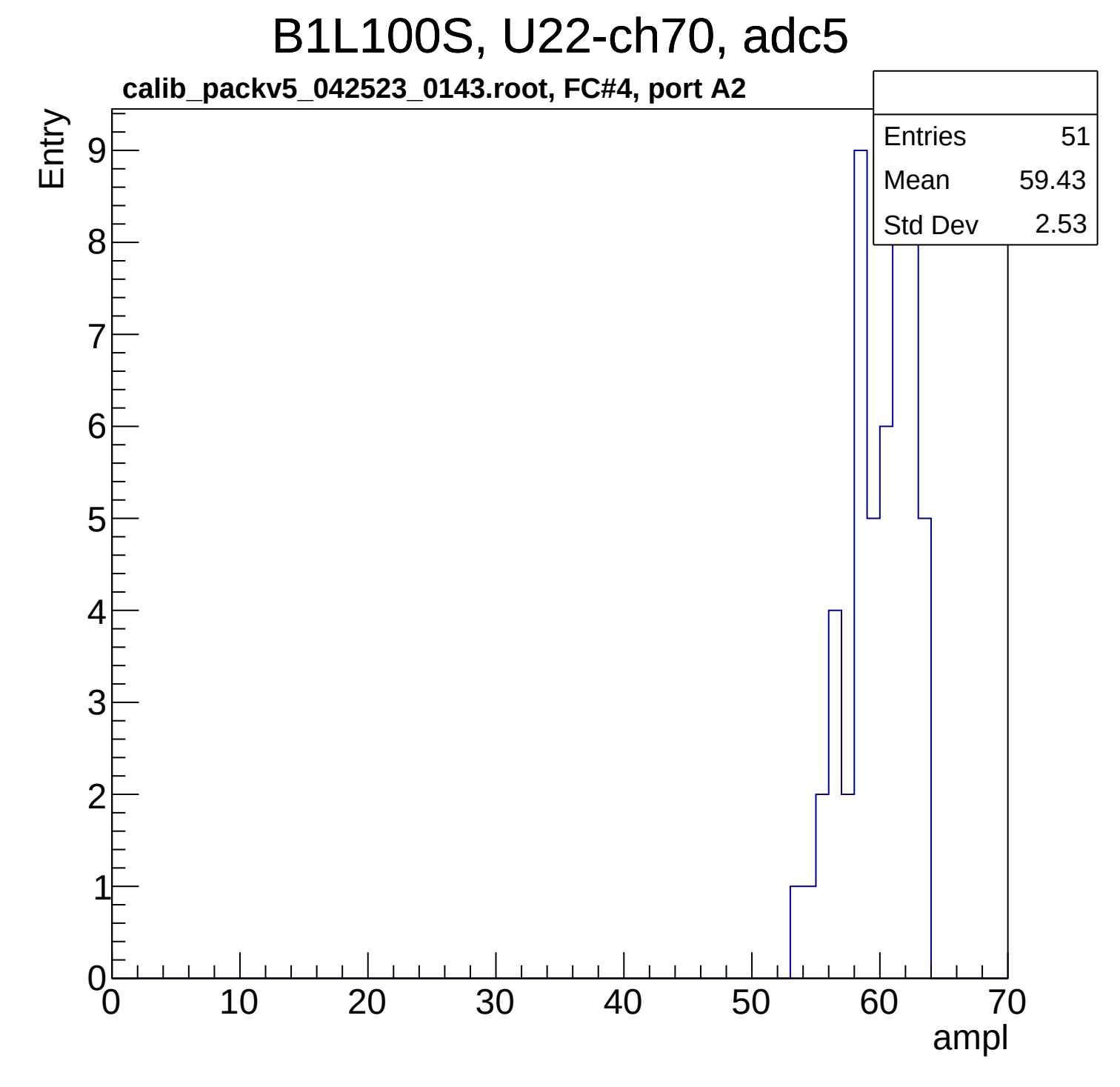
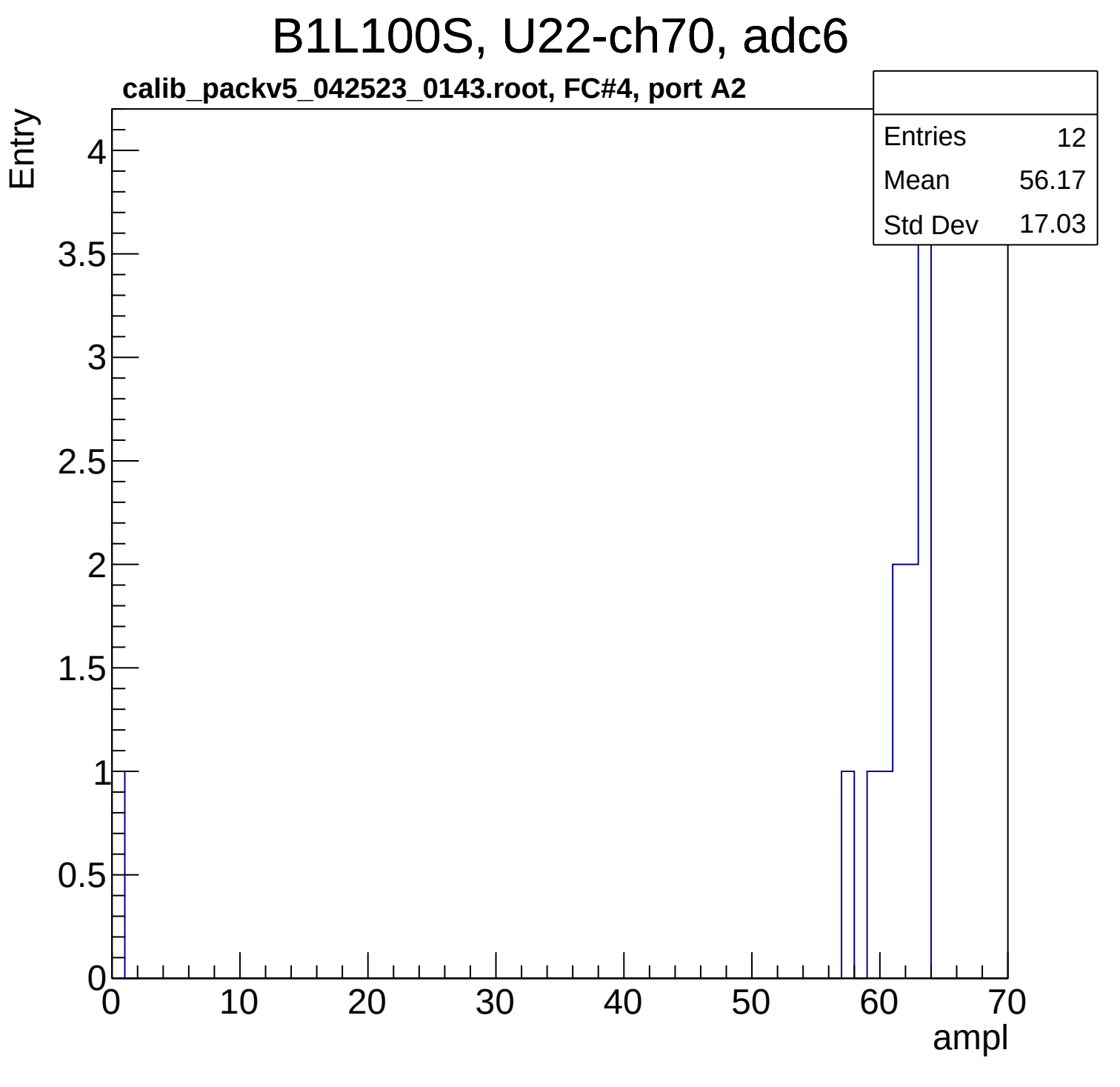
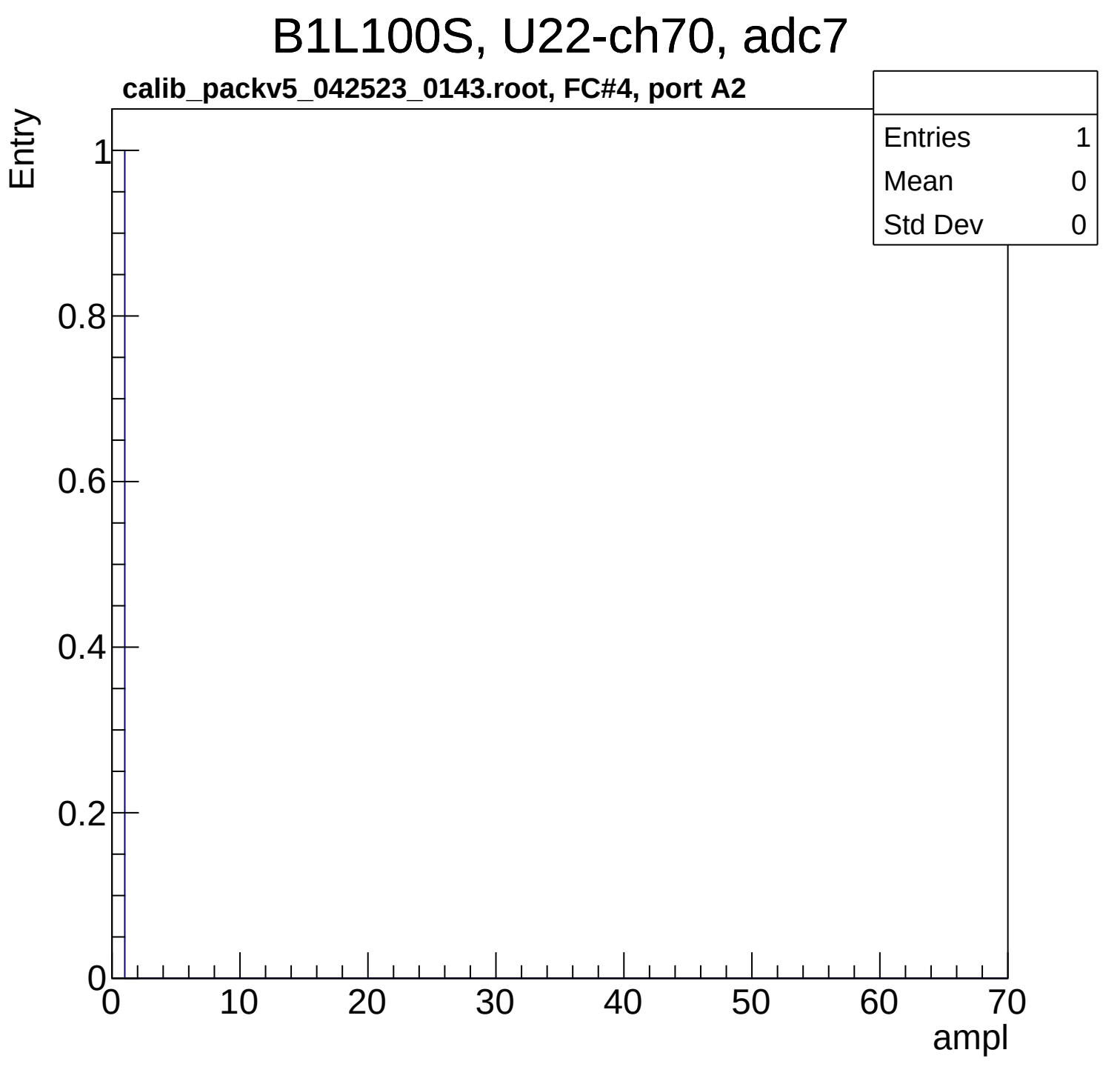
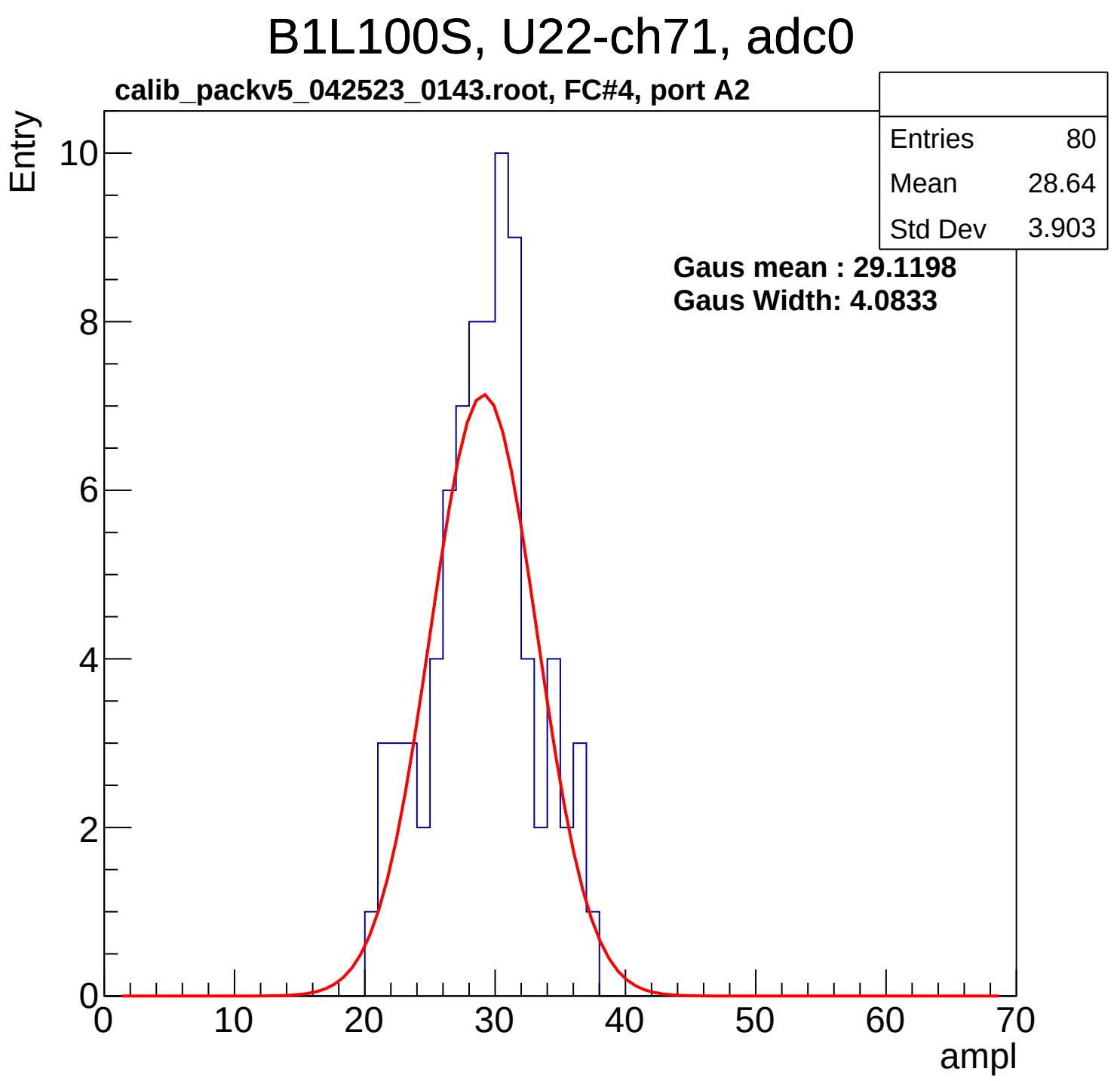
60
70

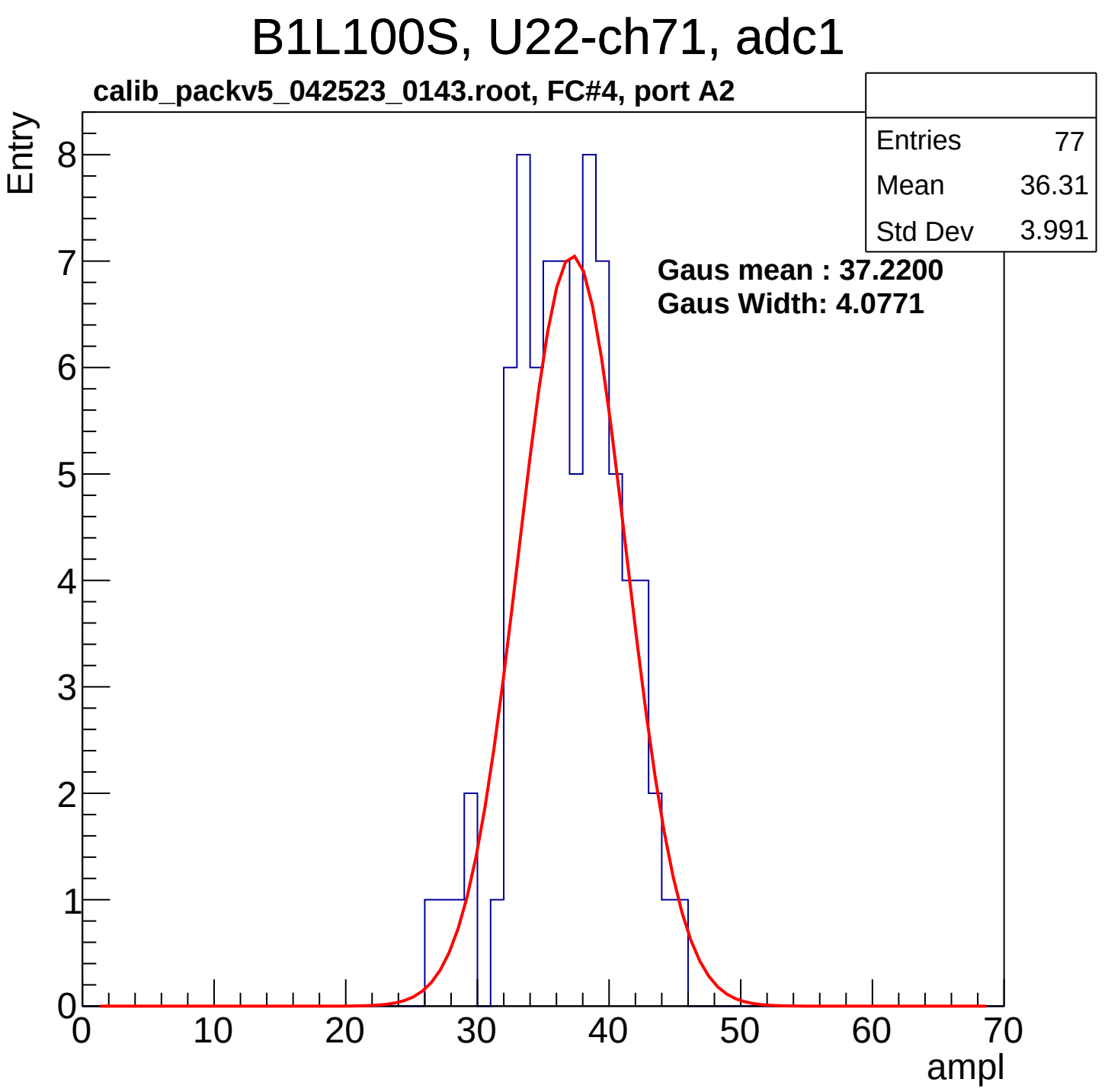

B1L100S, U22-ch70, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 55.6
Std Dev 2.829
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

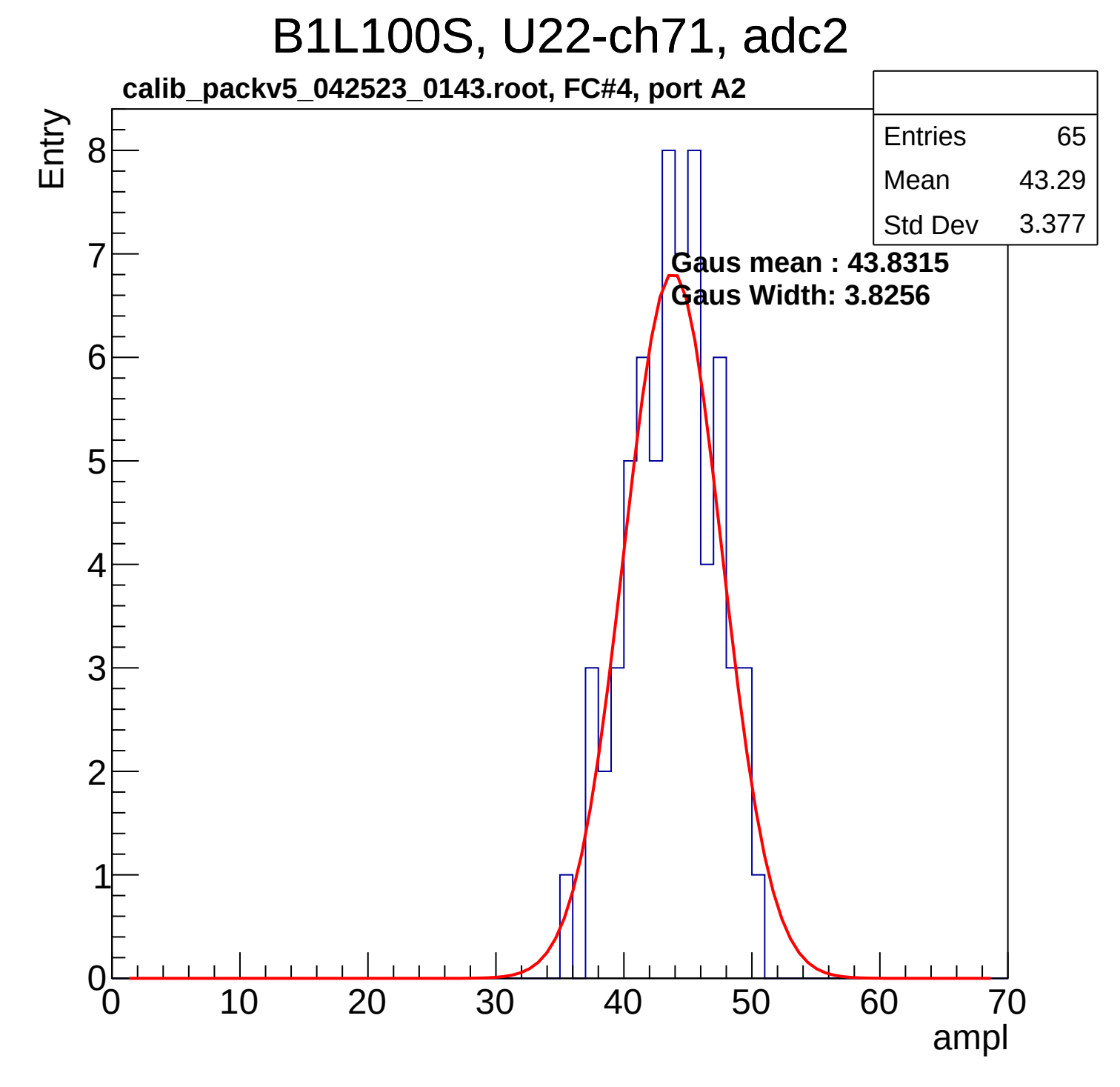

B1L100S, U22-ch70, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 59.43
Std Dev 2.53
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

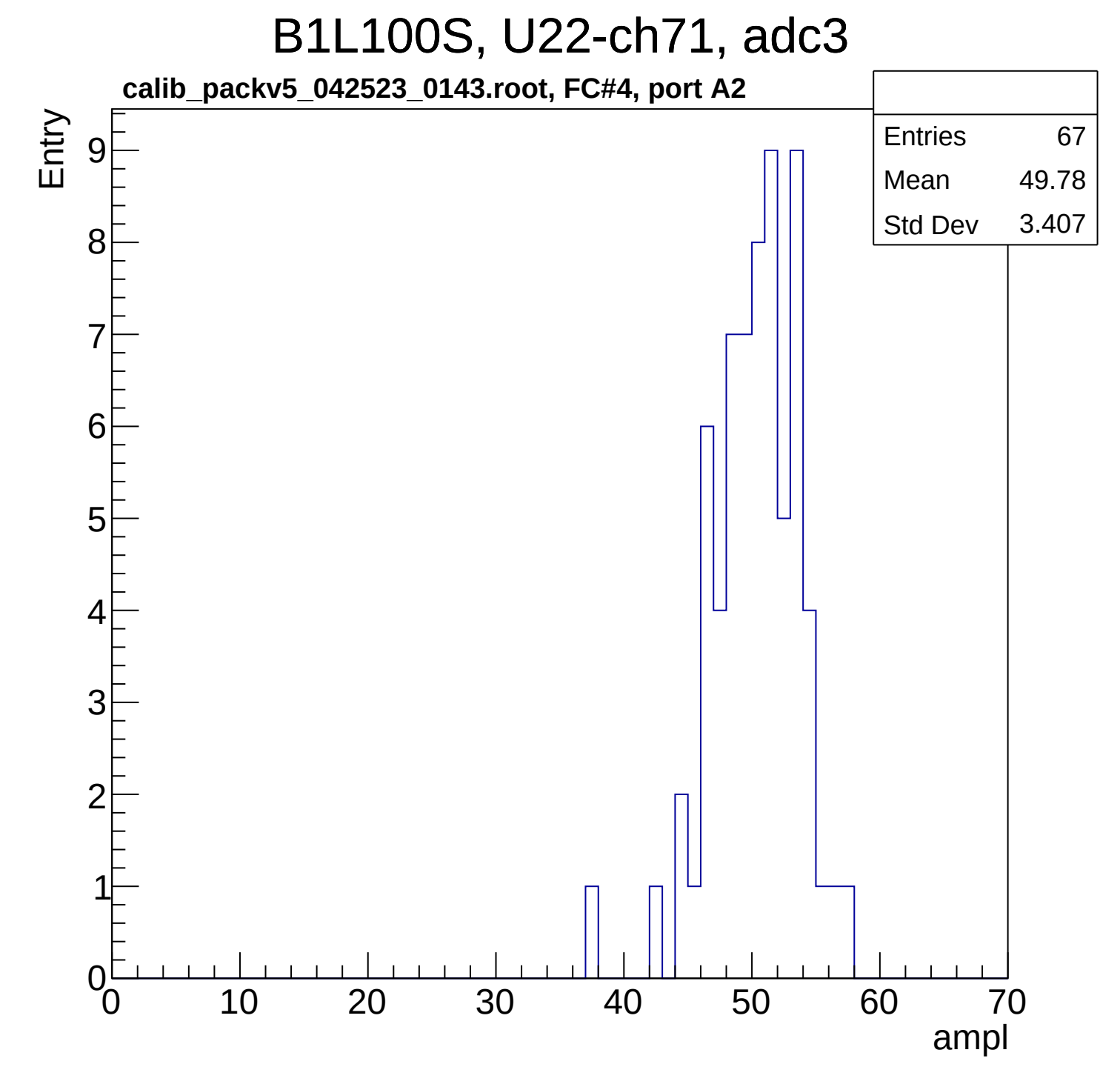

B1L100S, U22-ch70, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.17
Std Dev 17.03
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

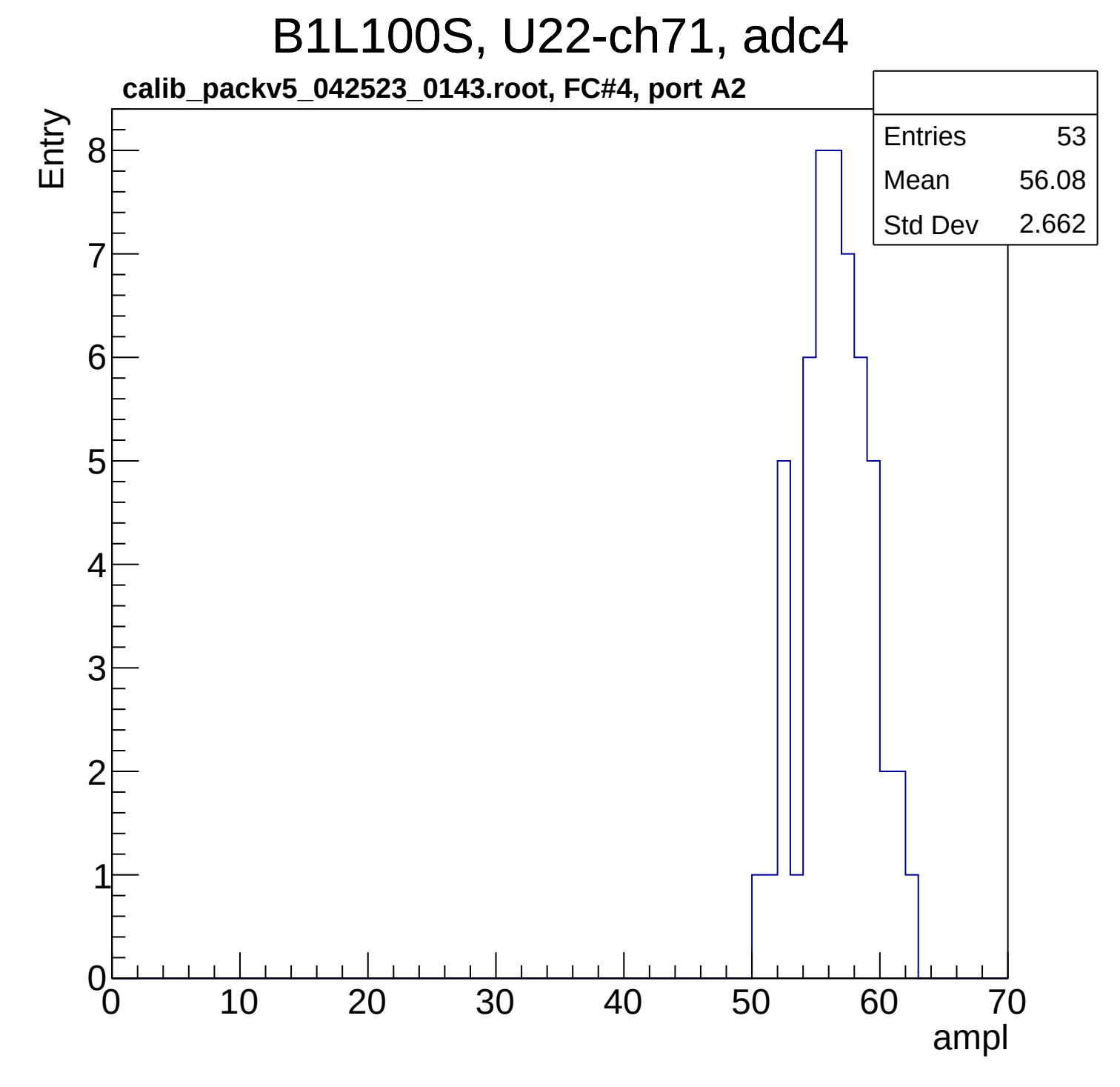

B1L100S, U22-ch70, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch71, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 28.64
Std Dev 3.903
Gaus mean : 29.1198
Gaus Width: 4.0833
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.31
Std Dev 3.991
Gaus mean : 37.2200
Gaus Width: 4.0771
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.29
Std Dev 3.377
Gaus mean : 43.8315
Gaus Width: 3.8256
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 49.78
Std Dev 3.407
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 56.08
Std Dev 2.662
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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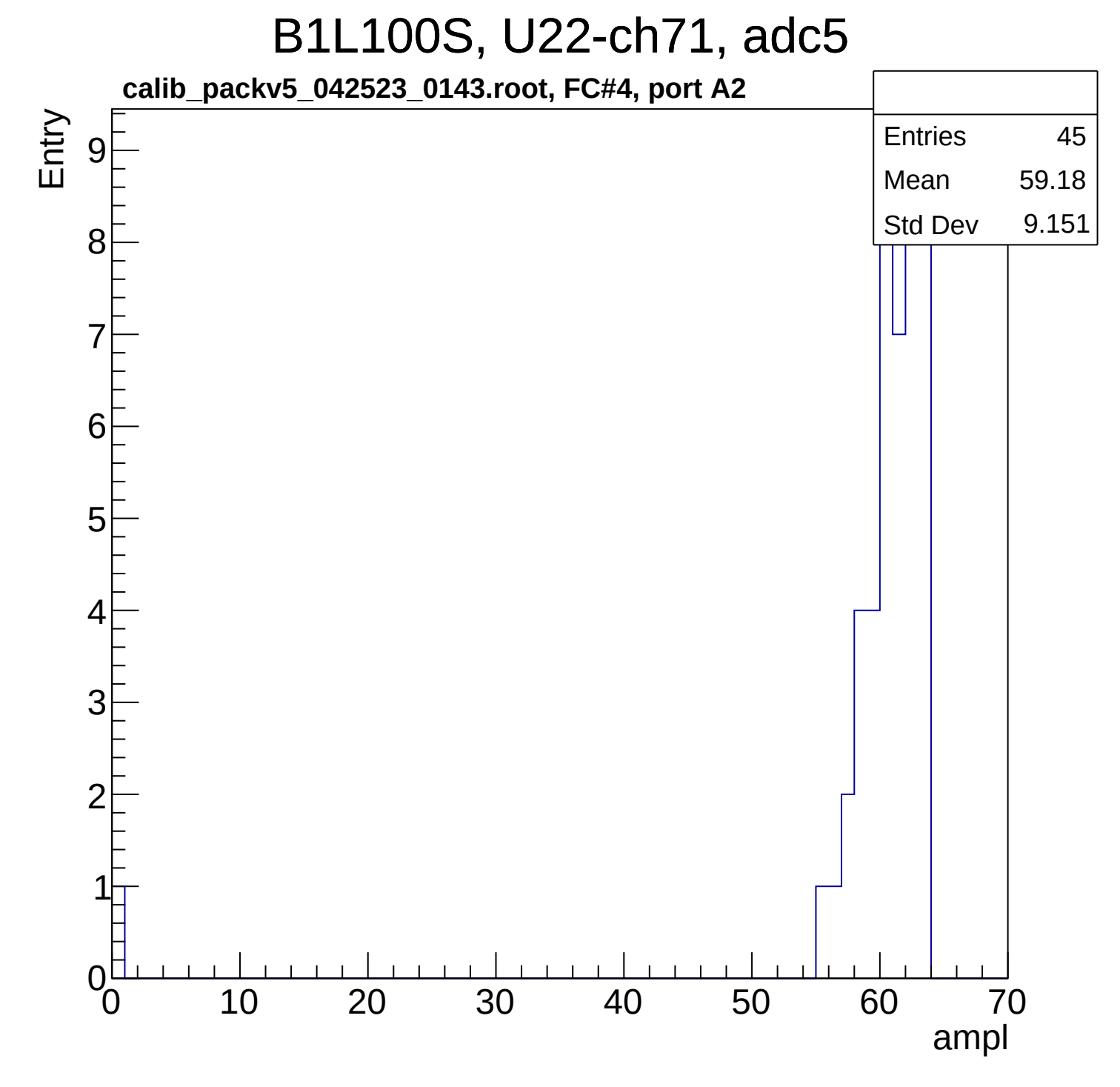
B1L100S, U22-ch71, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.18
Std Dev 9.151
Entry
ampl
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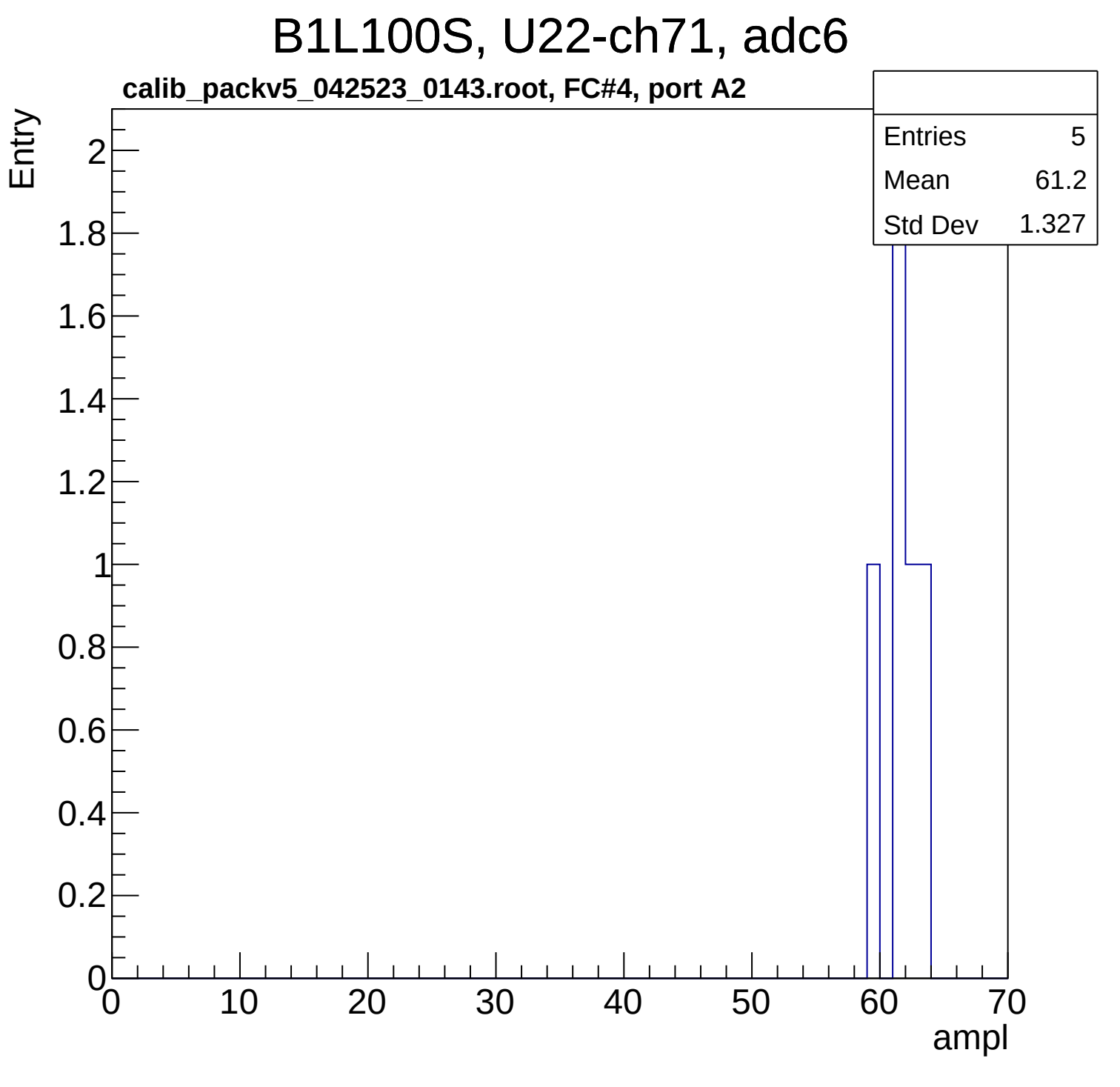
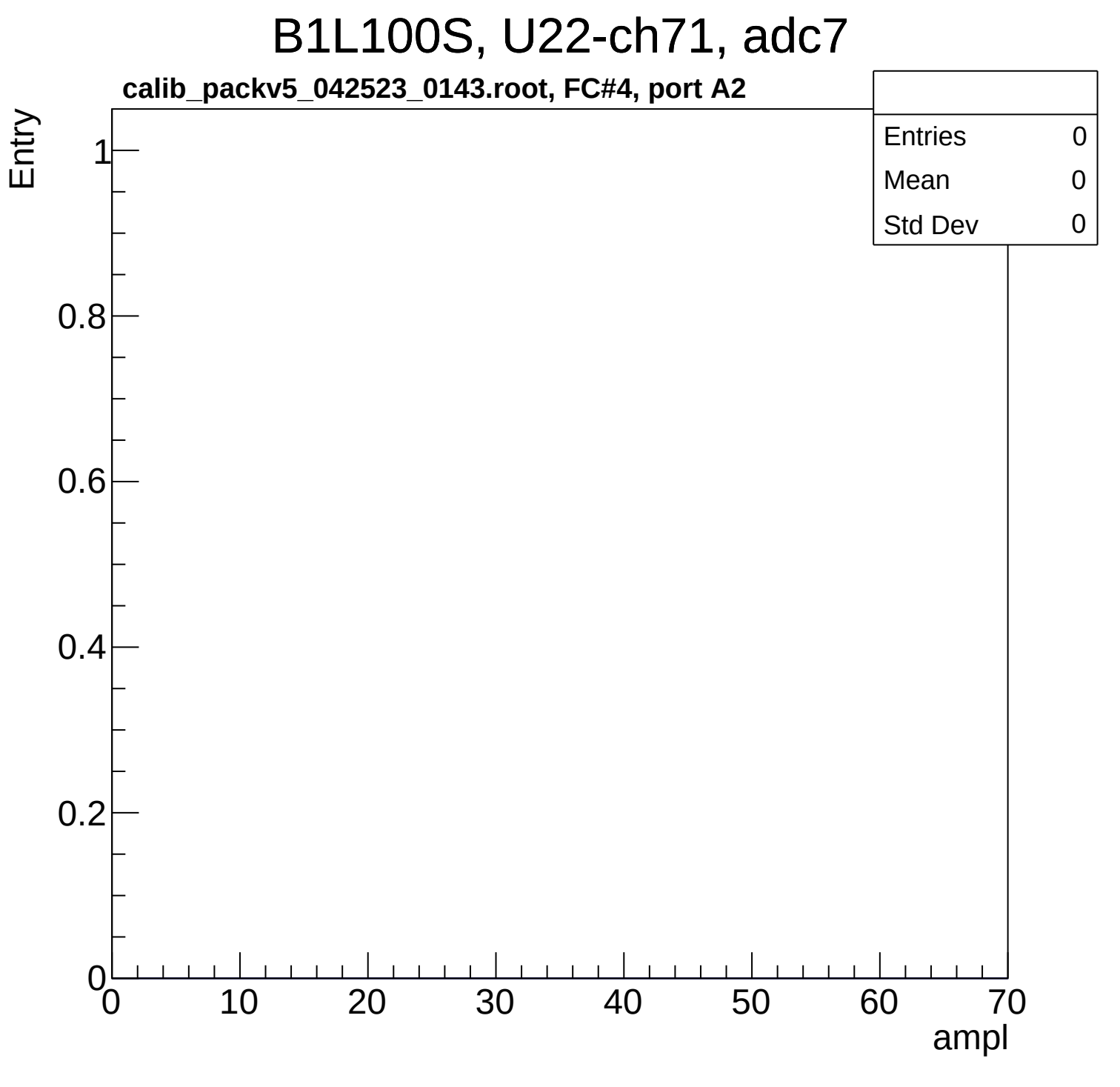
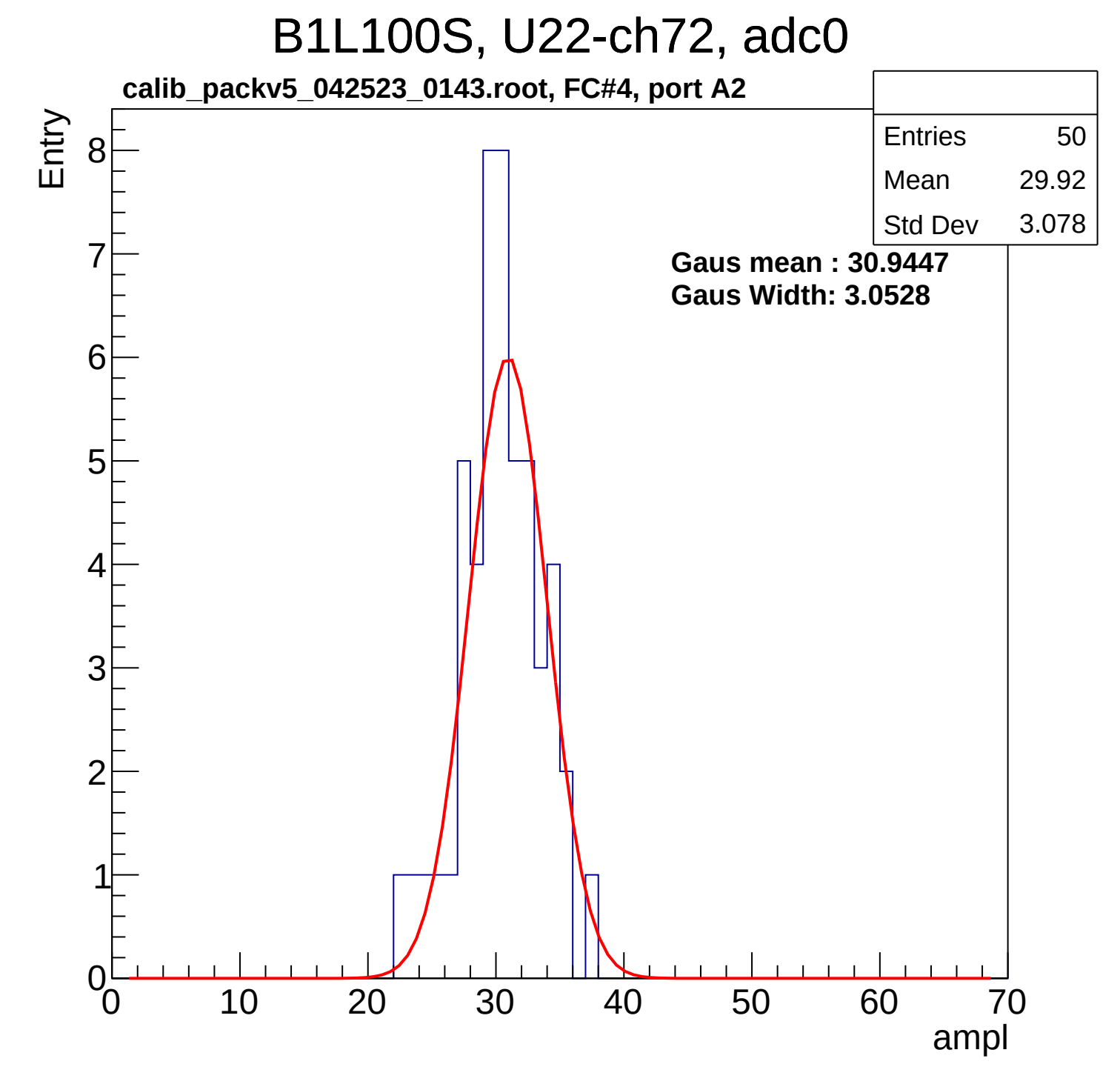
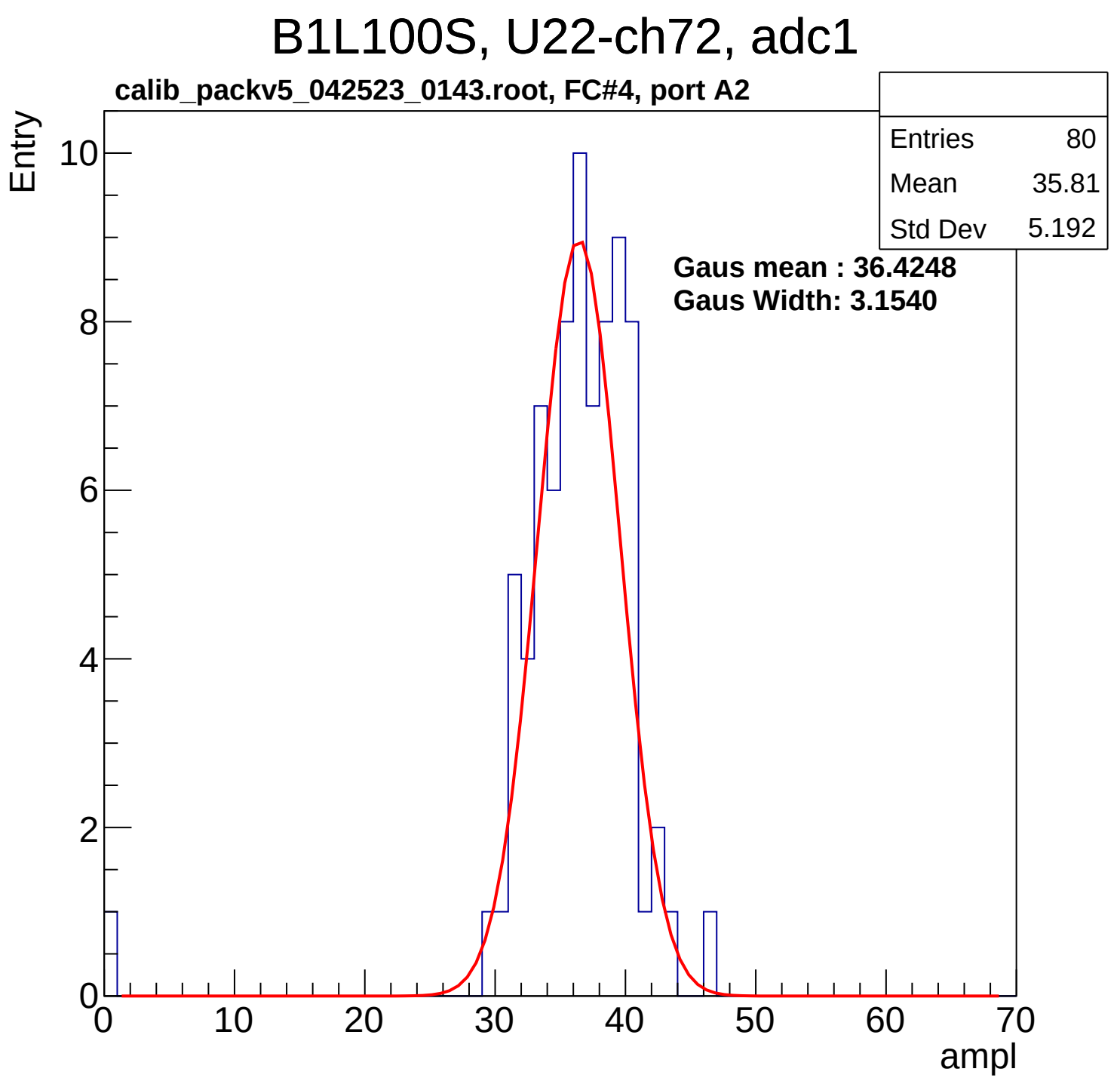
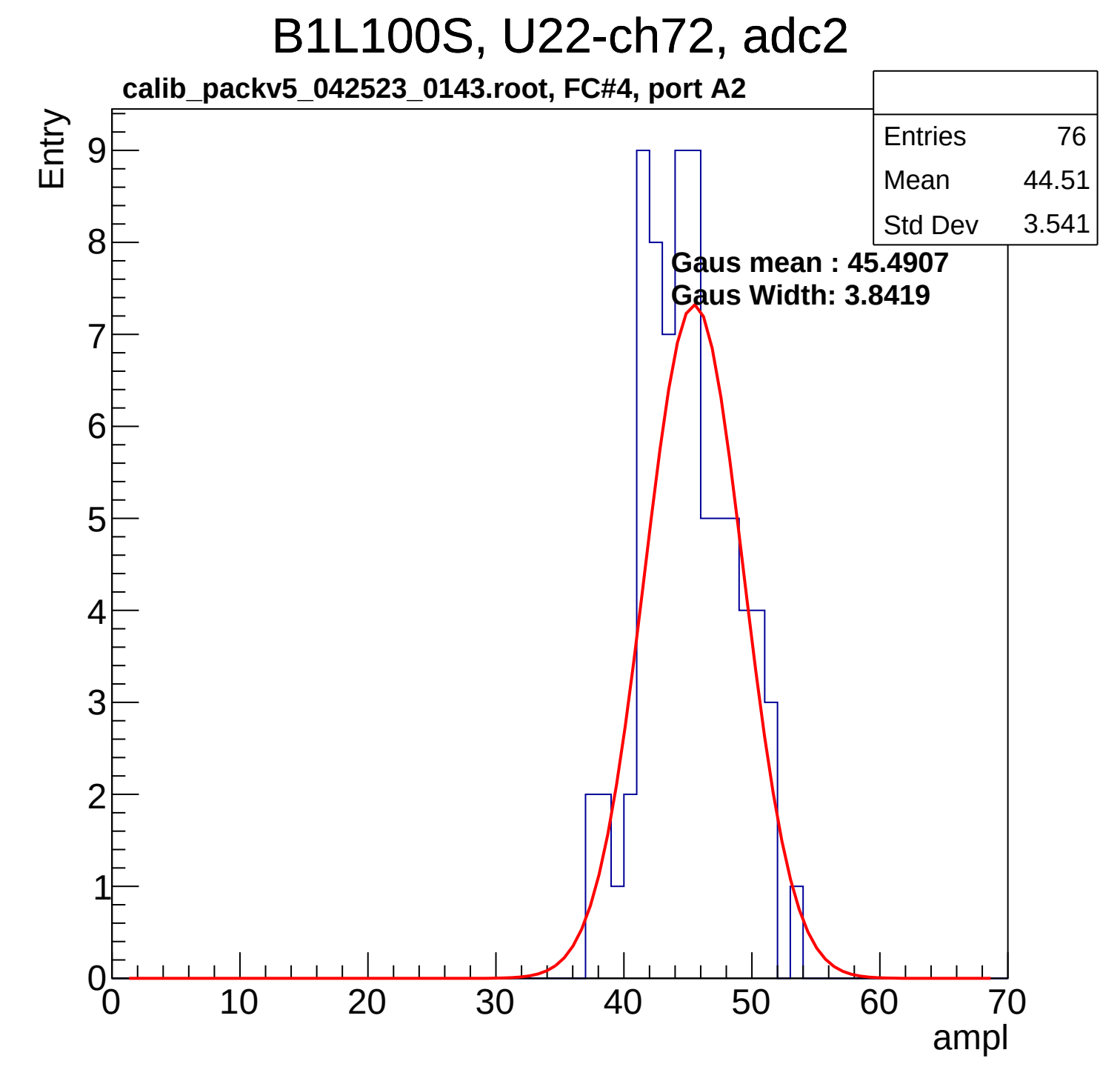
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.2
Std Dev 1.327
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch71, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch72, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 29.92
Std Dev 3.078
Gaus mean : 30.9447
Gaus Width: 3.0528
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch72, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.81
Std Dev 5.192
Gaus mean : 36.4248
Gaus Width: 3.1540
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch72, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 44.51
Std Dev 3.541
Gaus mean : 45.4907
Gaus Width: 3.8419
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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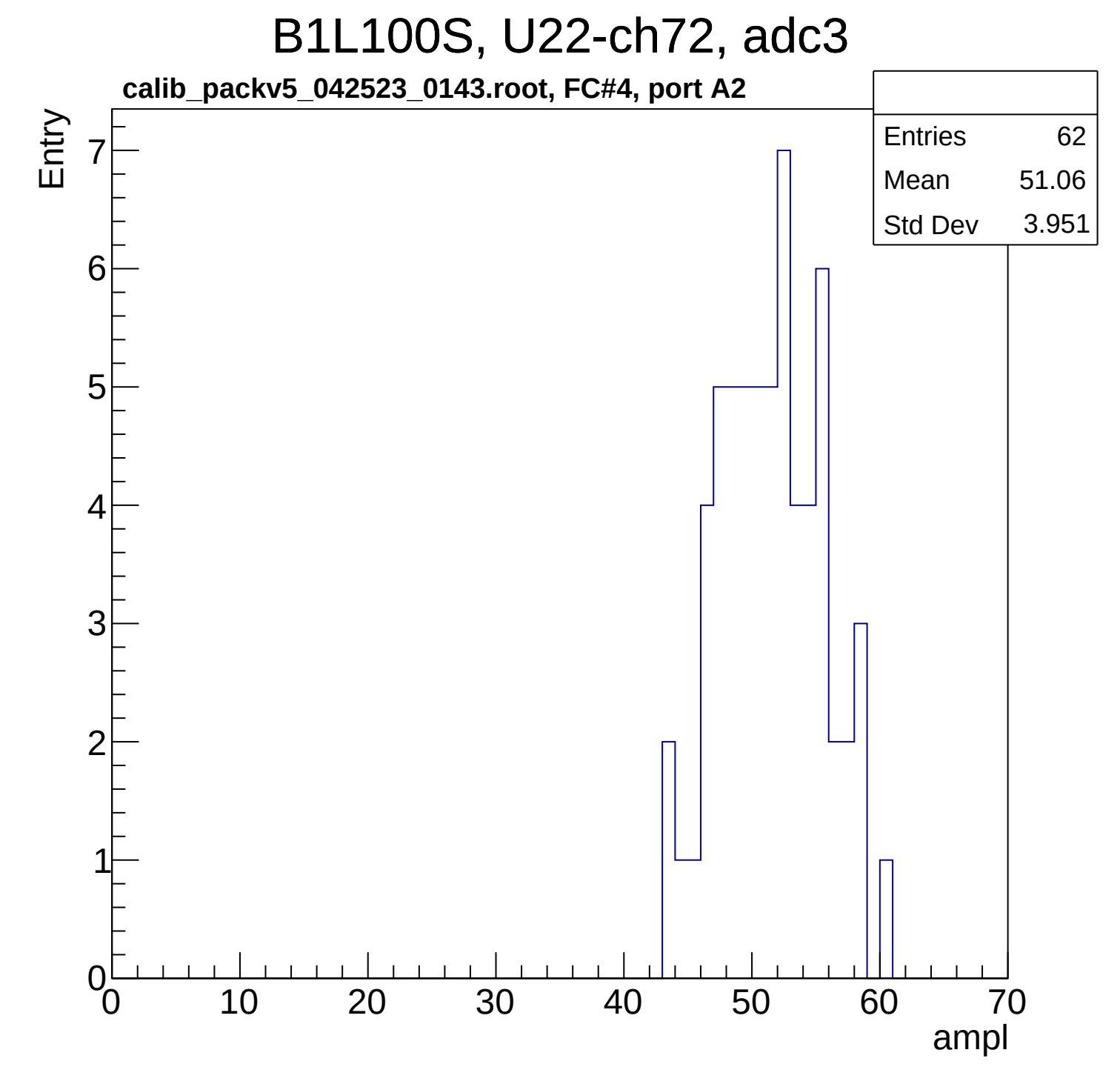
40
50
60
70


B1L100S, U22-ch72, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.06
Std Dev 3.951
Entry
ampl
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2
3
4
5
6
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0
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40
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60
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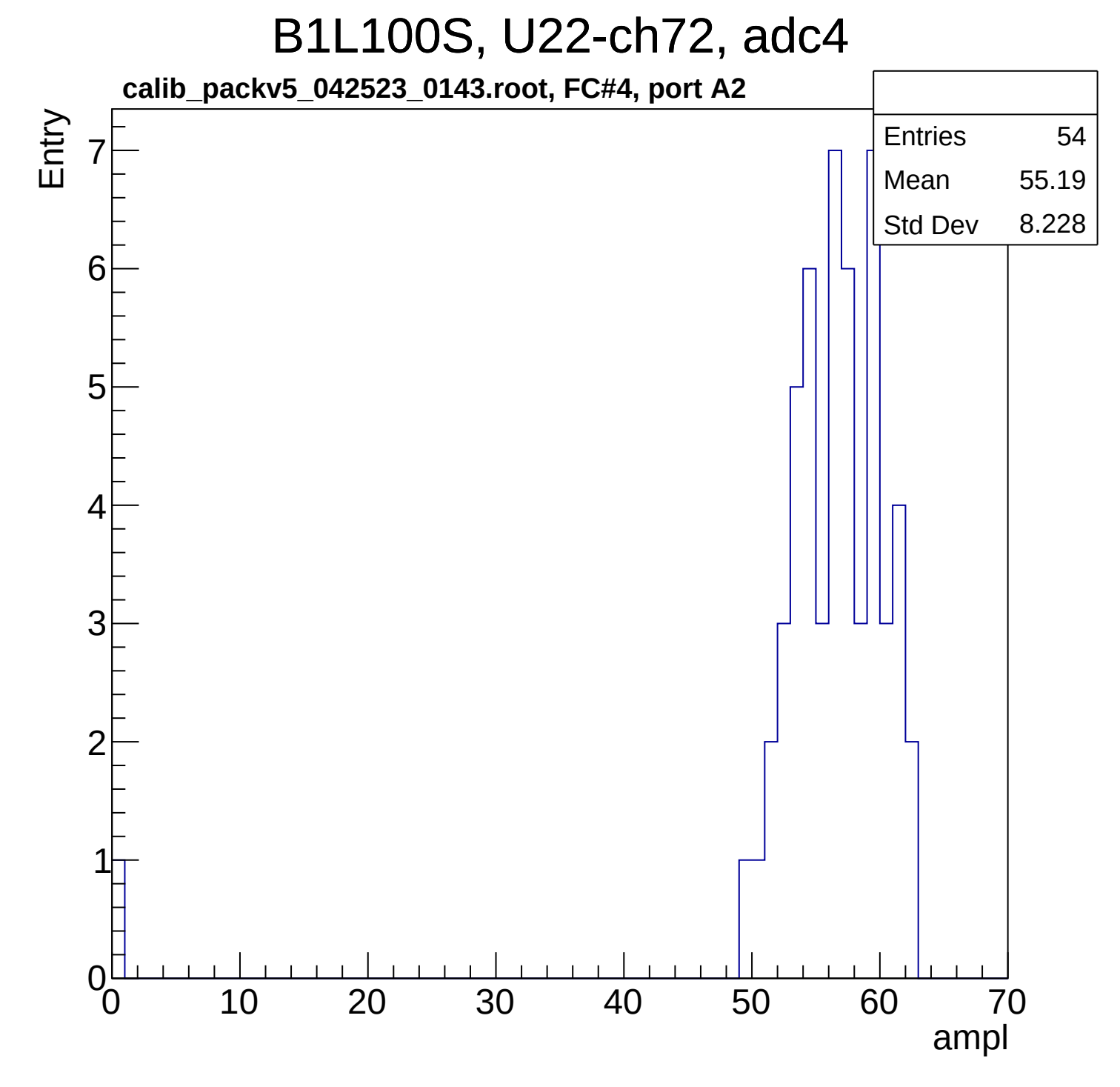
B1L100S, U22-ch72, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.19
Std Dev 8.228
Entry
ampl
0
1
2
3
4
5
6
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0
10
20
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40
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60
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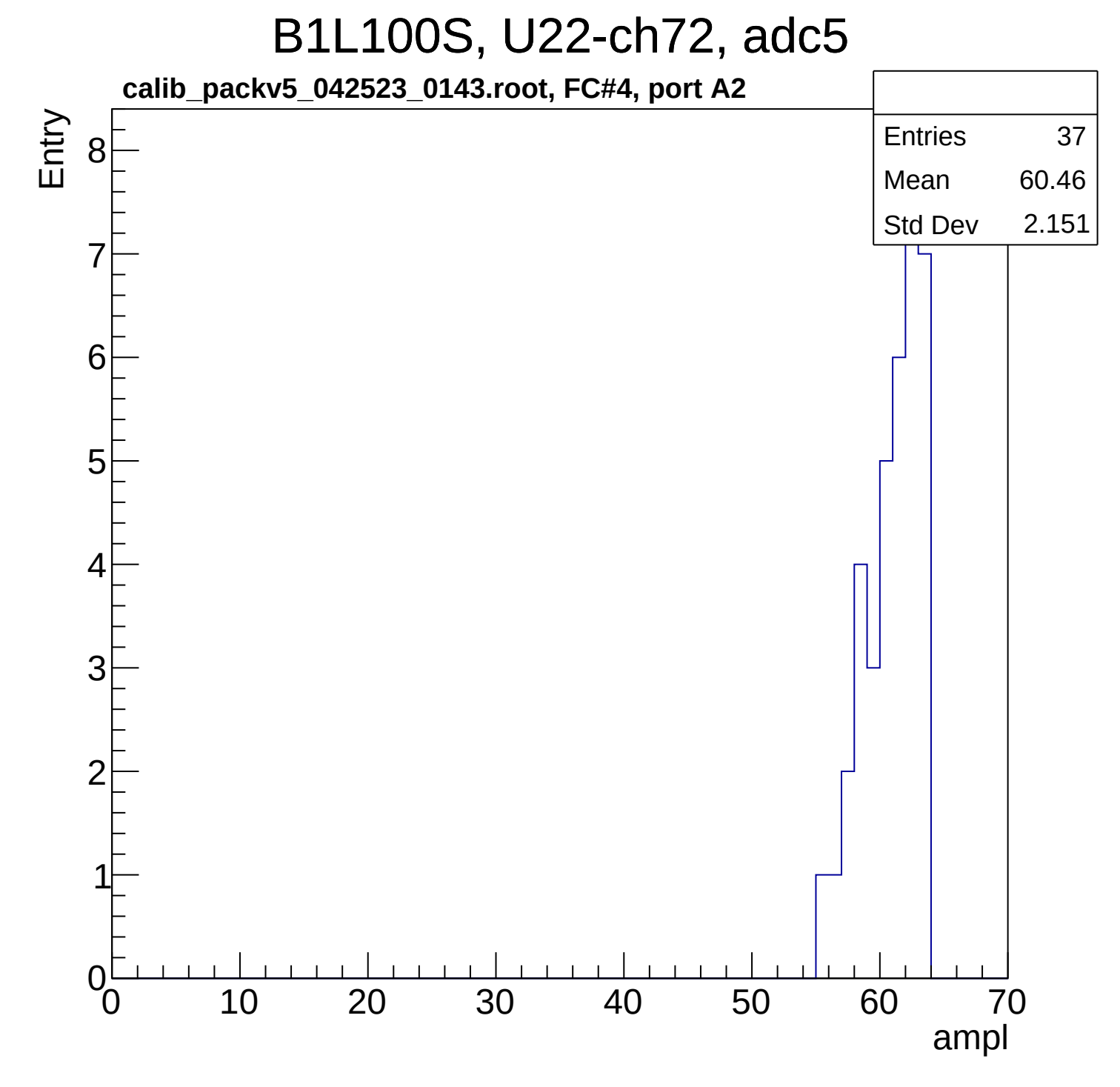
B1L100S, U22-ch72, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.46
Std Dev 2.151
Entry
ampl
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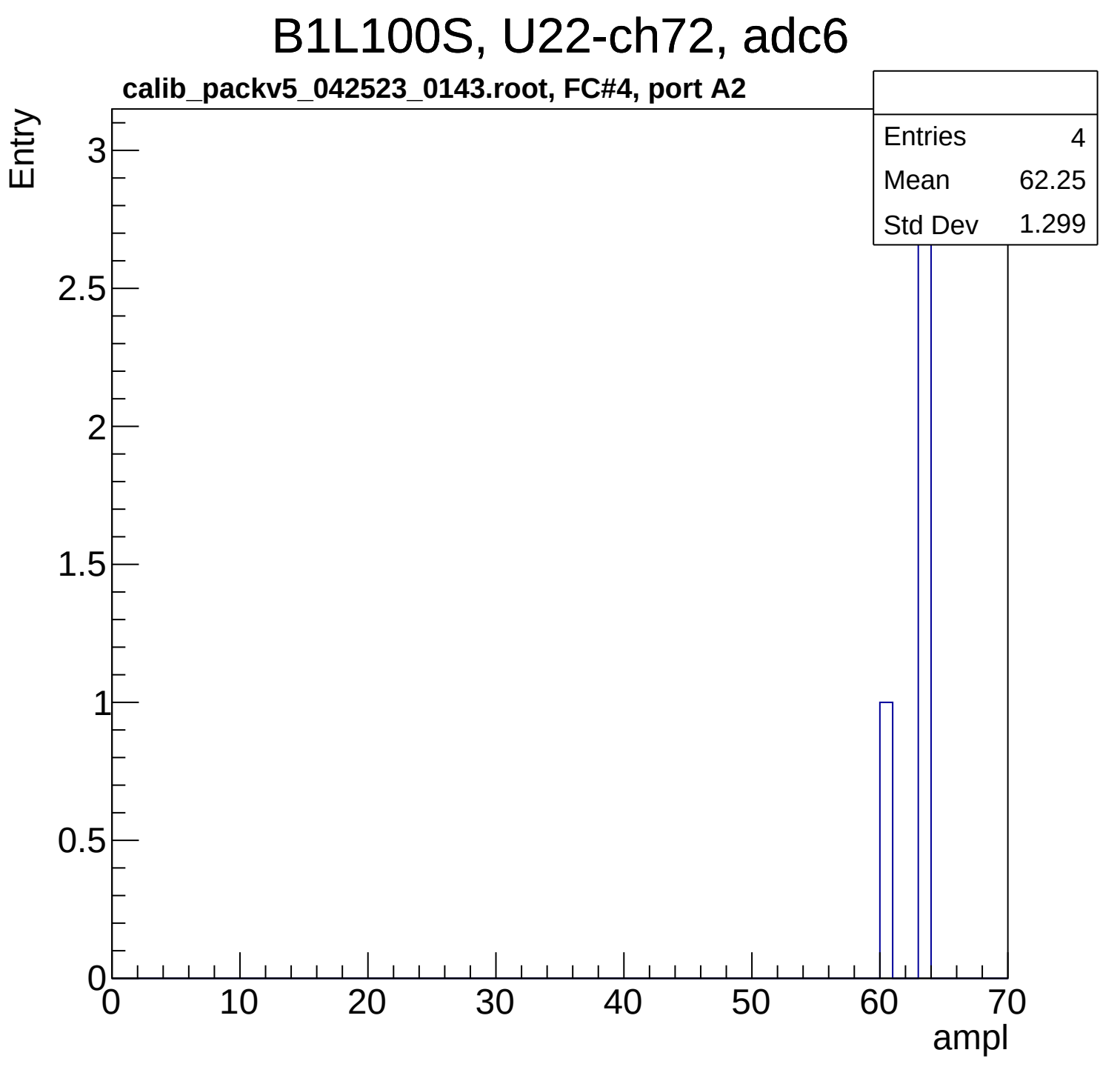
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch72, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.25
Std Dev 1.299
Entry
ampl
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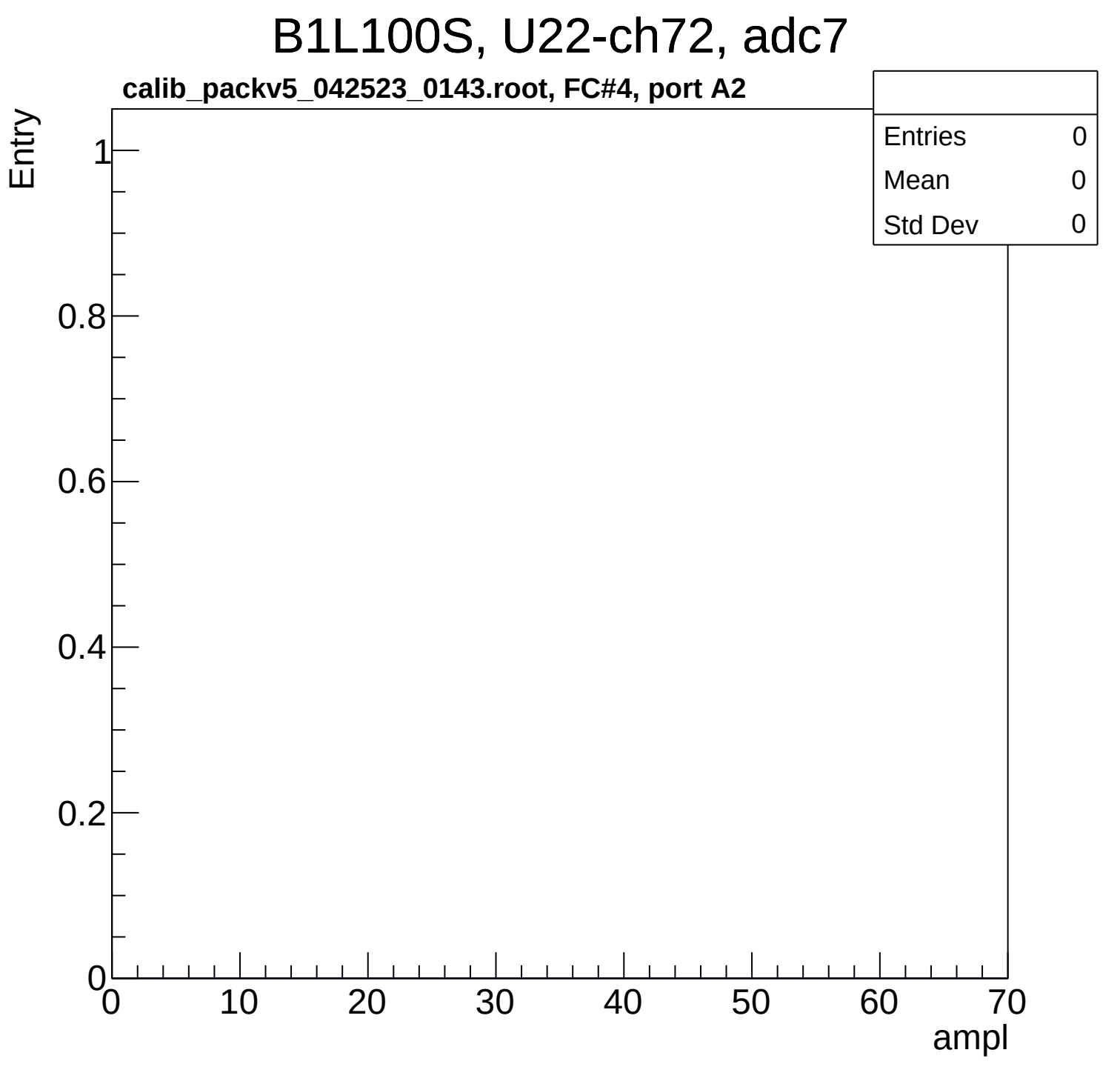
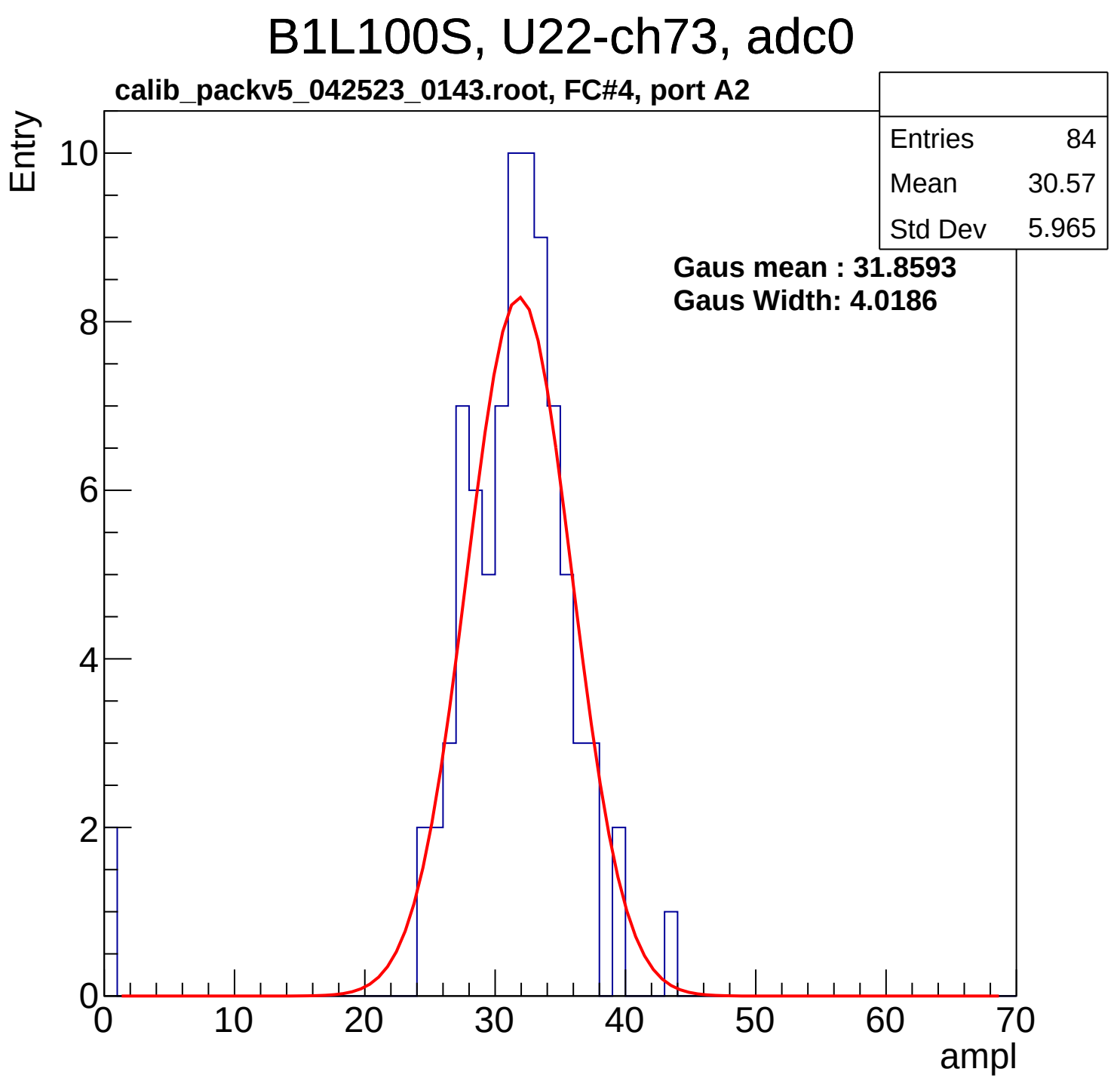
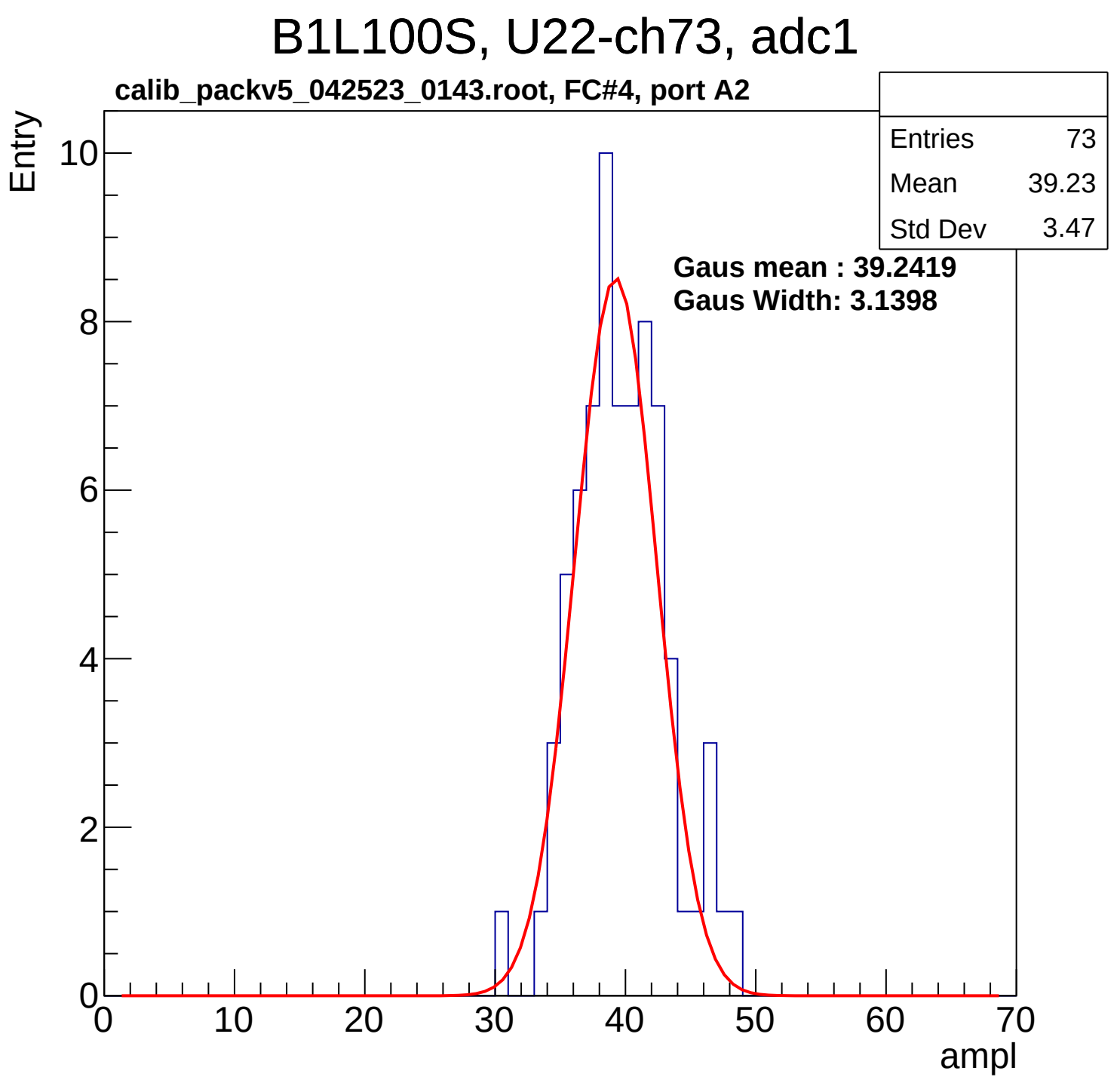
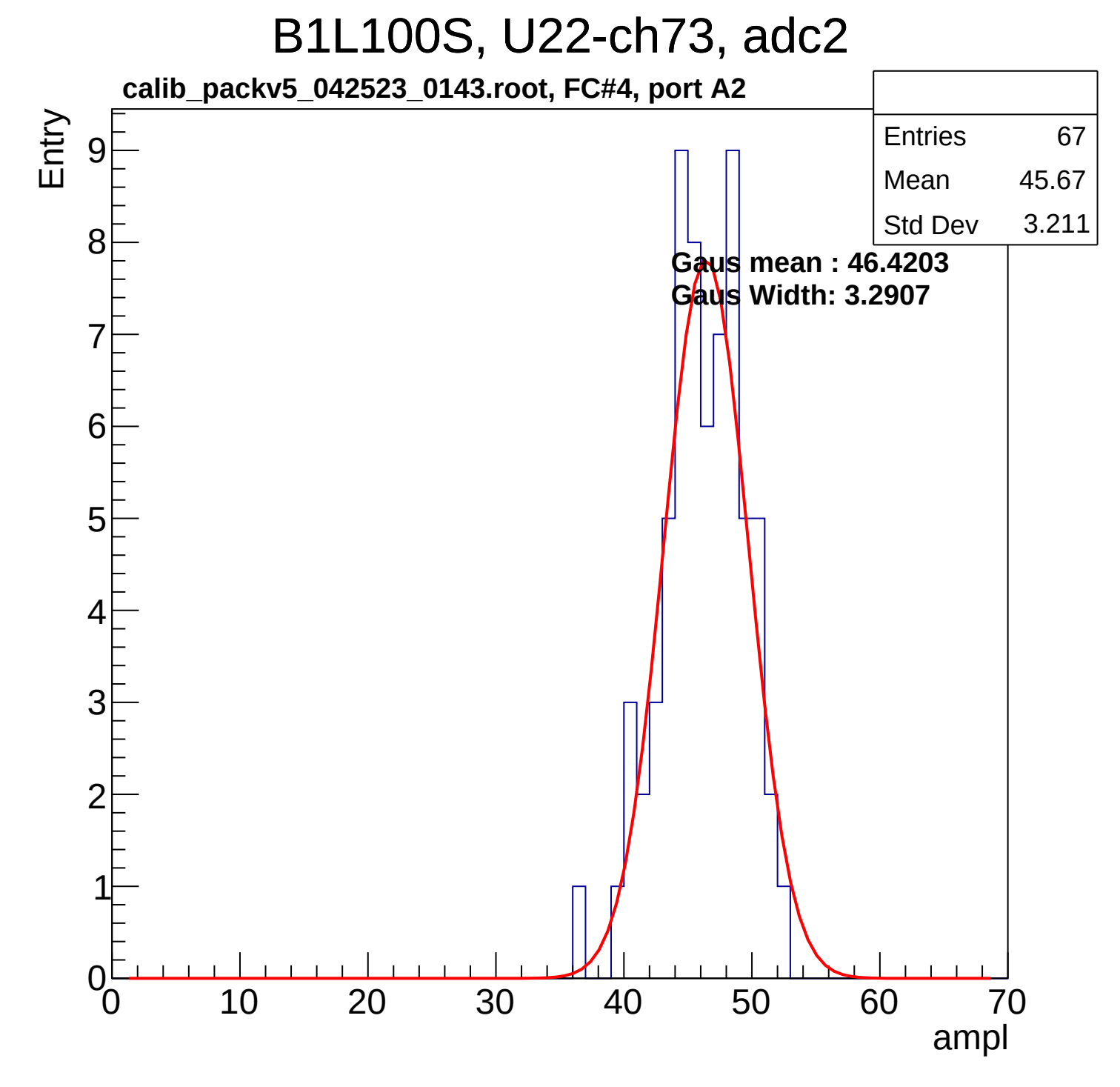
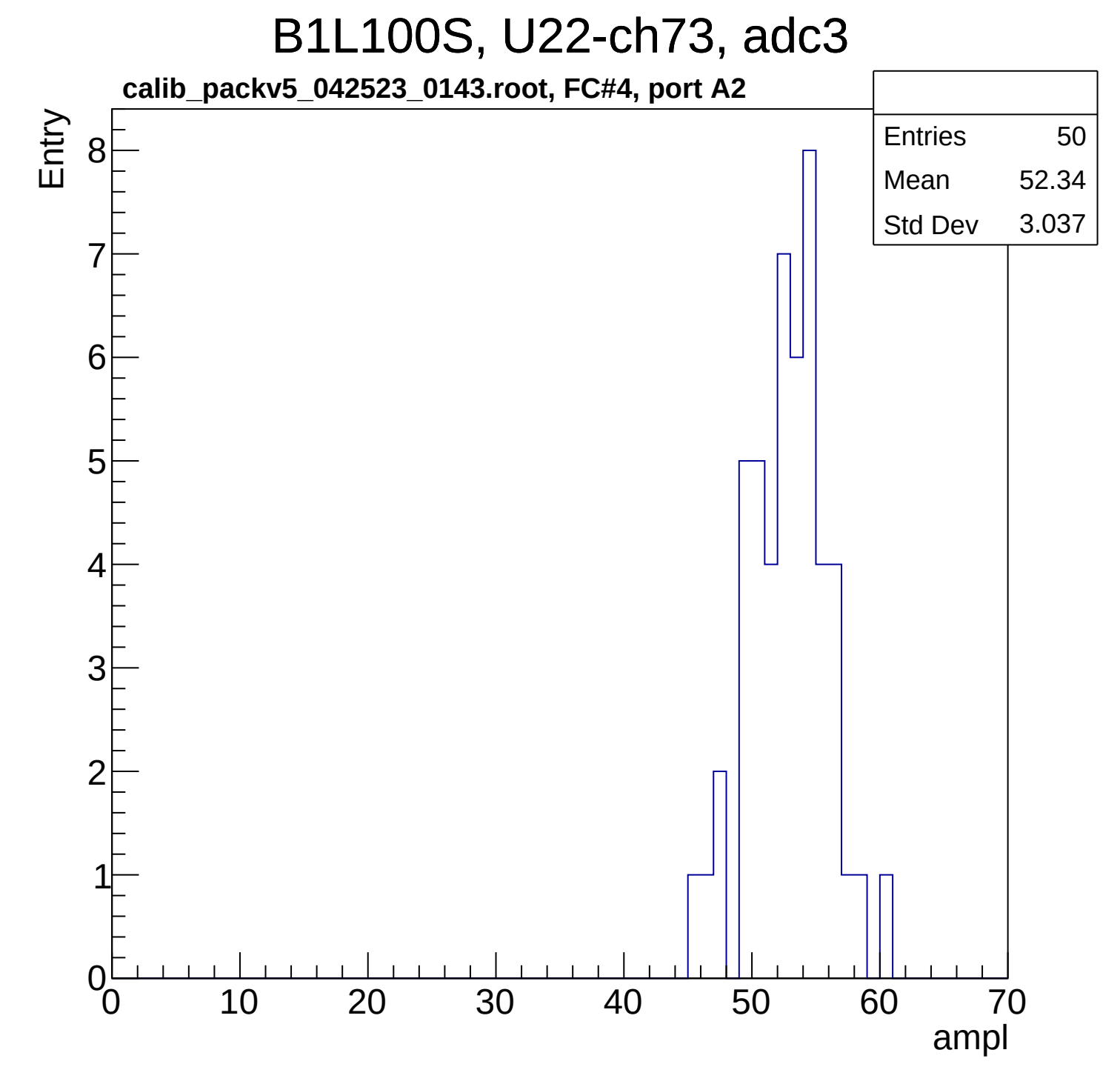
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch72, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch73, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 30.57
Std Dev 5.965
Gaus mean : 31.8593
Gaus Width: 4.0186
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch73, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 39.23
Std Dev 3.47
Gaus mean : 39.2419
Gaus Width: 3.1398
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch73, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 45.67
Std Dev 3.211
Gaus mean : 46.4203
Gaus Width: 3.2907
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch73, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 50
Mean 52.34
Std Dev 3.037
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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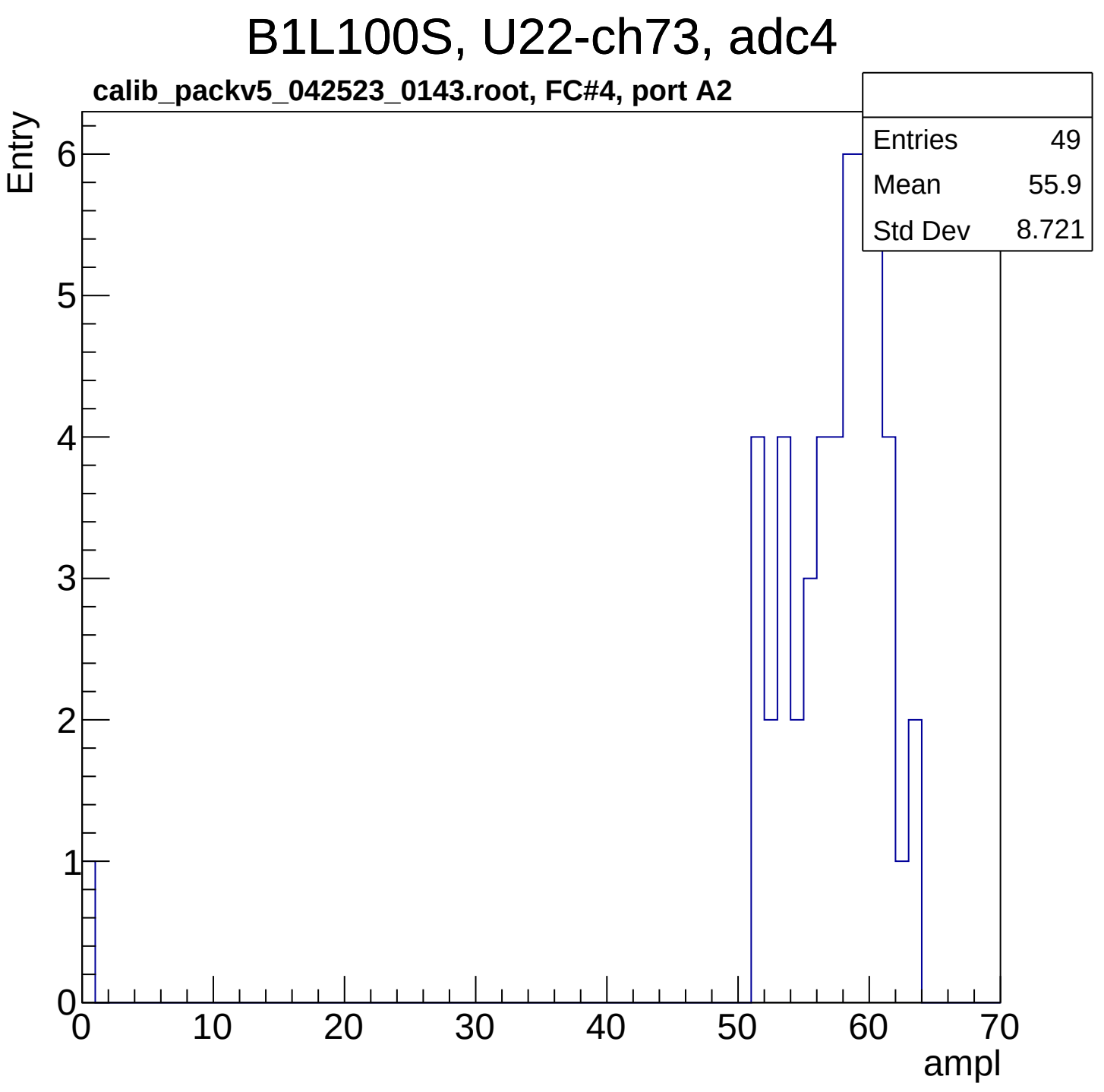
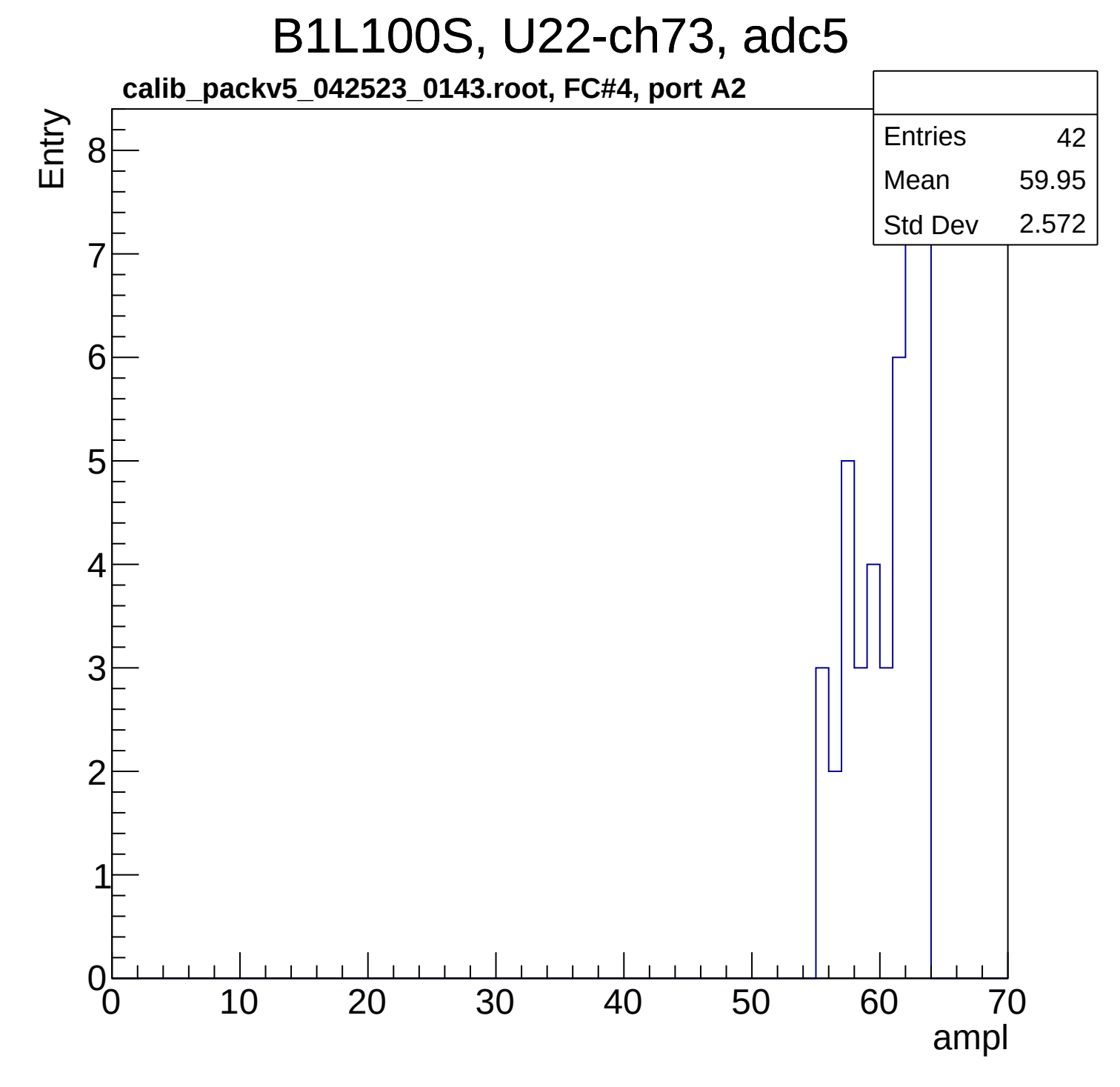
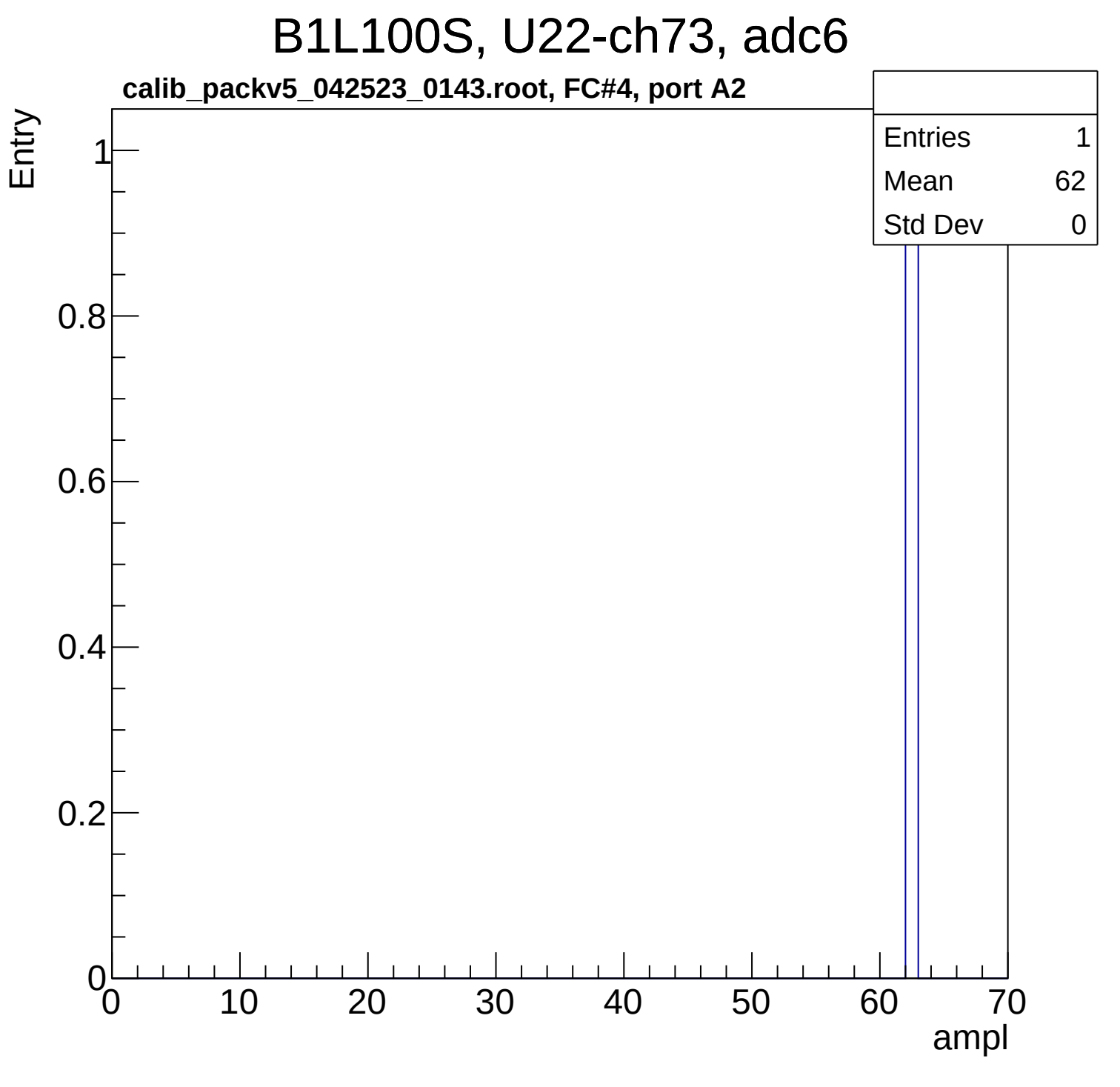
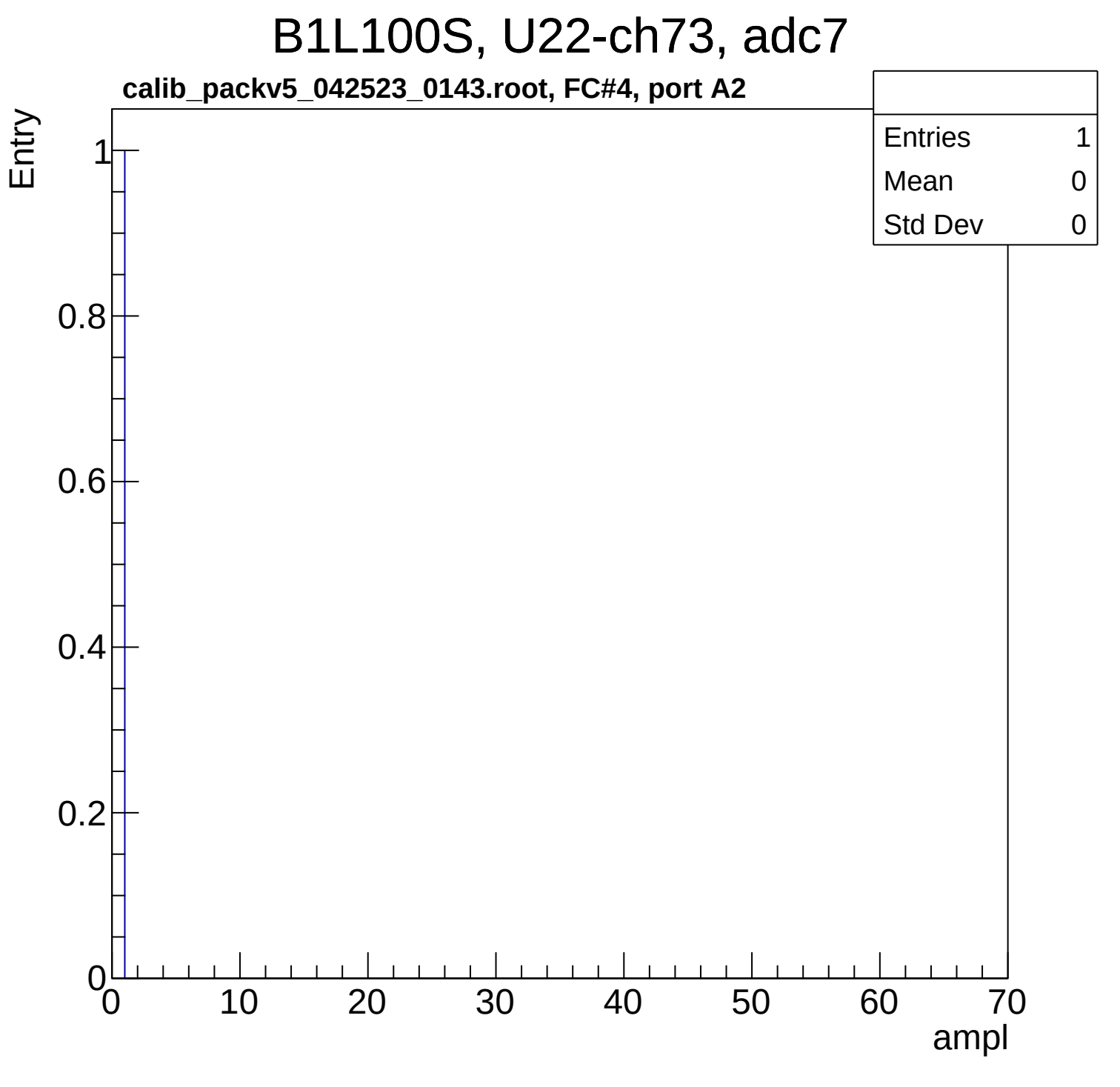
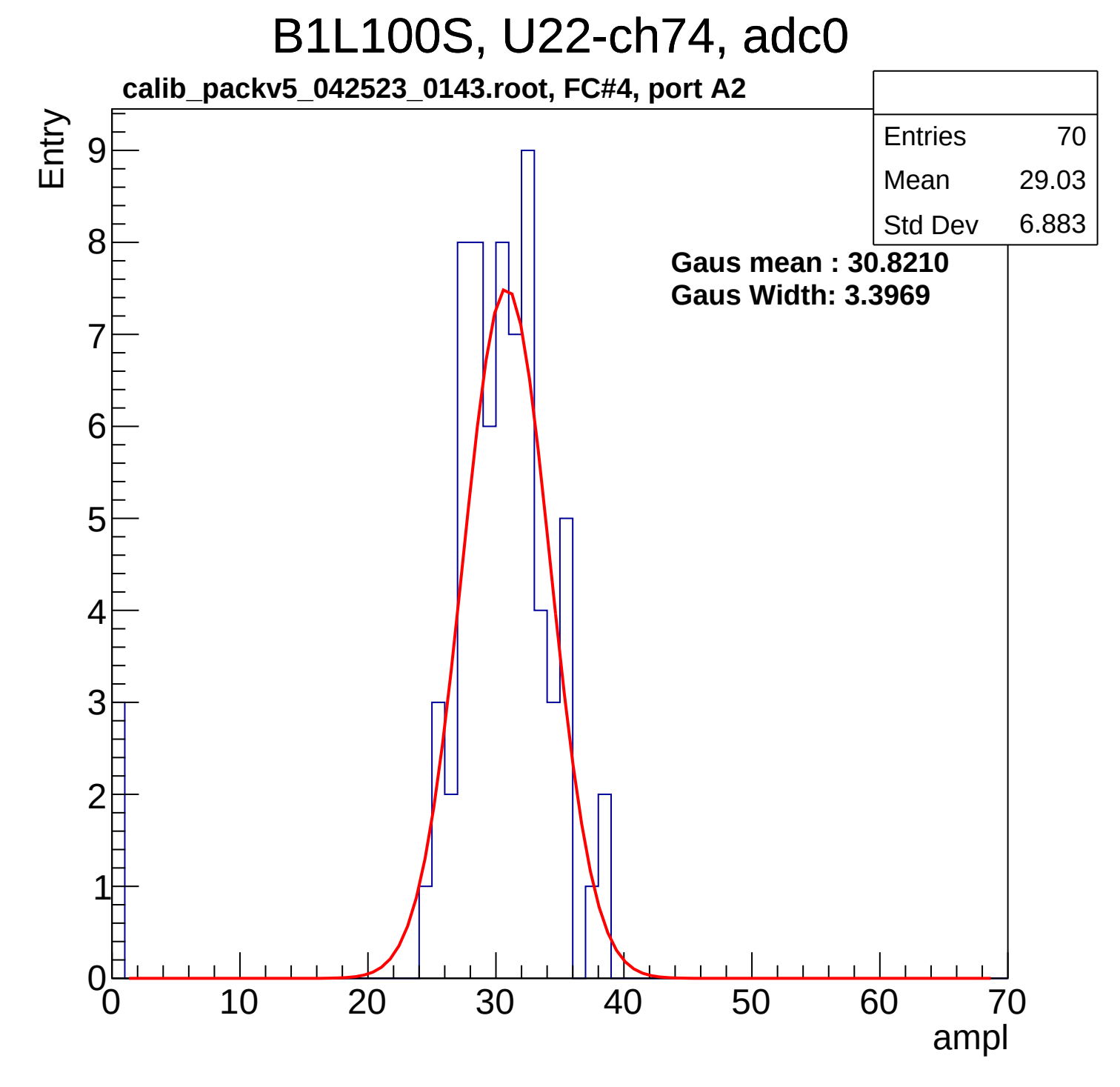
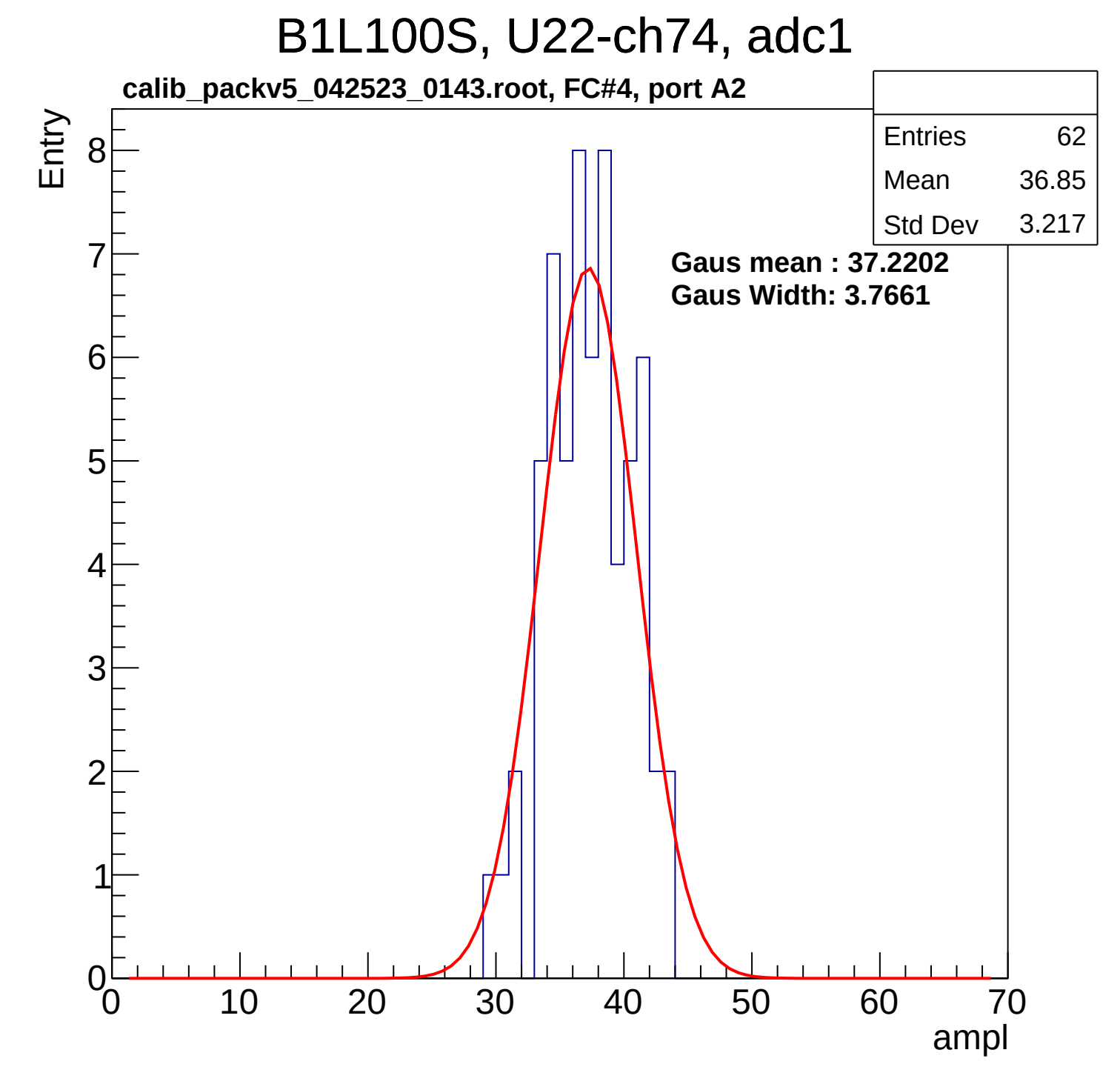

B1L100S, U22-ch73, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 55.9
Std Dev 8.721
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

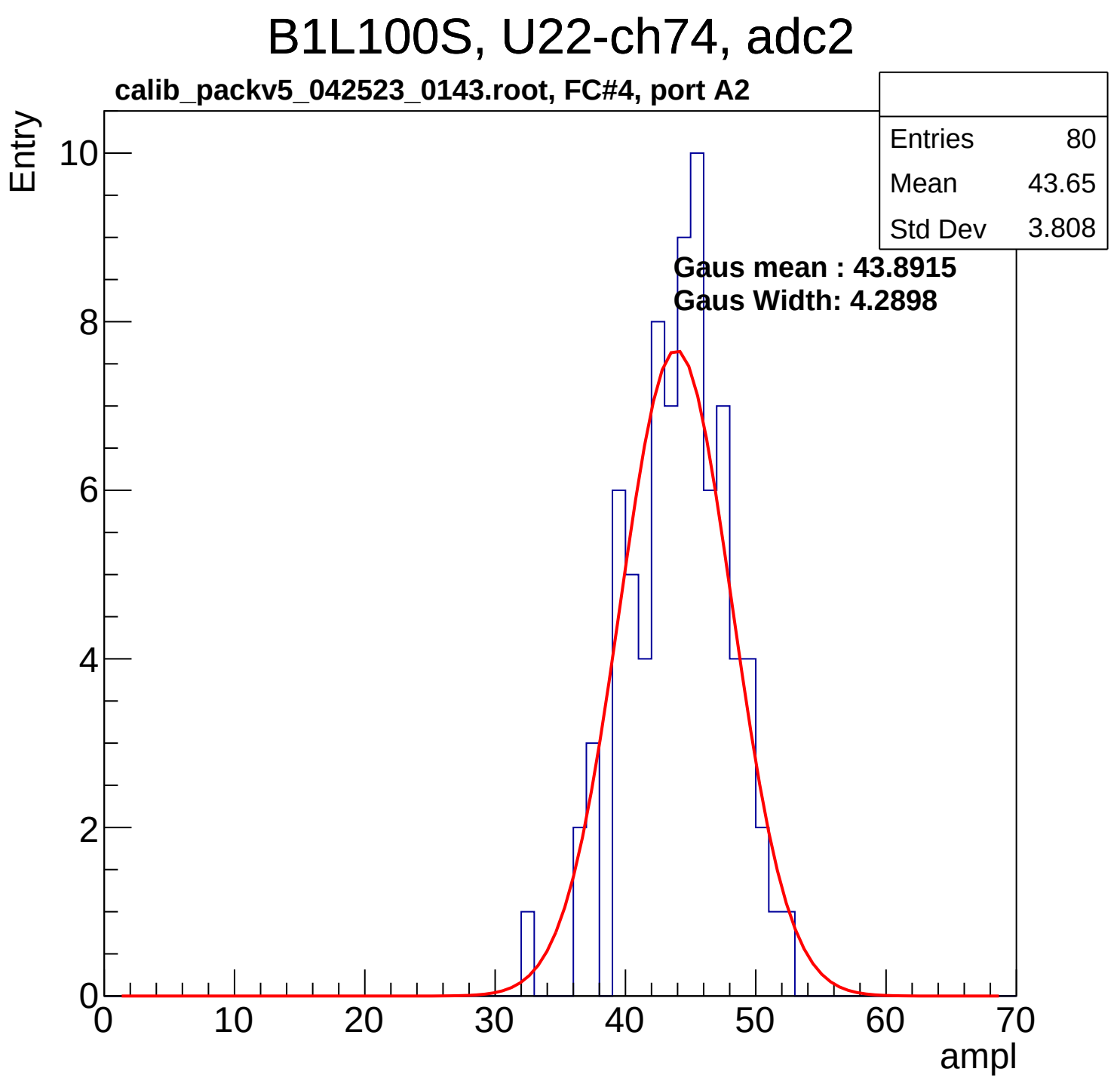

B1L100S, U22-ch73, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.95
Std Dev 2.572
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

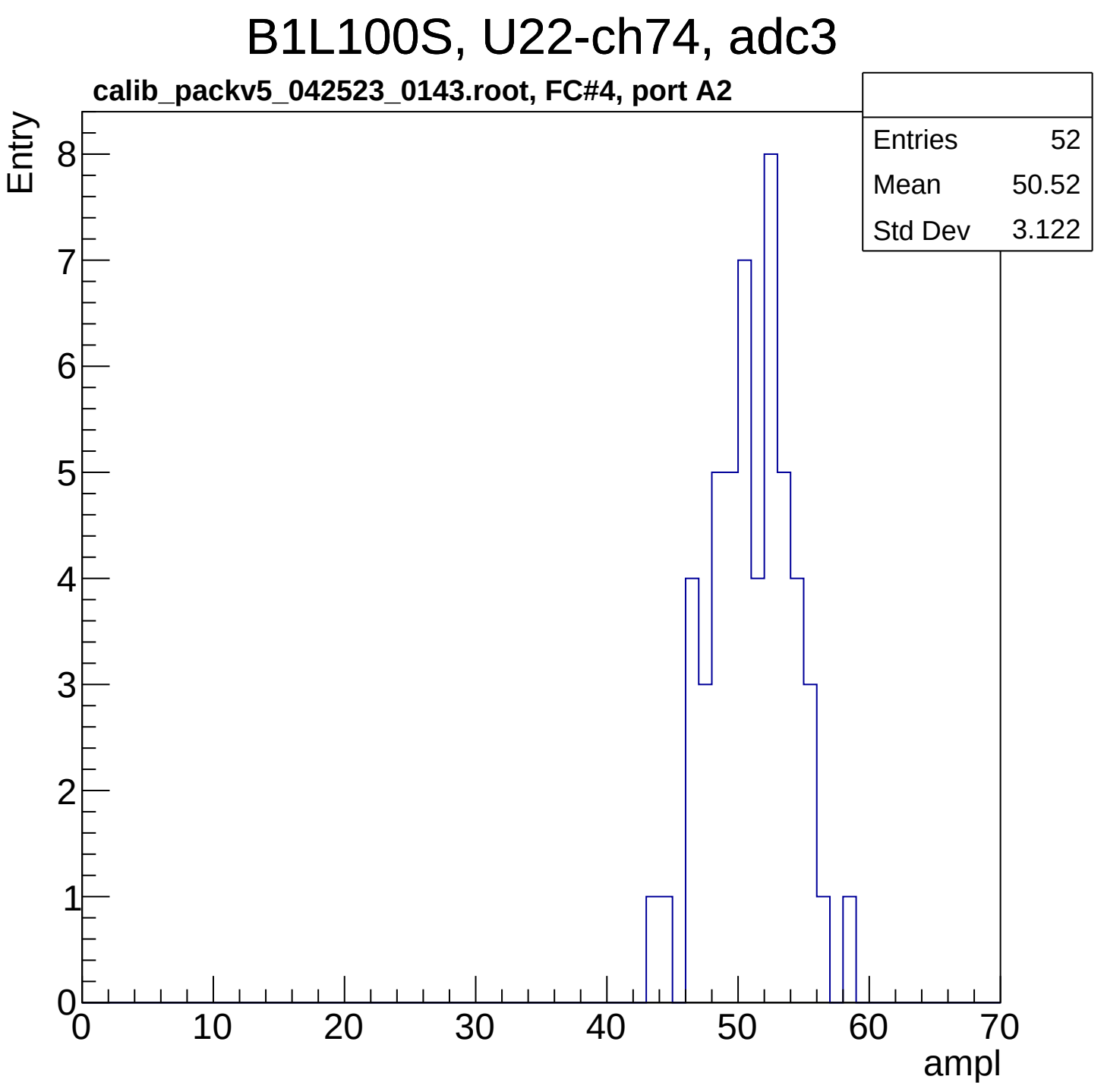

B1L100S, U22-ch73, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

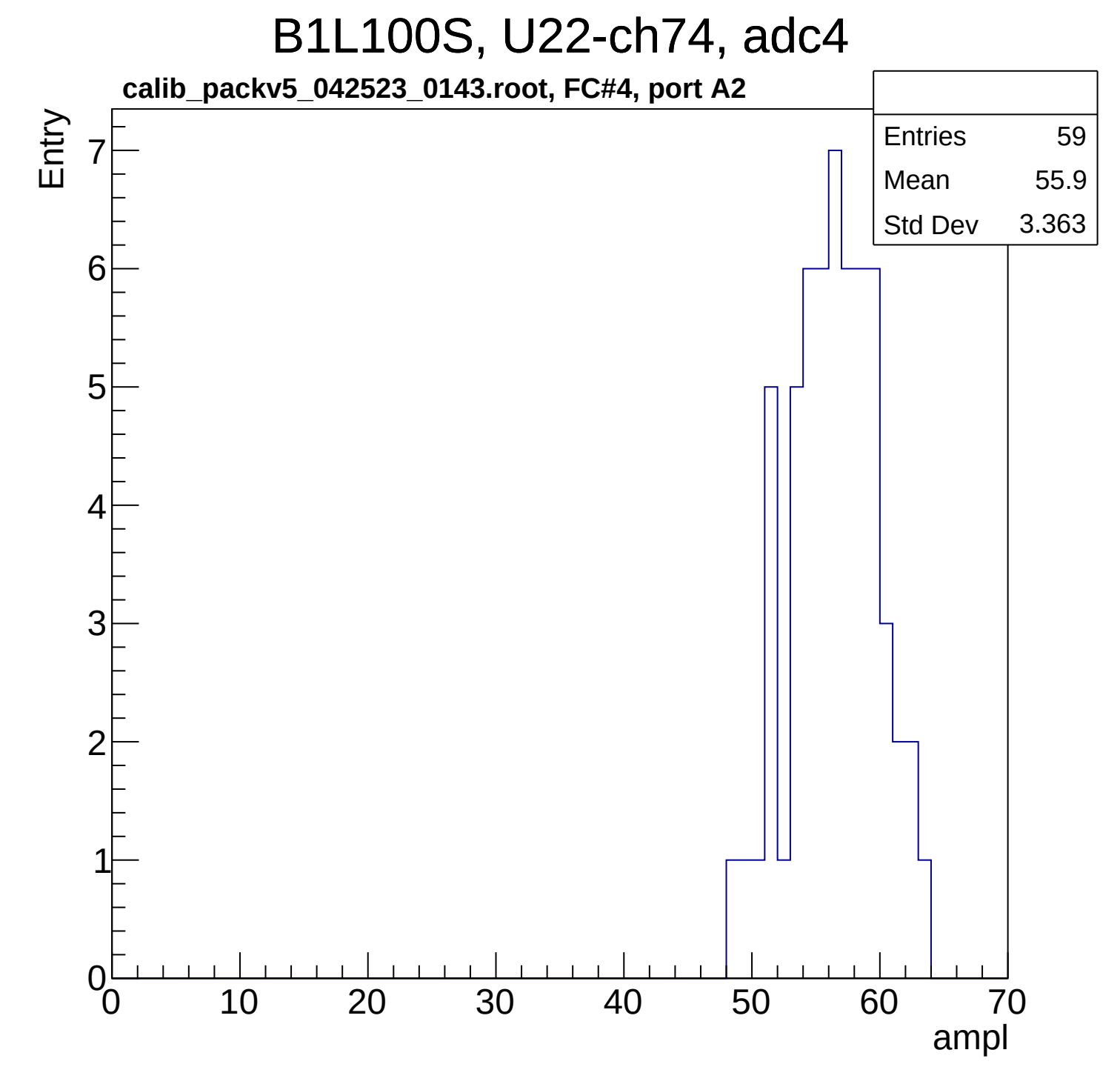

B1L100S, U22-ch73, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

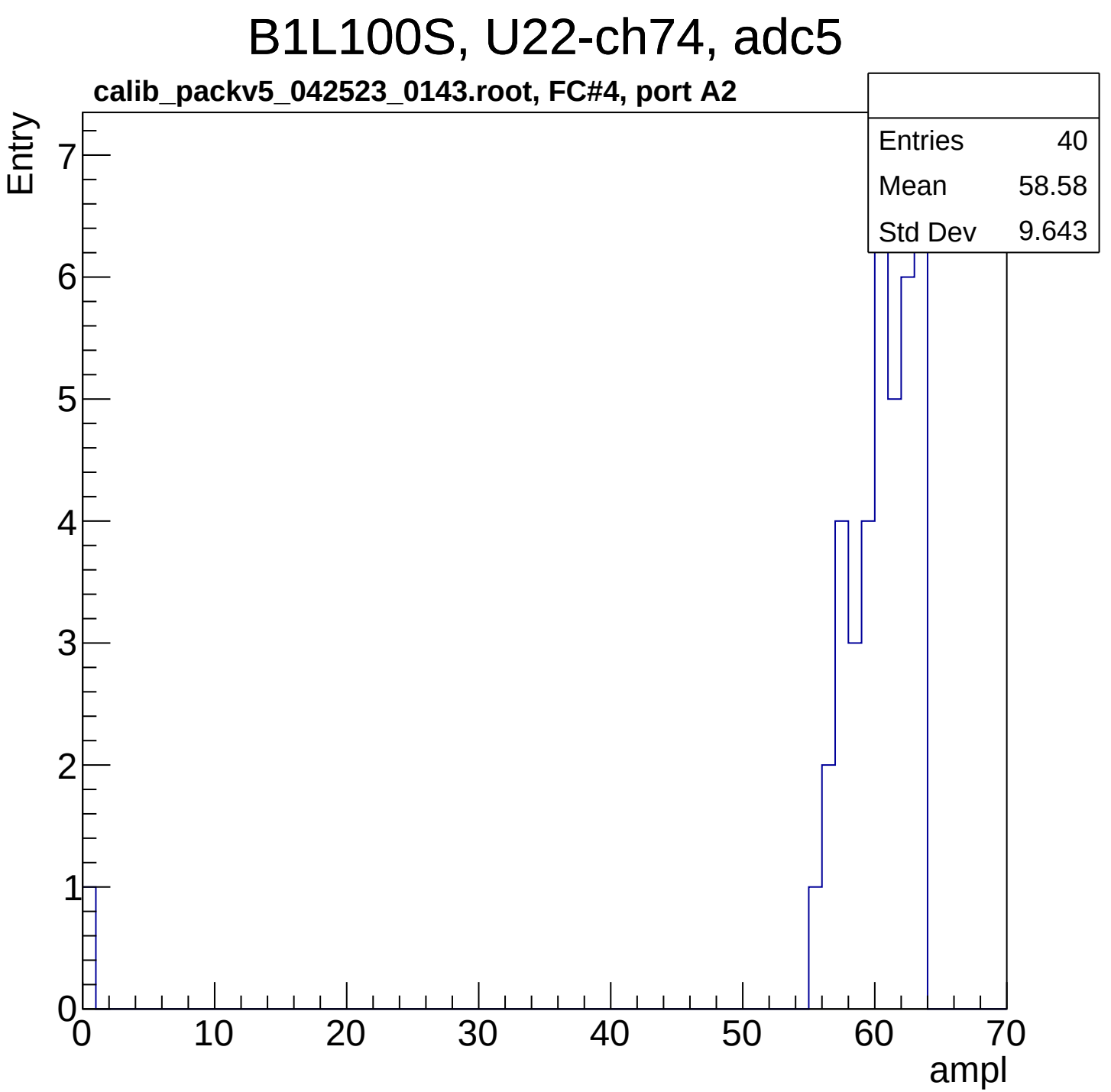

B1L100S, U22-ch74, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 29.03
Std Dev 6.883
Gaus mean : 30.8210
Gaus Width: 3.3969
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

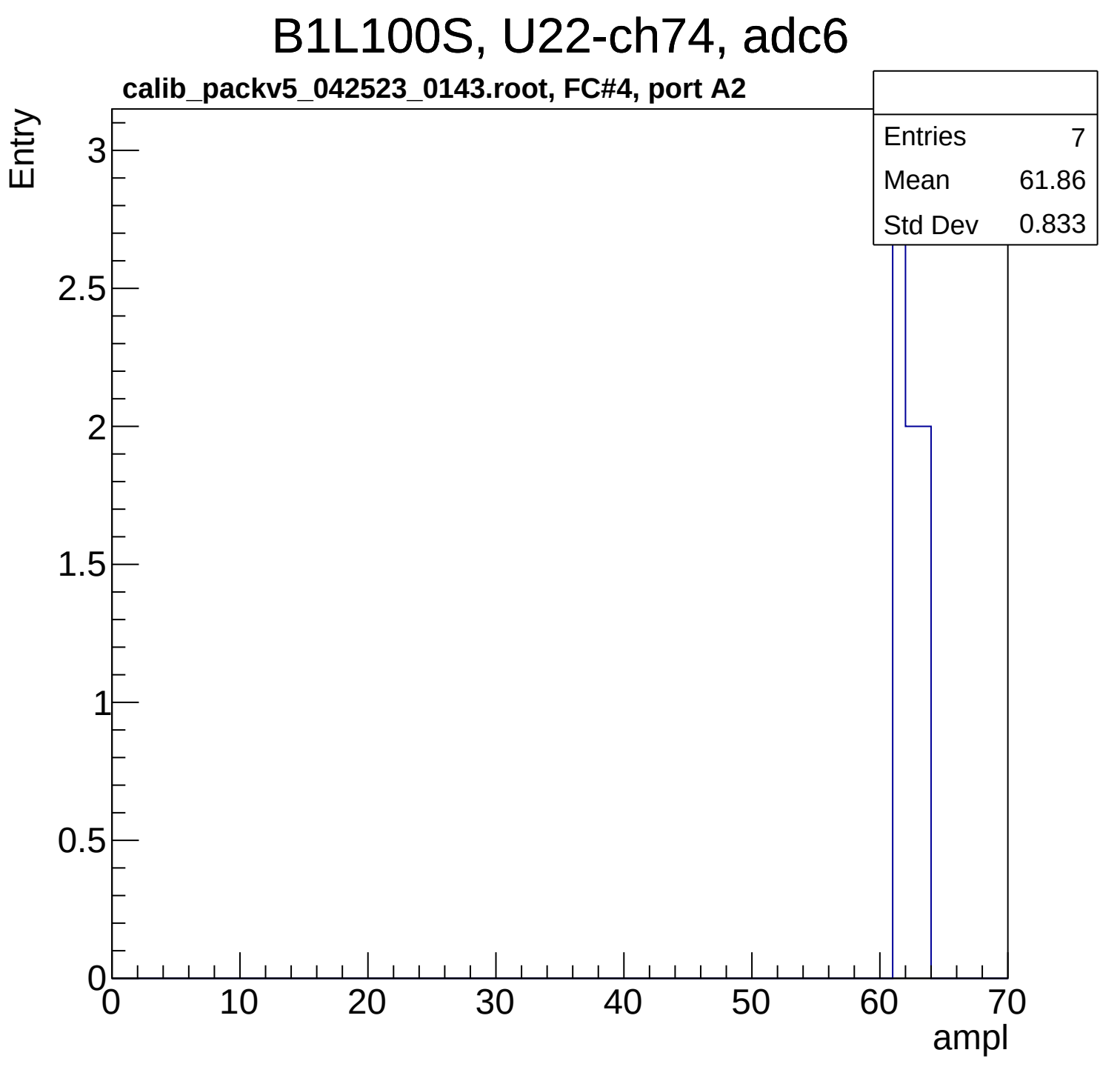

B1L100S, U22-ch74, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 36.85
Std Dev 3.217
Gaus mean : 37.2202
Gaus Width: 3.7661
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

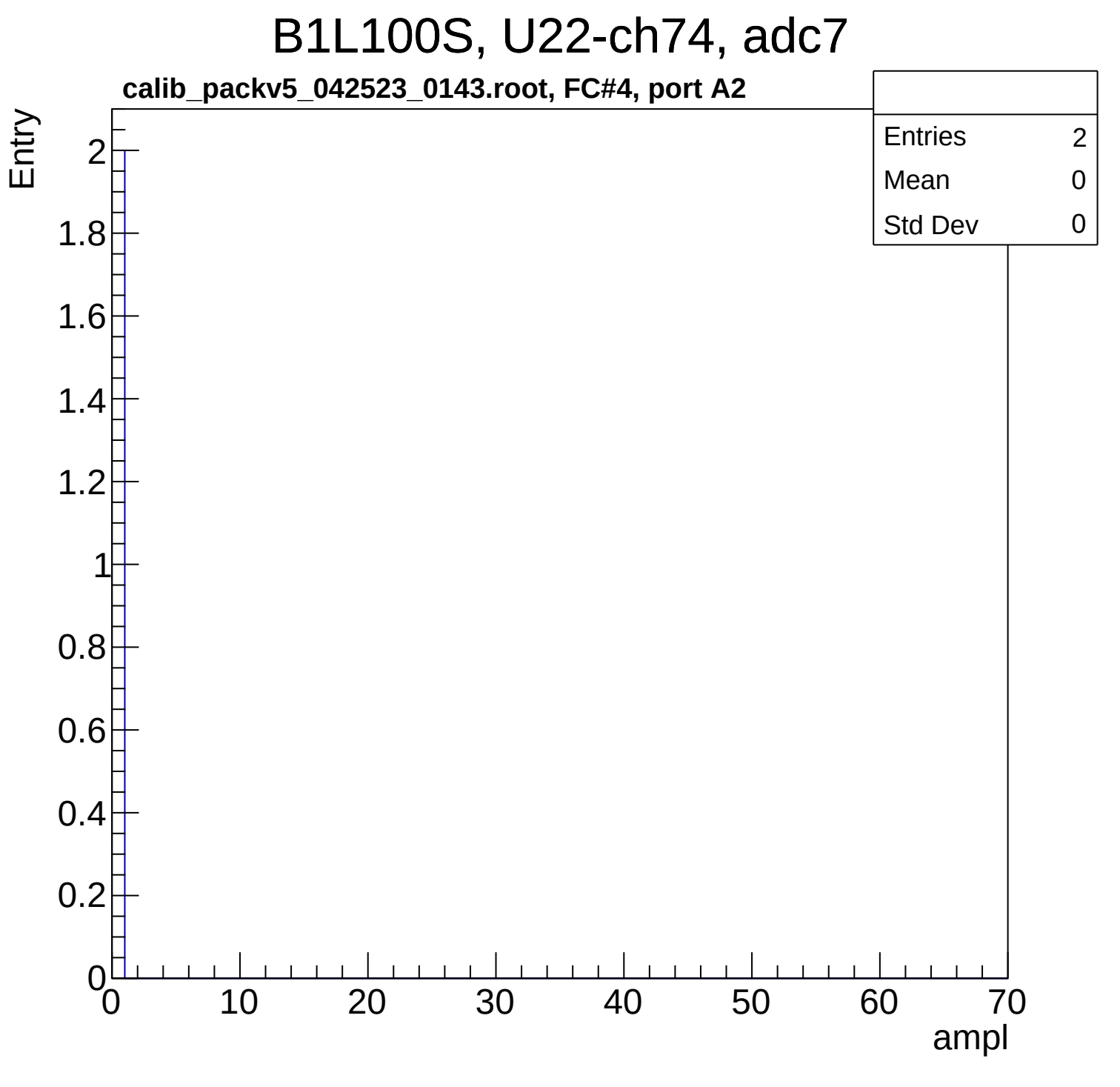

B1L100S, U22-ch74, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 43.65
Std Dev 3.808
Gaus mean : 43.8915
Gaus Width: 4.2898
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

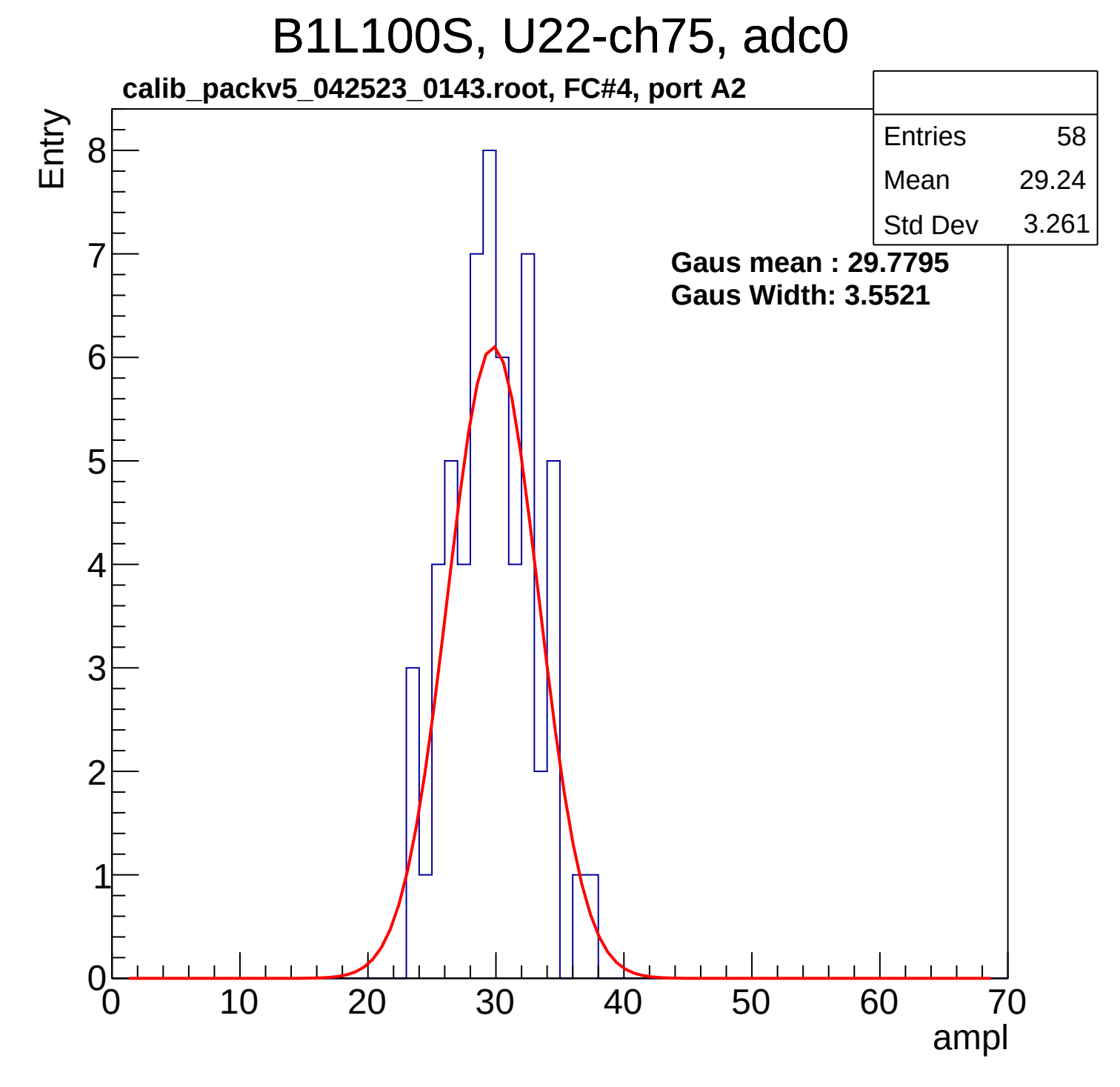

B1L100S, U22-ch74, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 50.52
Std Dev 3.122
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

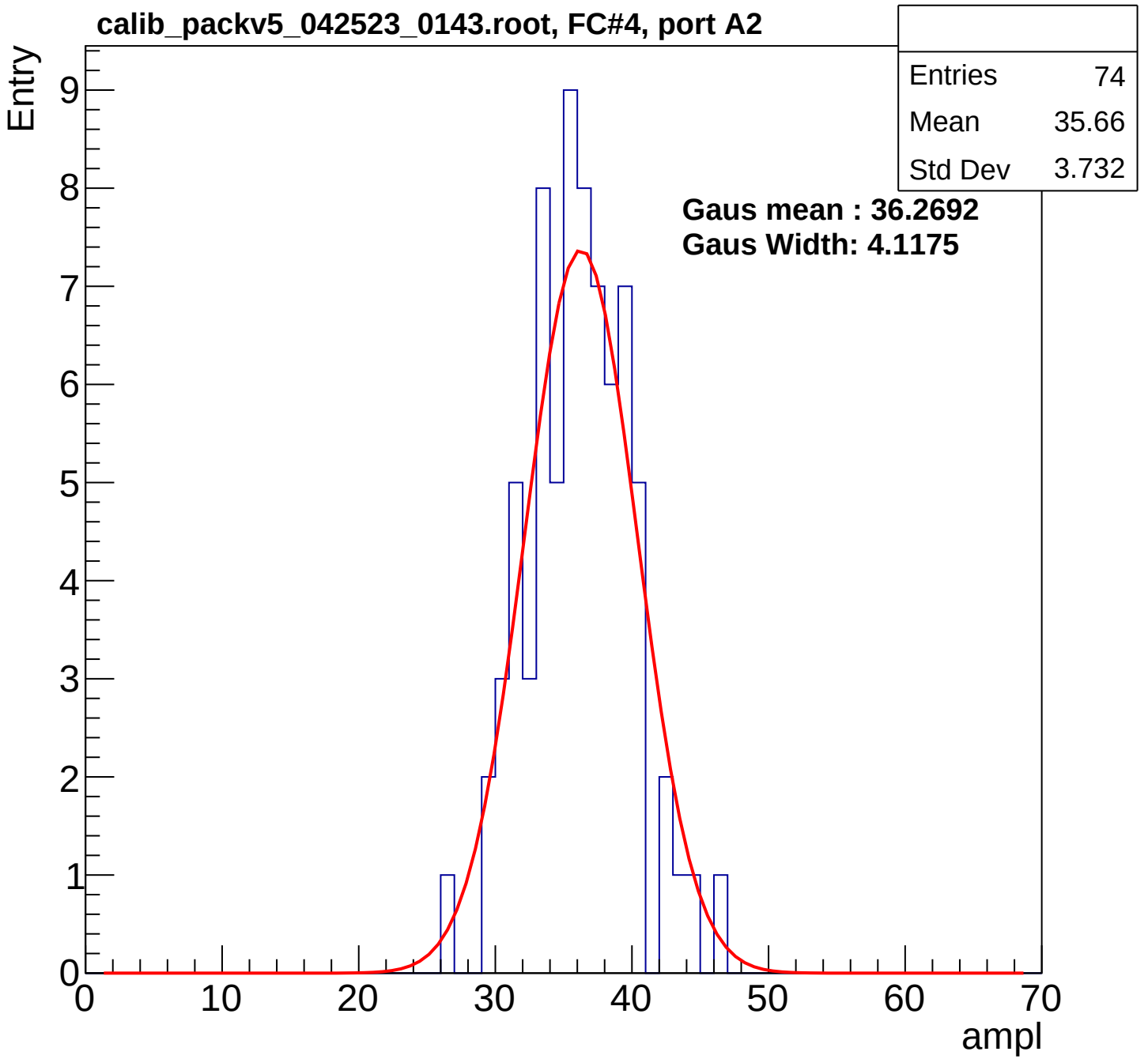

B1L100S, U22-ch74, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 55.9
Std Dev 3.363
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

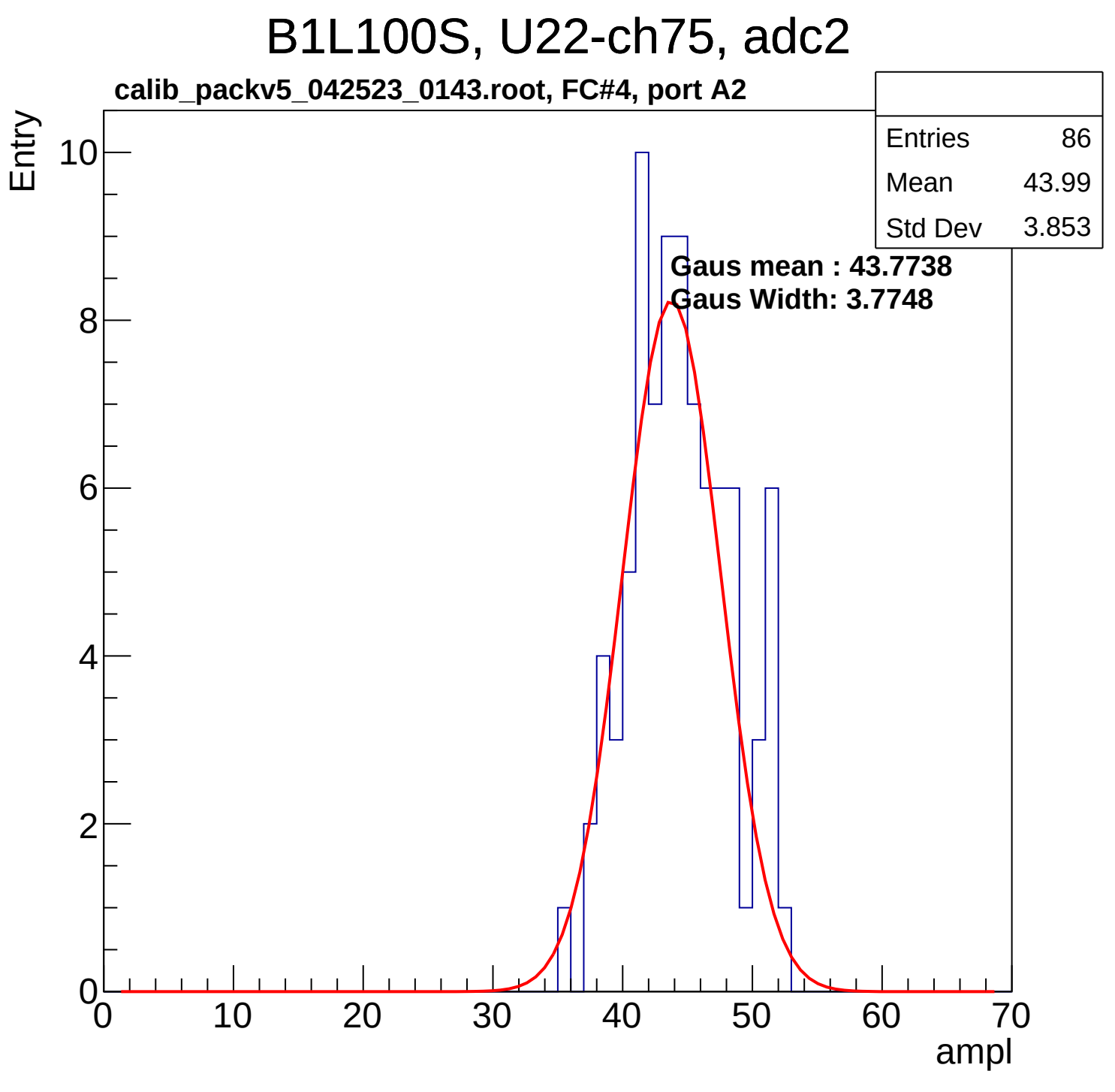

B1L100S, U22-ch74, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.58
Std Dev 9.643
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

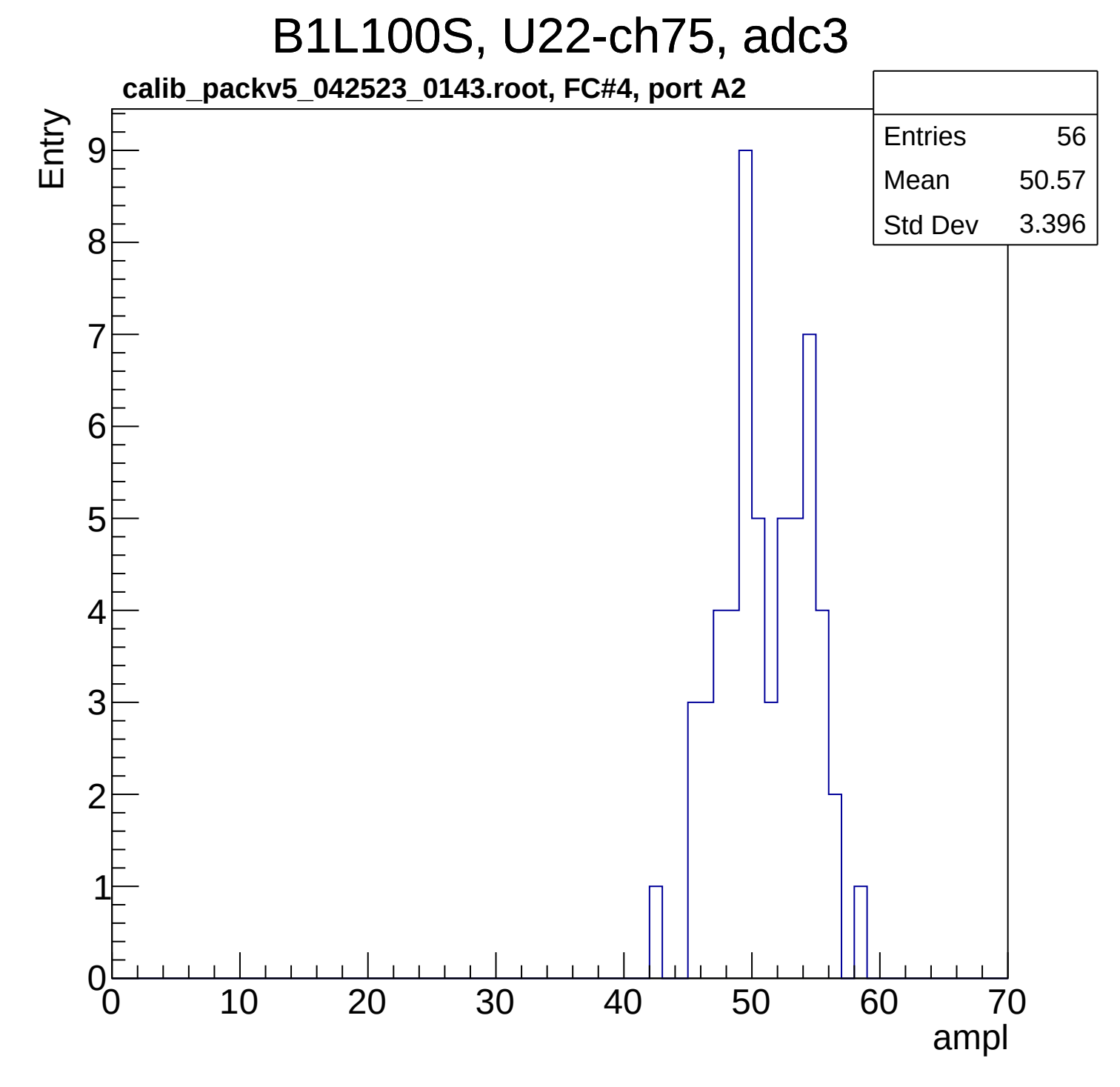

B1L100S, U22-ch74, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 0.833
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70

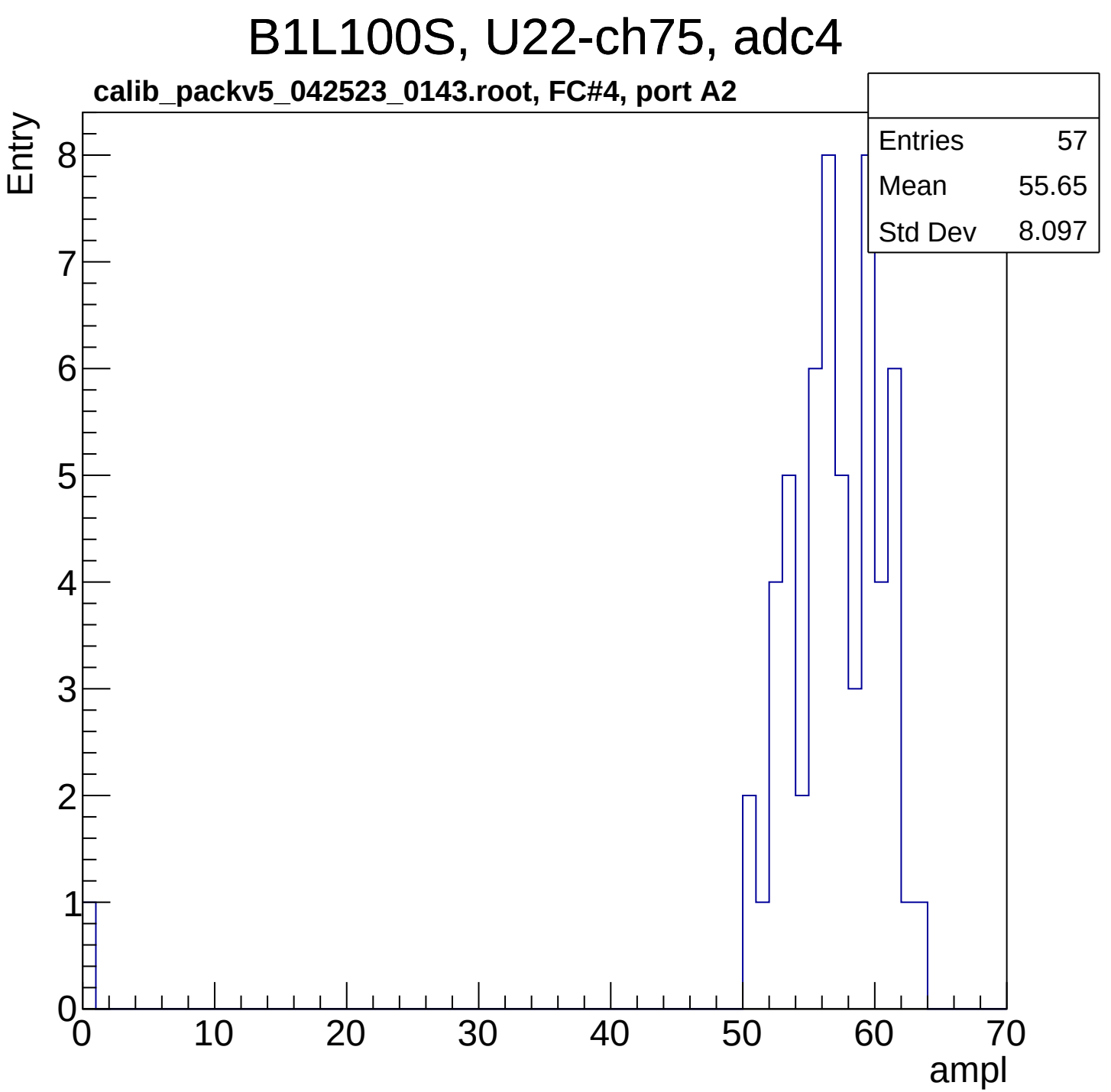

B1L100S, U22-ch74, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

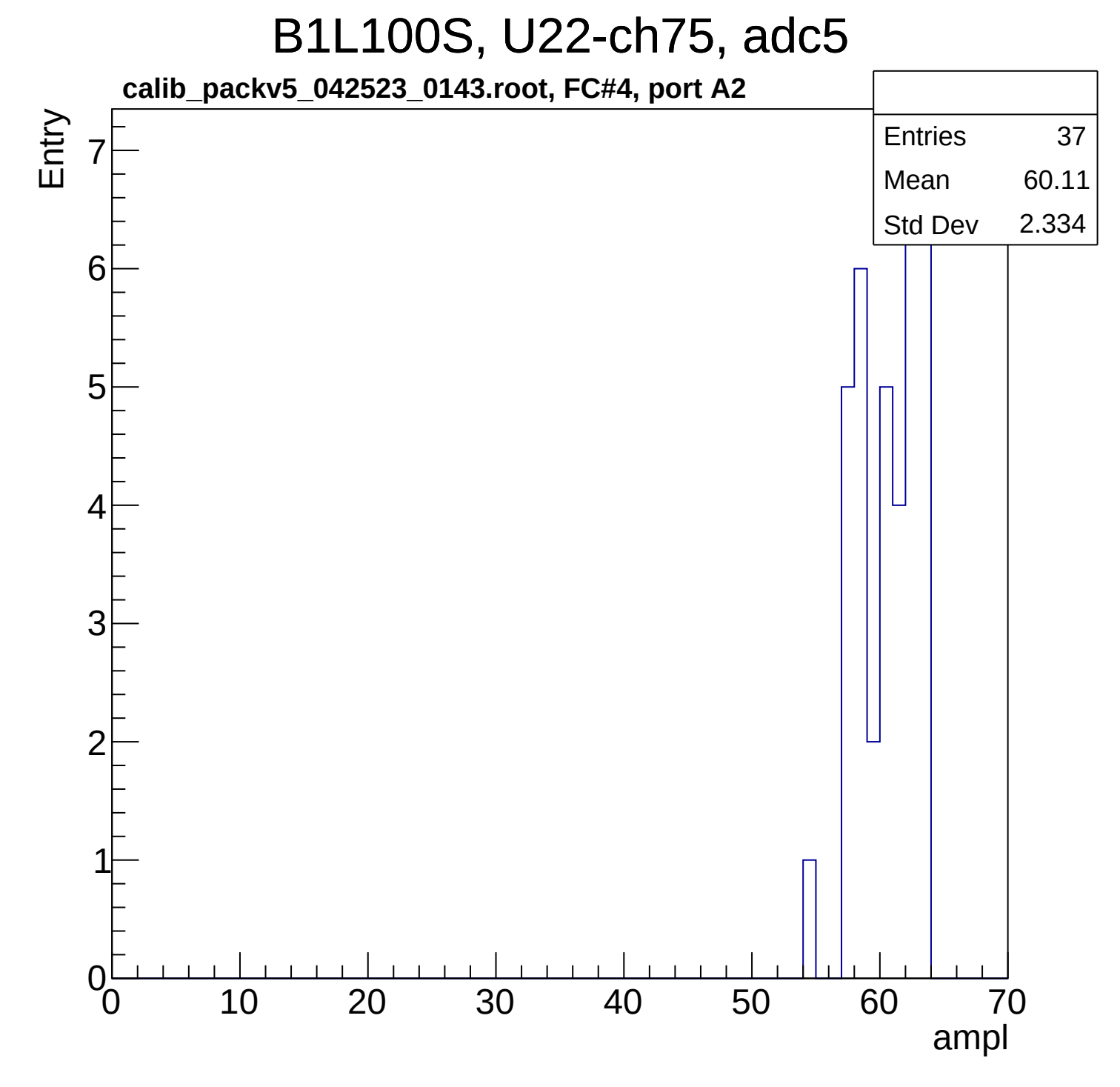

B1L100S, U22-ch75, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 29.24
Std Dev 3.261
Gaus mean : 29.7795
Gaus Width: 3.5521
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

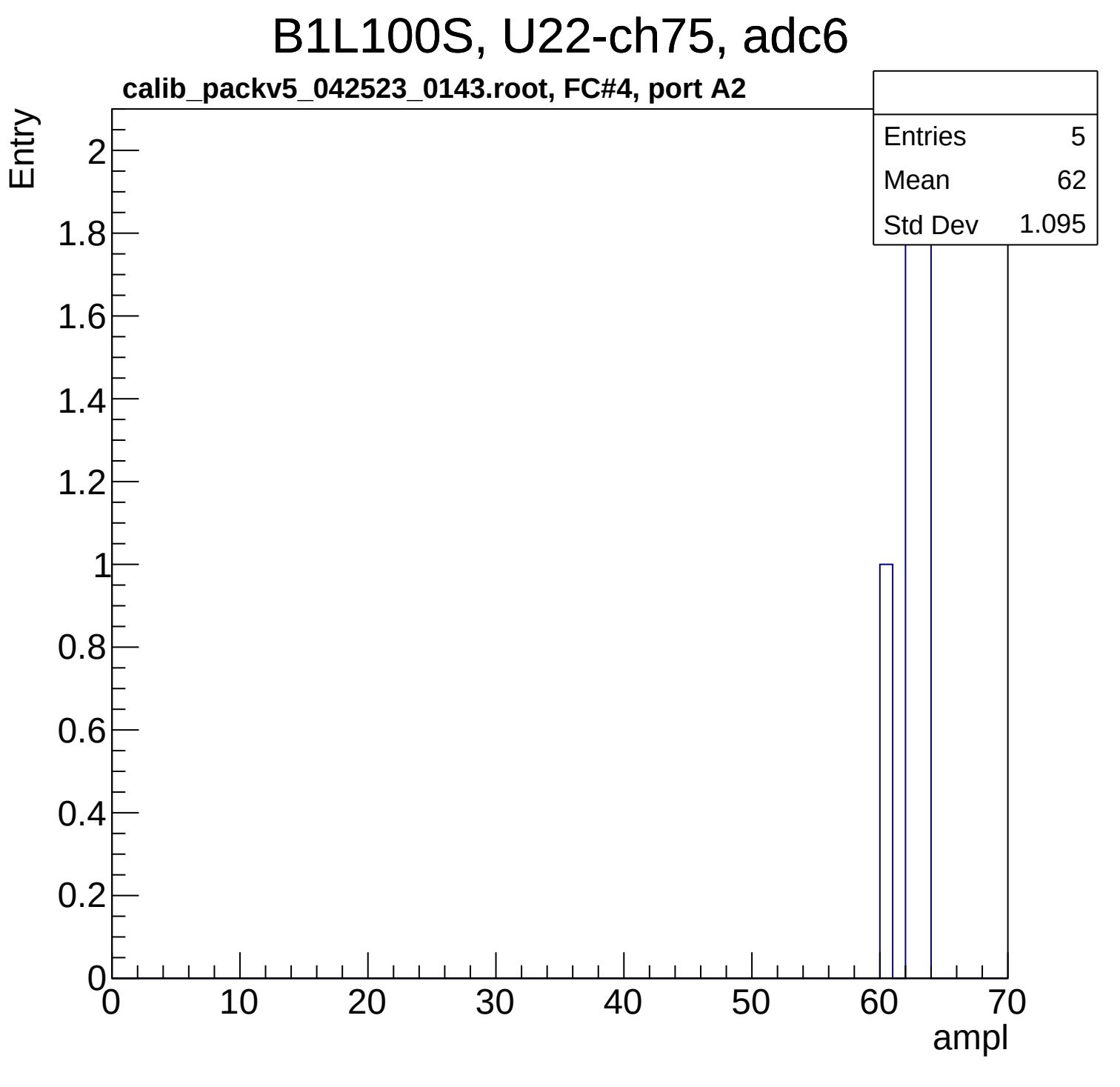

B1L100S, U22-ch75, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 35.66
Std Dev 3.732
Gaus mean : 36.2692
Gaus Width: 4.1175
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

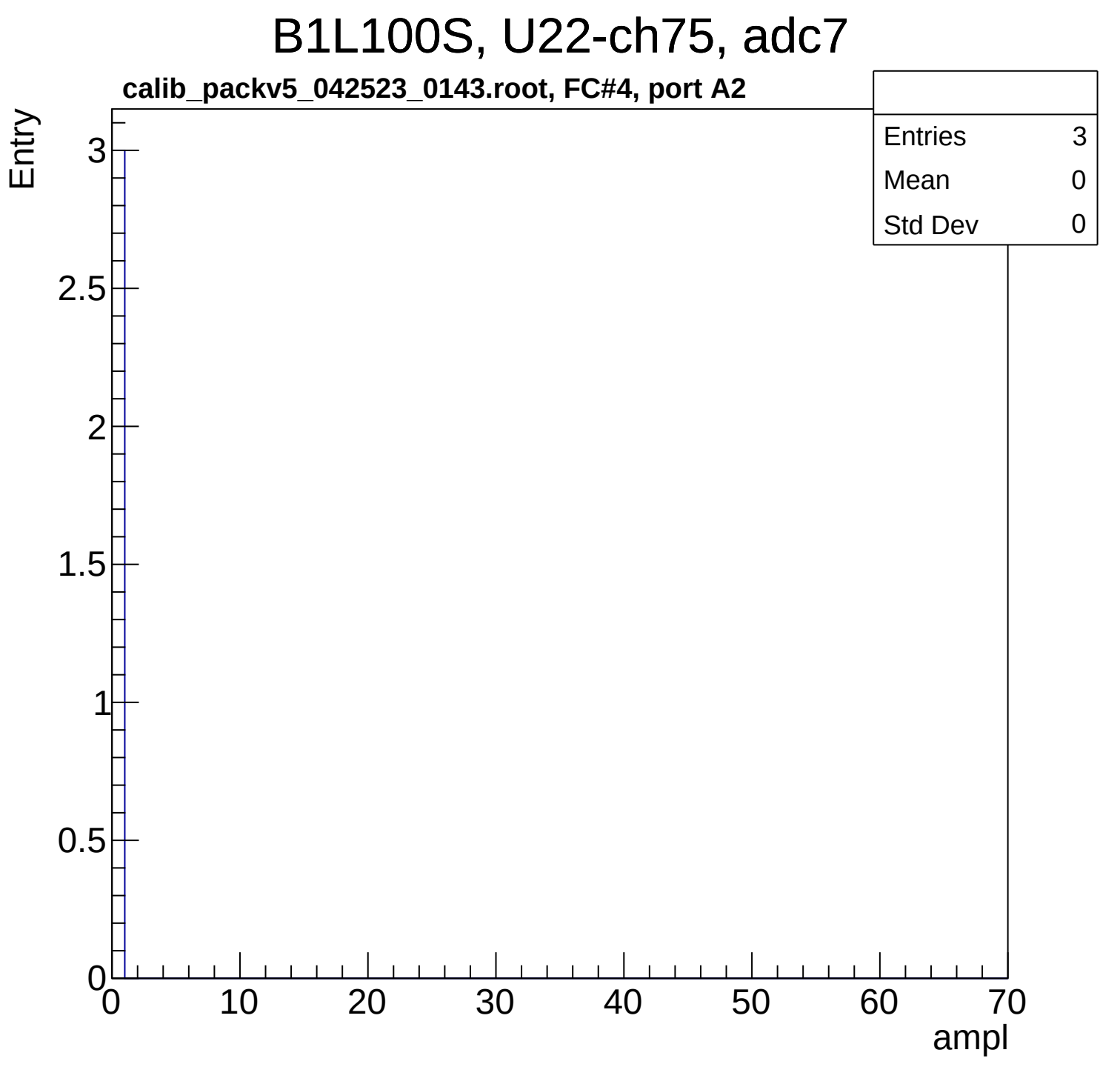

B1L100S, U22-ch75, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 86
Mean 43.99
Std Dev 3.853
Gaus mean : 43.7738
Gaus Width: 3.7748
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

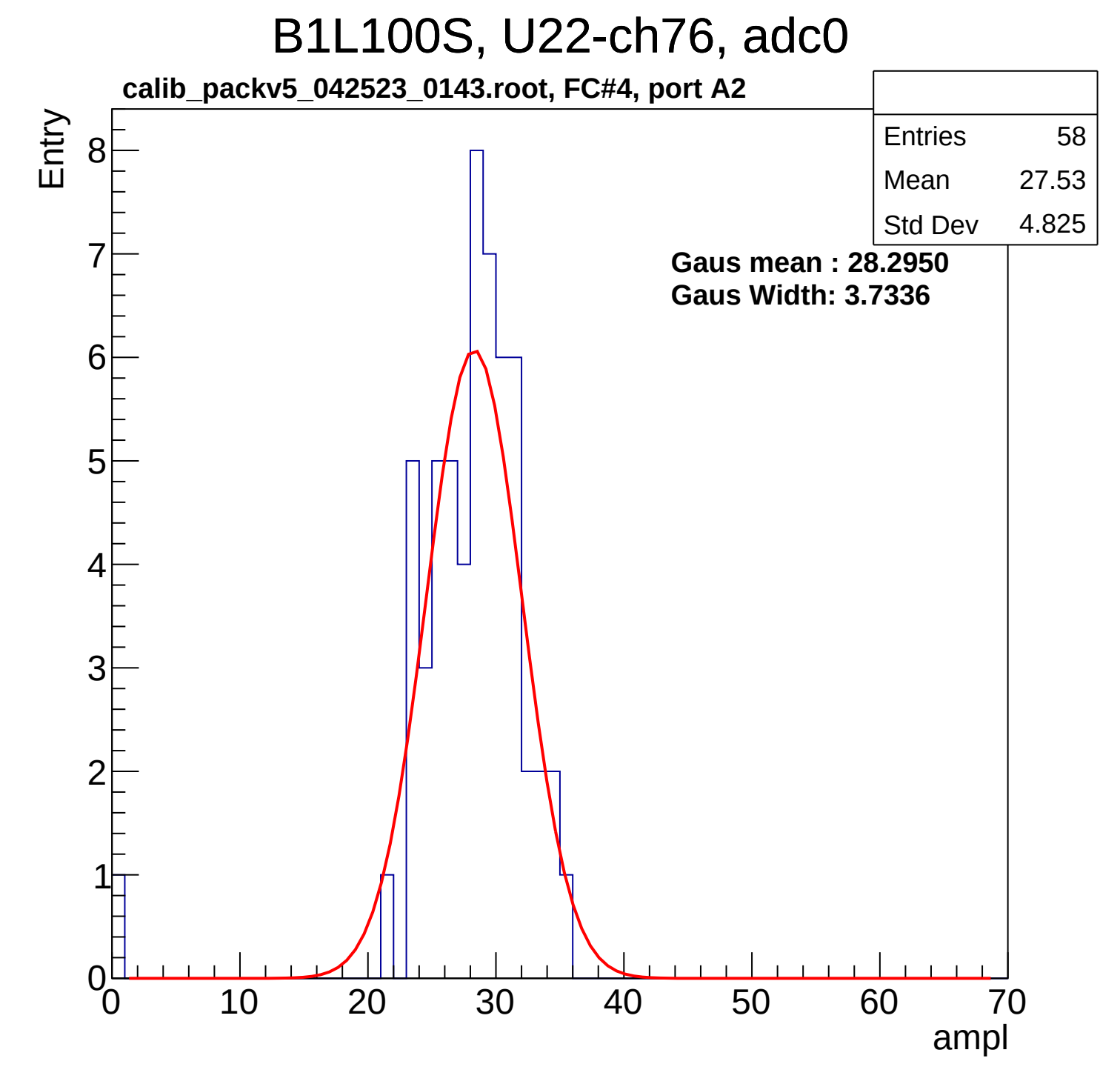

B1L100S, U22-ch75, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 50.57
Std Dev 3.396
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

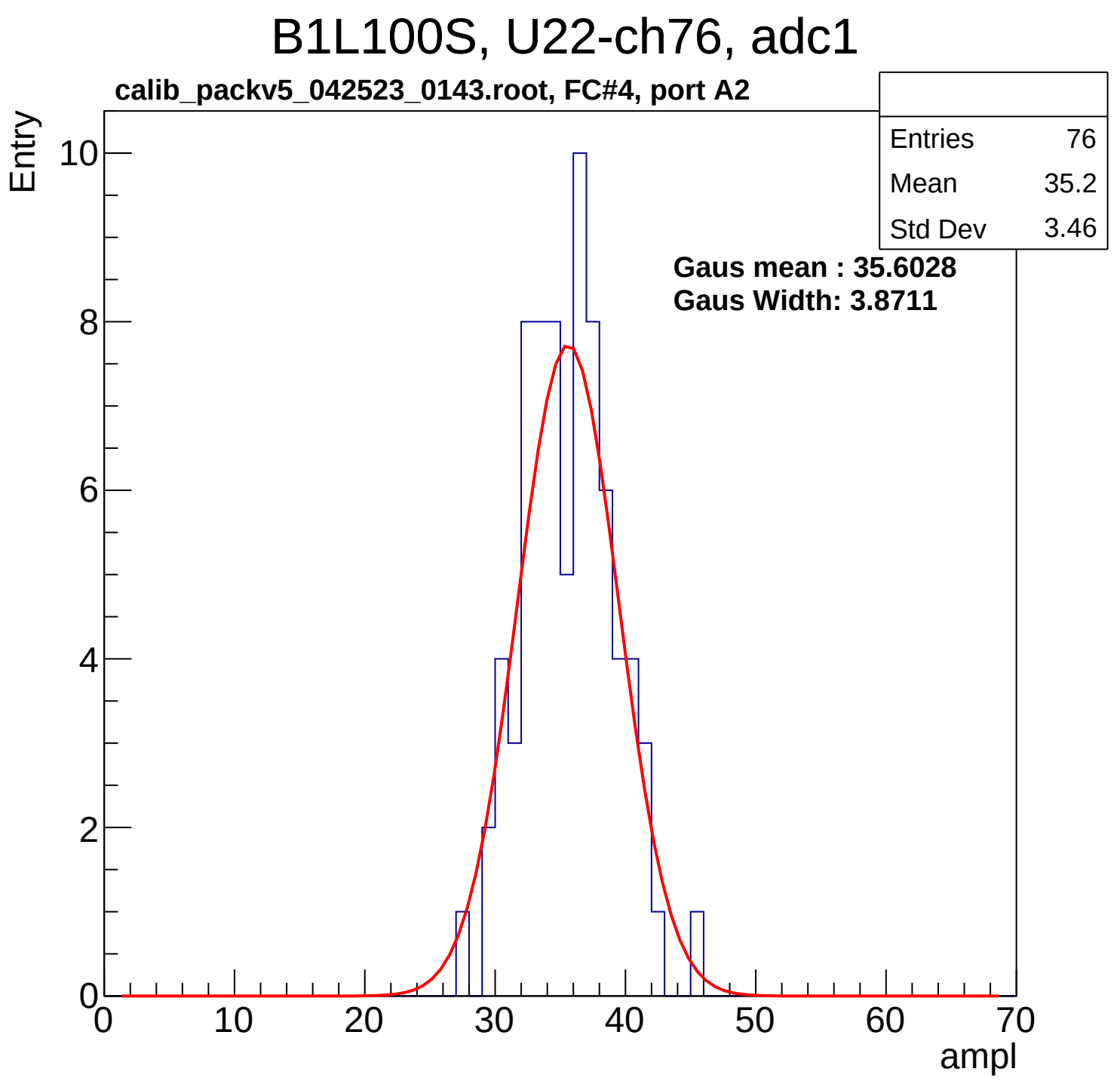

B1L100S, U22-ch75, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 55.65
Std Dev 8.097
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

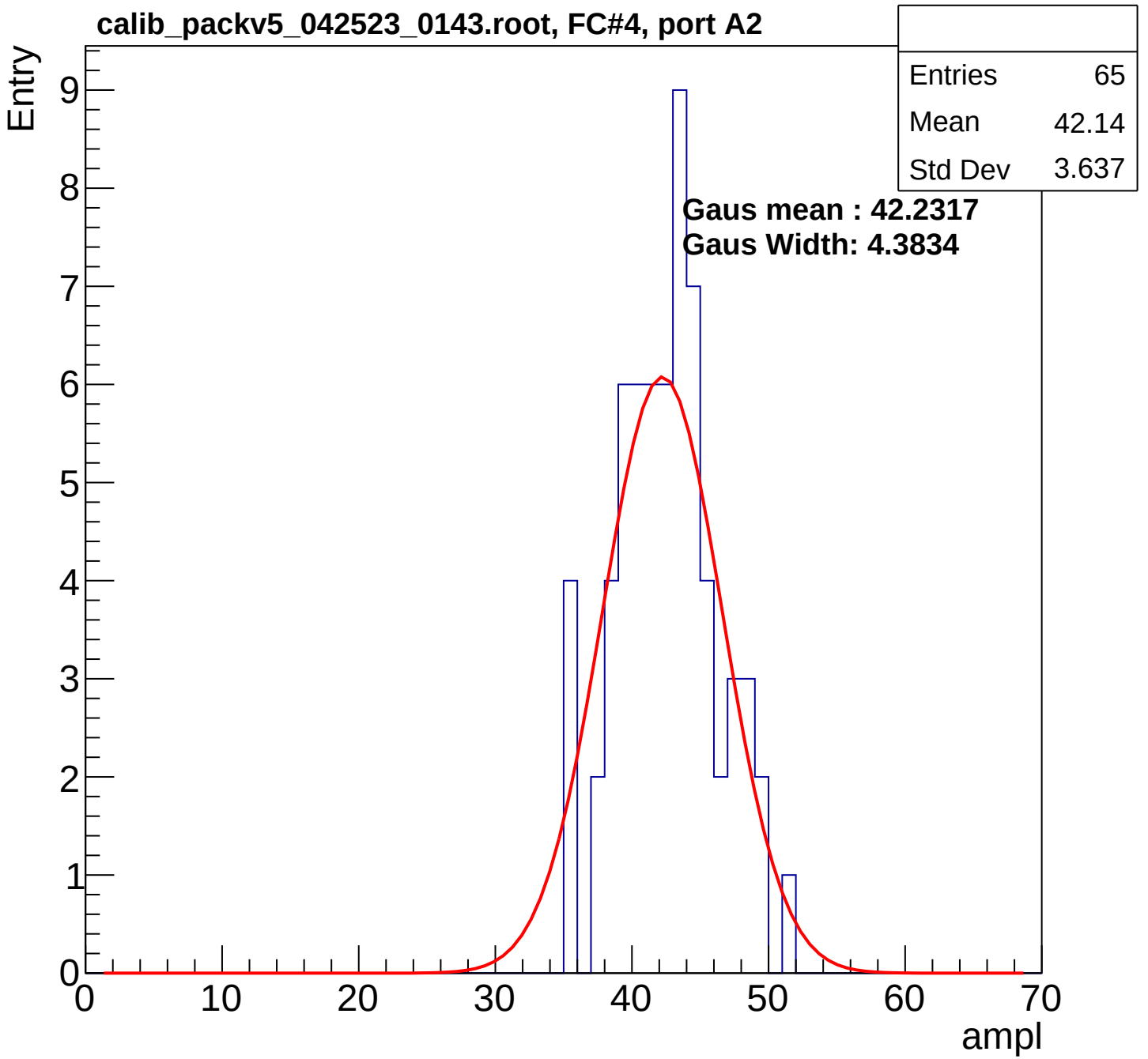

B1L100S, U22-ch75, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.11
Std Dev 2.334
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

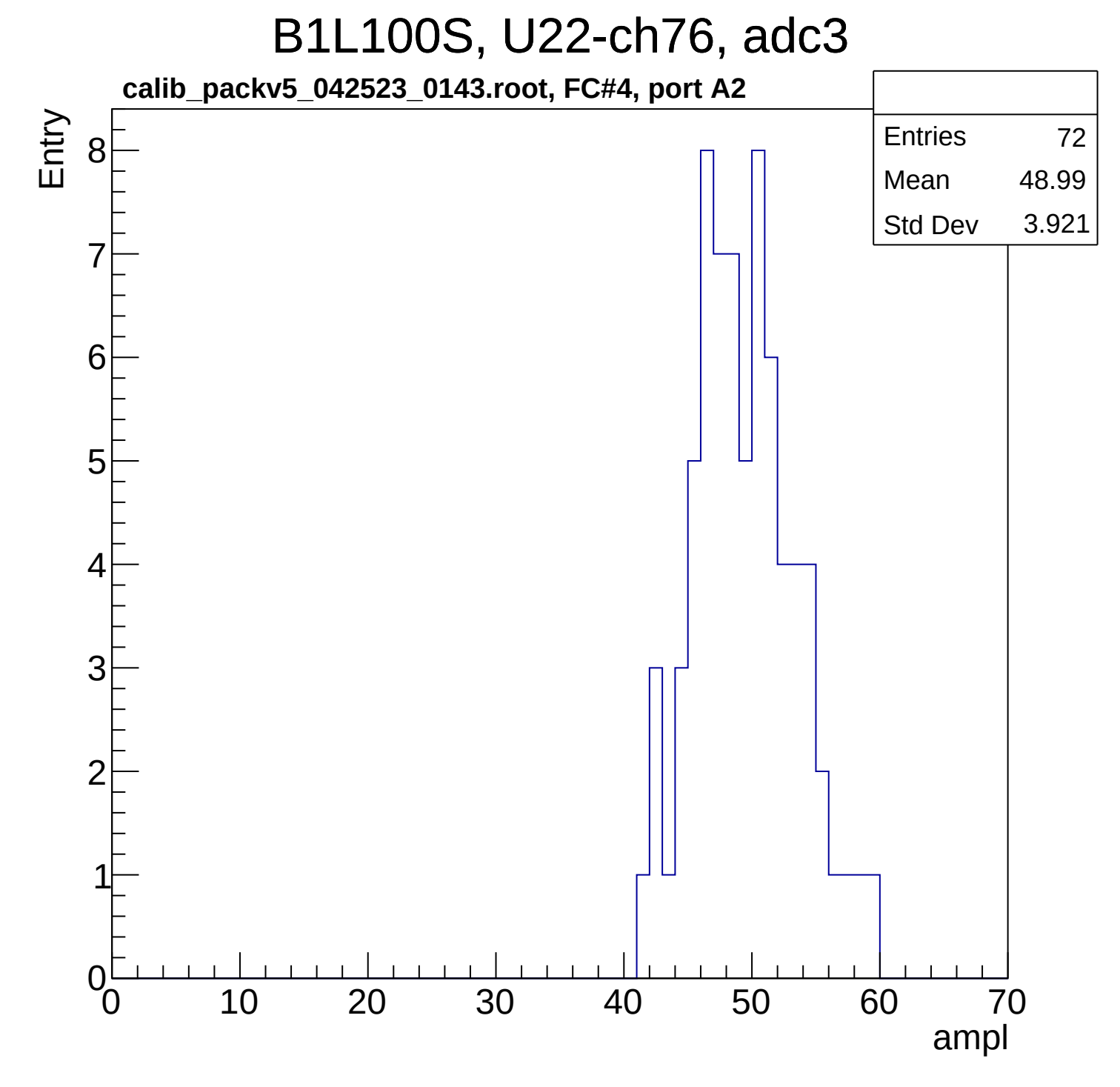

B1L100S, U22-ch75, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62
Std Dev 1.095
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

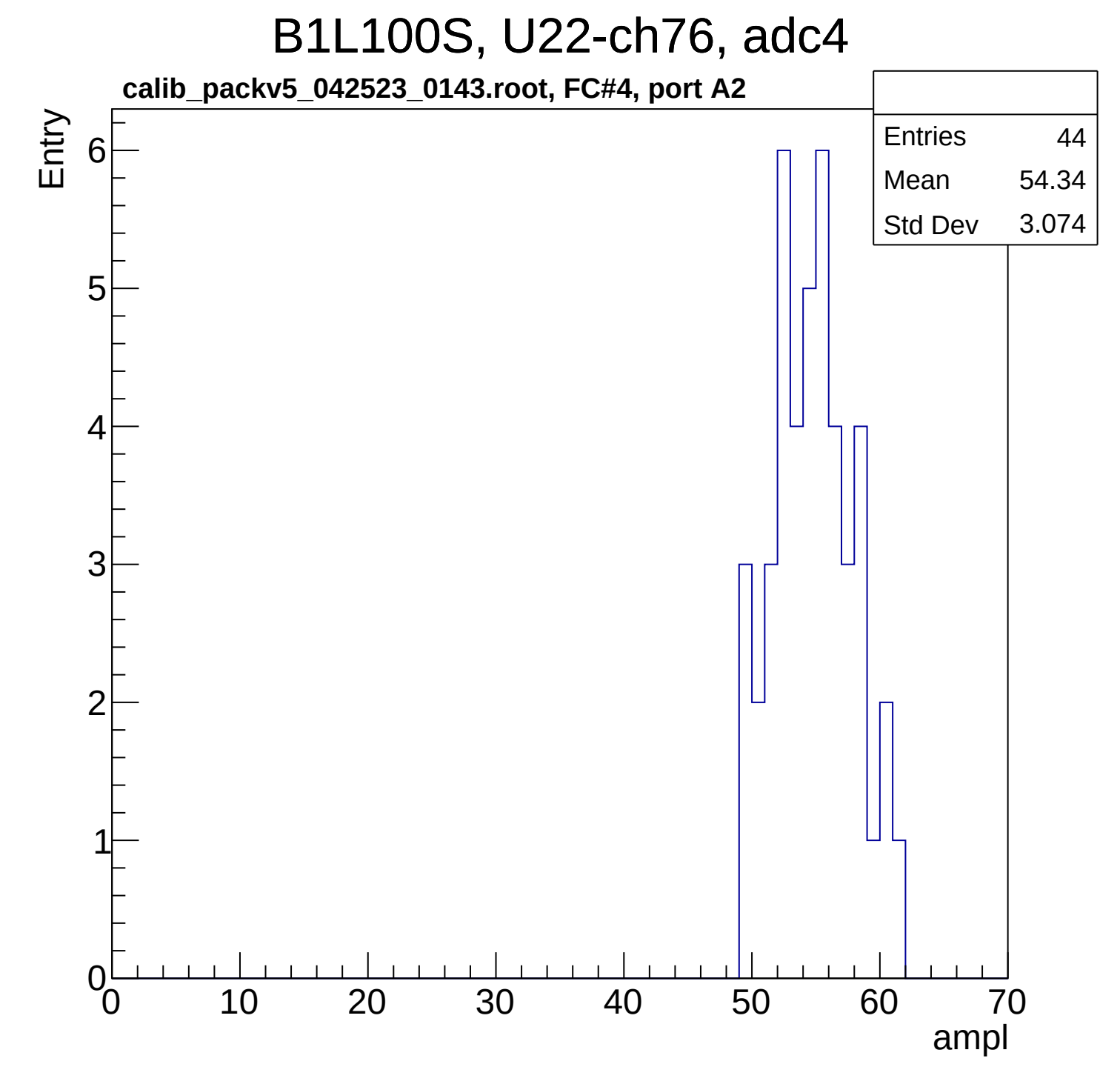

B1L100S, U22-ch75, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
3
2.5
2
1.5
1
0.5
0
0
10
20
30
40
50
60
70
ampl

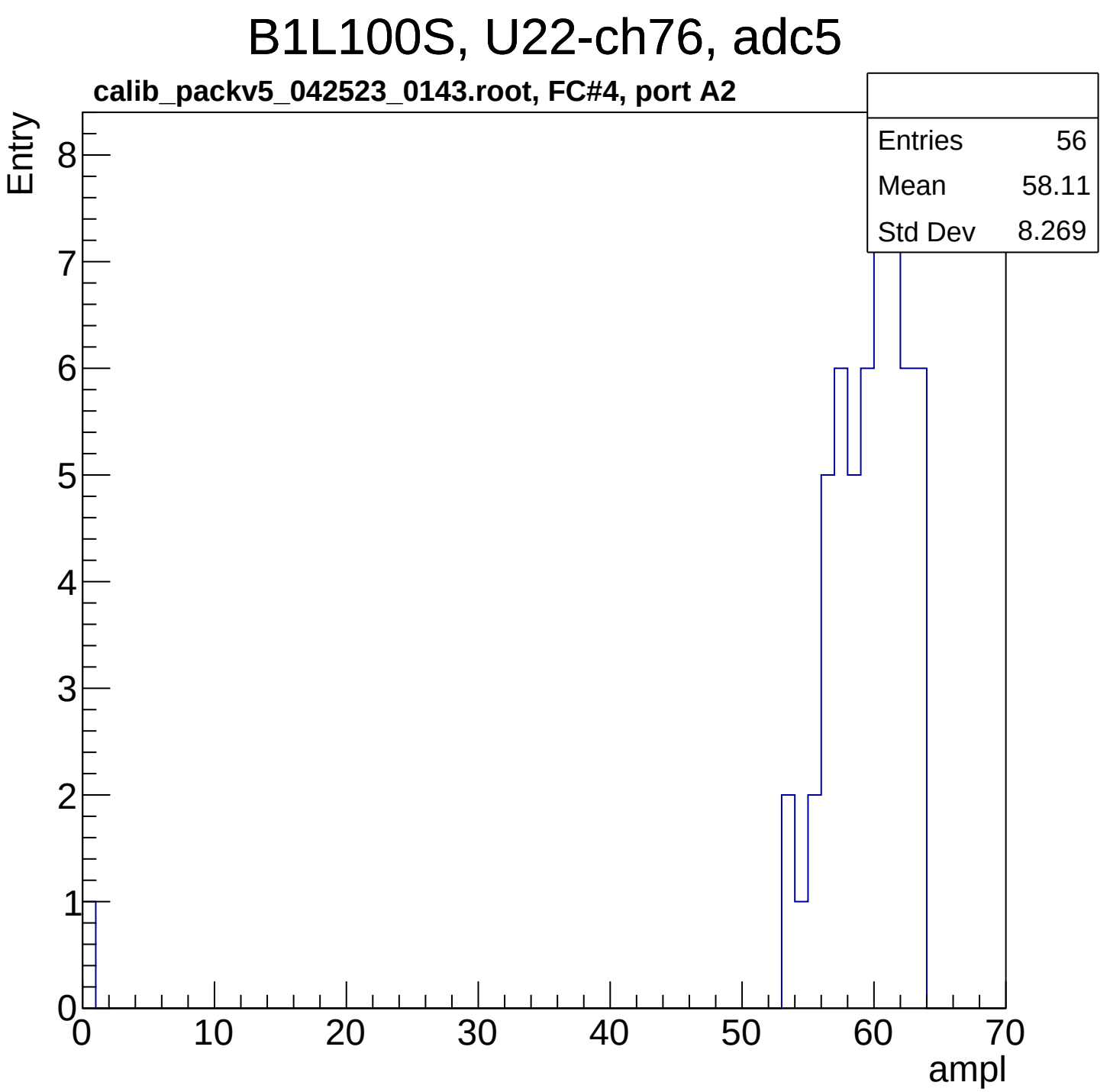

B1L100S, U22-ch76, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 27.53
Std Dev 4.825
Gaus mean : 28.2950
Gaus Width: 3.7336
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

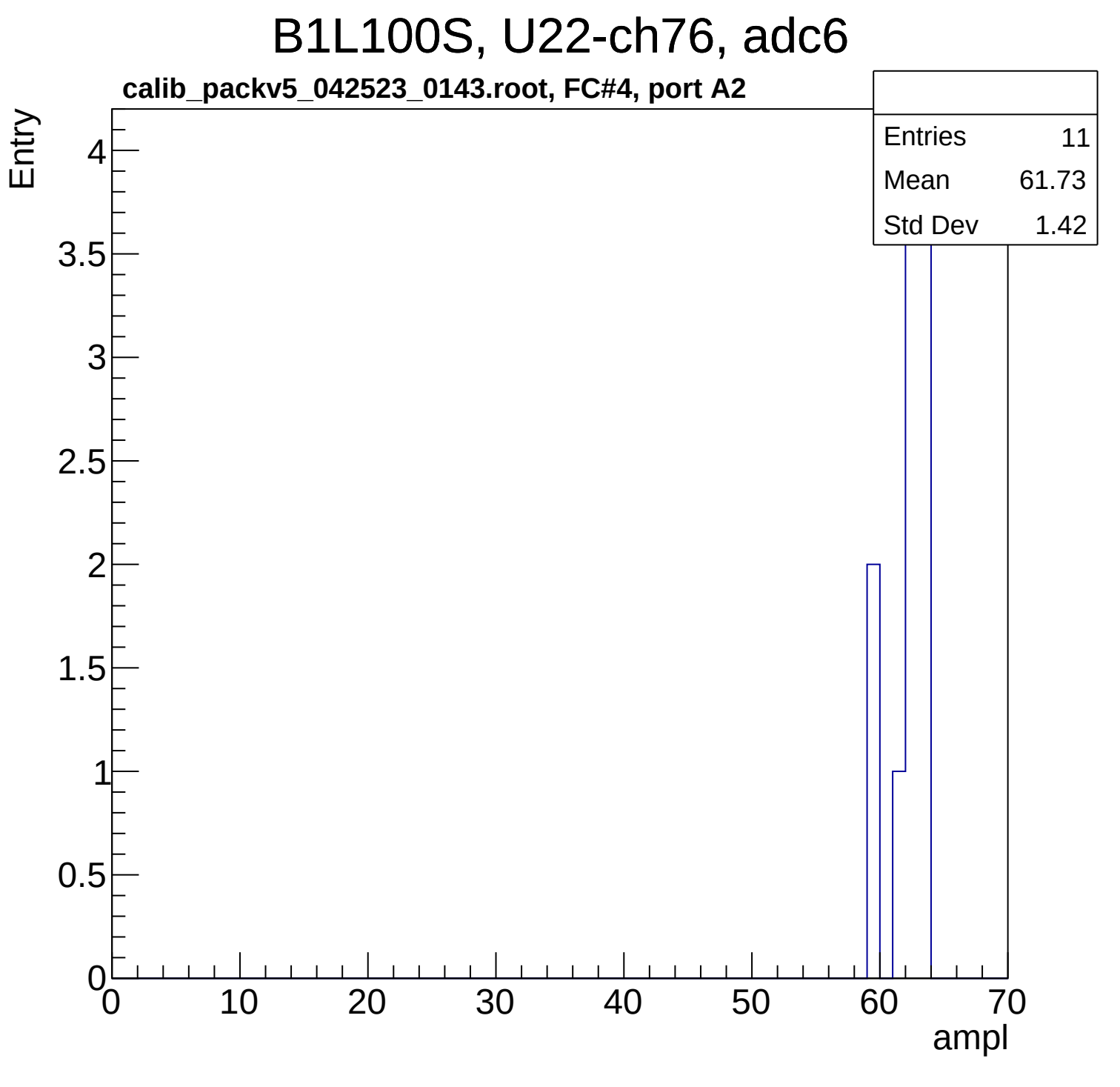

B1L100S, U22-ch76, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.2
Std Dev 3.46
Gaus mean : 35.6028
Gaus Width: 3.8711
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70

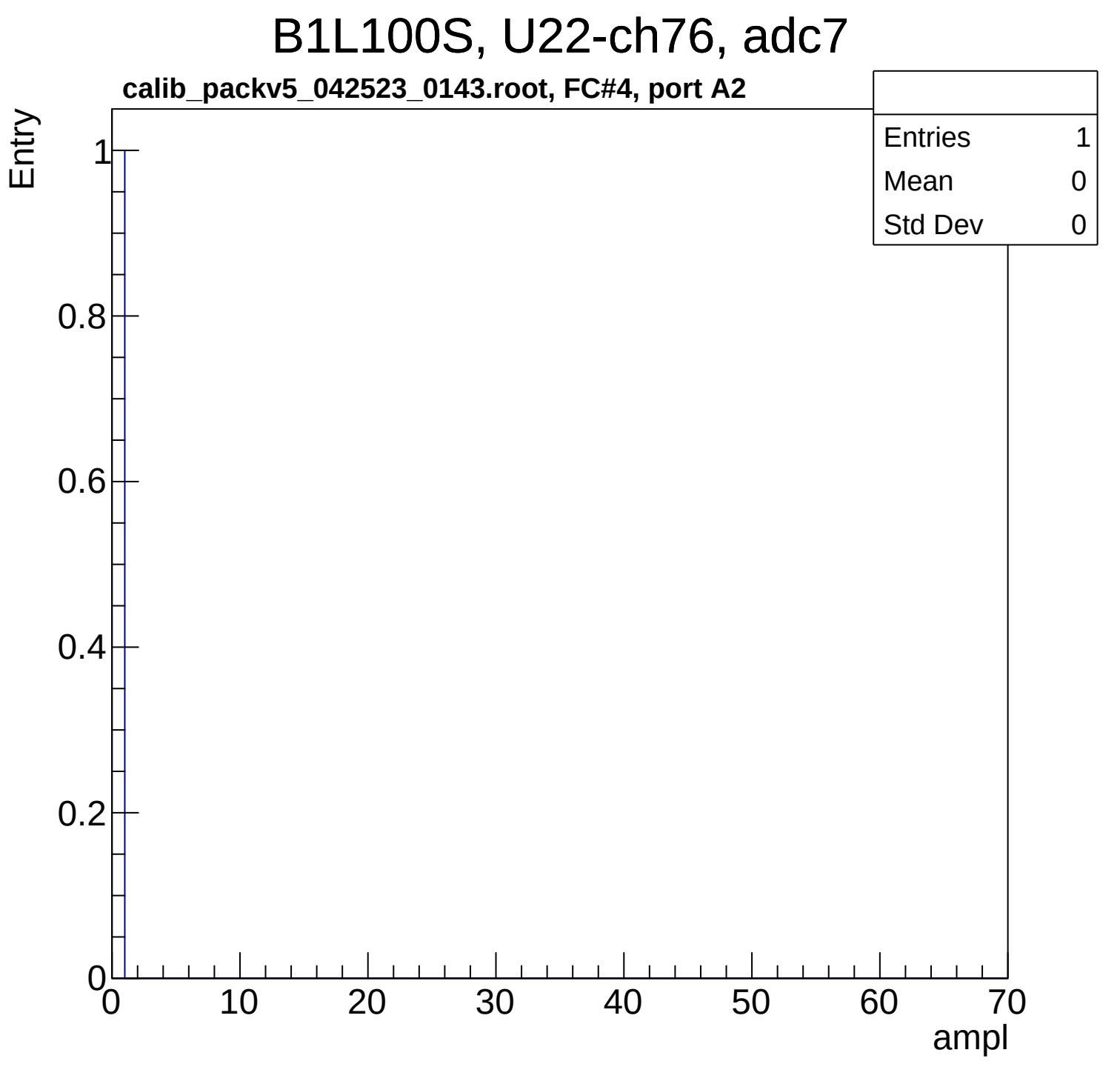

B1L100S, U22-ch76, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 42.14
Std Dev 3.637
Gaus mean : 42.2317
Gaus Width: 4.3834
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch76, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 48.99
Std Dev 3.921
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch76, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 54.34
Std Dev 3.074
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch76, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.11
Std Dev 8.269
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch76, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.73
Std Dev 1.42
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch76, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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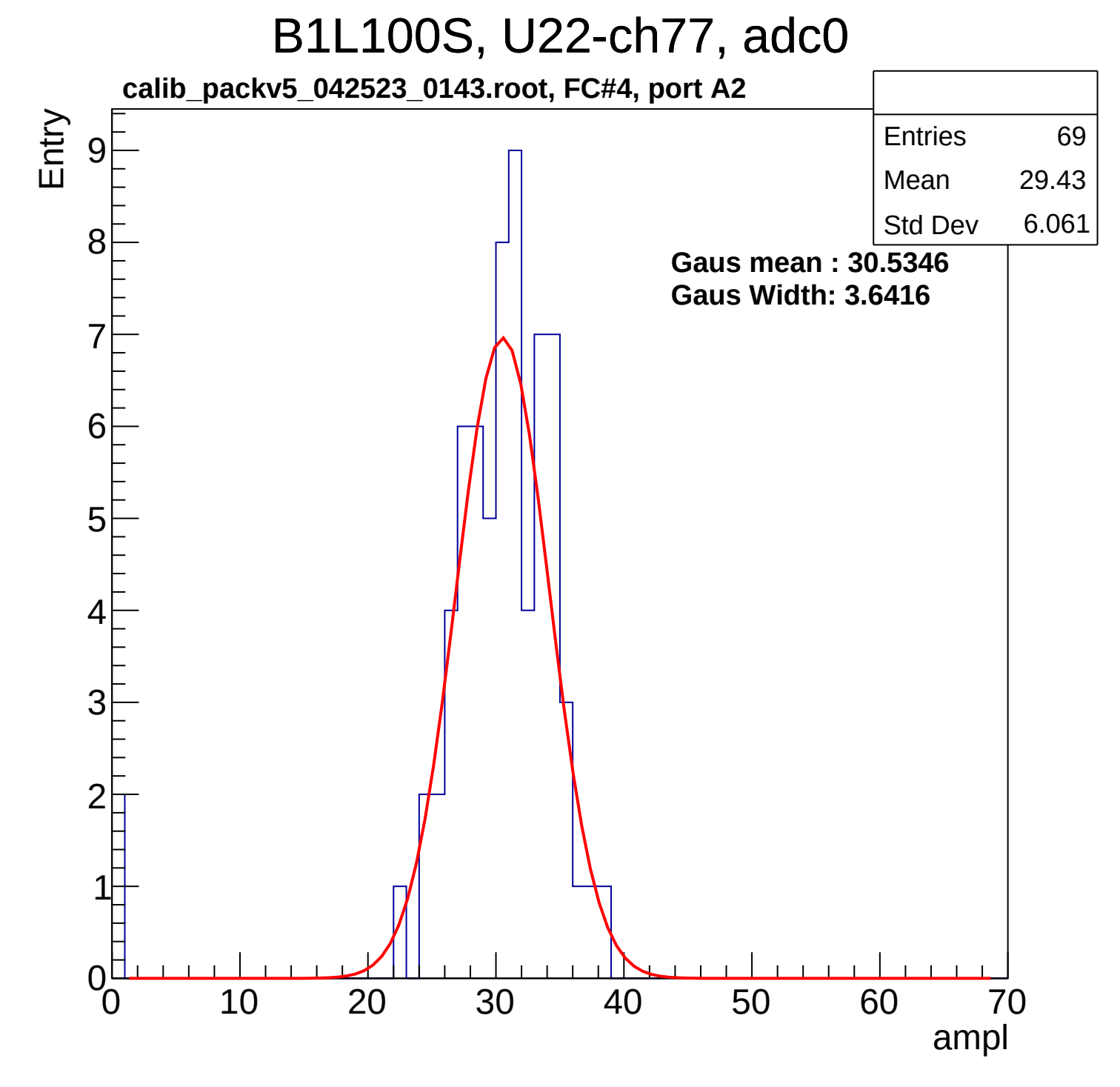
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch77, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.43
Std Dev 6.061
Gaus mean : 30.5346
Gaus Width: 3.6416
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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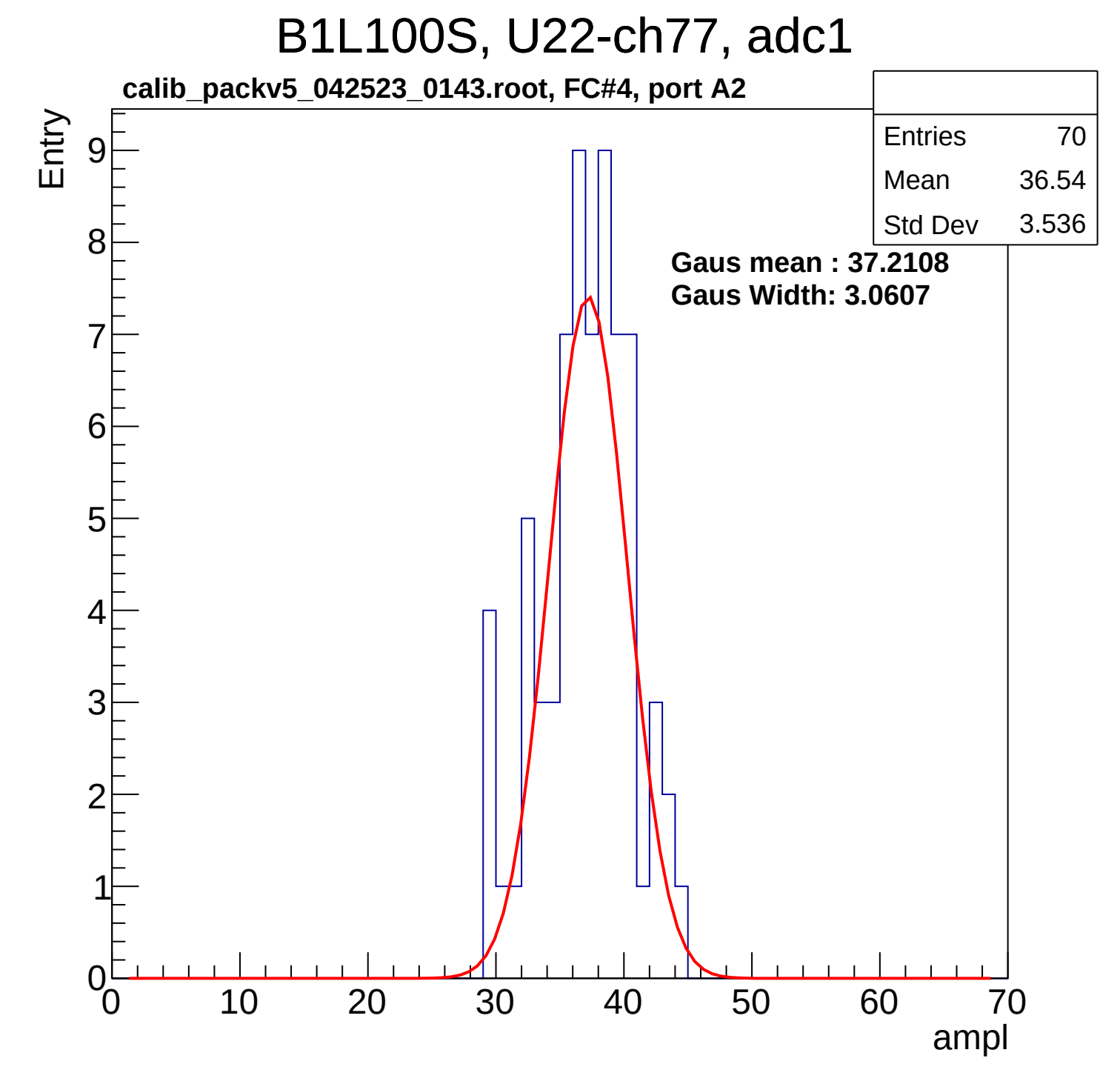
B1L100S, U22-ch77, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 36.54
Std Dev 3.536
Gaus mean : 37.2108
Gaus Width: 3.0607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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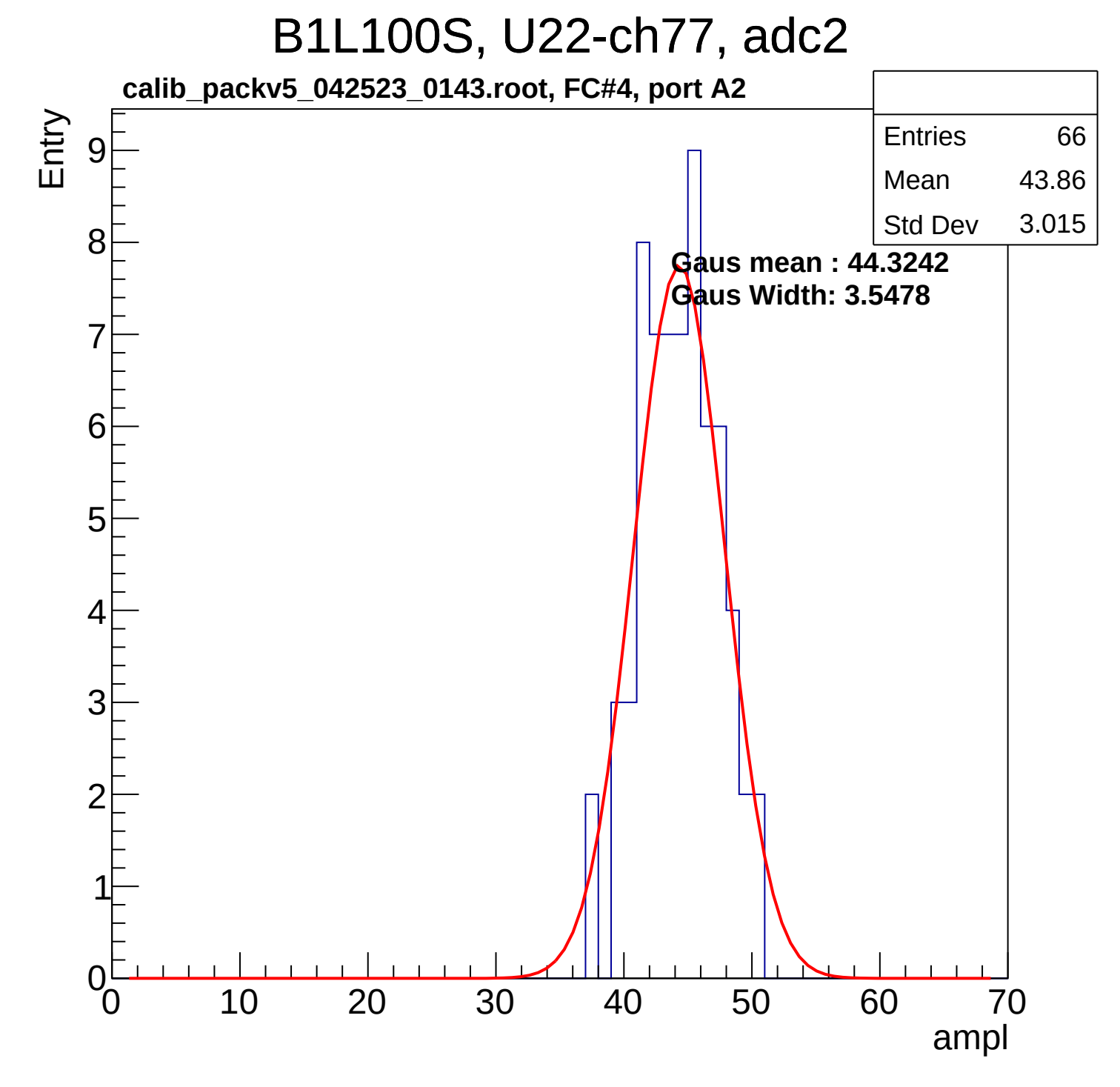
B1L100S, U22-ch77, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.86
Std Dev 3.015
Gaus mean : 44.3242
Gaus Width: 3.5478
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
30
40
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60
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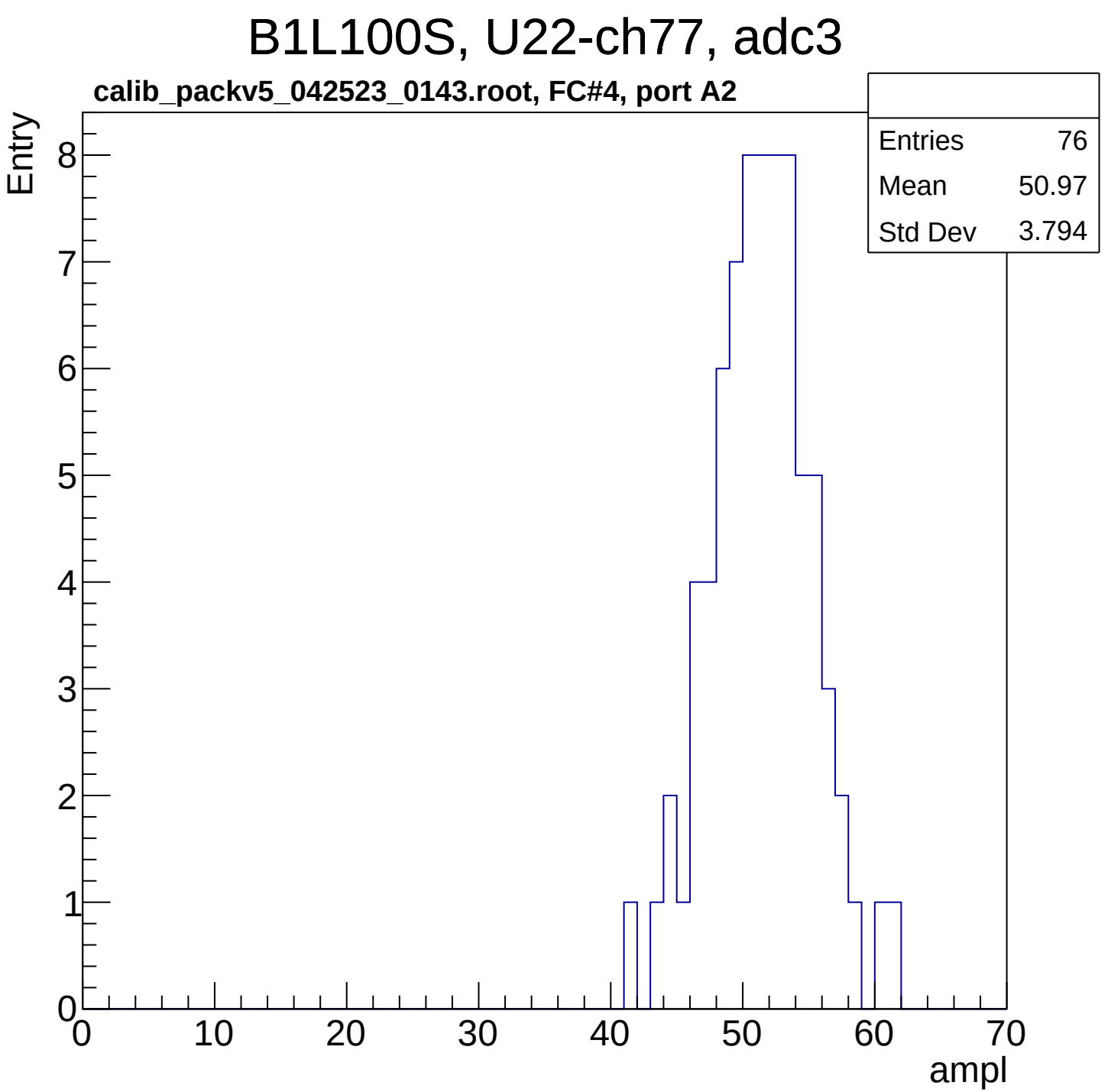
B1L100S, U22-ch77, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 50.97
Std Dev 3.794
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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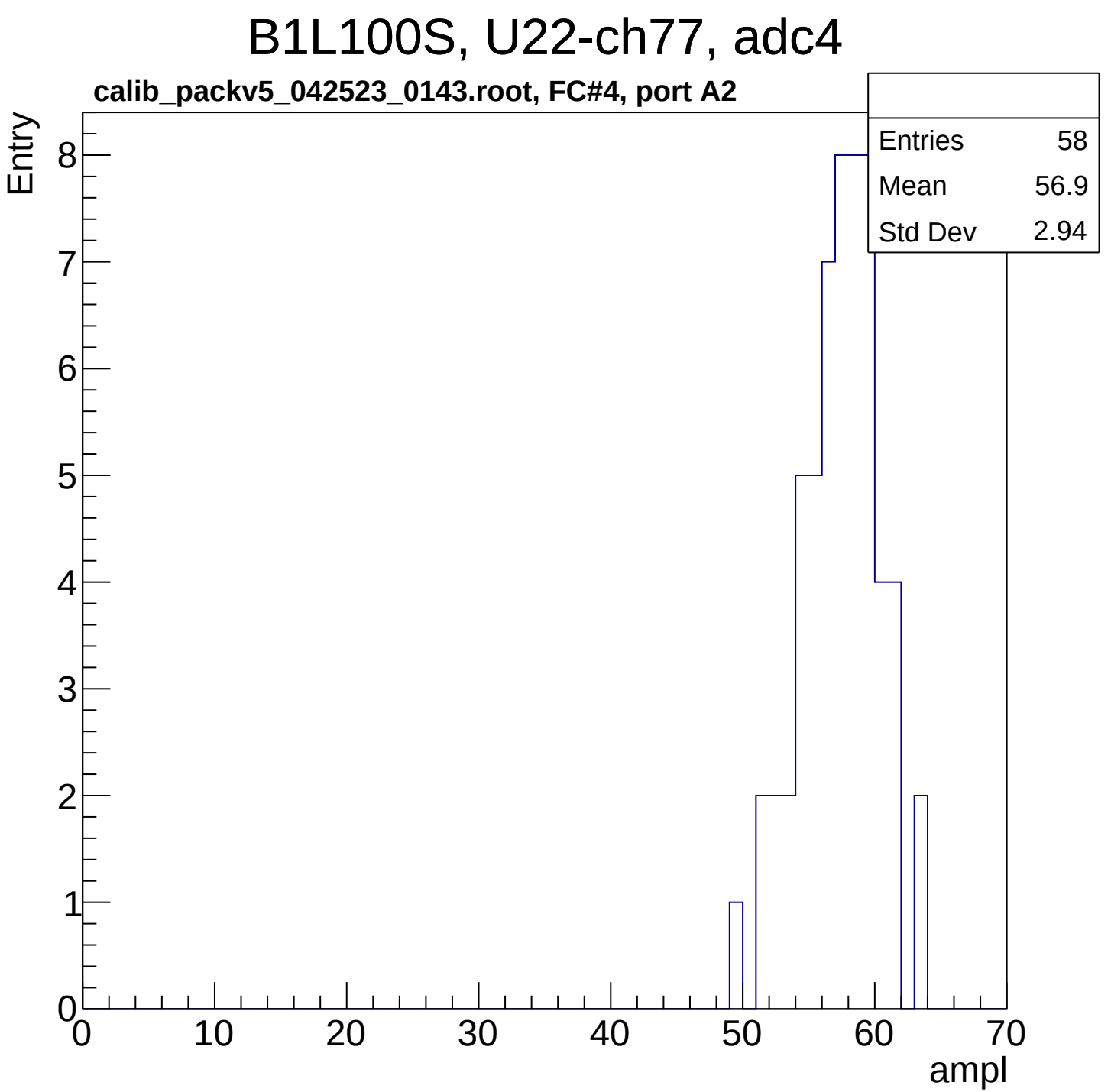
B1L100S, U22-ch77, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 56.9
Std Dev 2.94
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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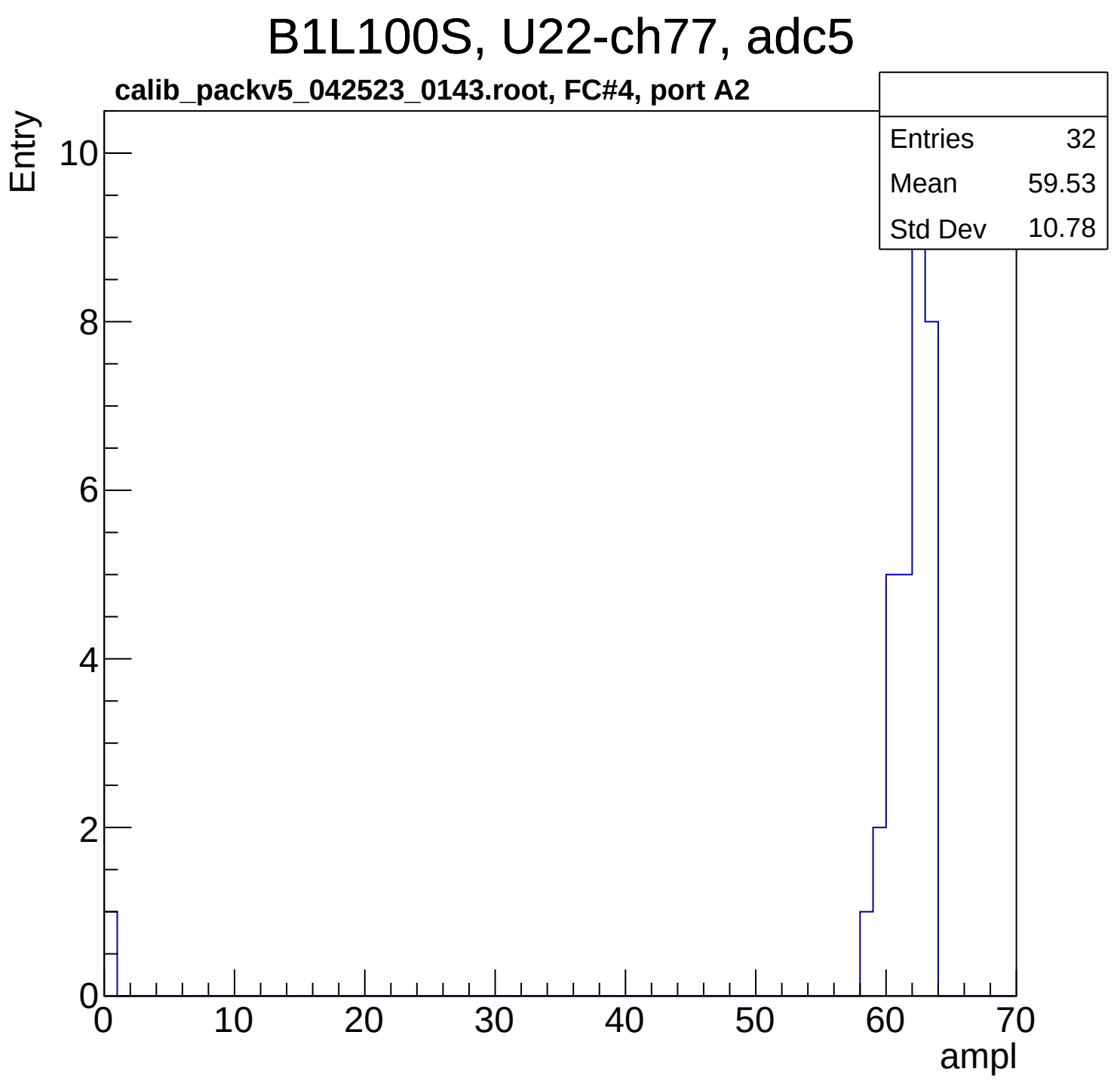
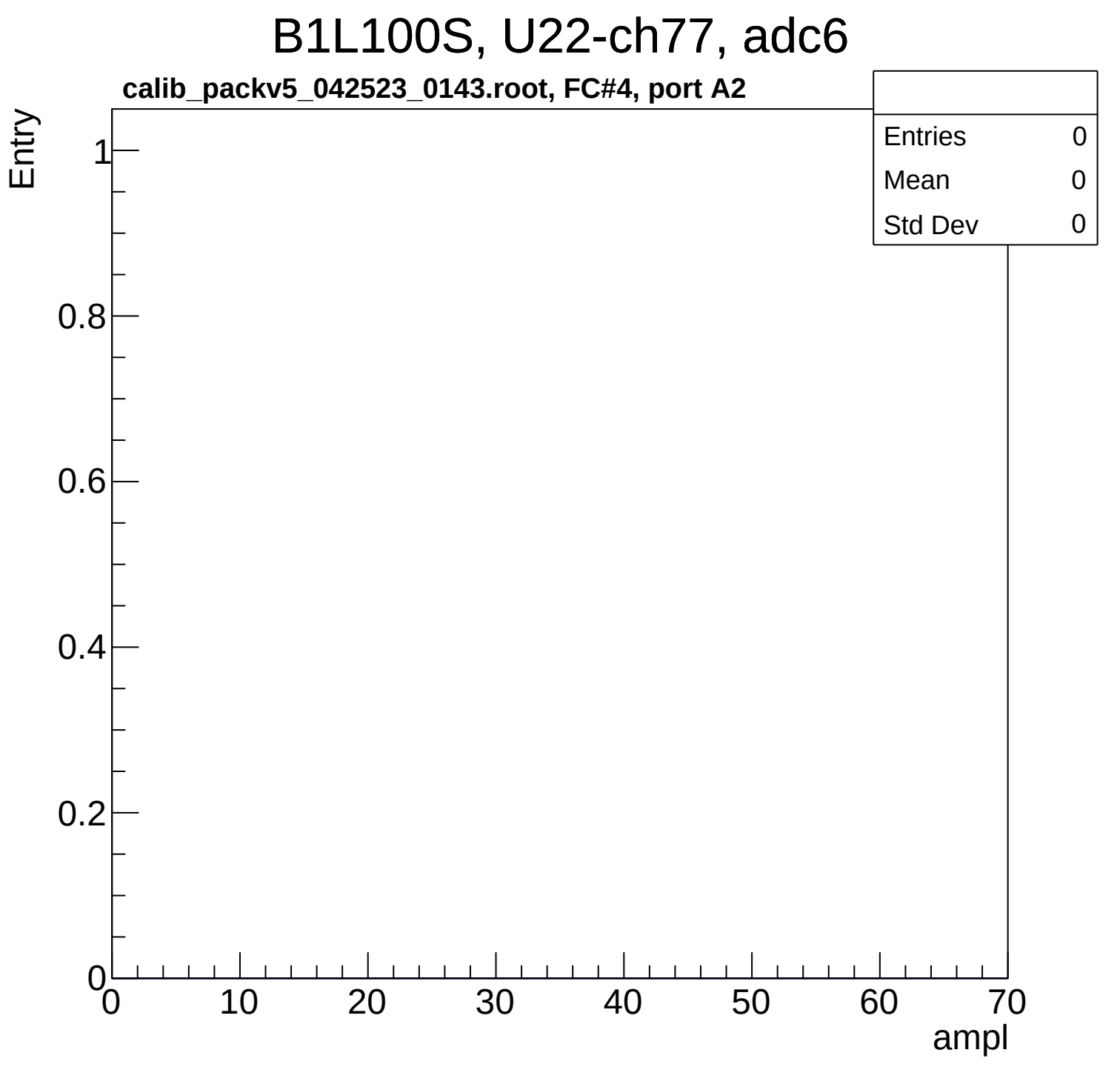
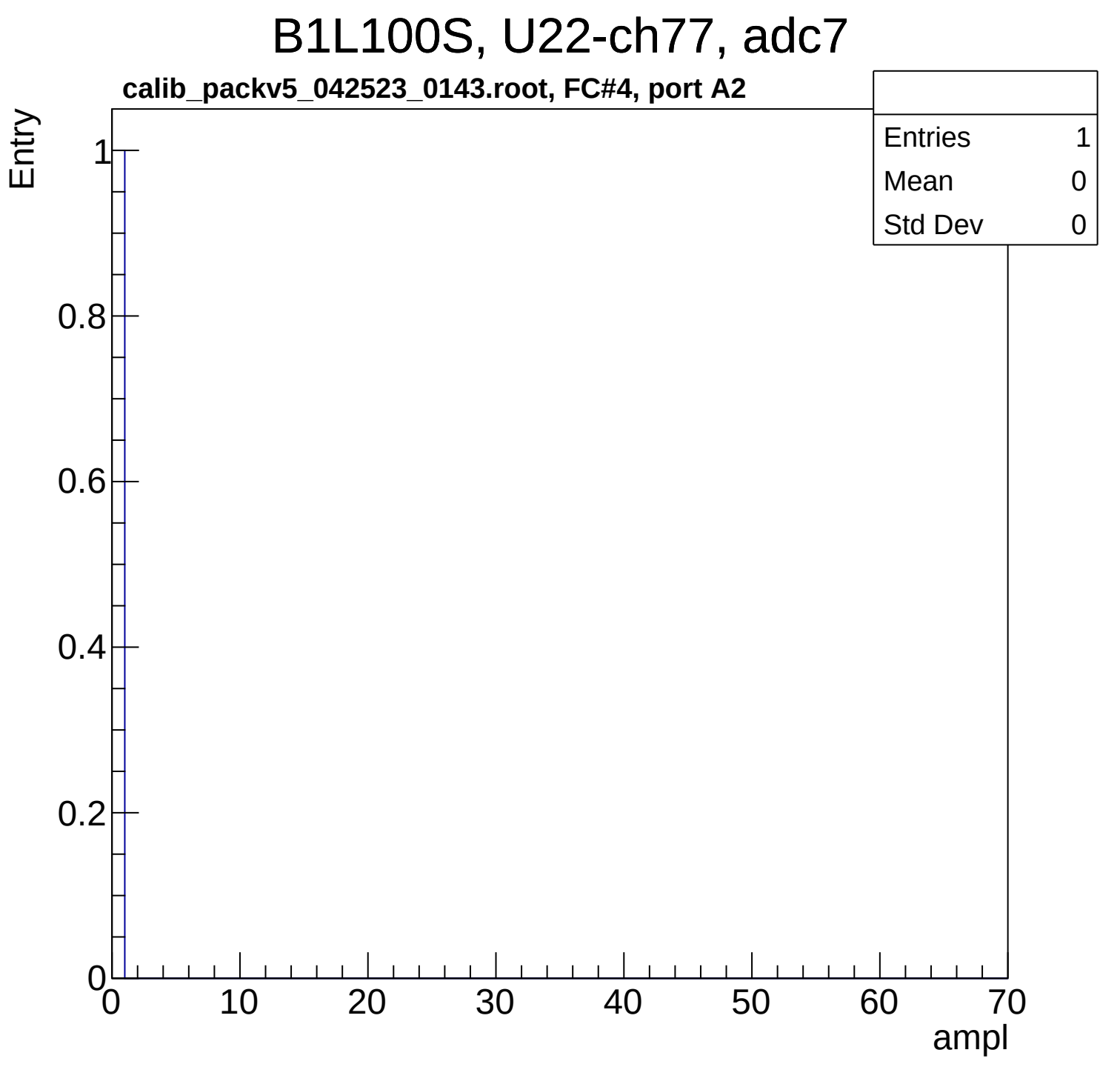
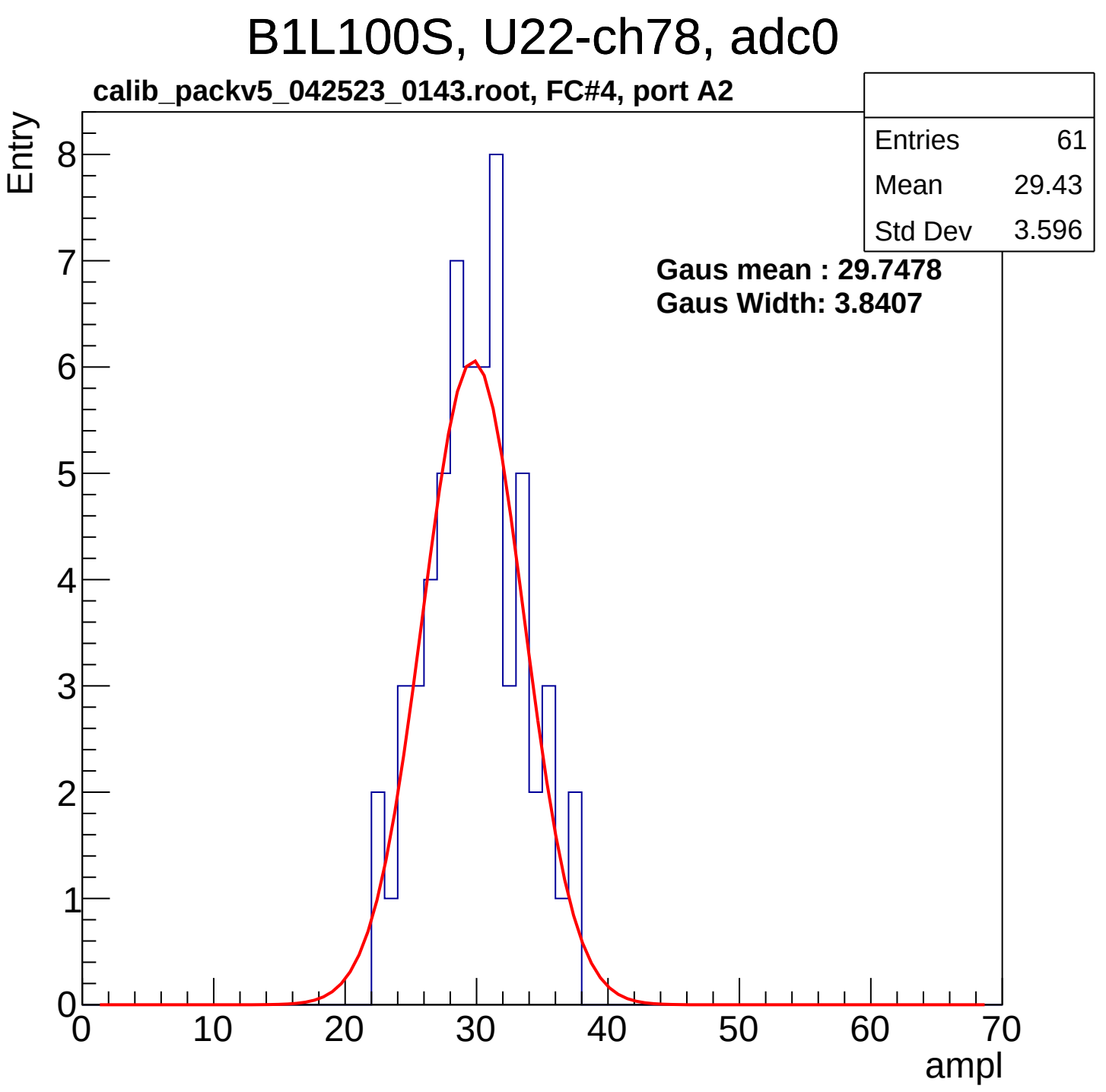
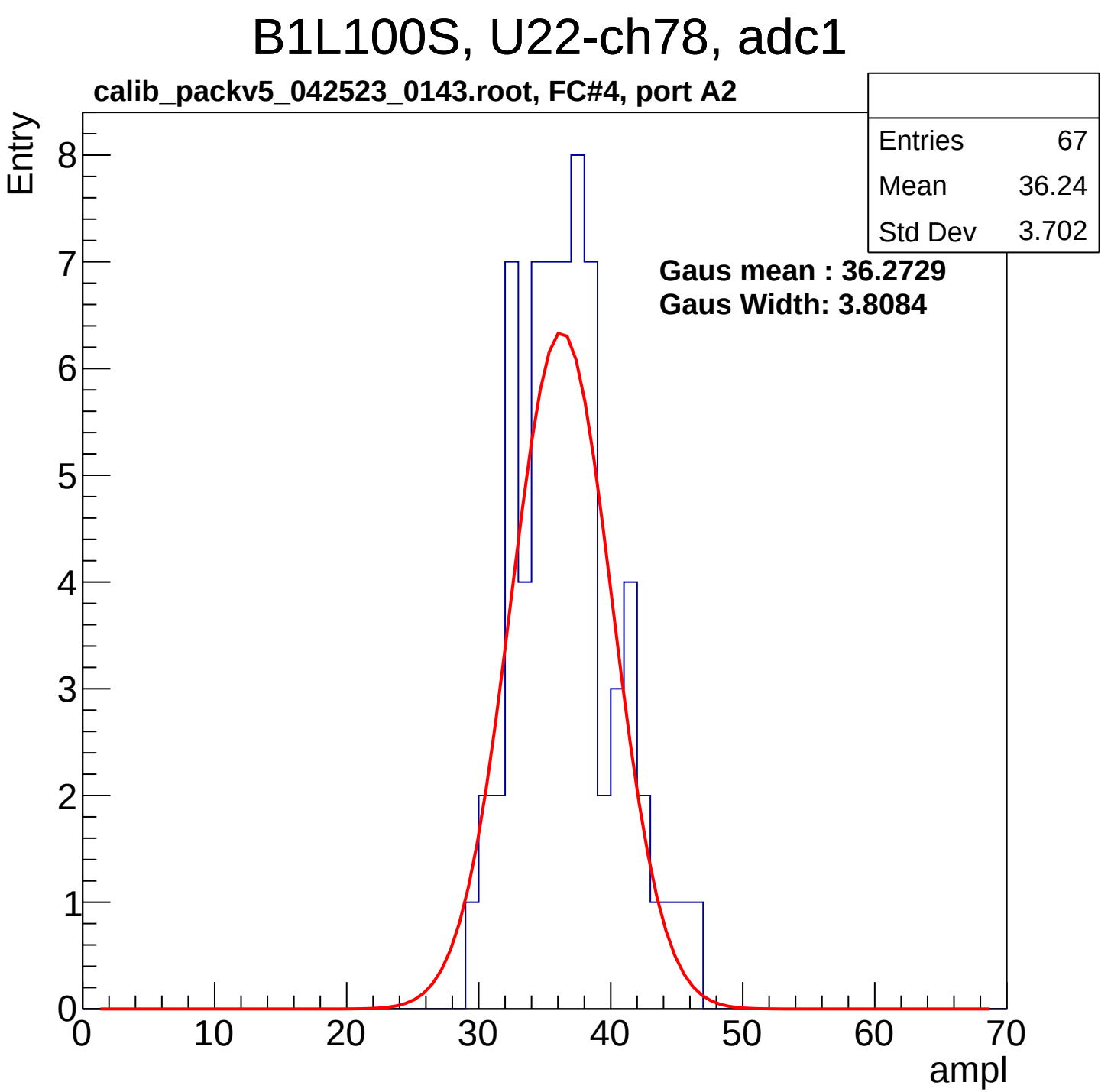
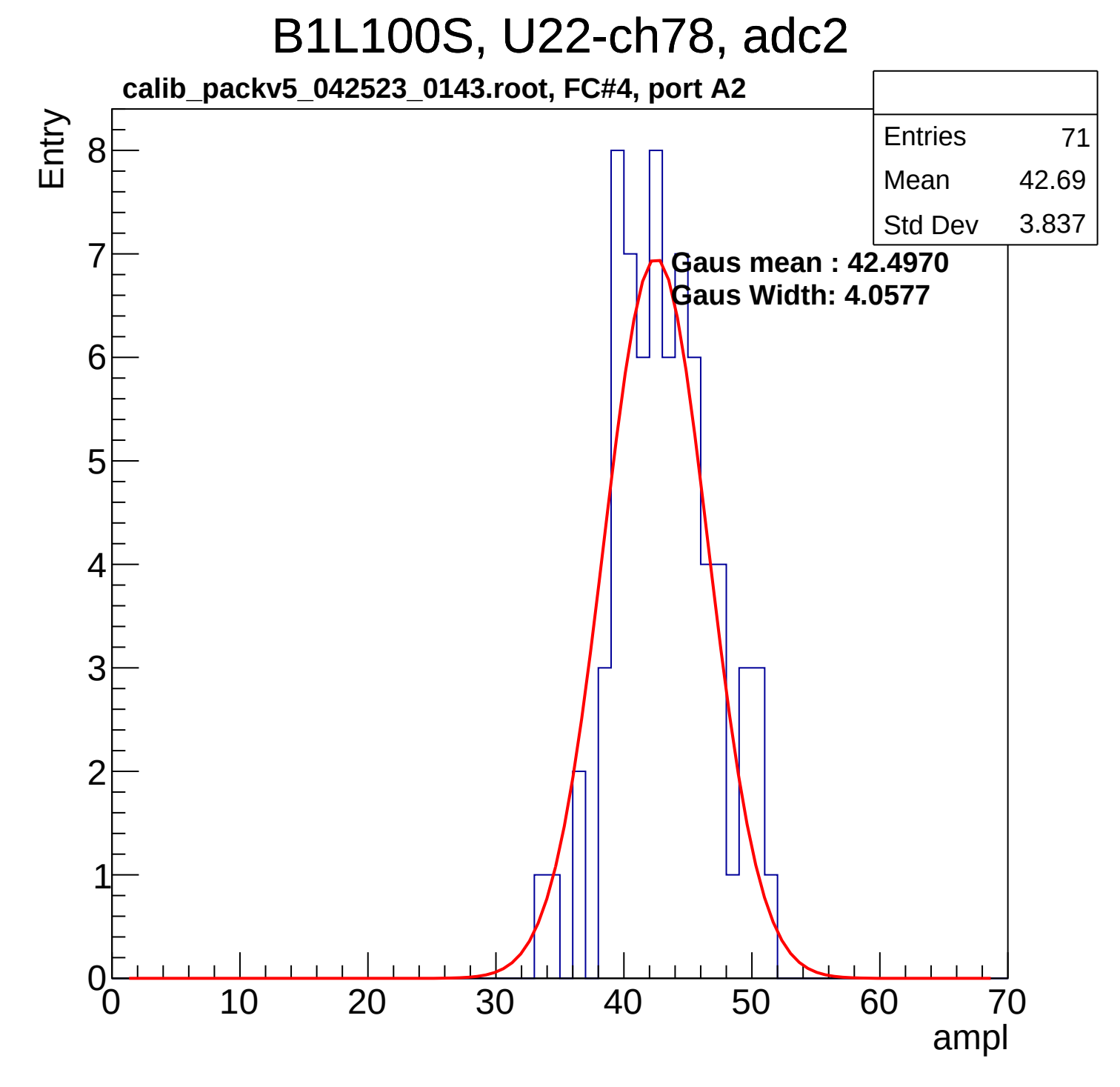

B1L100S, U22-ch77, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 32
Mean 59.53
Std Dev 10.78
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl

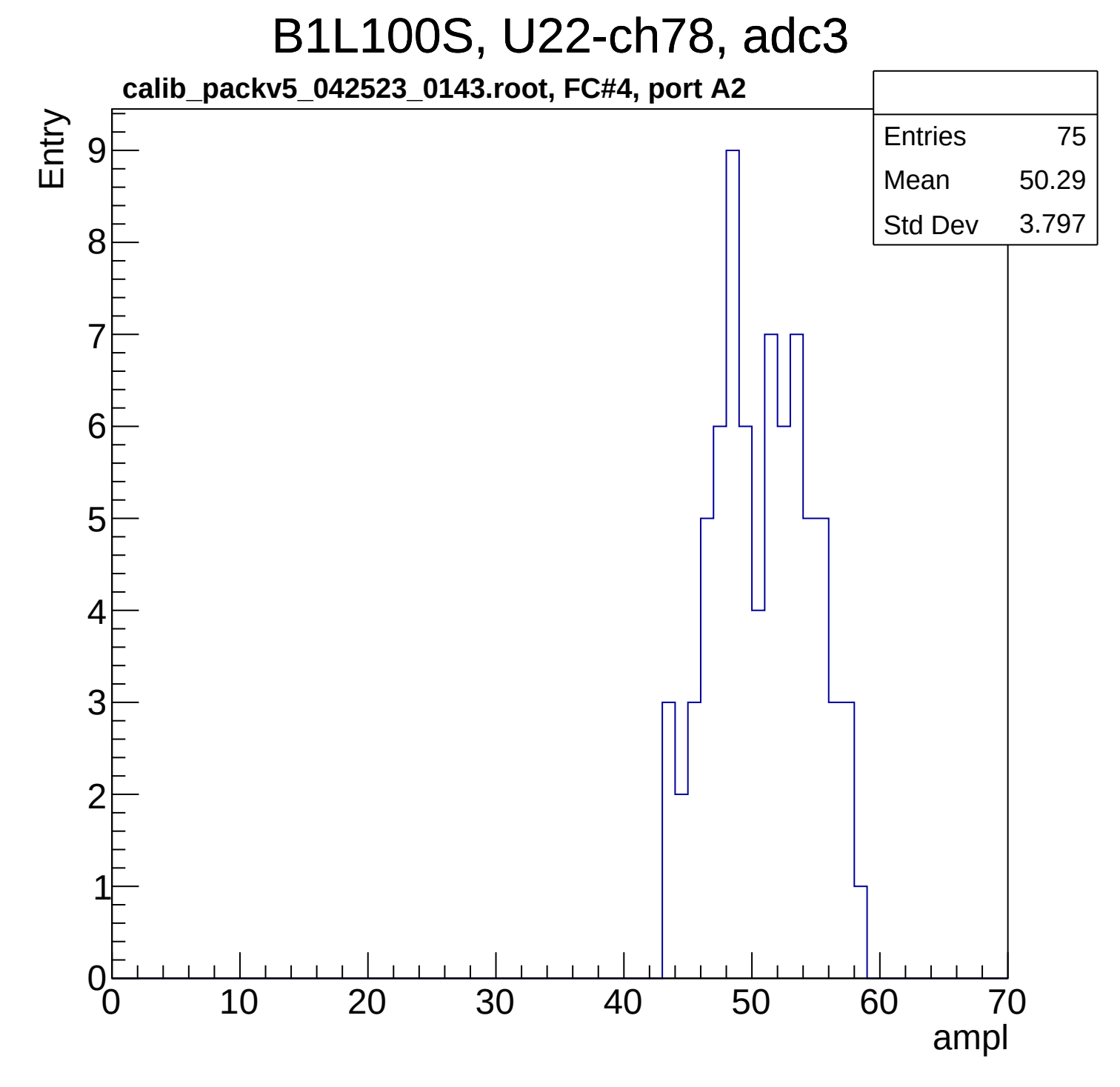

B1L100S, U22-ch77, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch77, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch78, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.43
Std Dev 3.596
Gaus mean : 29.7478
Gaus Width: 3.8407
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch78, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 36.24
Std Dev 3.702
Gaus mean : 36.2729
Gaus Width: 3.8084
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch78, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.69
Std Dev 3.837
Gaus mean : 42.4970
Gaus Width: 4.0577
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch78, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 50.29
Std Dev 3.797
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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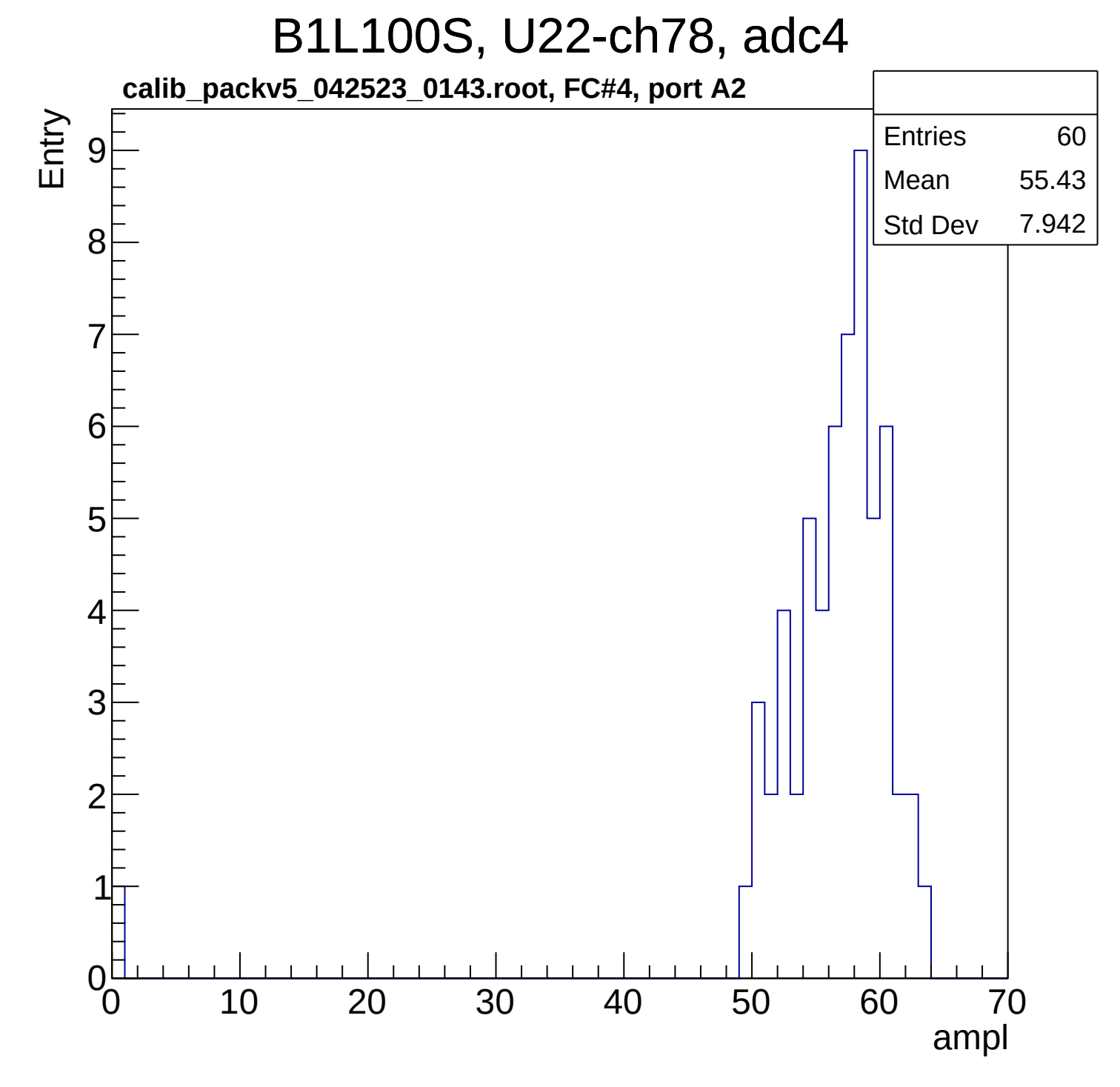
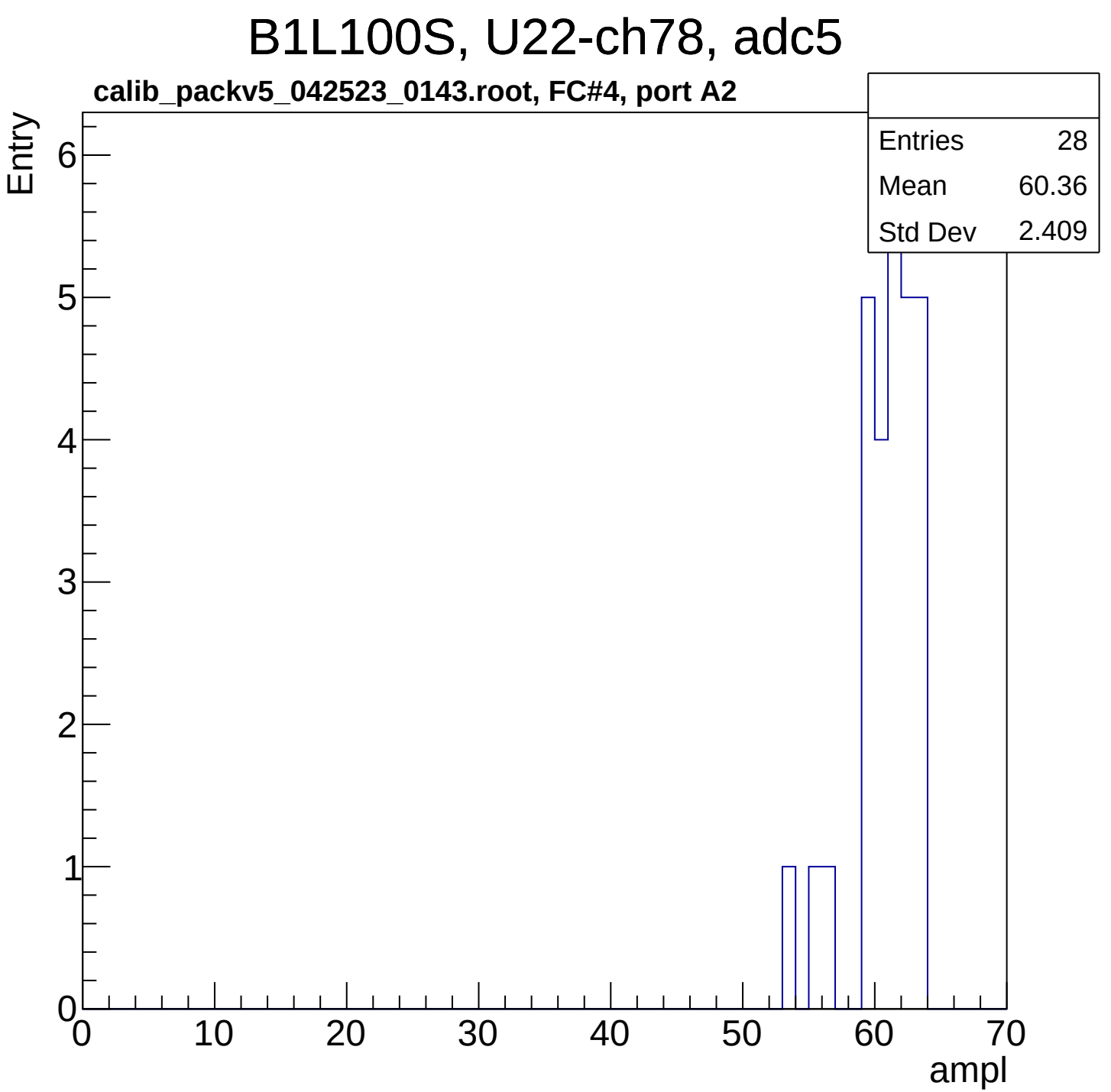
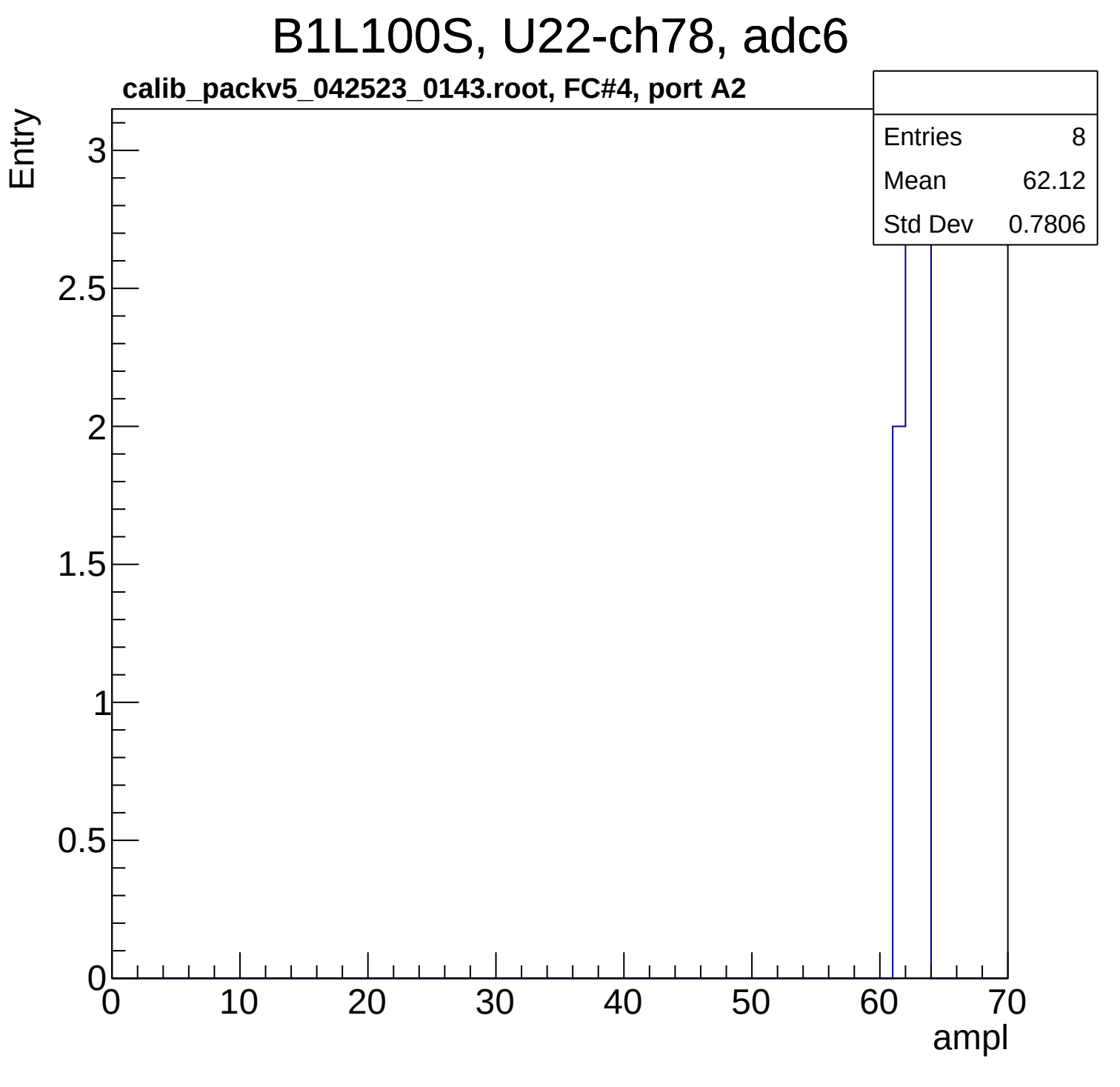

B1L100S, U22-ch78, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 55.43
Std Dev 7.942
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch78, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 60.36
Std Dev 2.409
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch78, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.12
Std Dev 0.7806
Entry
ampl
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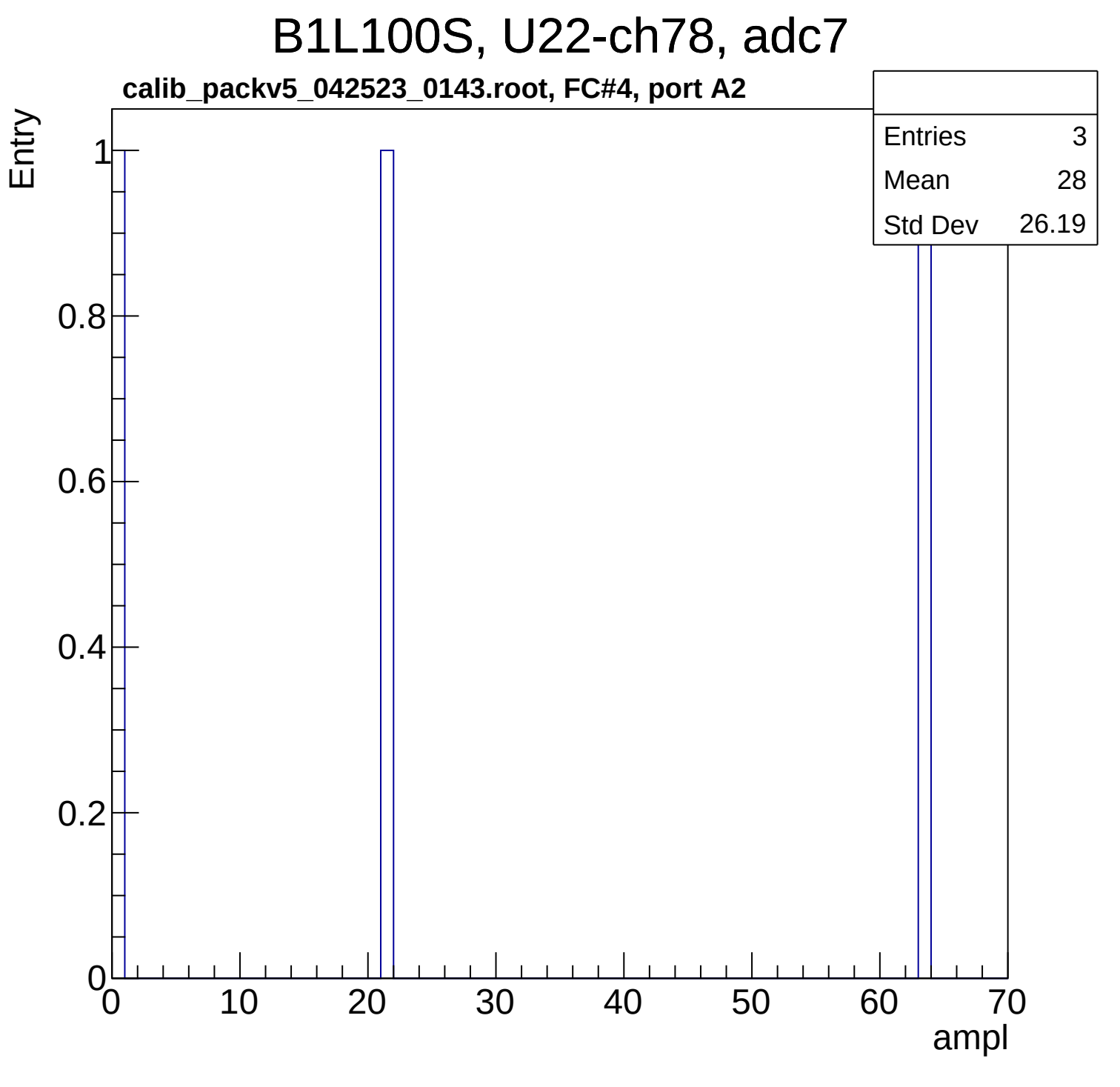
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch78, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 28
Std Dev 26.19
Entry
ampl
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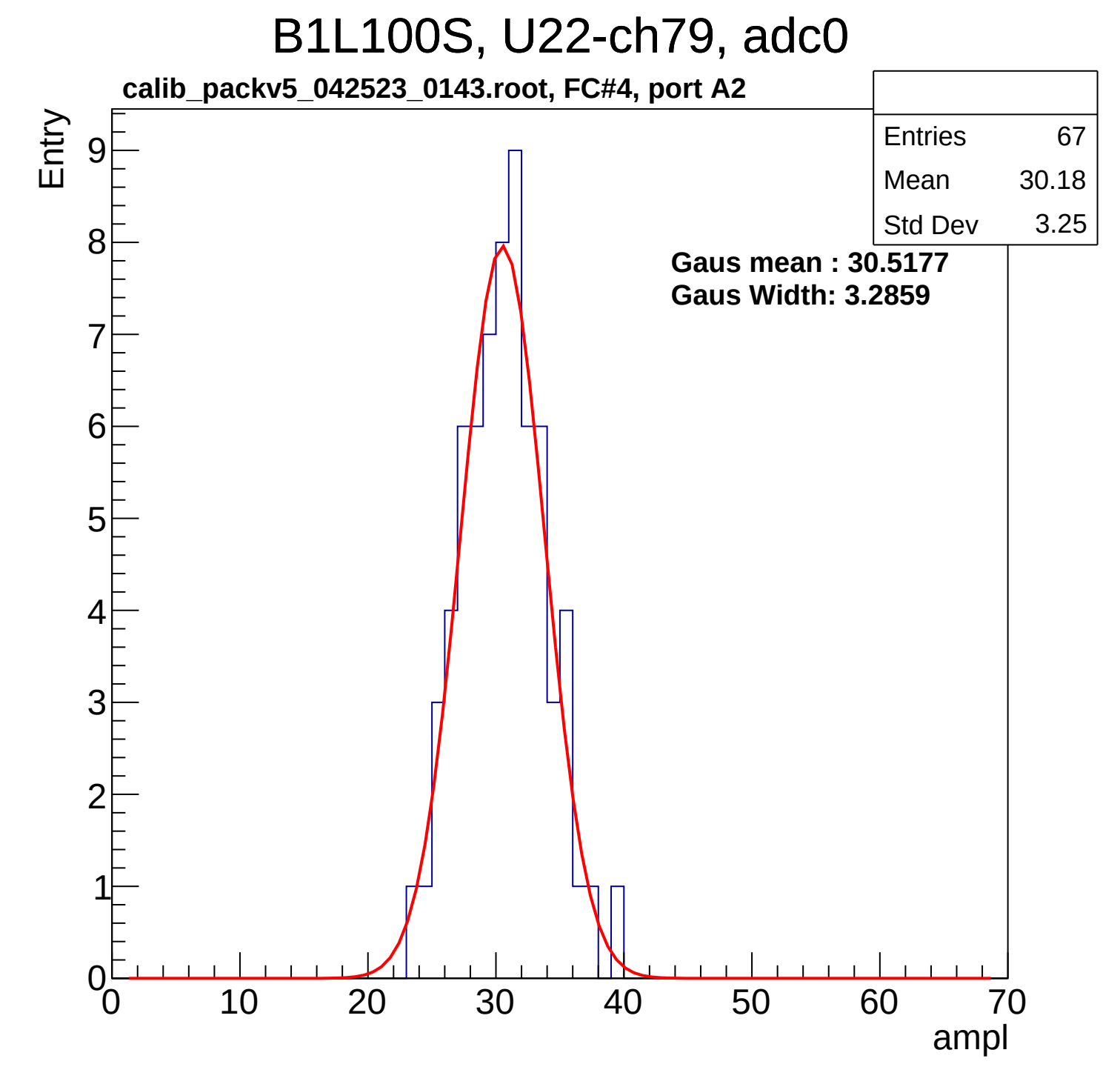
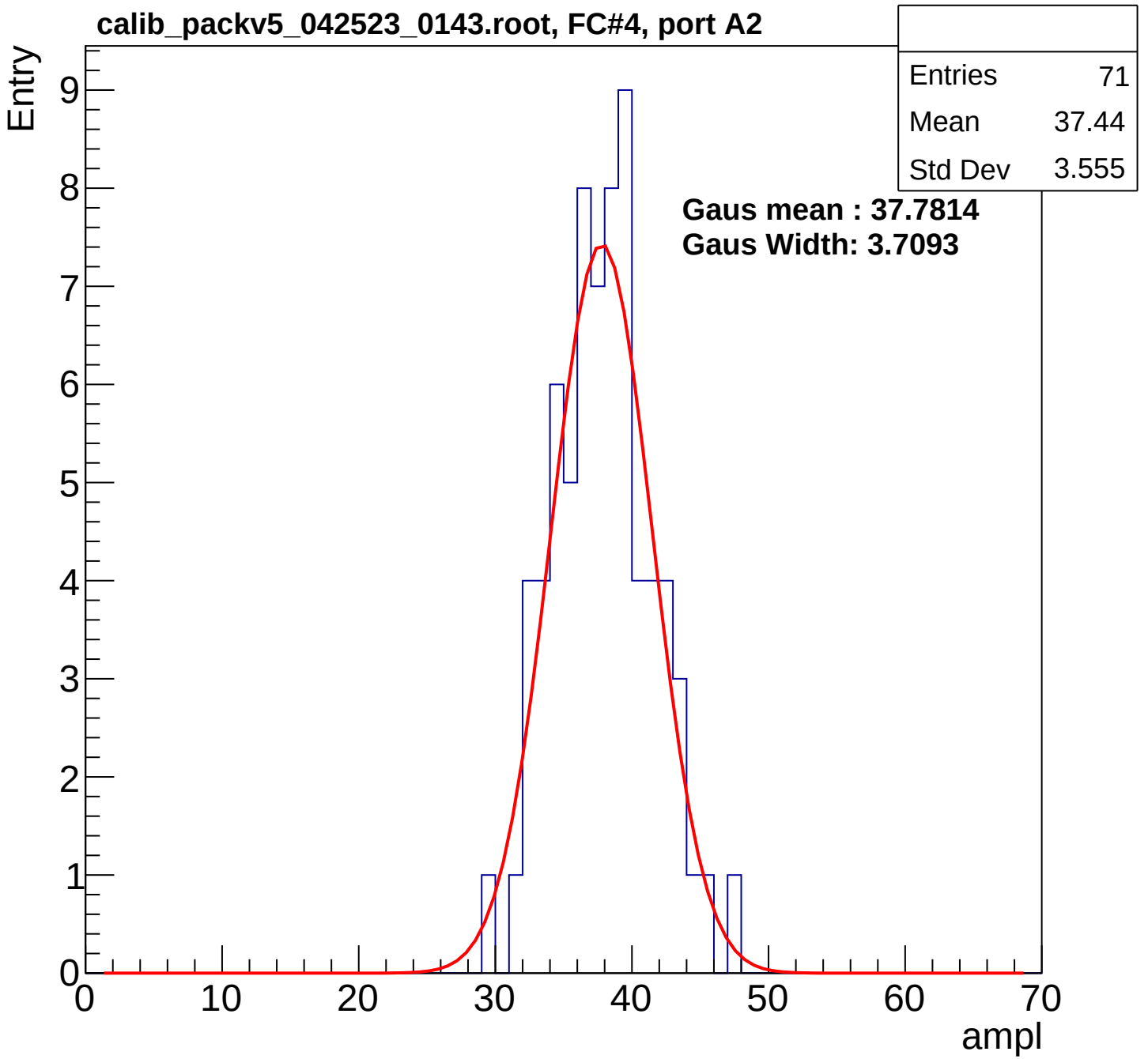
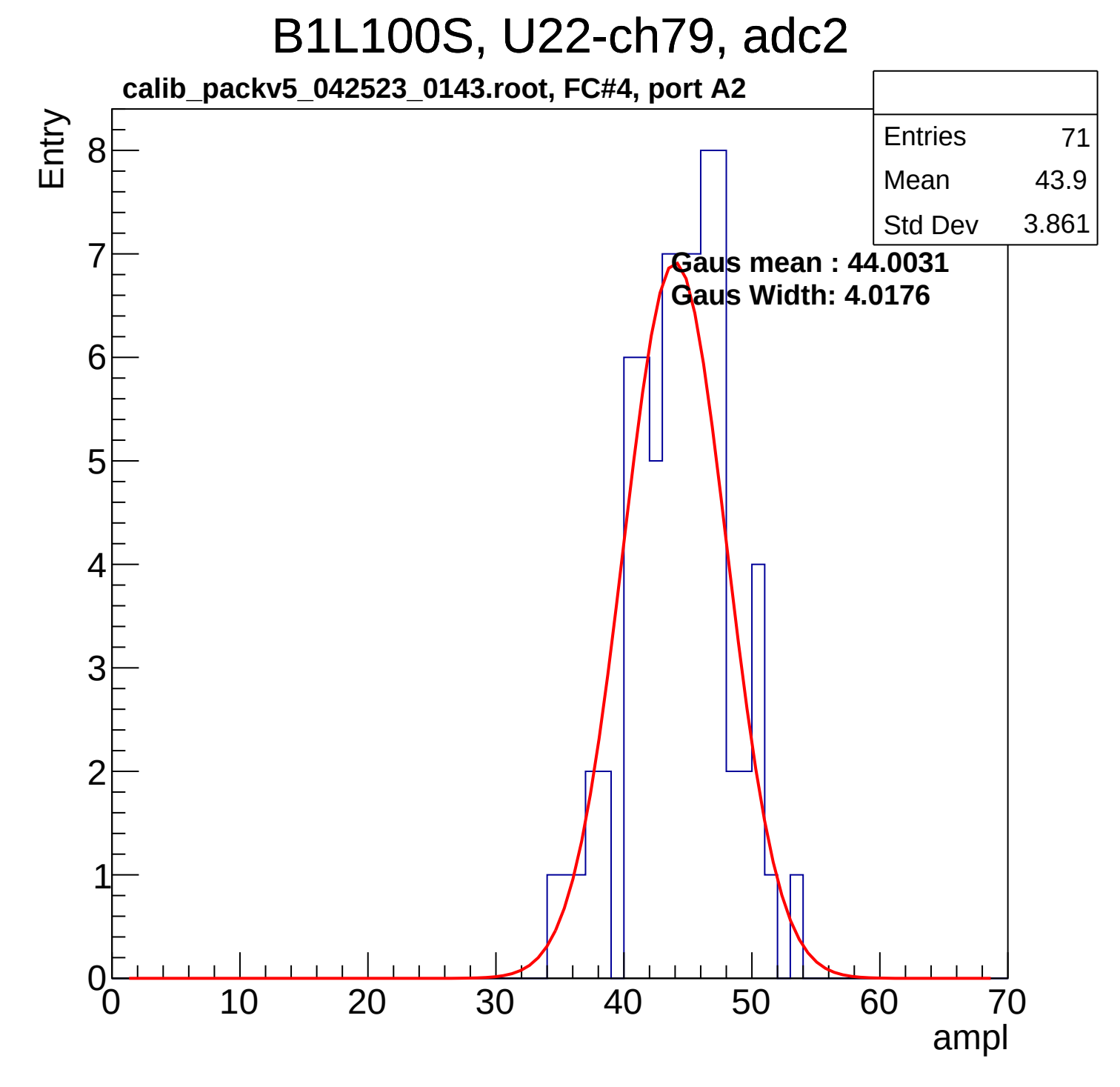
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U22-ch79, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 30.18
Std Dev 3.25
Gaus mean : 30.5177
Gaus Width: 3.2859
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch79, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.44
Std Dev 3.555
Gaus mean : 37.7814
Gaus Width: 3.7093
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch79, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 43.9
Std Dev 3.861
Gaus mean : 44.0031
Gaus Width: 4.0176
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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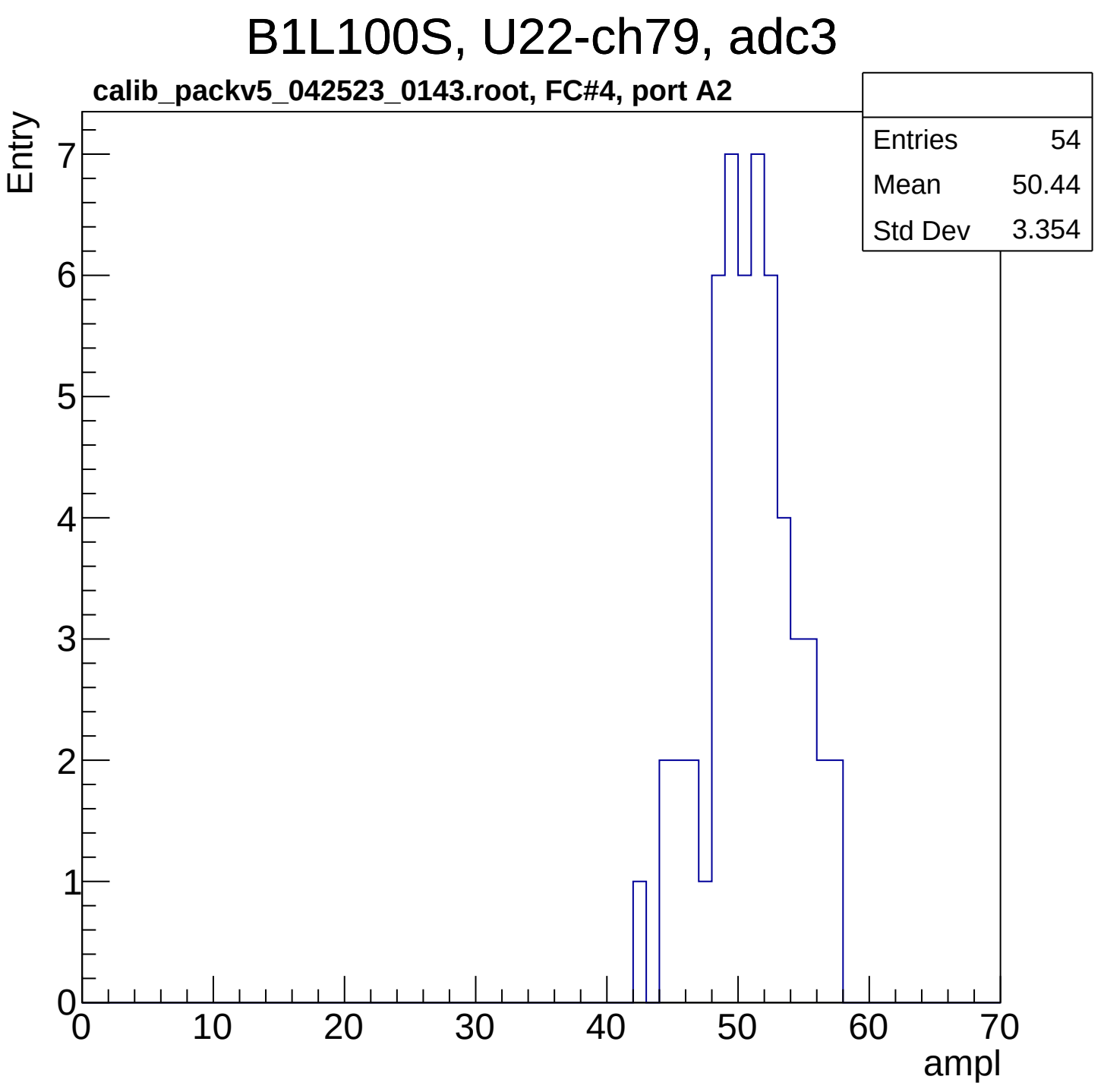
B1L100S, U22-ch79, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 50.44
Std Dev 3.354
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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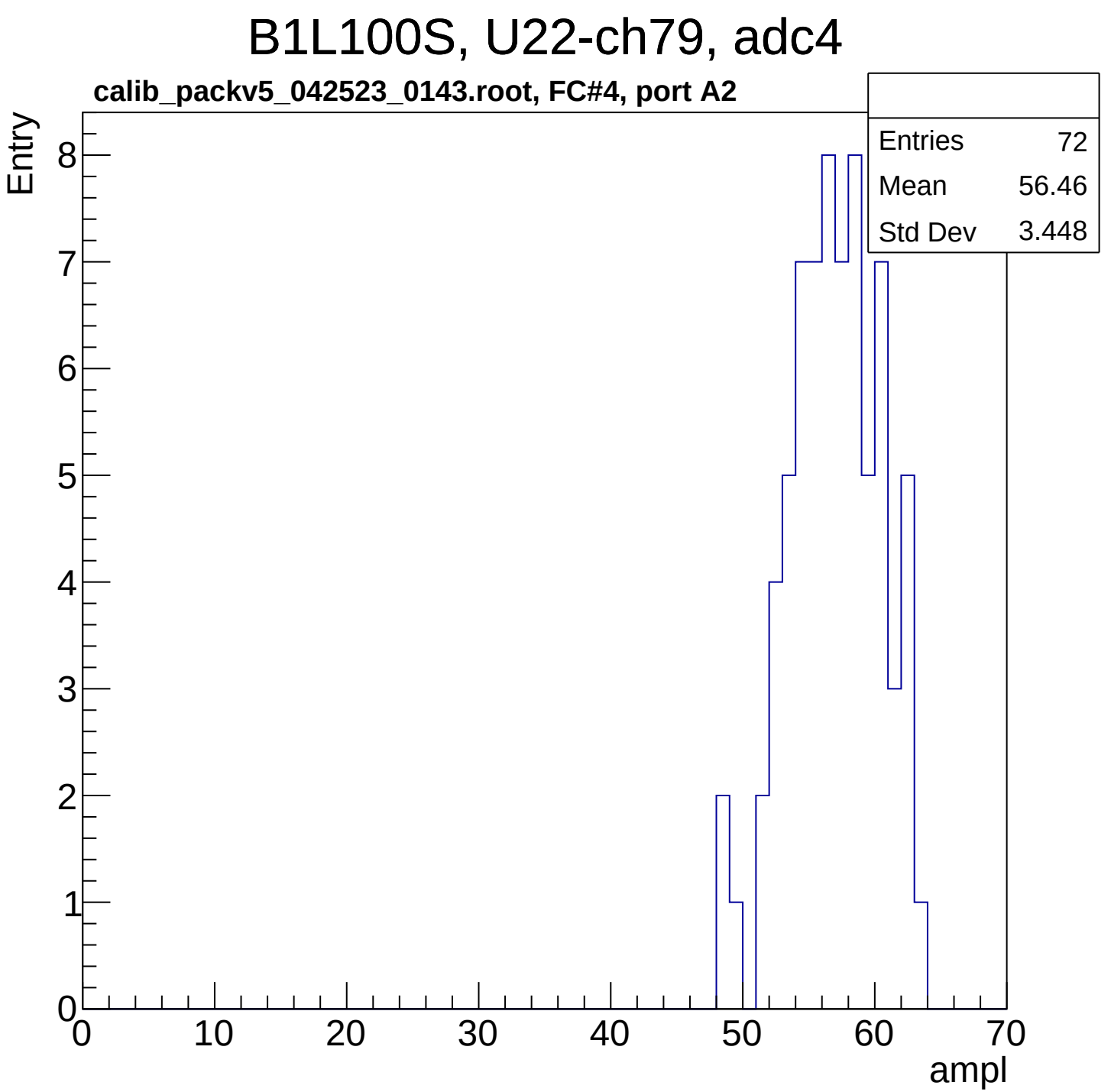
40
50
60
70


B1L100S, U22-ch79, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 56.46
Std Dev 3.448
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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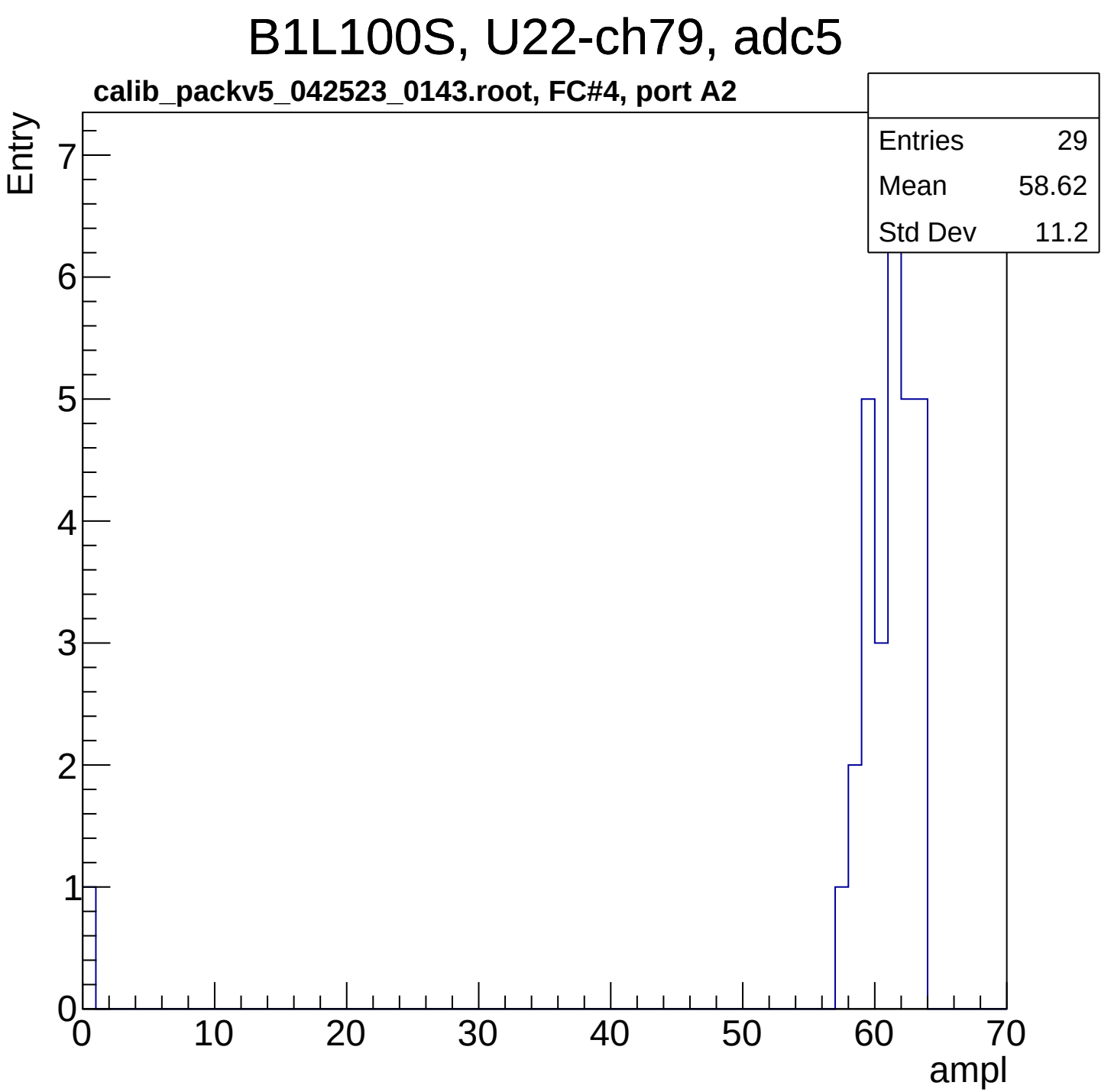
40
50
60
70


B1L100S, U22-ch79, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 58.62
Std Dev 11.2
Entry
ampl
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1
2
3
4
5
6
7
0
10
20
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40
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60
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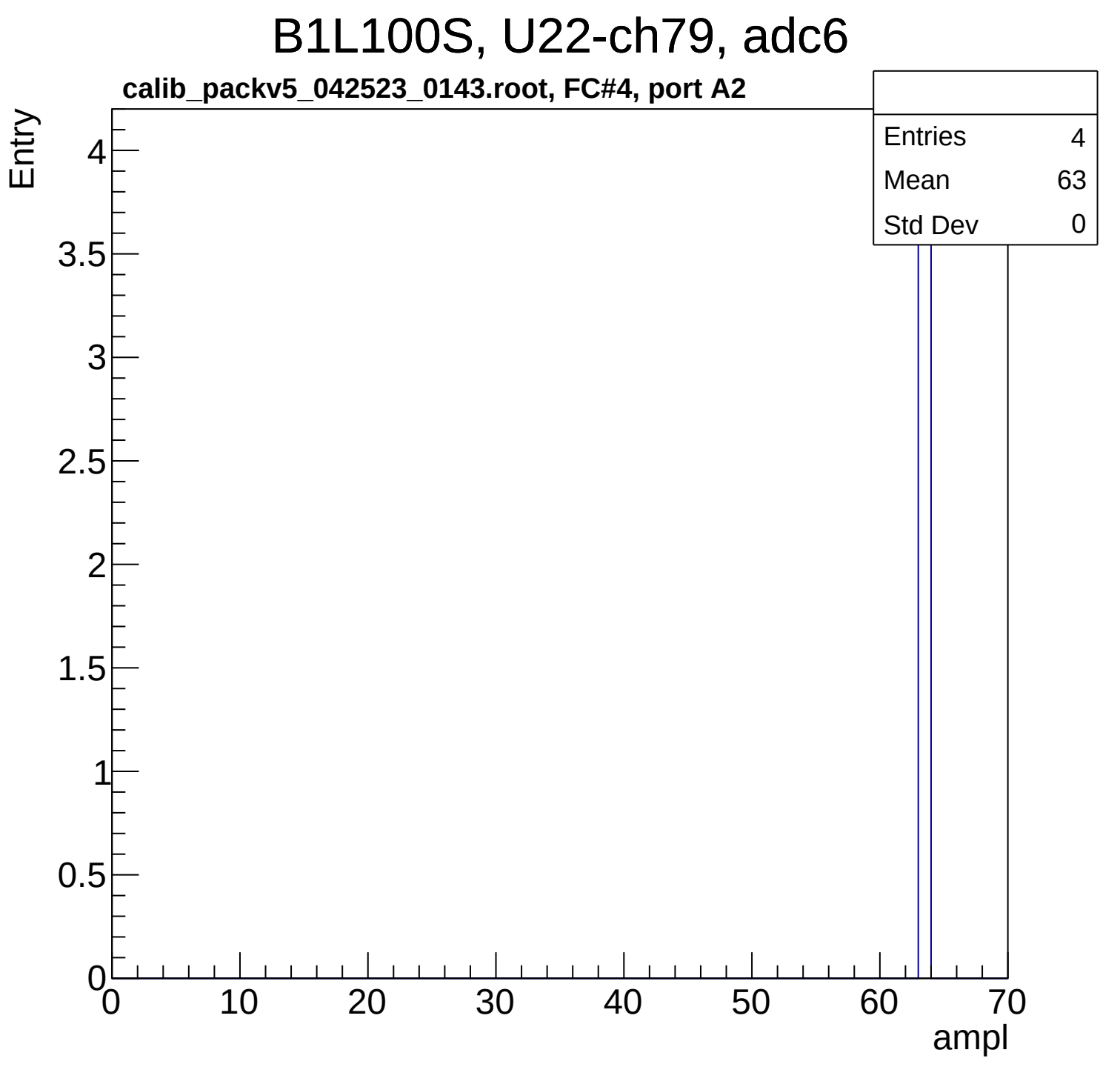
B1L100S, U22-ch79, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 63
Std Dev 0
Entry
ampl
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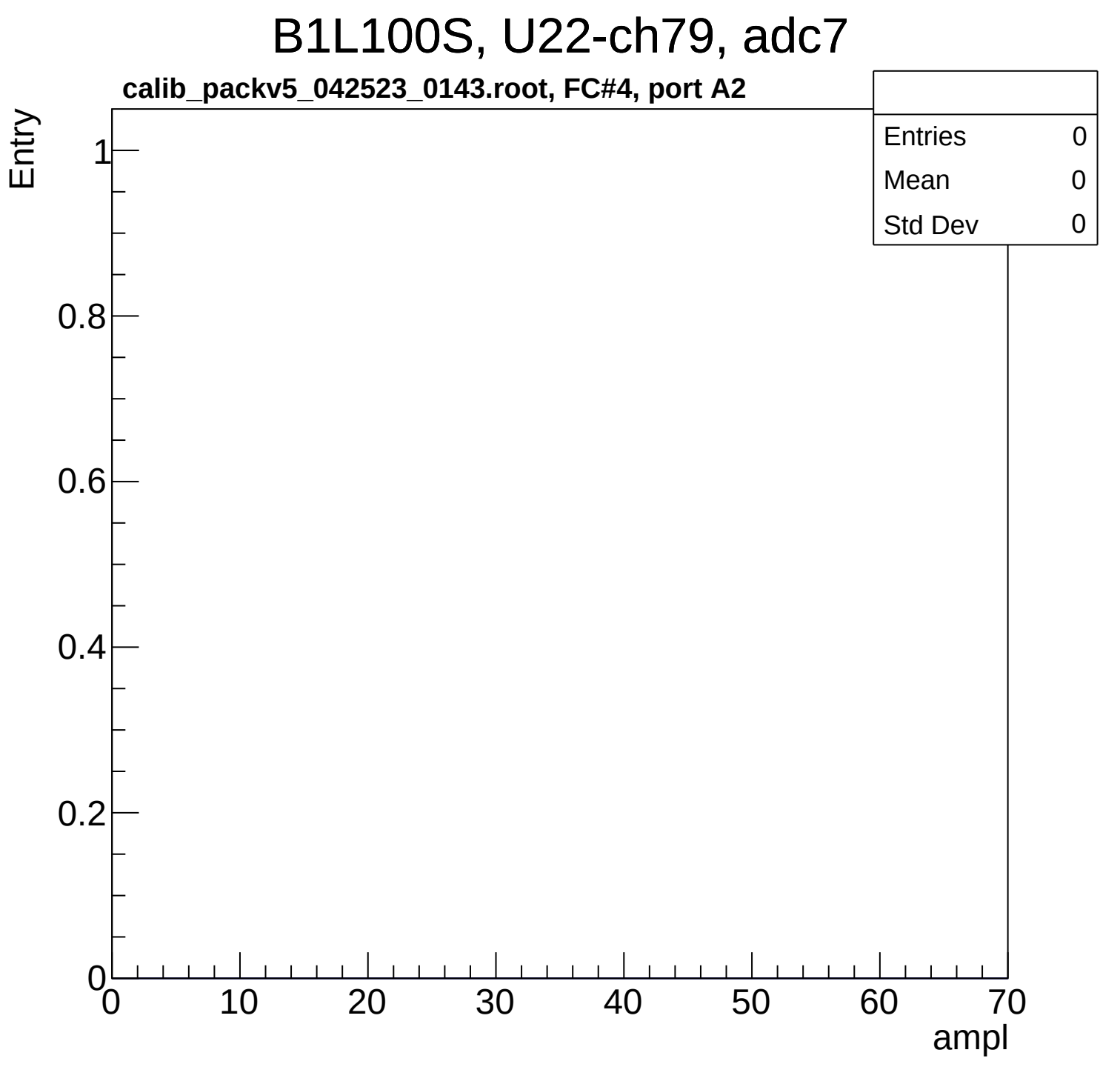
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch79, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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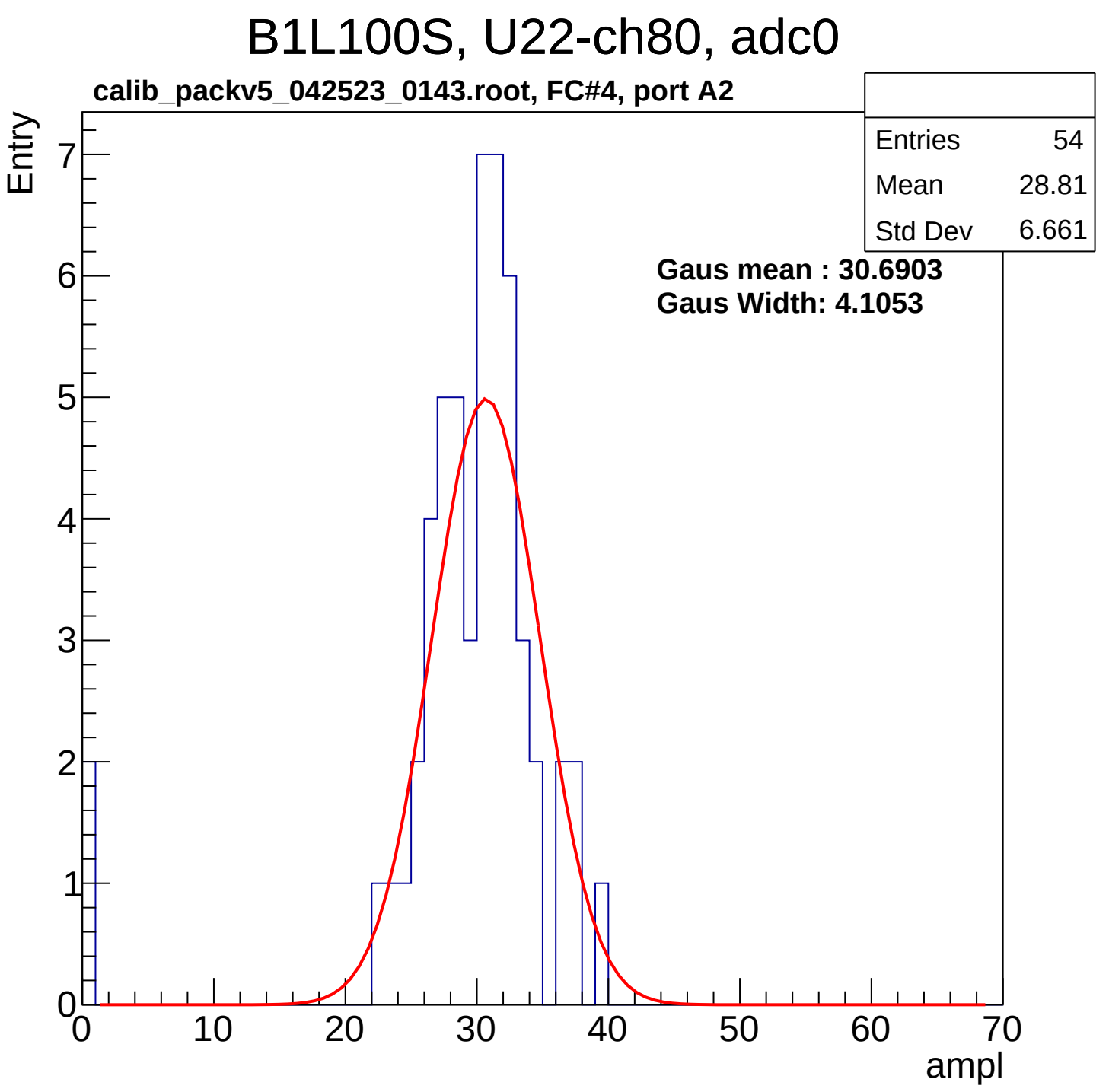
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch80, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 28.81
Std Dev 6.661
Gaus mean : 30.6903
Gaus Width: 4.1053
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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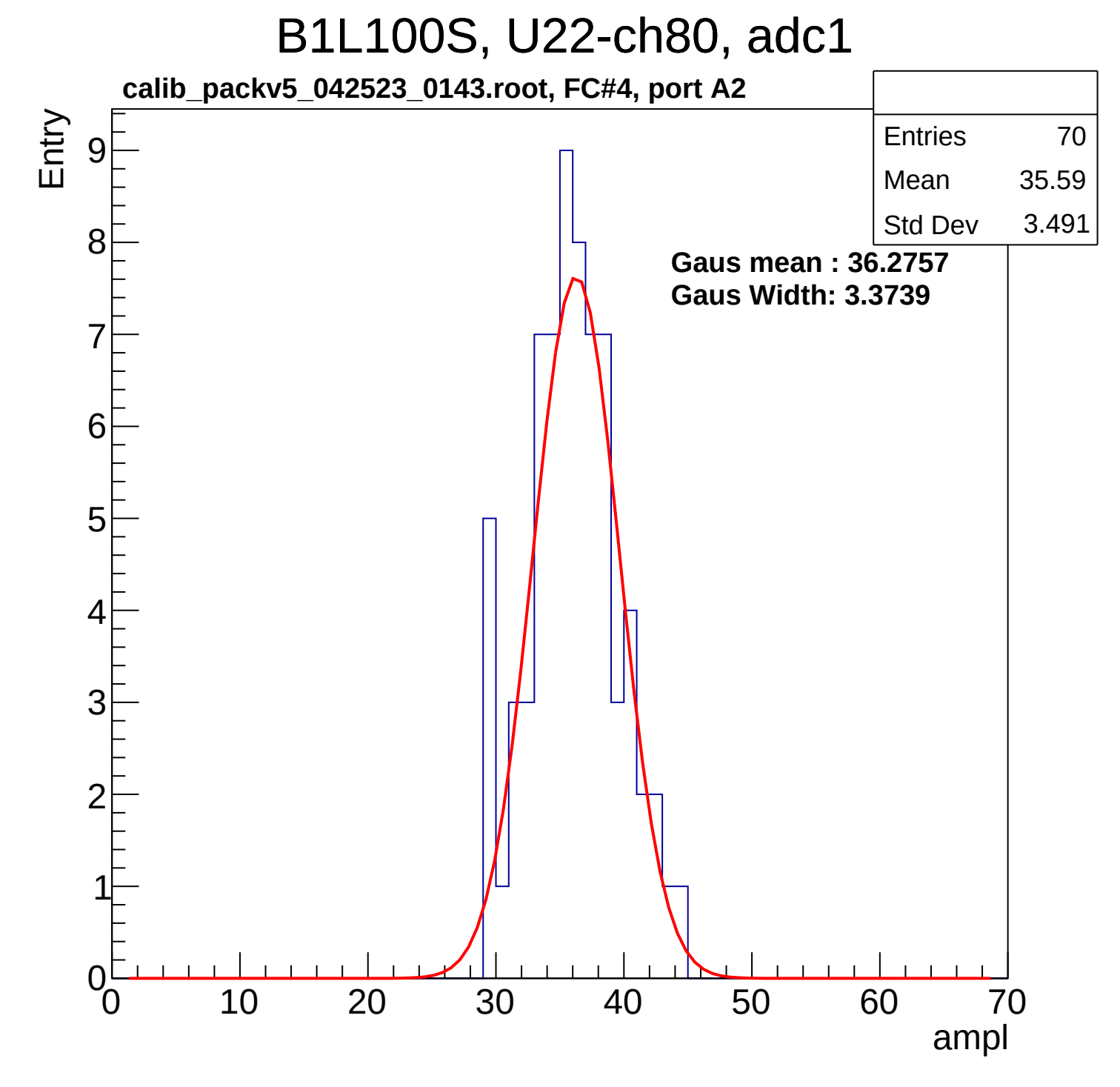
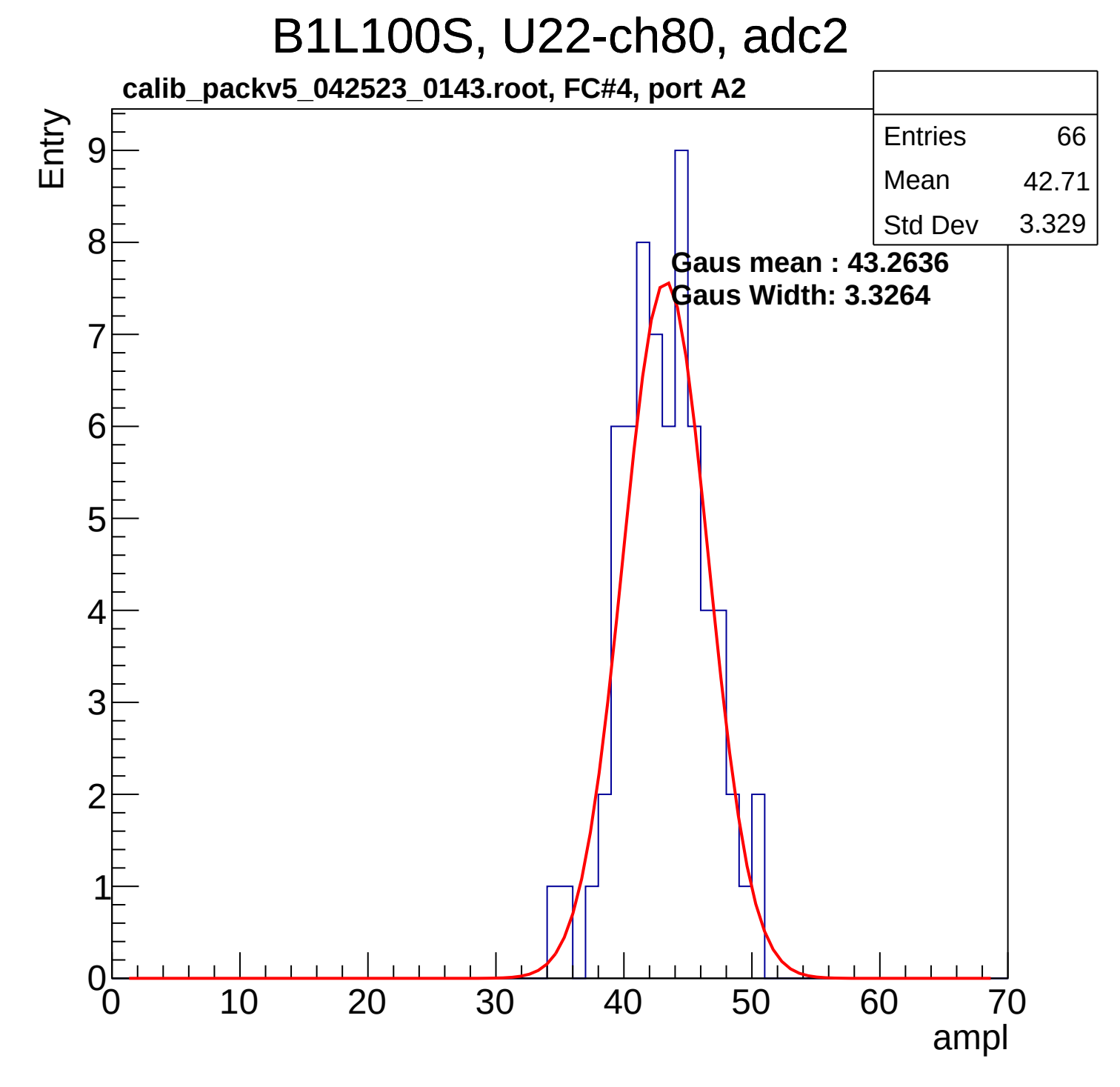
40
50
60
70


B1L100S, U22-ch80, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 35.59
Std Dev 3.491
Gaus mean : 36.2757
Gaus Width: 3.3739
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch80, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 42.71
Std Dev 3.329
Gaus mean : 43.2636
Gaus Width: 3.3264
Entry
ampl
0
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2
3
4
5
6
7
8
9
0
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60
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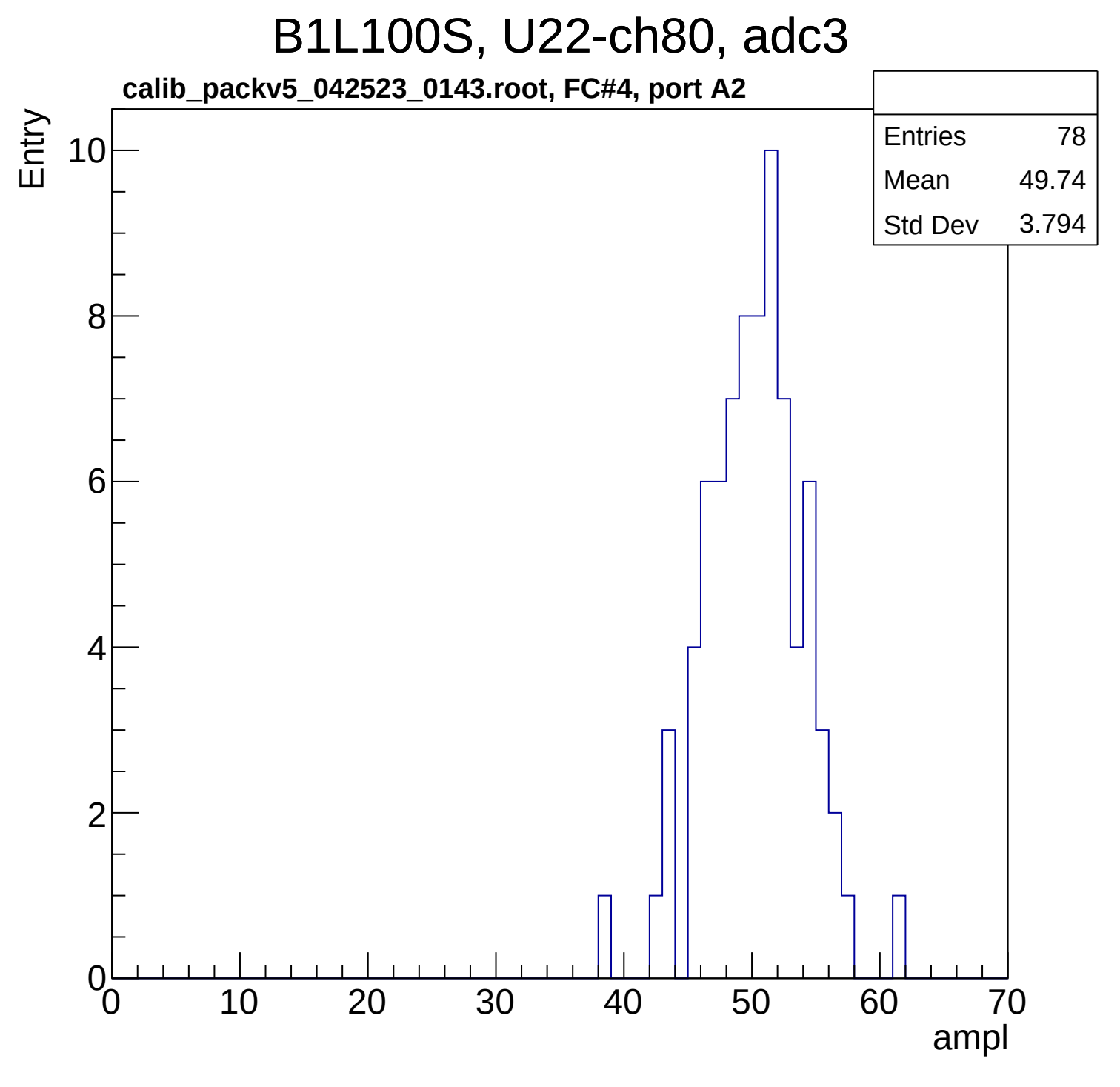
B1L100S, U22-ch80, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 49.74
Std Dev 3.794
Entry
ampl
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6
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10
0
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40
50
60
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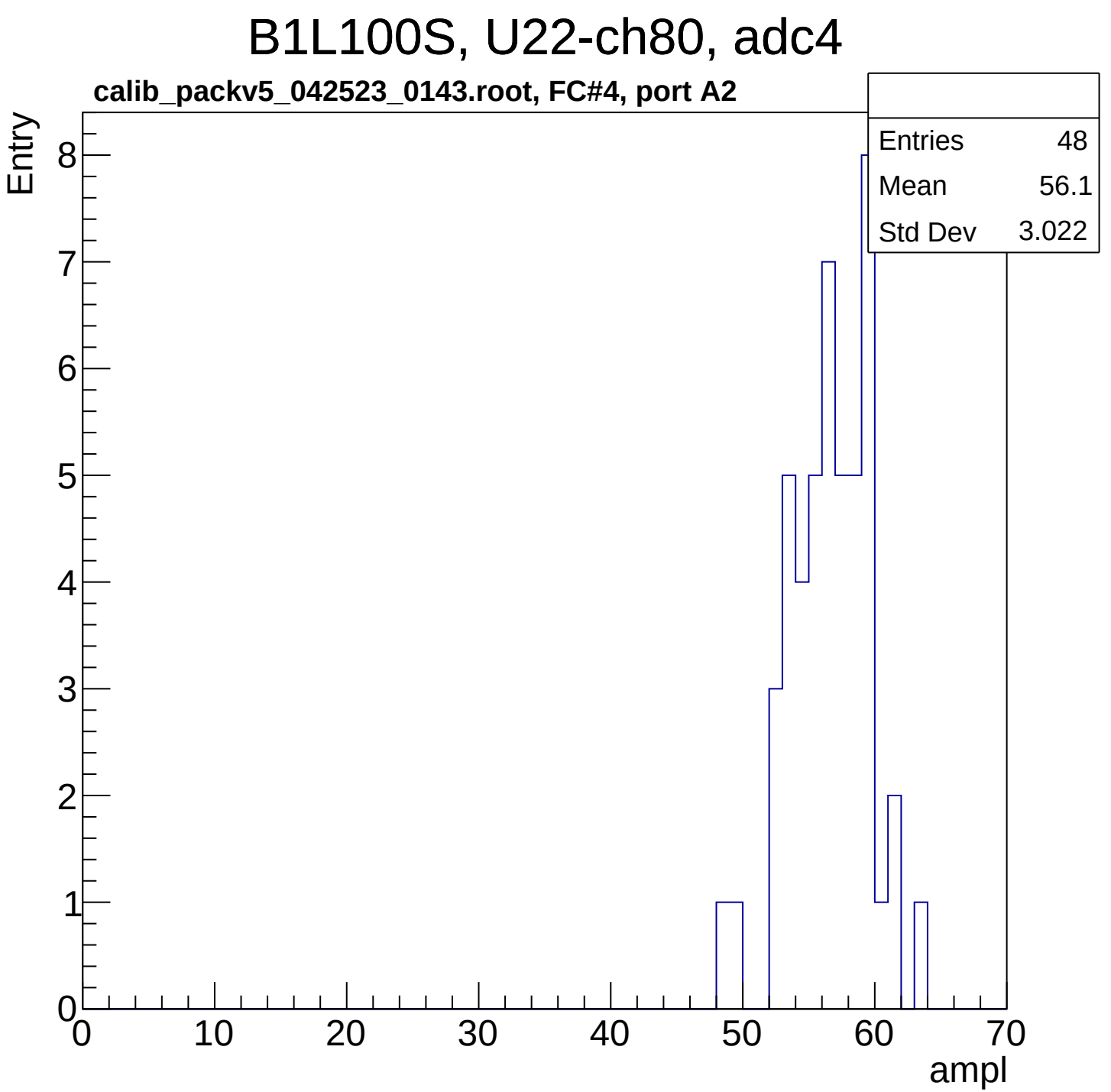
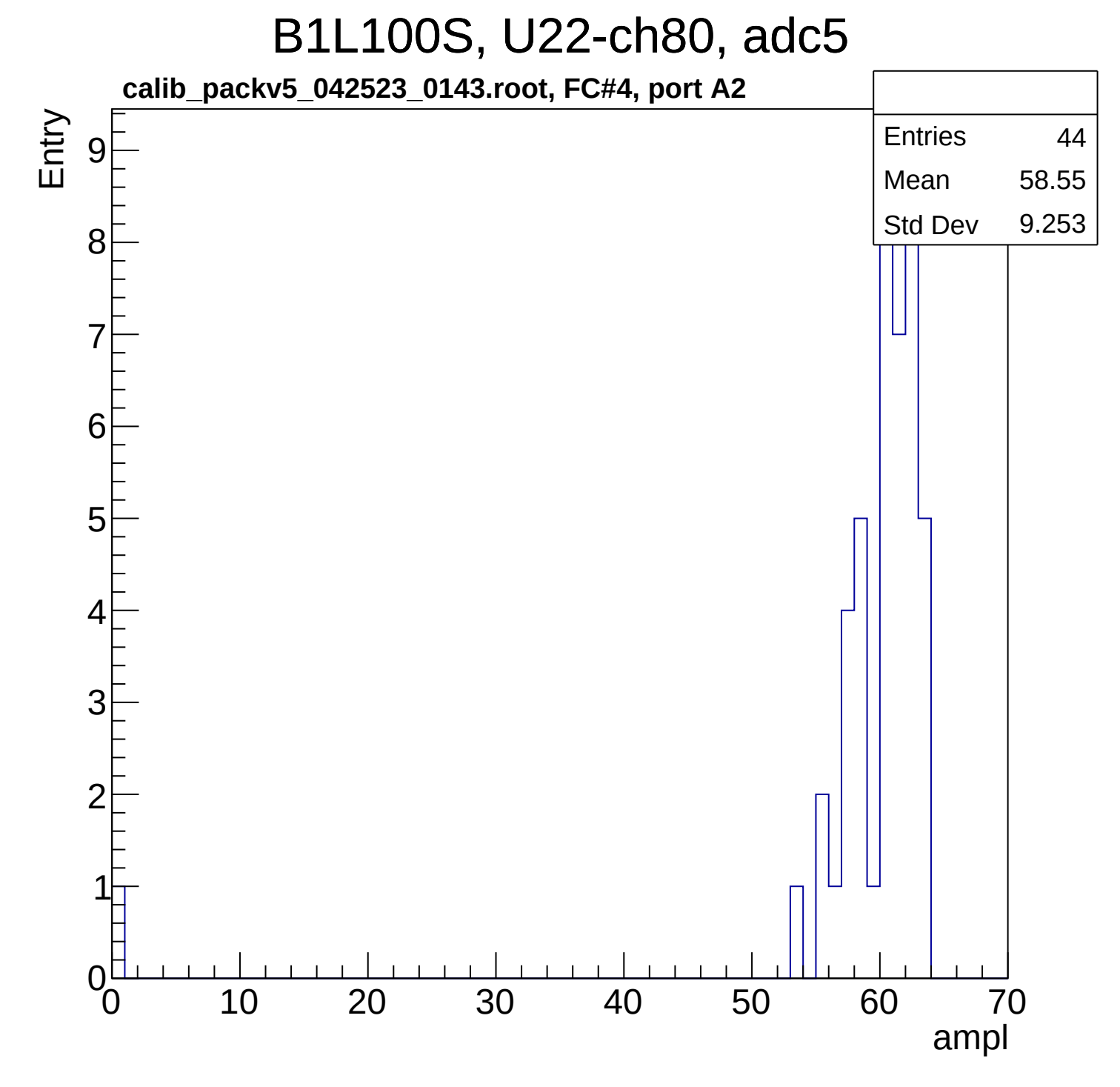

B1L100S, U22-ch80, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 56.1
Std Dev 3.022
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch80, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 58.55
Std Dev 9.253
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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60
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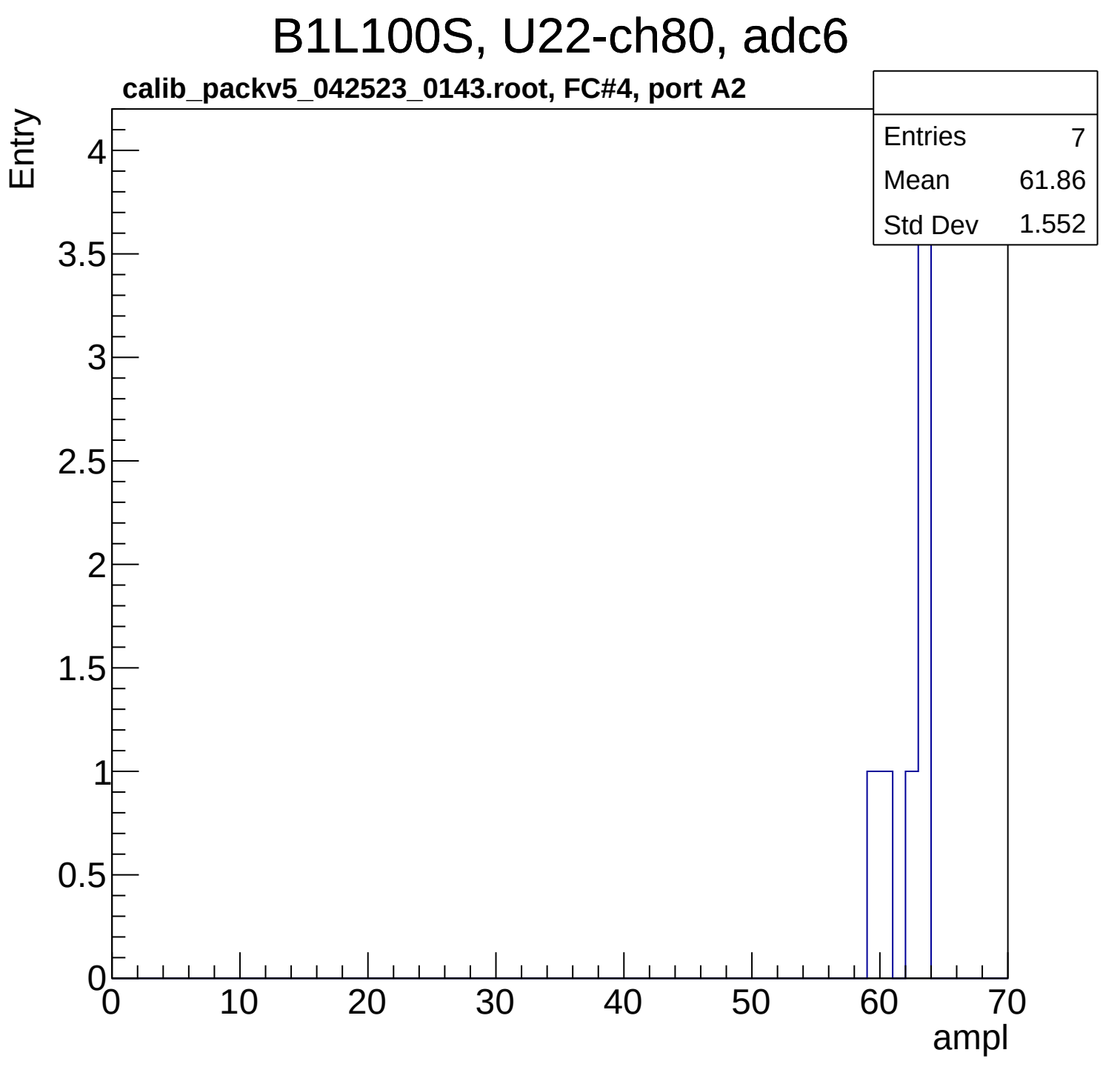
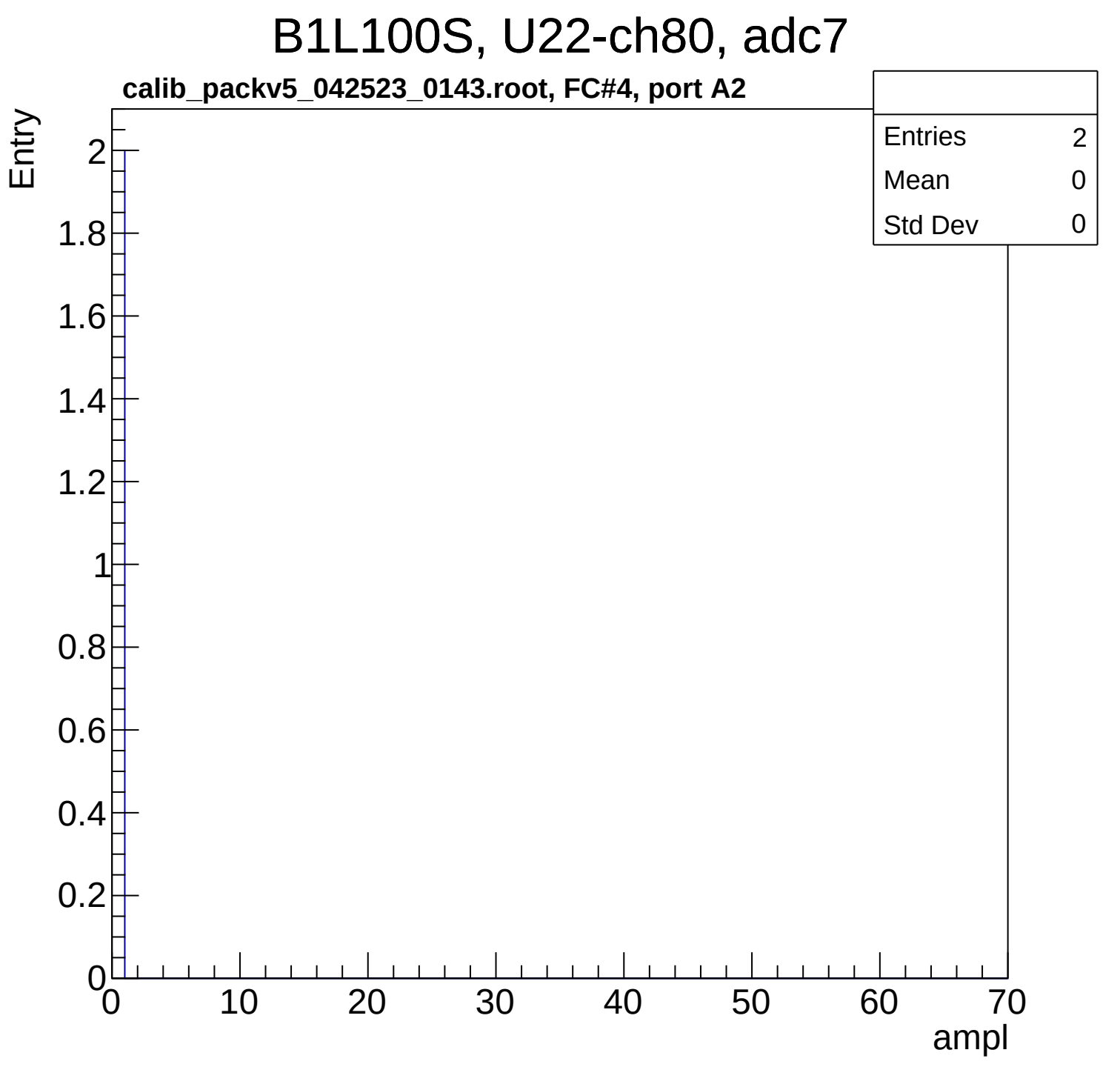
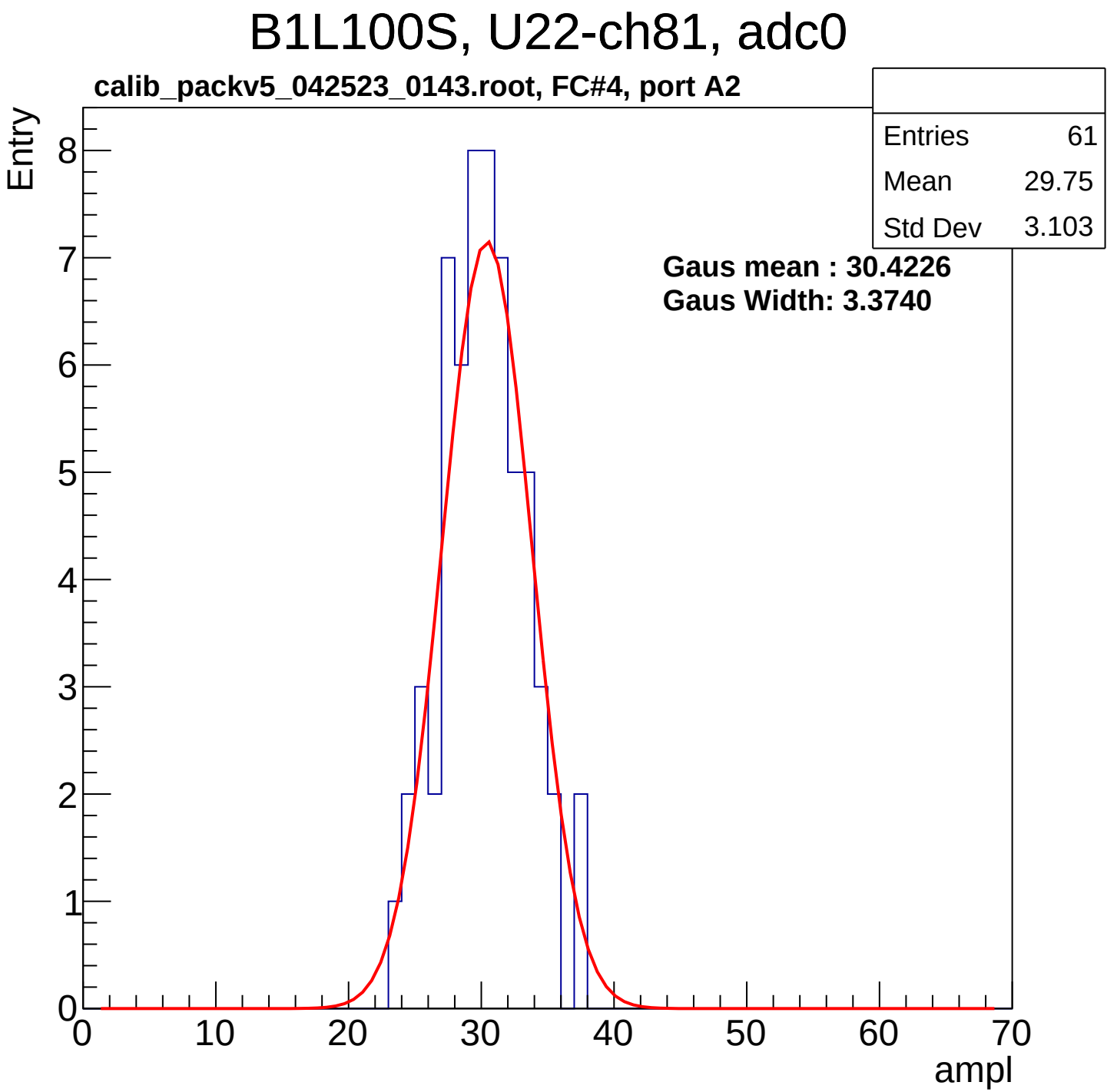

B1L100S, U22-ch80, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 61.86
Std Dev 1.552
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch80, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch81, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.75
Std Dev 3.103
Gaus mean : 30.4226
Gaus Width: 3.3740
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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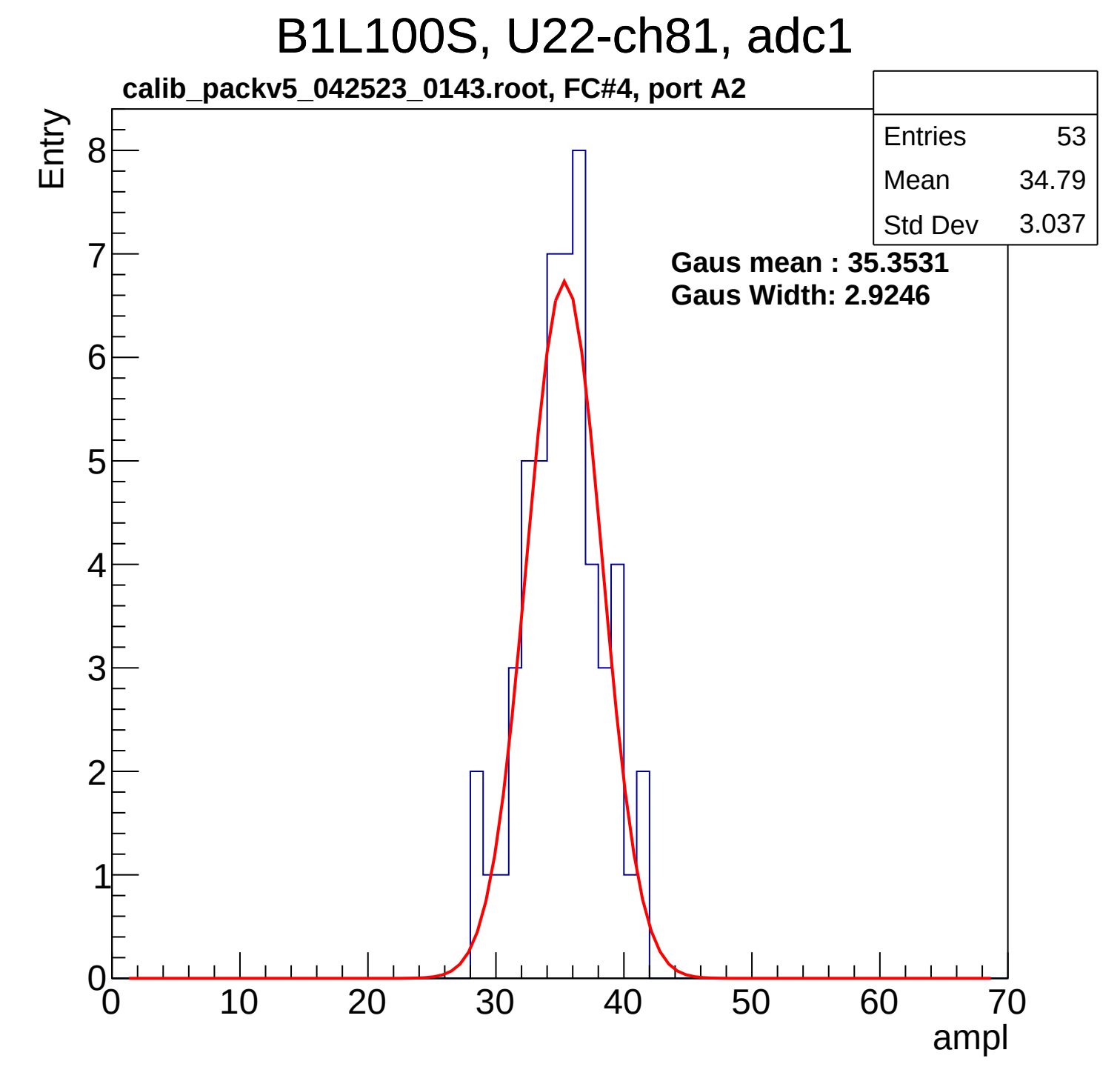
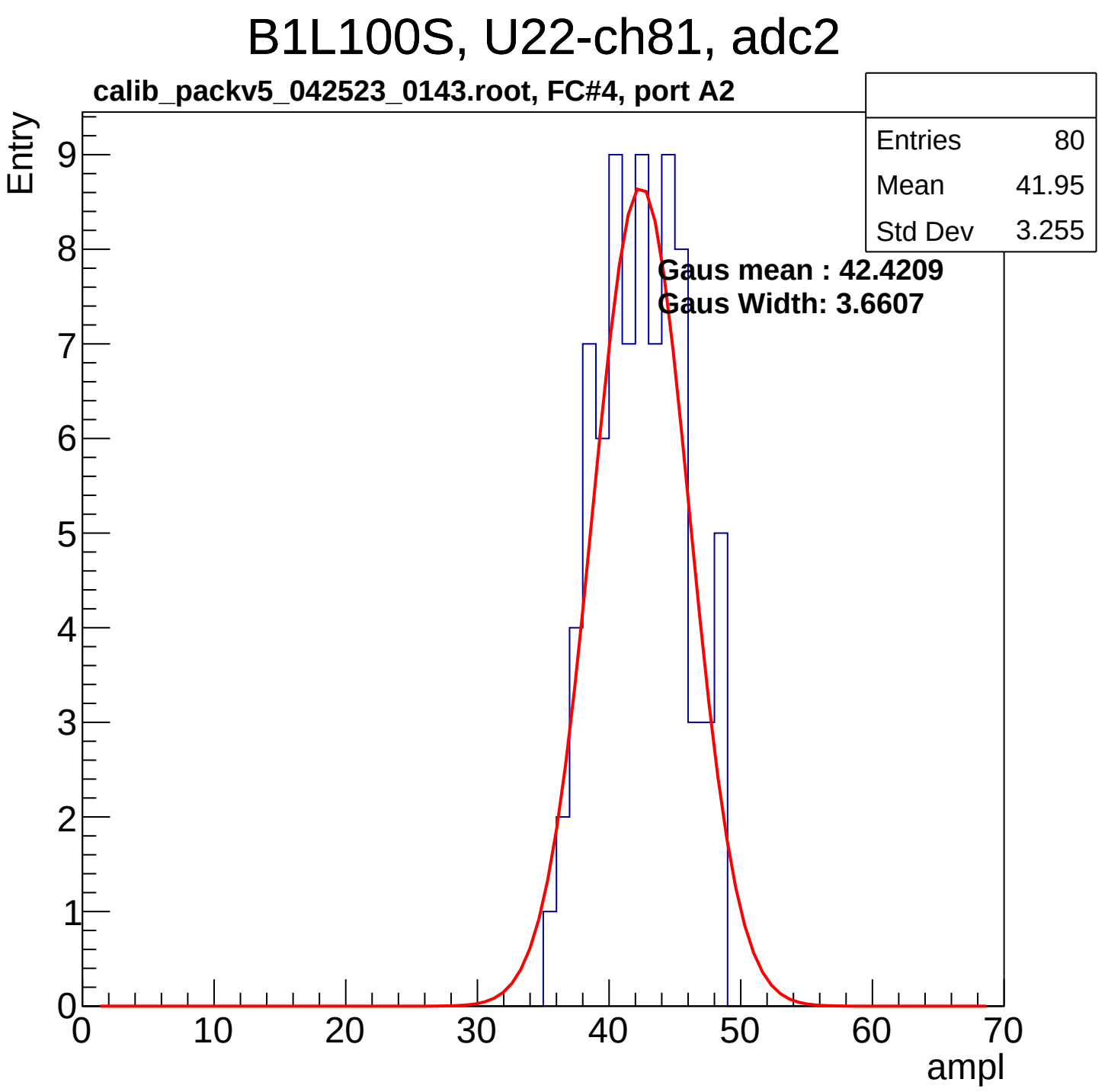

B1L100S, U22-ch81, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 34.79
Std Dev 3.037
Gaus mean : 35.3531
Gaus Width: 2.9246
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch81, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 41.95
Std Dev 3.255
Gaus mean : 42.4209
Gaus Width: 3.6607
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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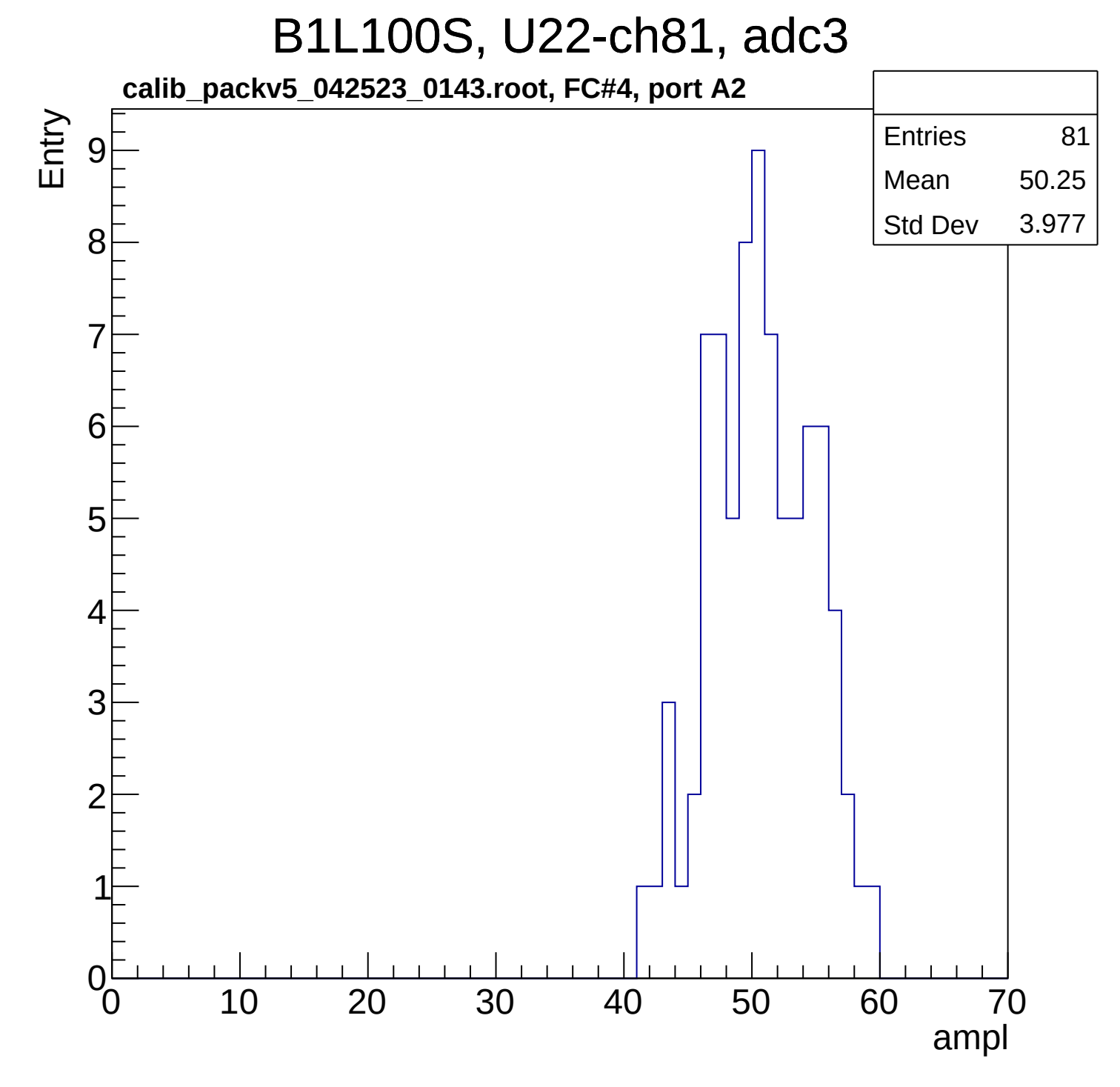
B1L100S, U22-ch81, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 50.25
Std Dev 3.977
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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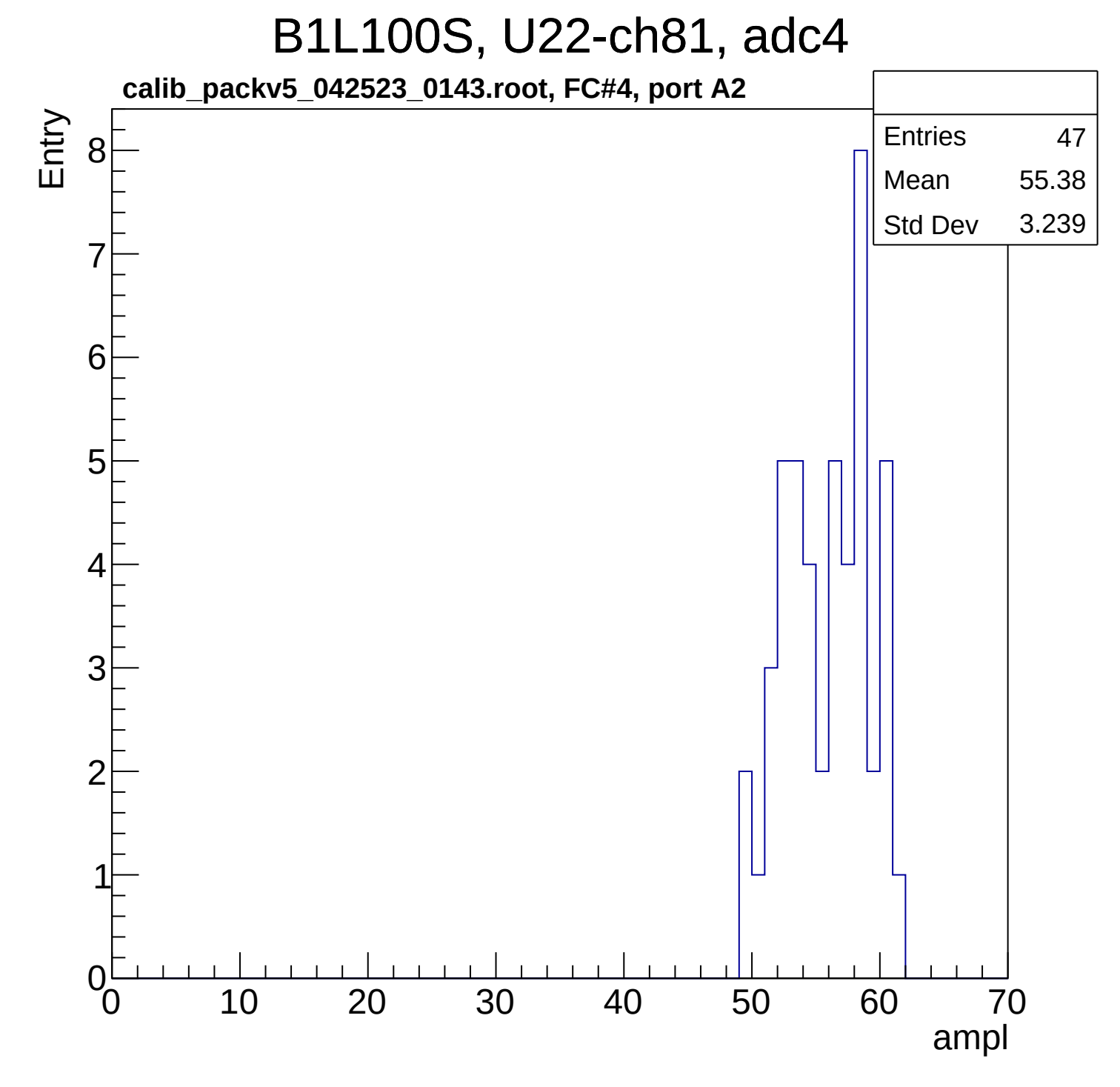
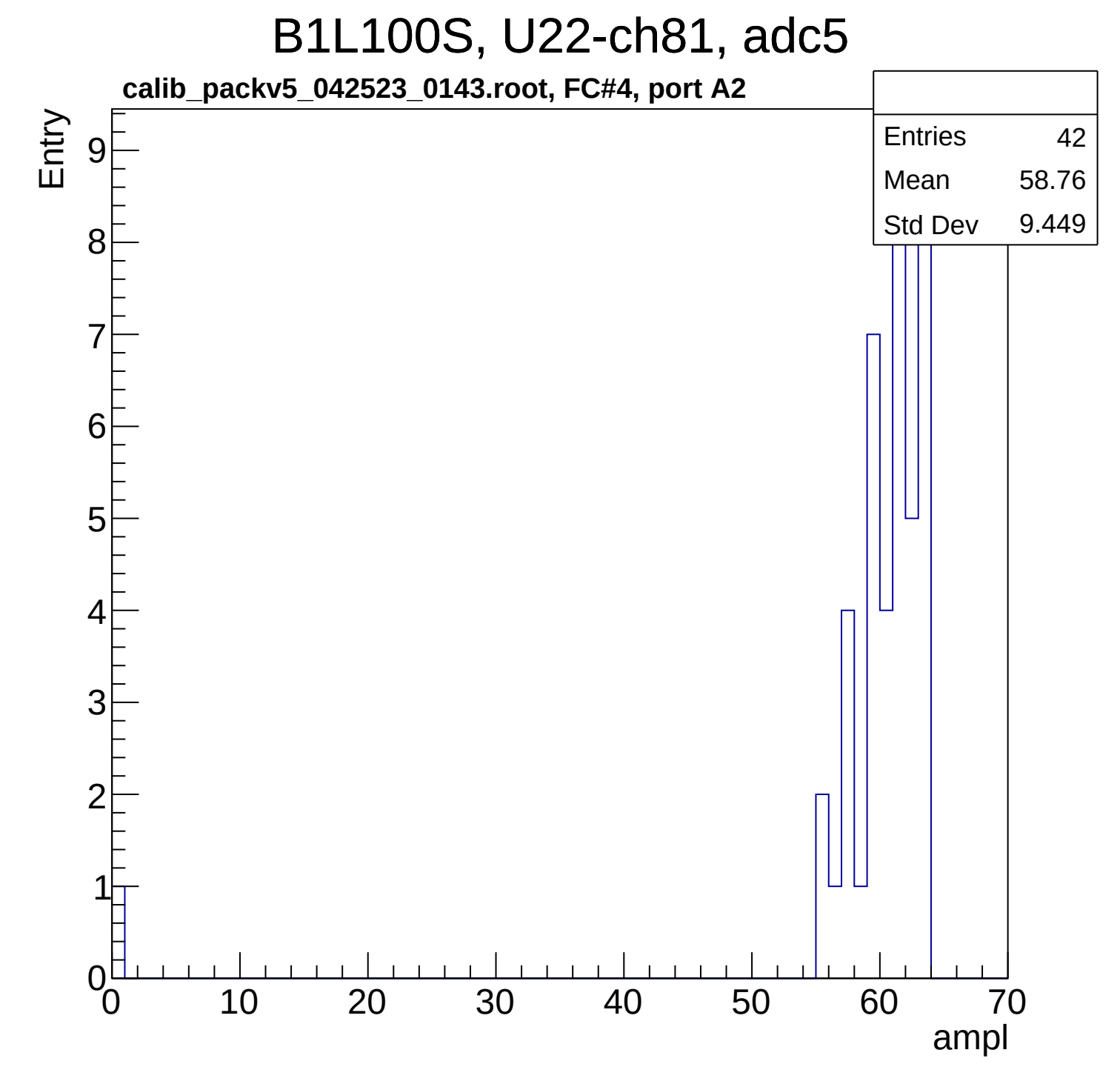

B1L100S, U22-ch81, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 55.38
Std Dev 3.239
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch81, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.76
Std Dev 9.449
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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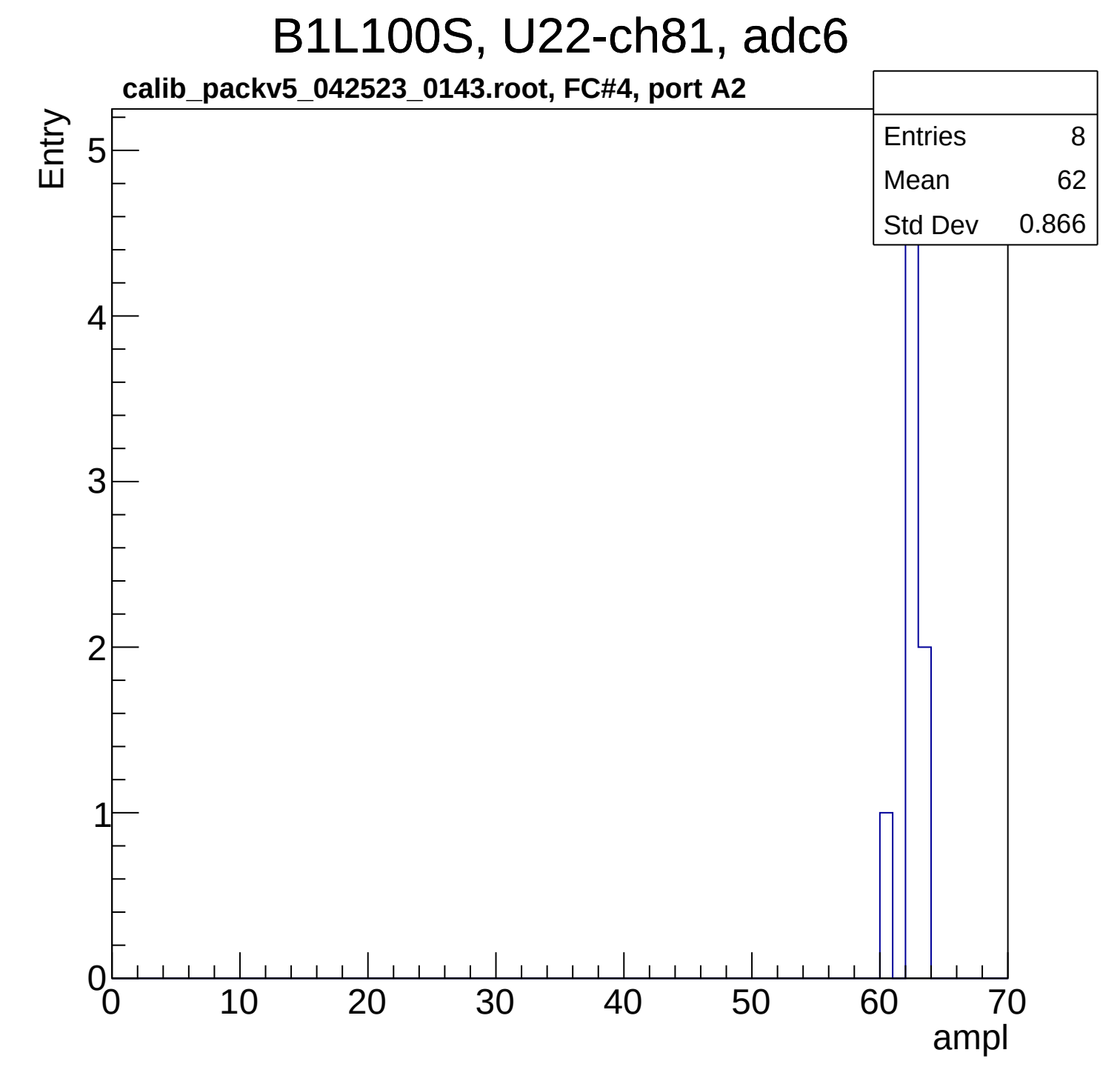
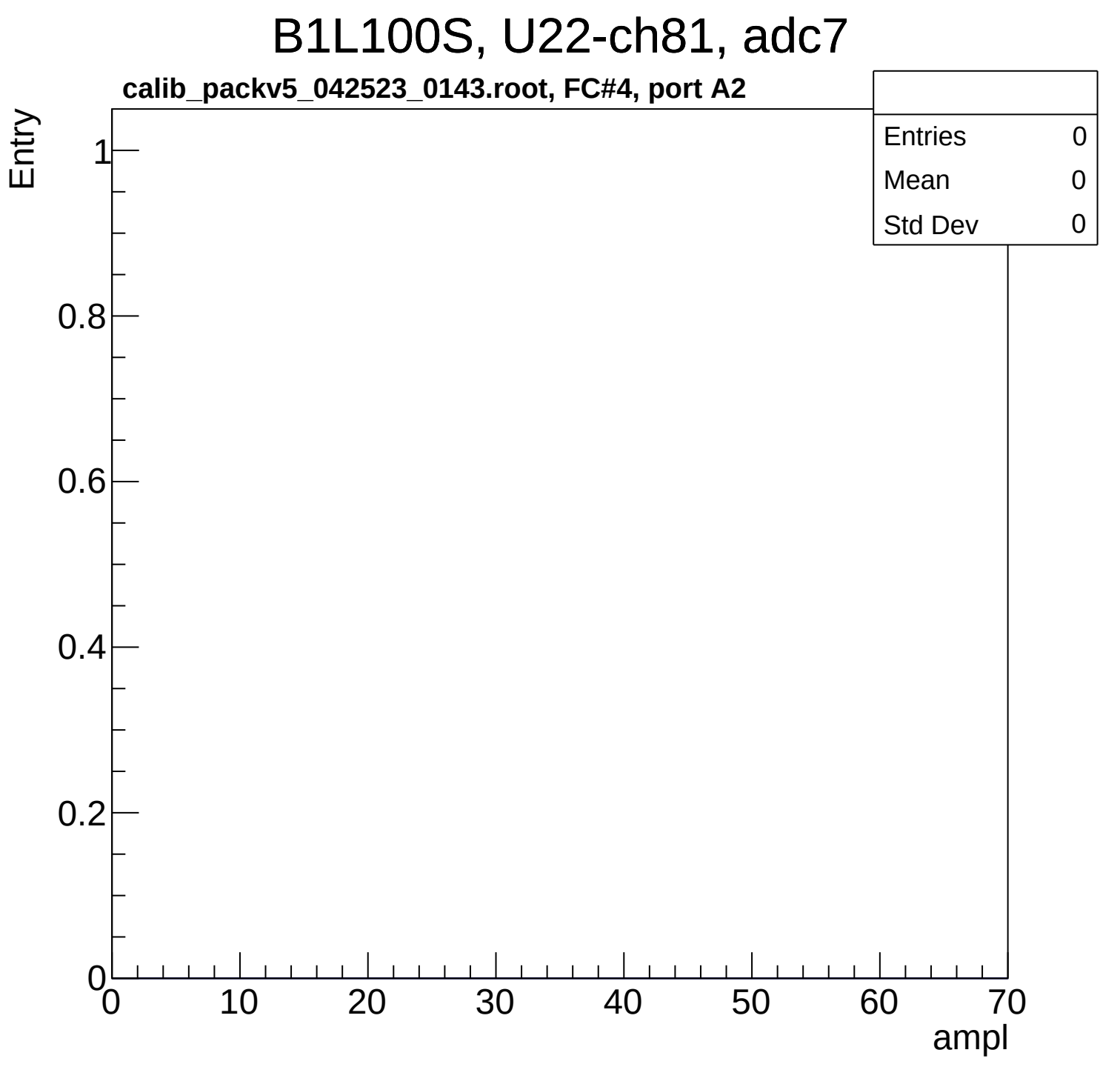
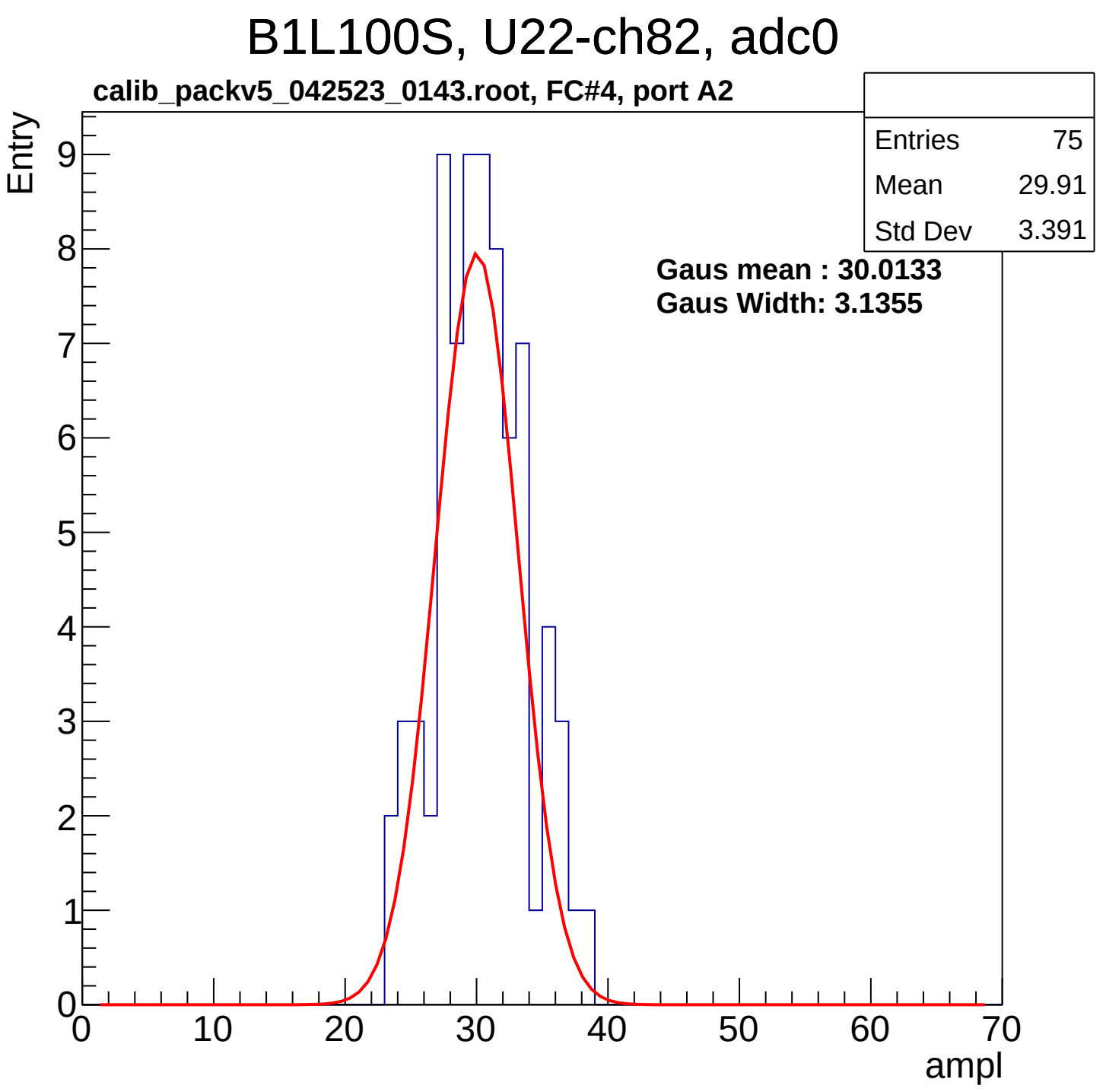
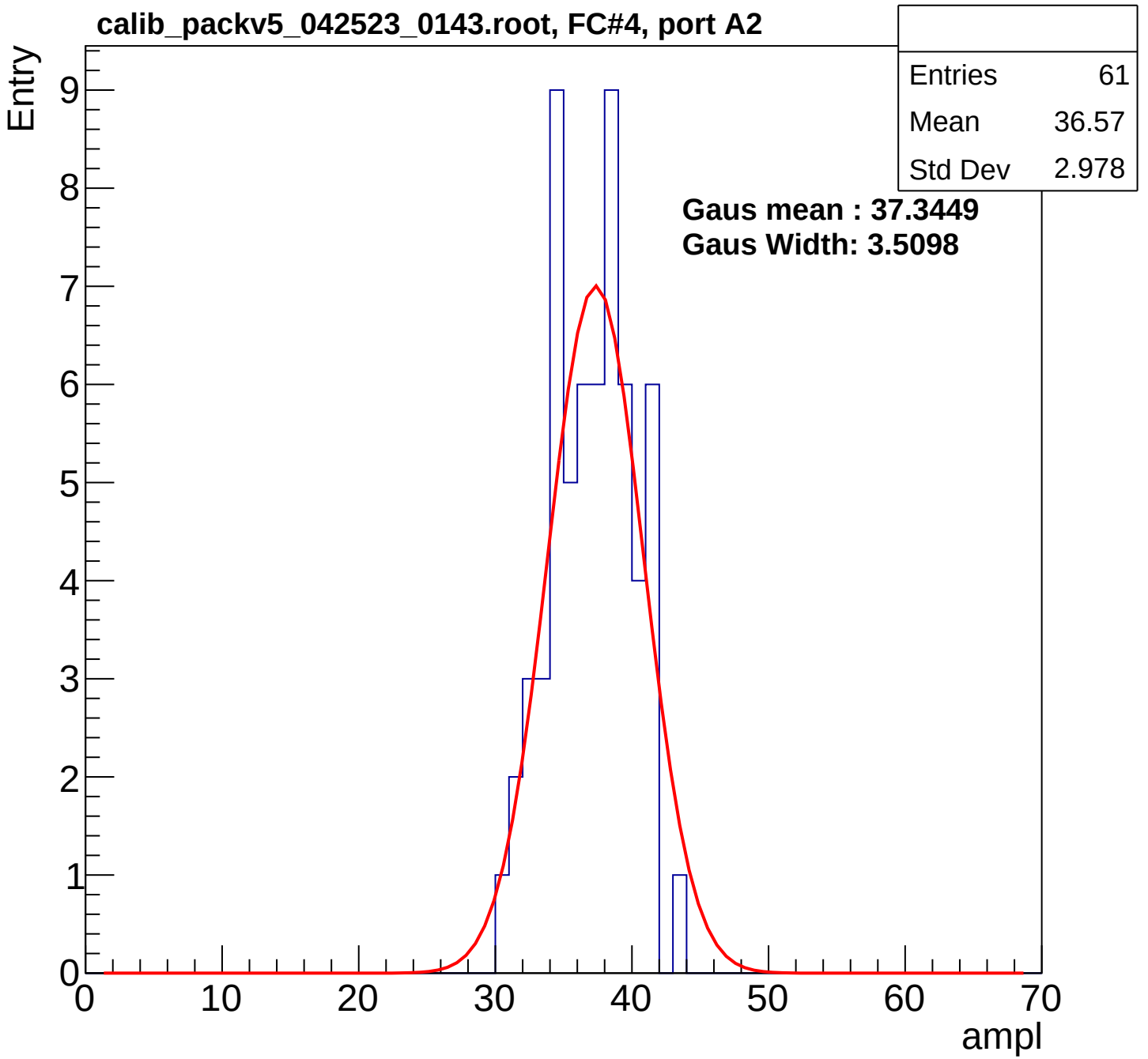
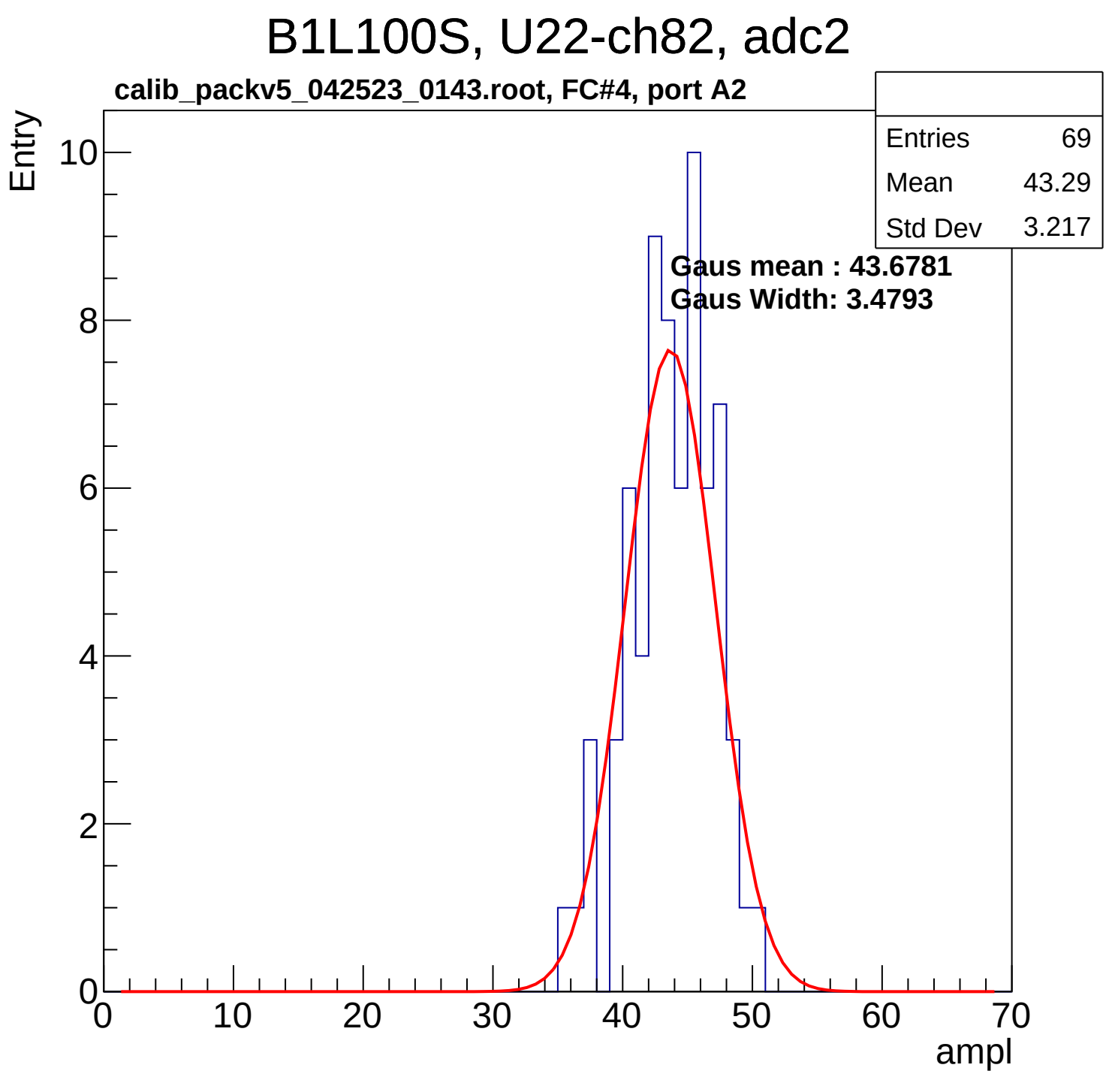
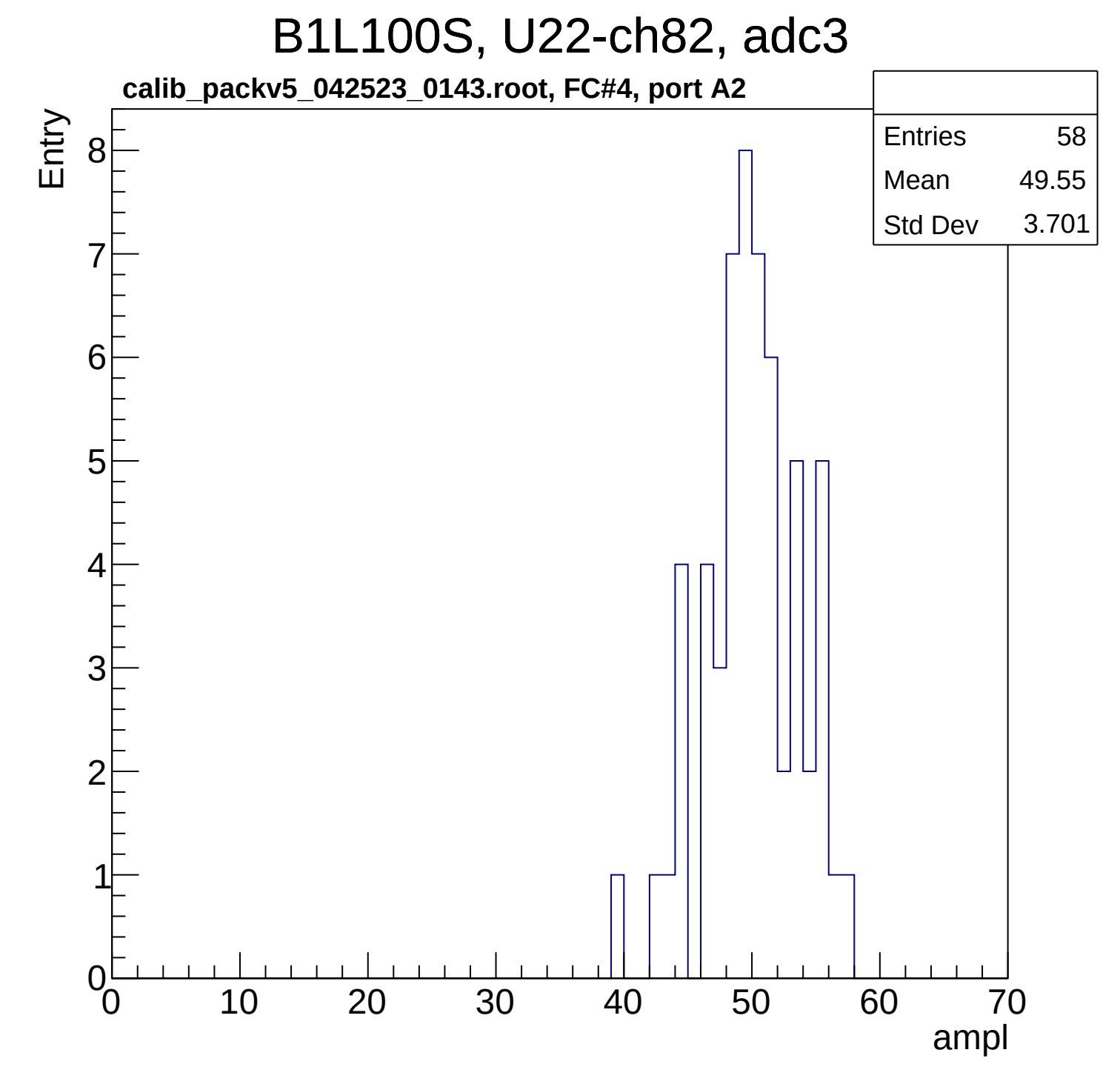

B1L100S, U22-ch81, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62
Std Dev 0.866
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U22-ch81, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch82, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.91
Std Dev 3.391
Gaus mean : 30.0133
Gaus Width: 3.1355
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 36.57
Std Dev 2.978
Gaus mean : 37.3449
Gaus Width: 3.5098
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.29
Std Dev 3.217
Gaus mean : 43.6781
Gaus Width: 3.4793
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 49.55
Std Dev 3.701
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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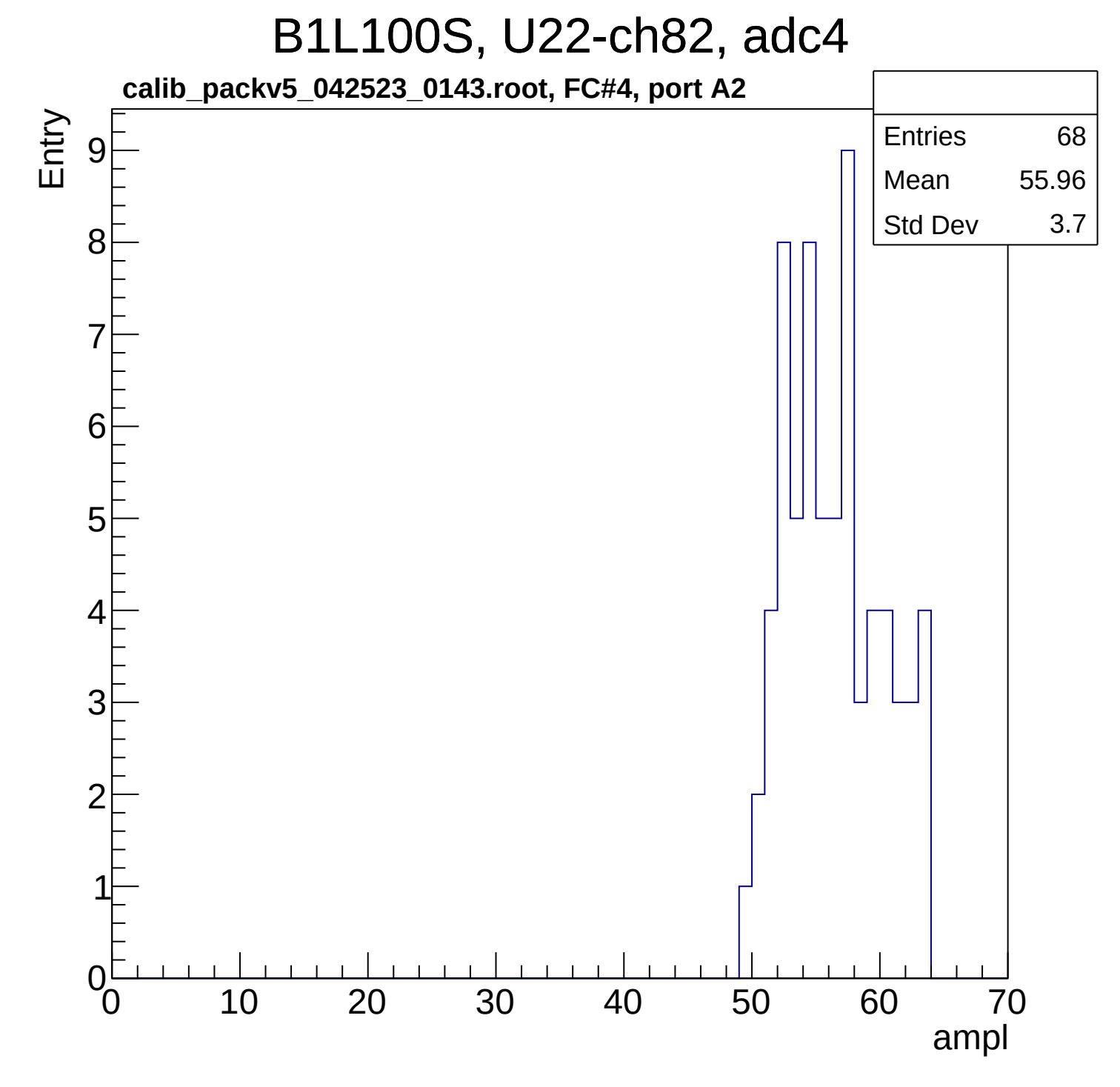
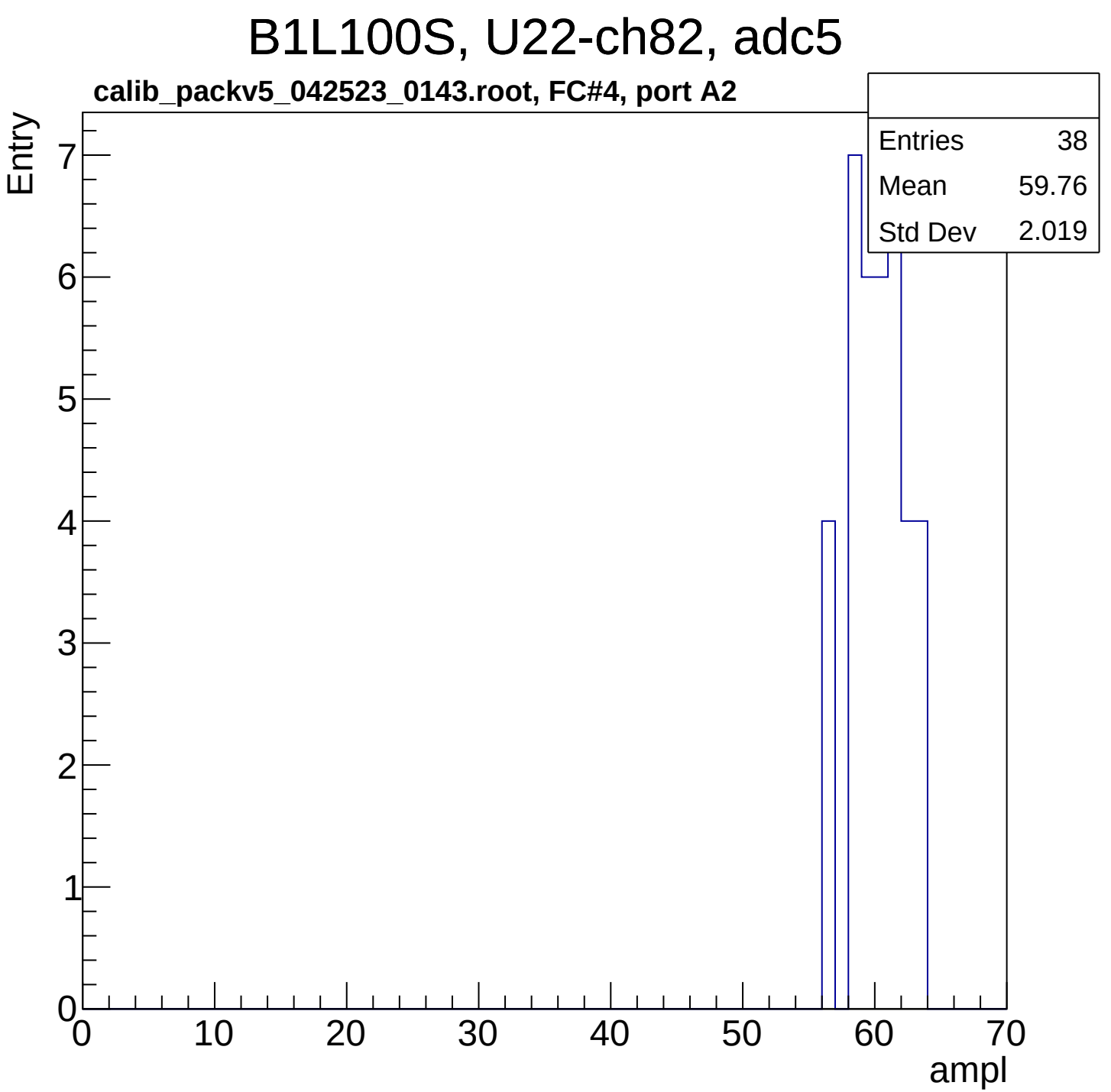
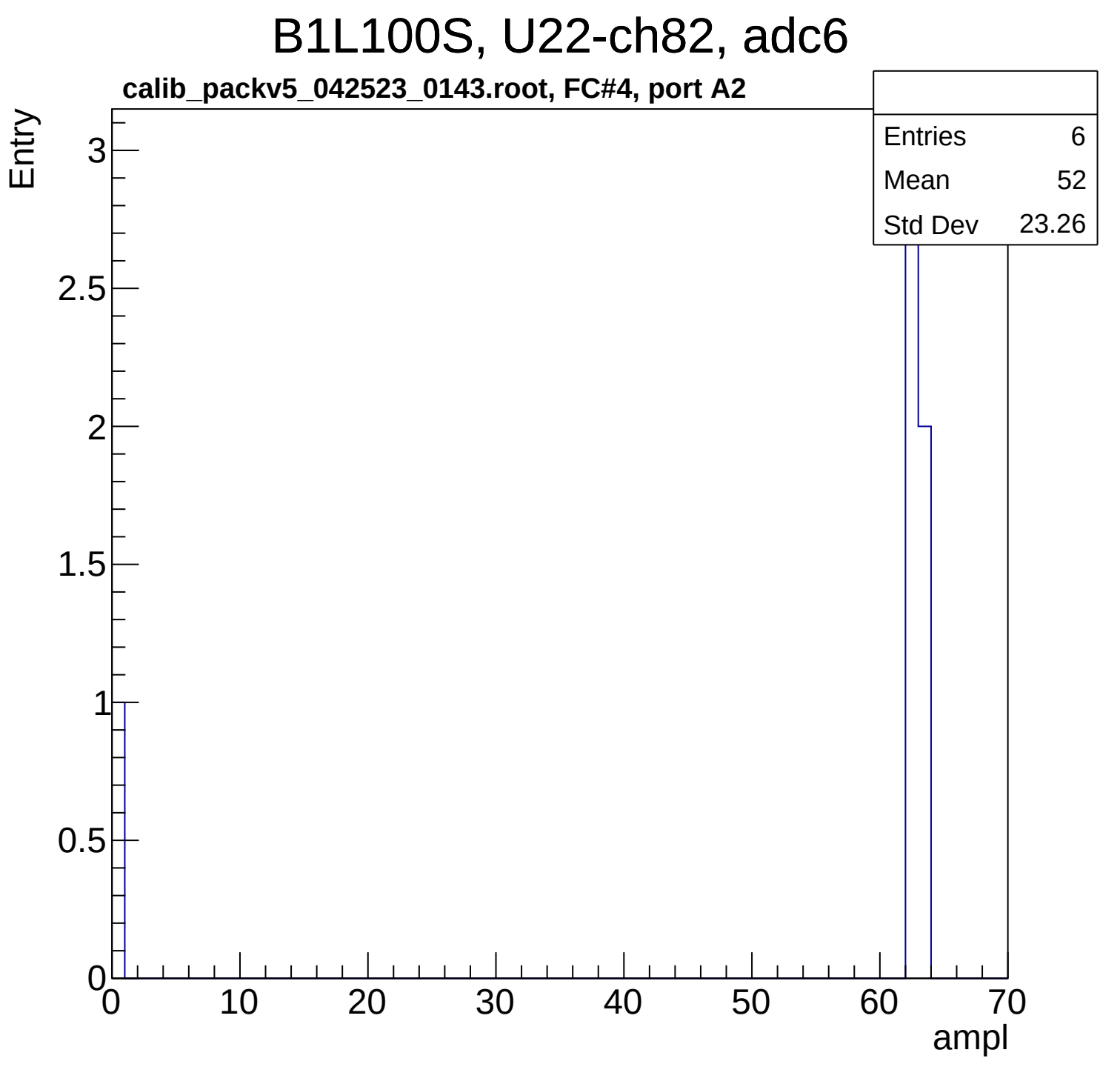
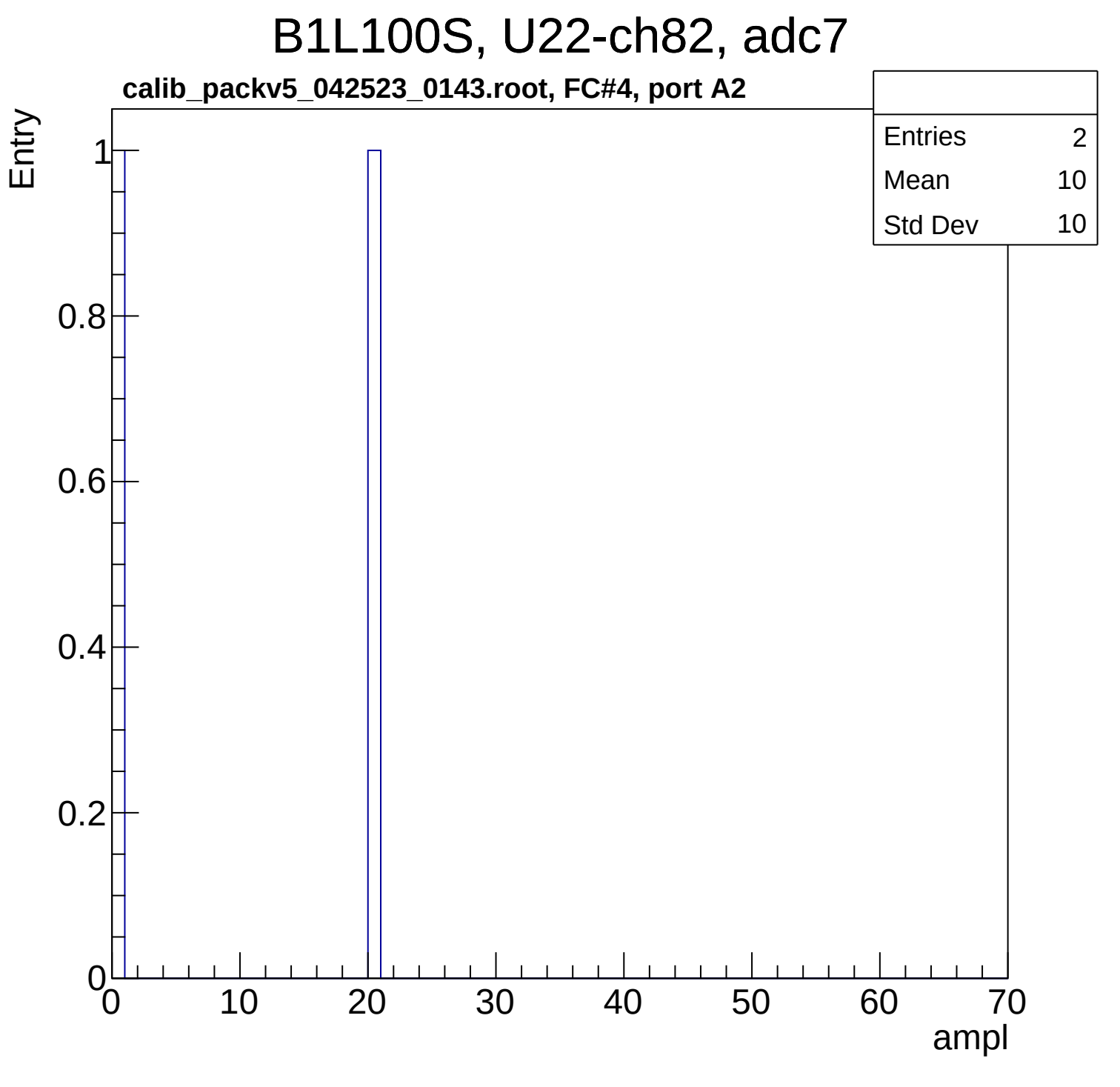

B1L100S, U22-ch82, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 55.96
Std Dev 3.7
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 59.76
Std Dev 2.019
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 52
Std Dev 23.26
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch82, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10
Std Dev 10
Entry
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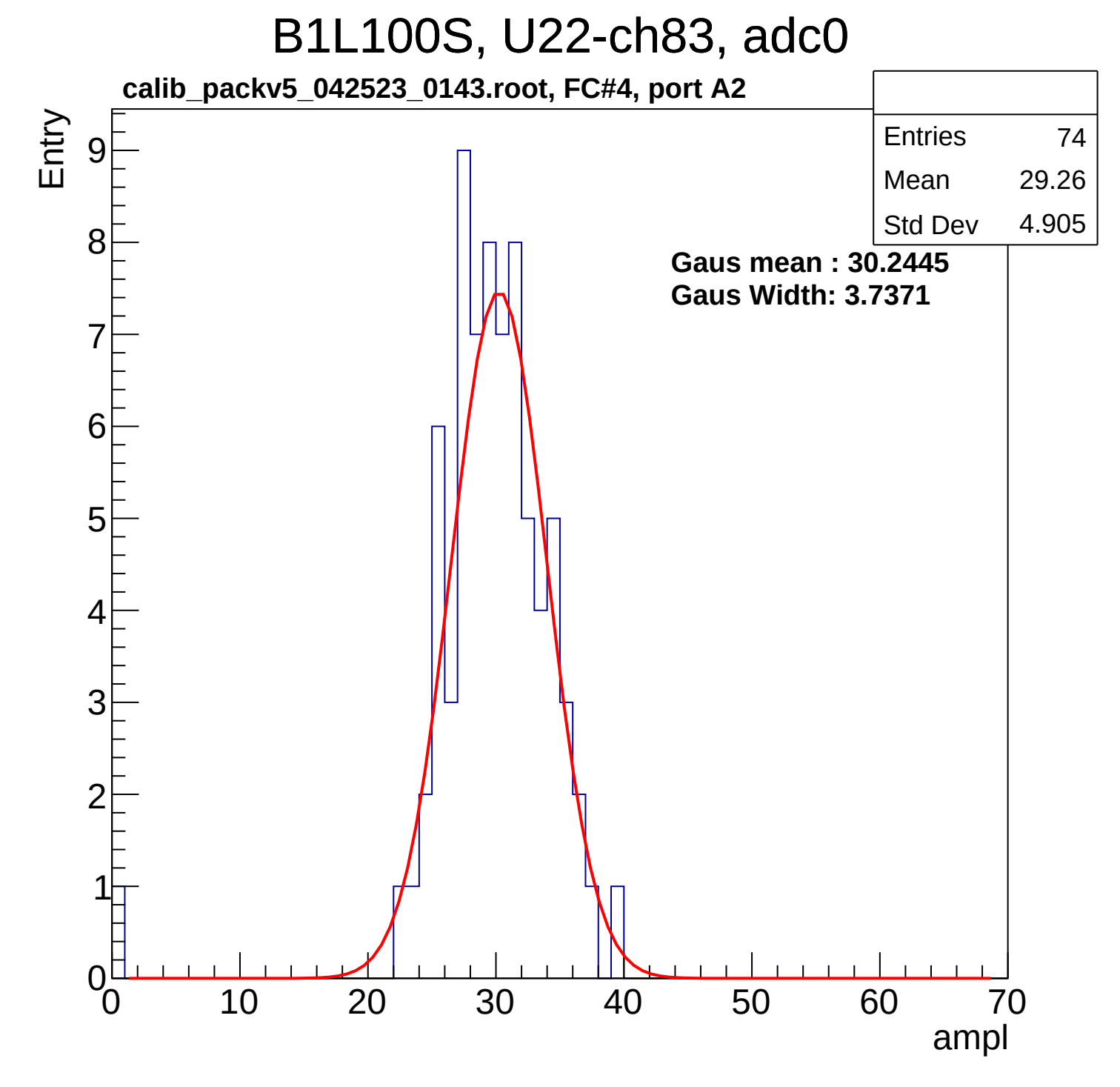
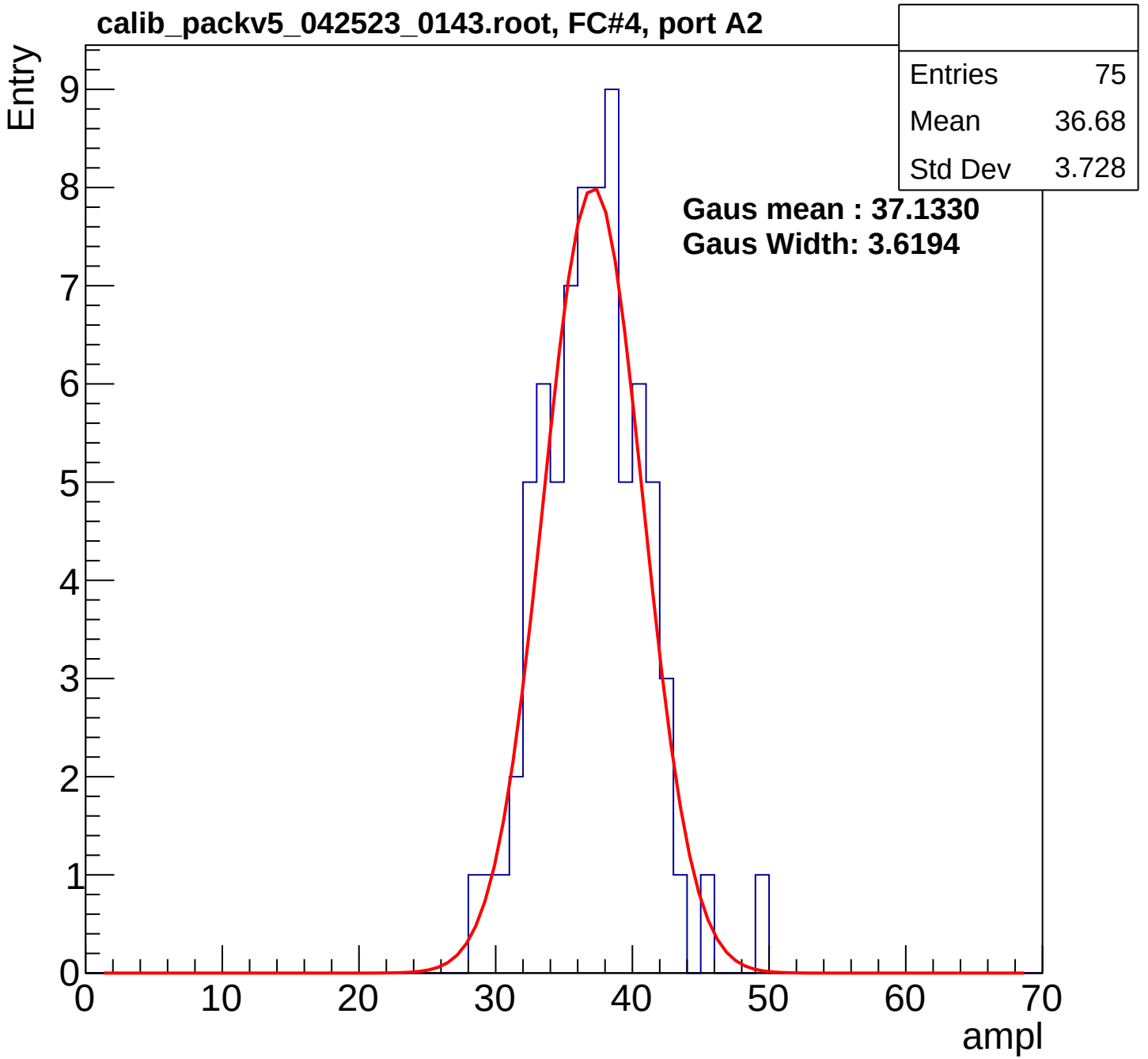
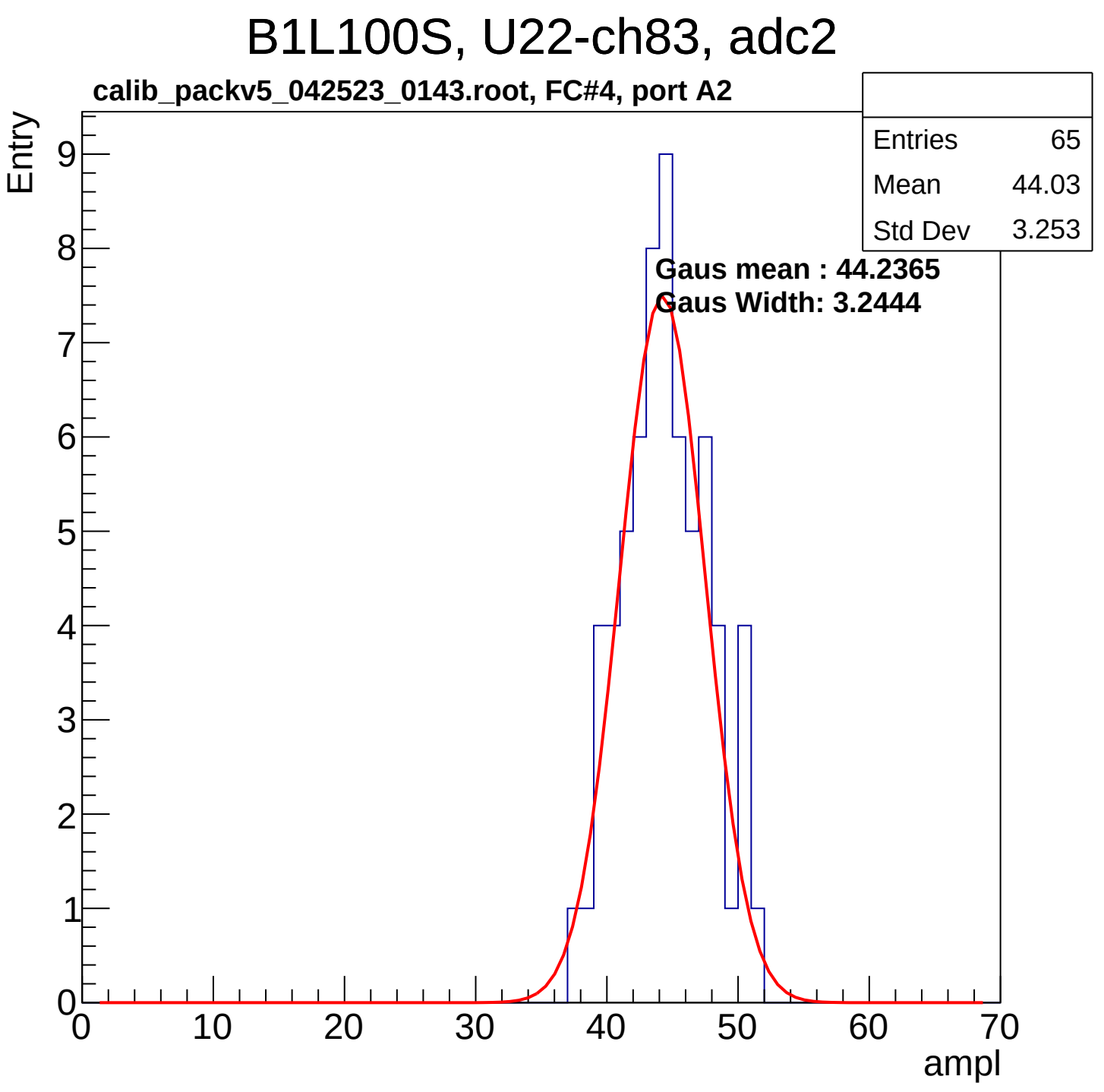
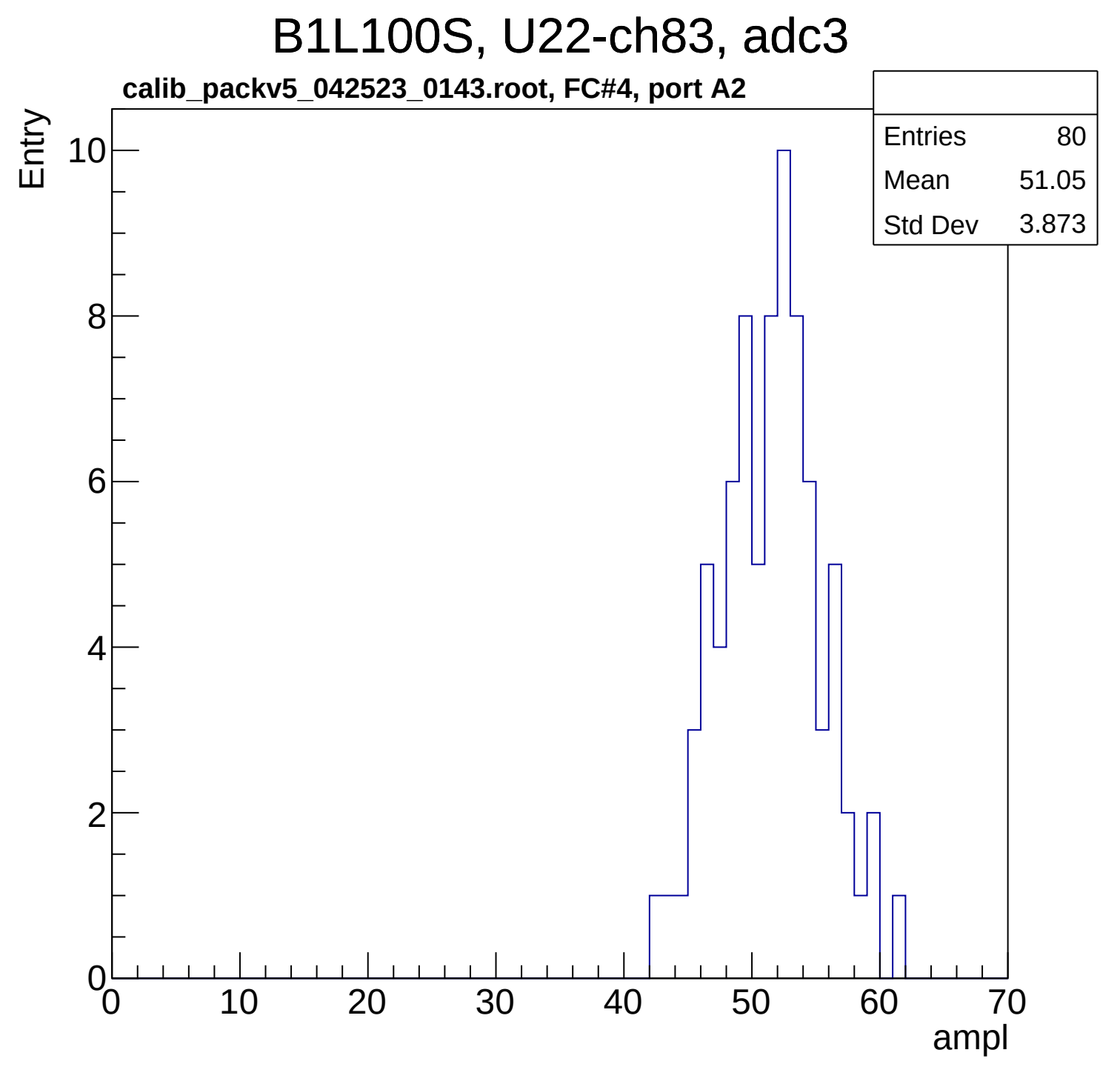
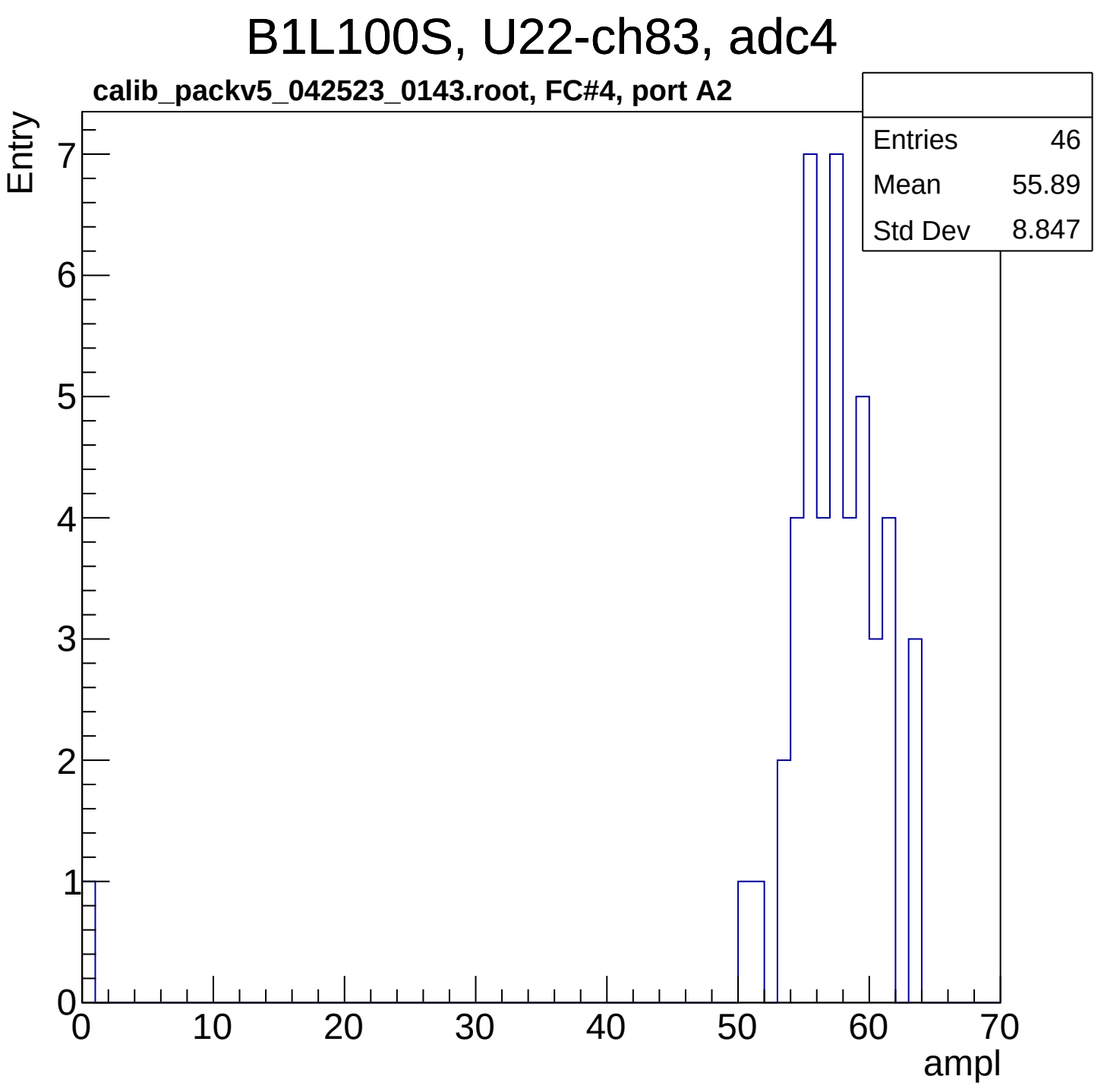
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch83, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 29.26
Std Dev 4.905
Gaus mean : 30.2445
Gaus Width: 3.7371
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch83, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 36.68
Std Dev 3.728
Gaus mean : 37.1330
Gaus Width: 3.6194
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch83, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 44.03
Std Dev 3.253
Gaus mean : 44.2365
Gaus Width: 3.2444
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch83, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 51.05
Std Dev 3.873
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch83, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 55.89
Std Dev 8.847
Entry
ampl
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2
3
4
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6
7
0
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40
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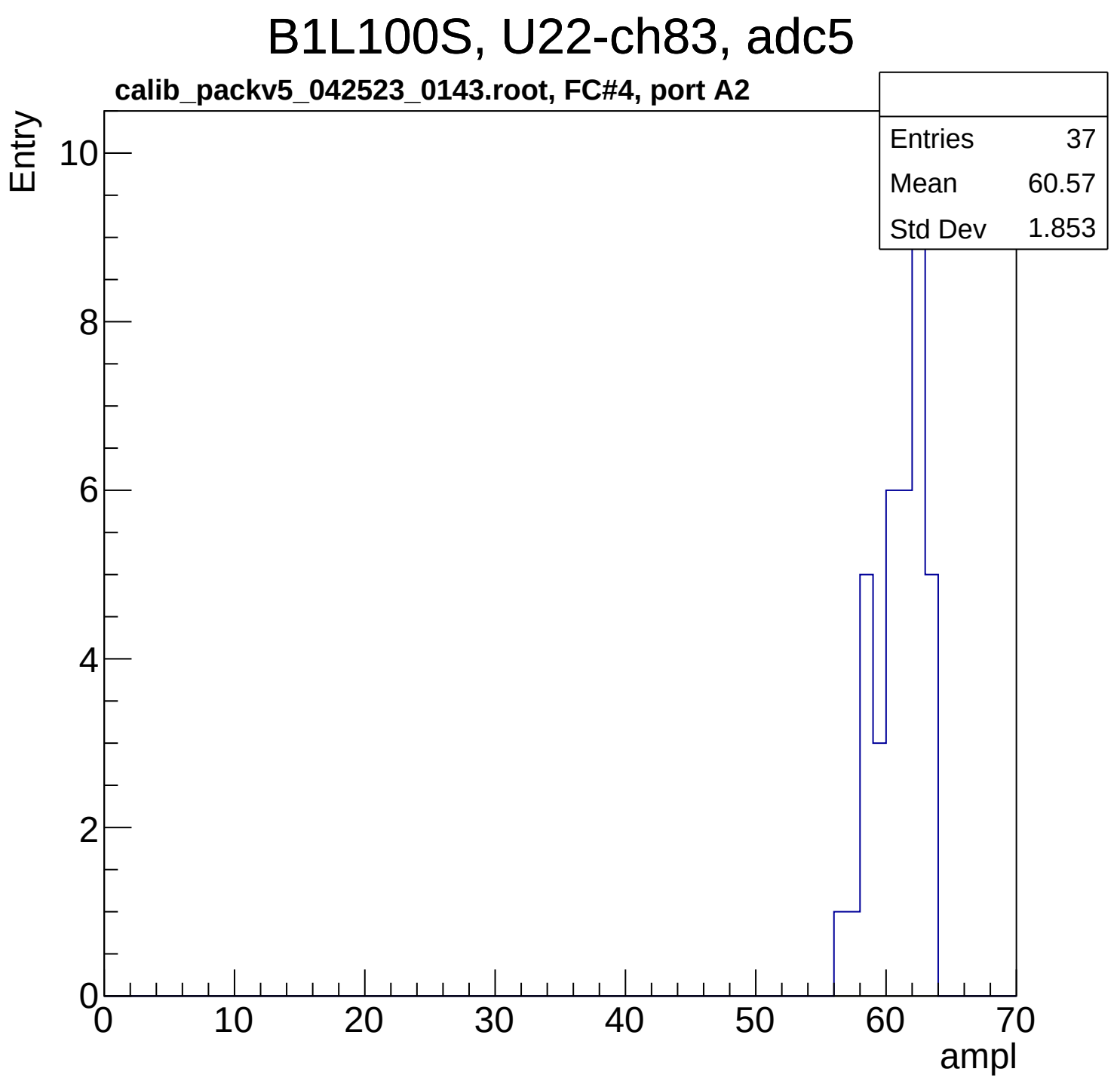
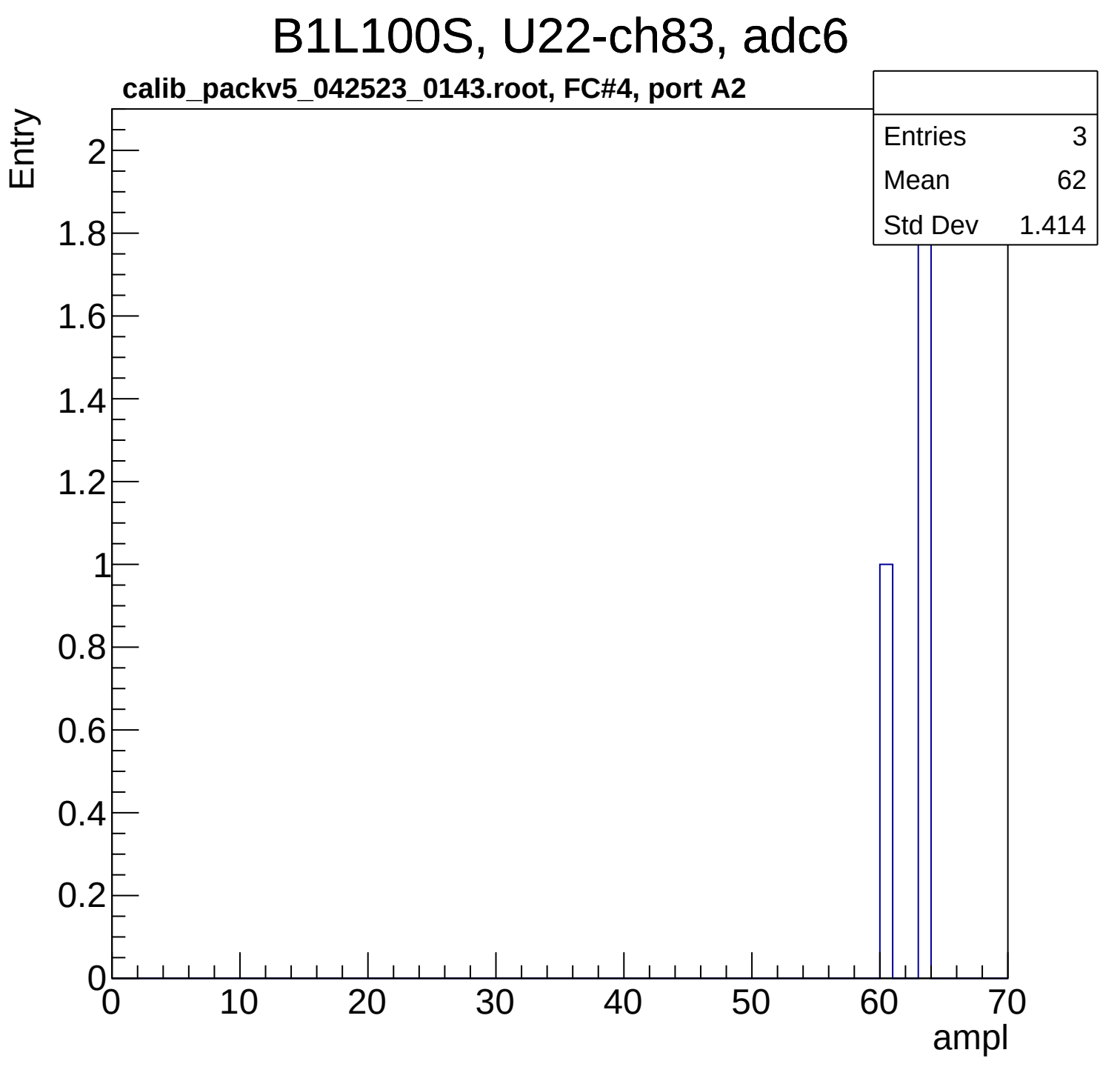
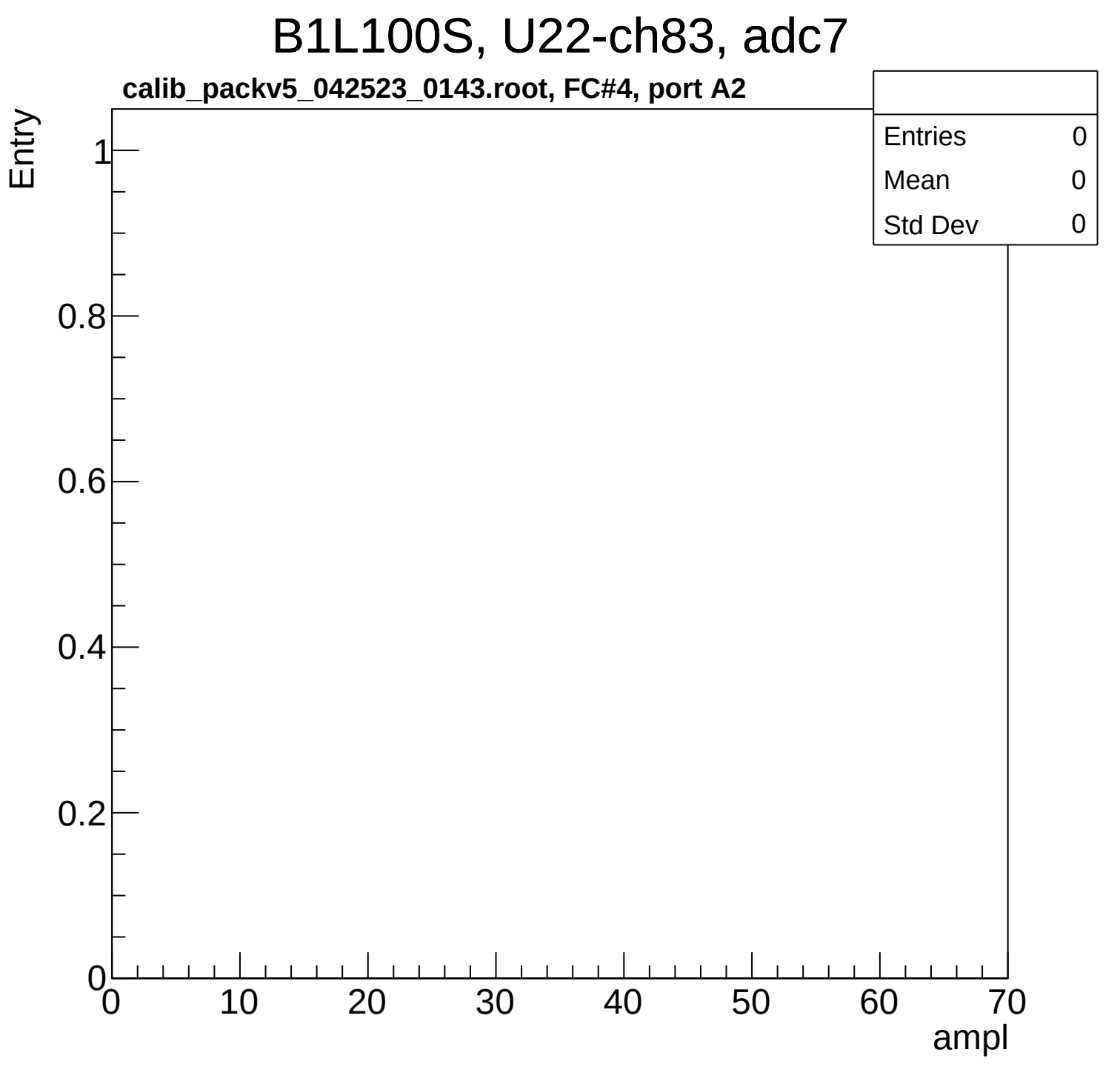
60
70


B1L100S, U22-ch83, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 37
Mean 60.57
Std Dev 1.853
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch83, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 1.414
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch83, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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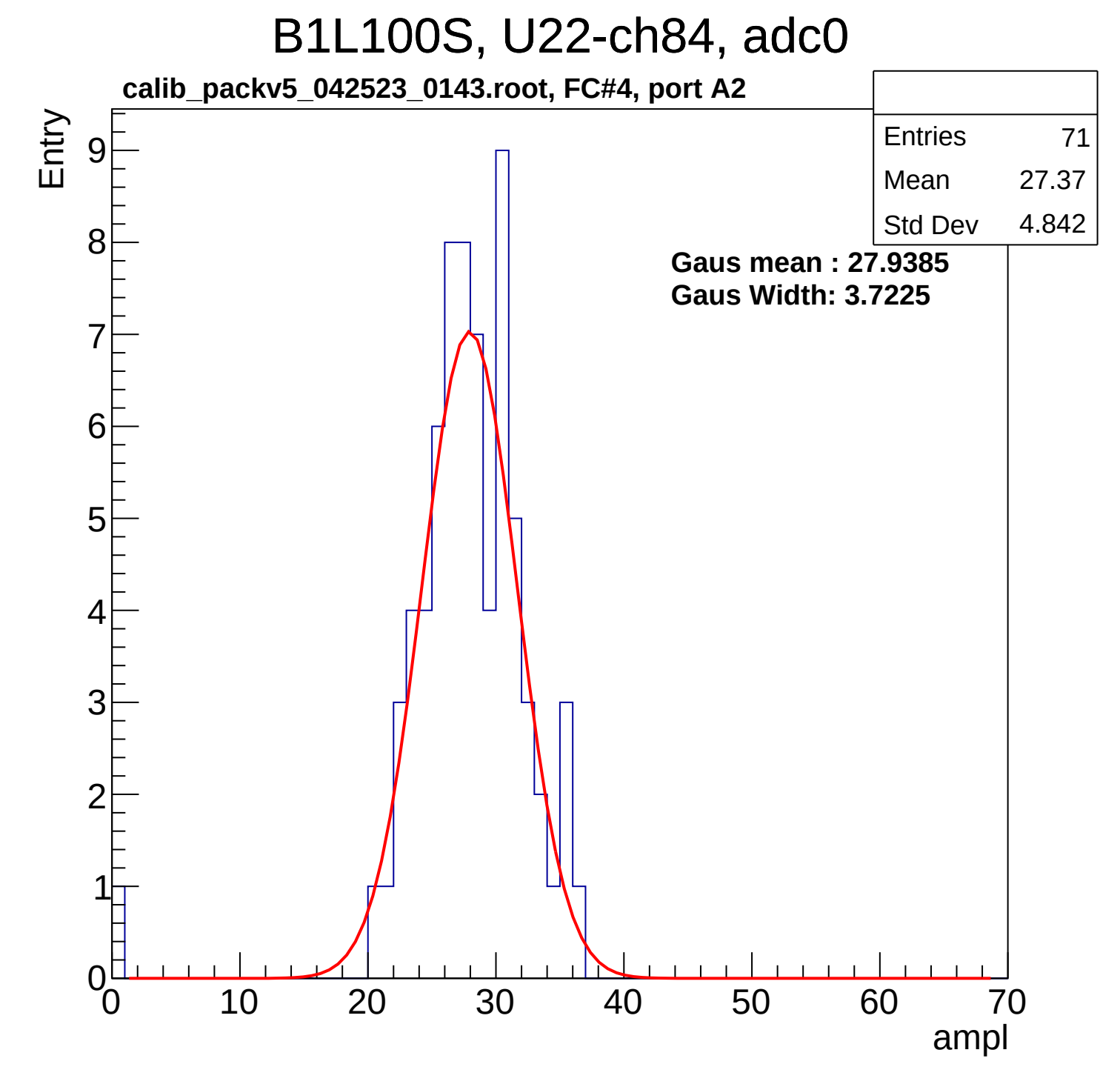
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch84, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 27.37
Std Dev 4.842
Gaus mean : 27.9385
Gaus Width: 3.7225
Entry
ampl
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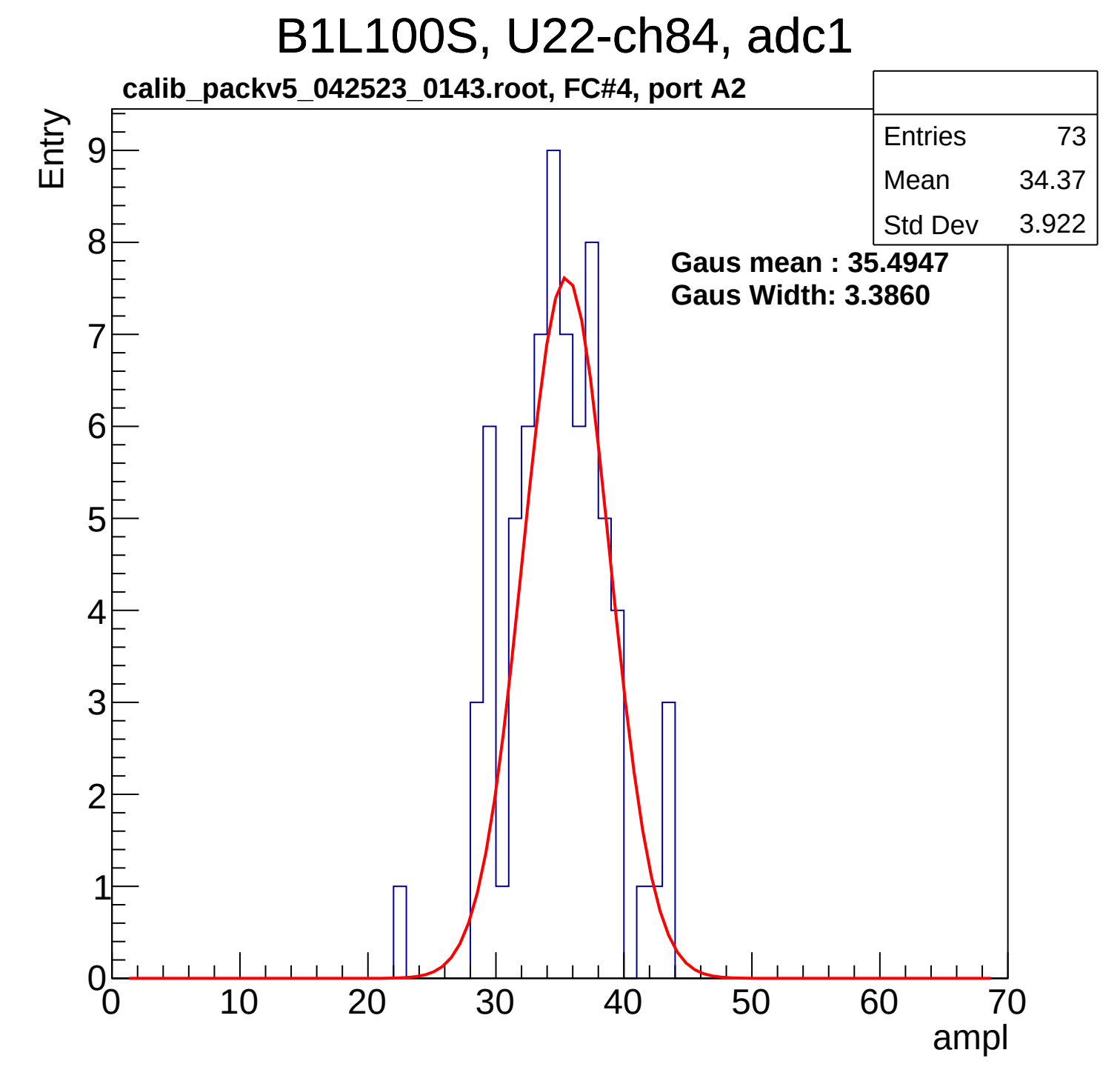
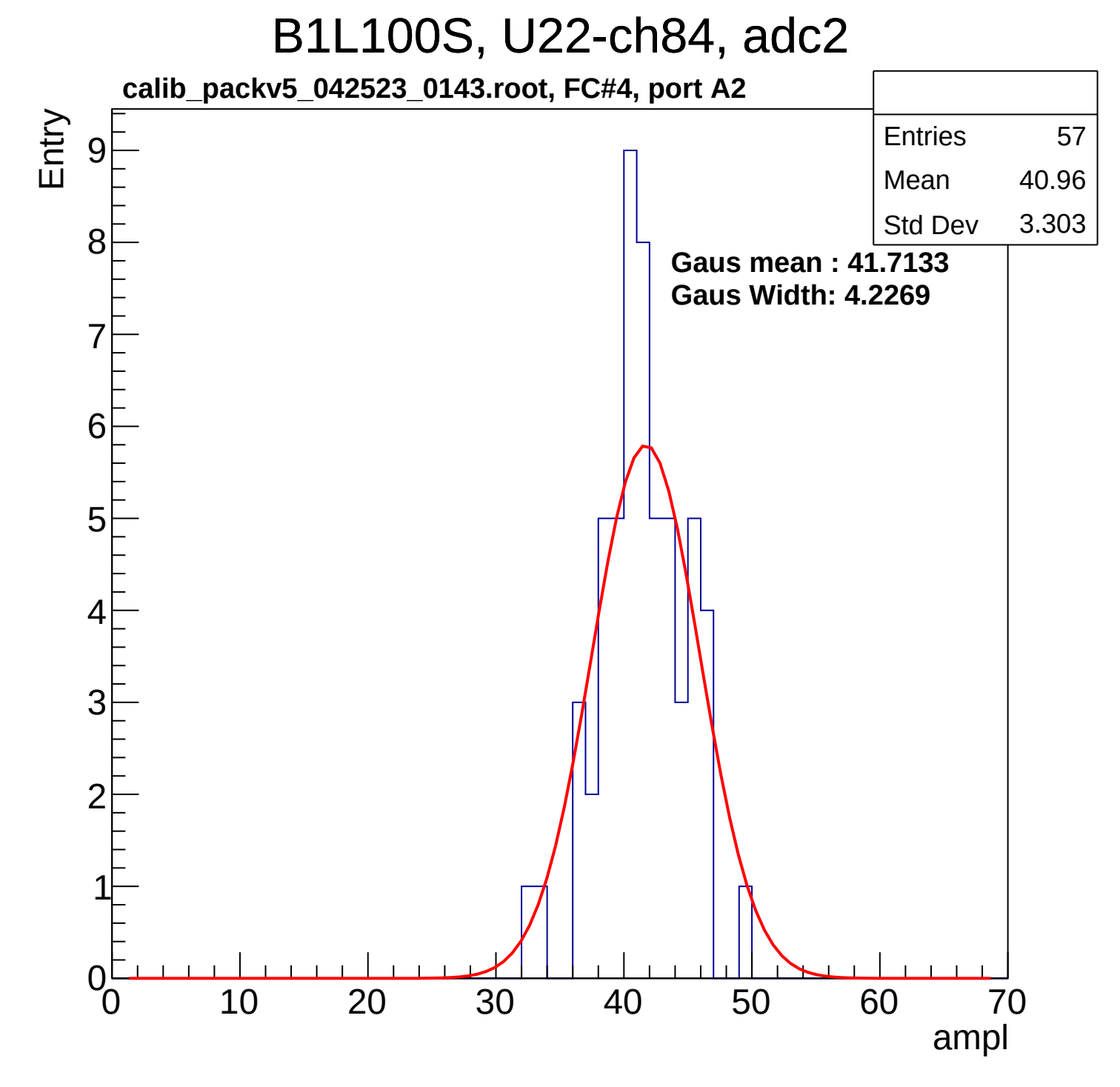
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch84, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 34.37
Std Dev 3.922
Gaus mean : 35.4947
Gaus Width: 3.3860
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch84, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 40.96
Std Dev 3.303
Gaus mean : 41.7133
Gaus Width: 4.2269
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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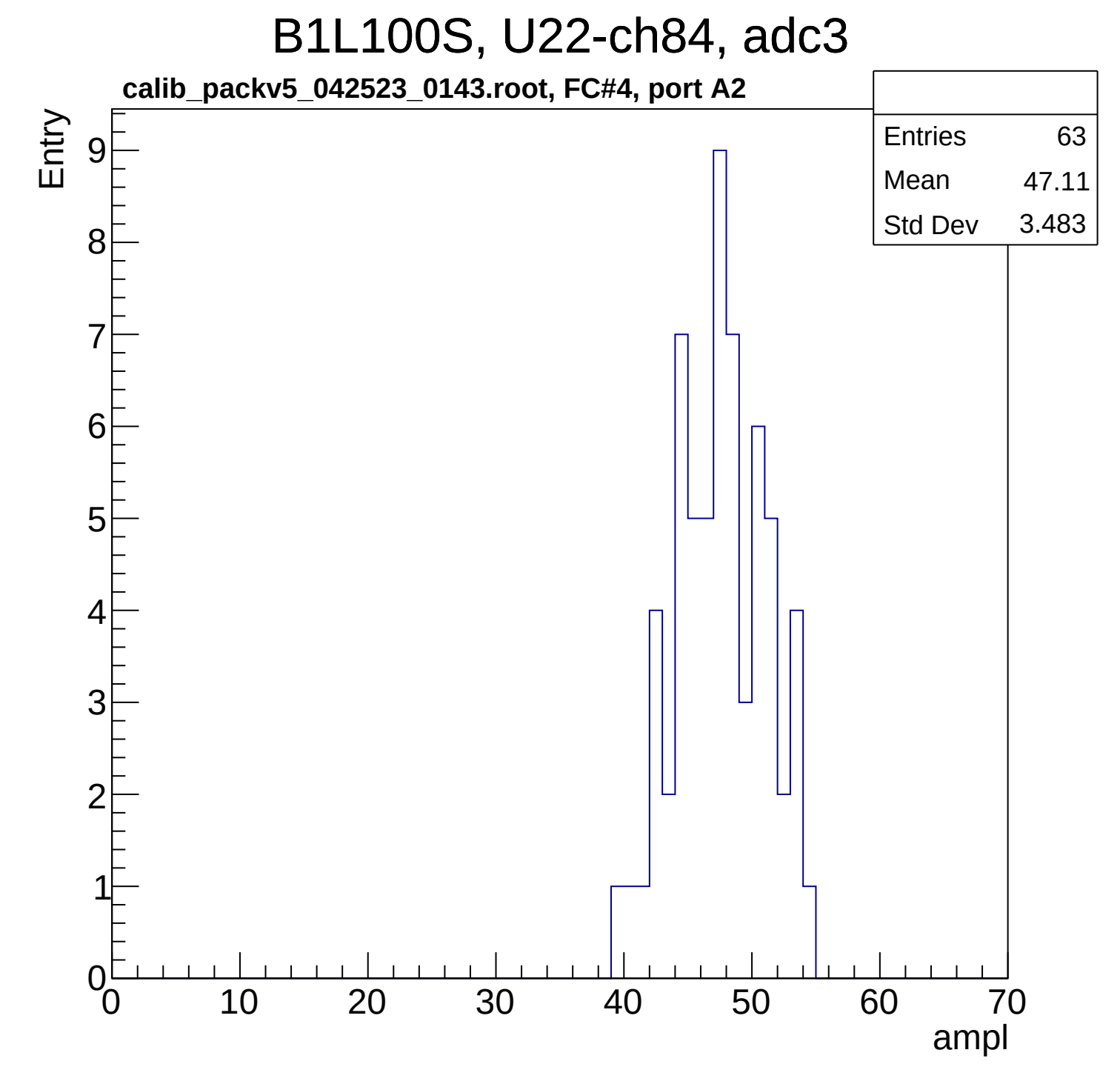
B1L100S, U22-ch84, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 47.11
Std Dev 3.483
Entry
ampl
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3
4
5
6
7
8
9
0
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40
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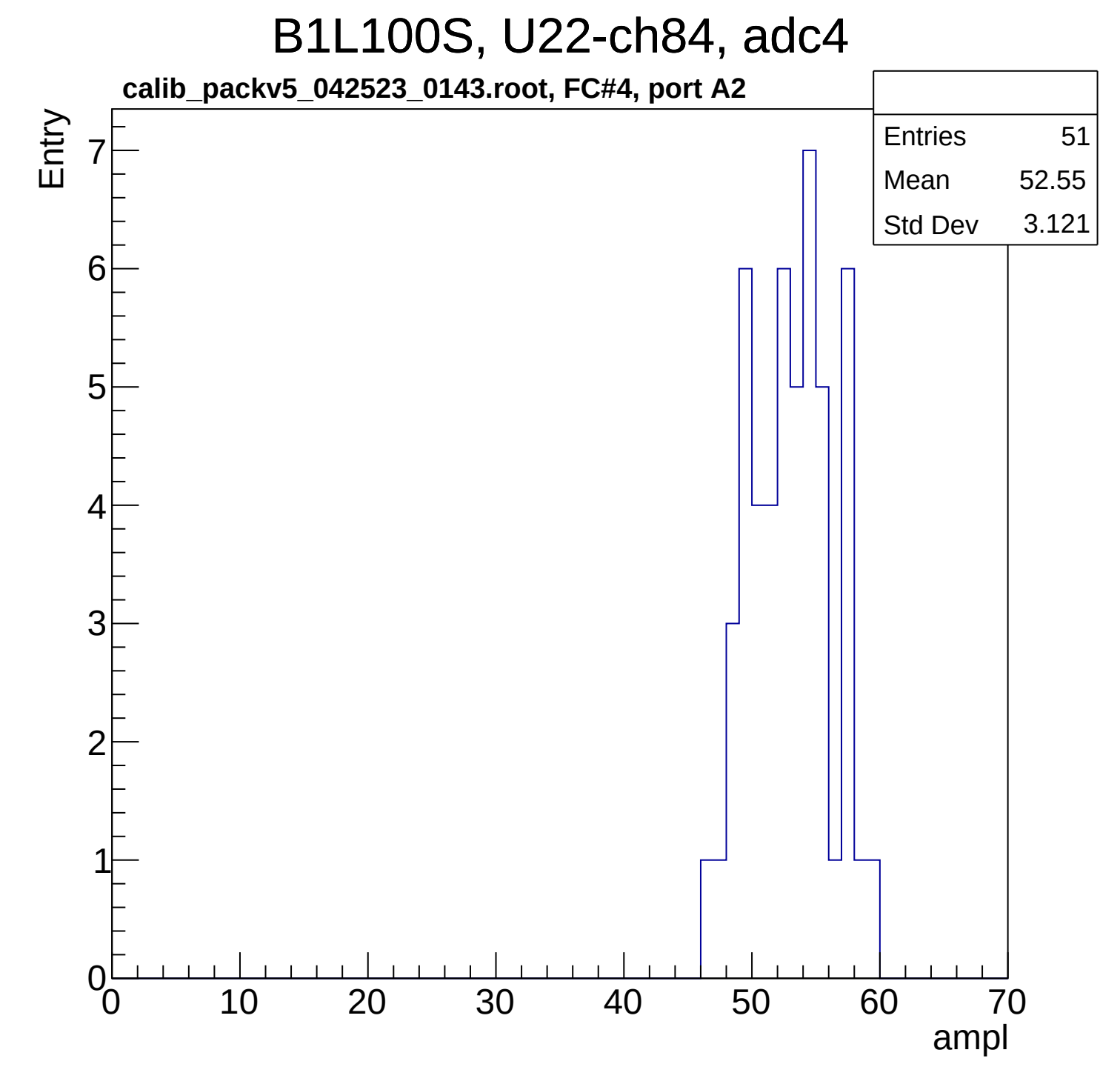
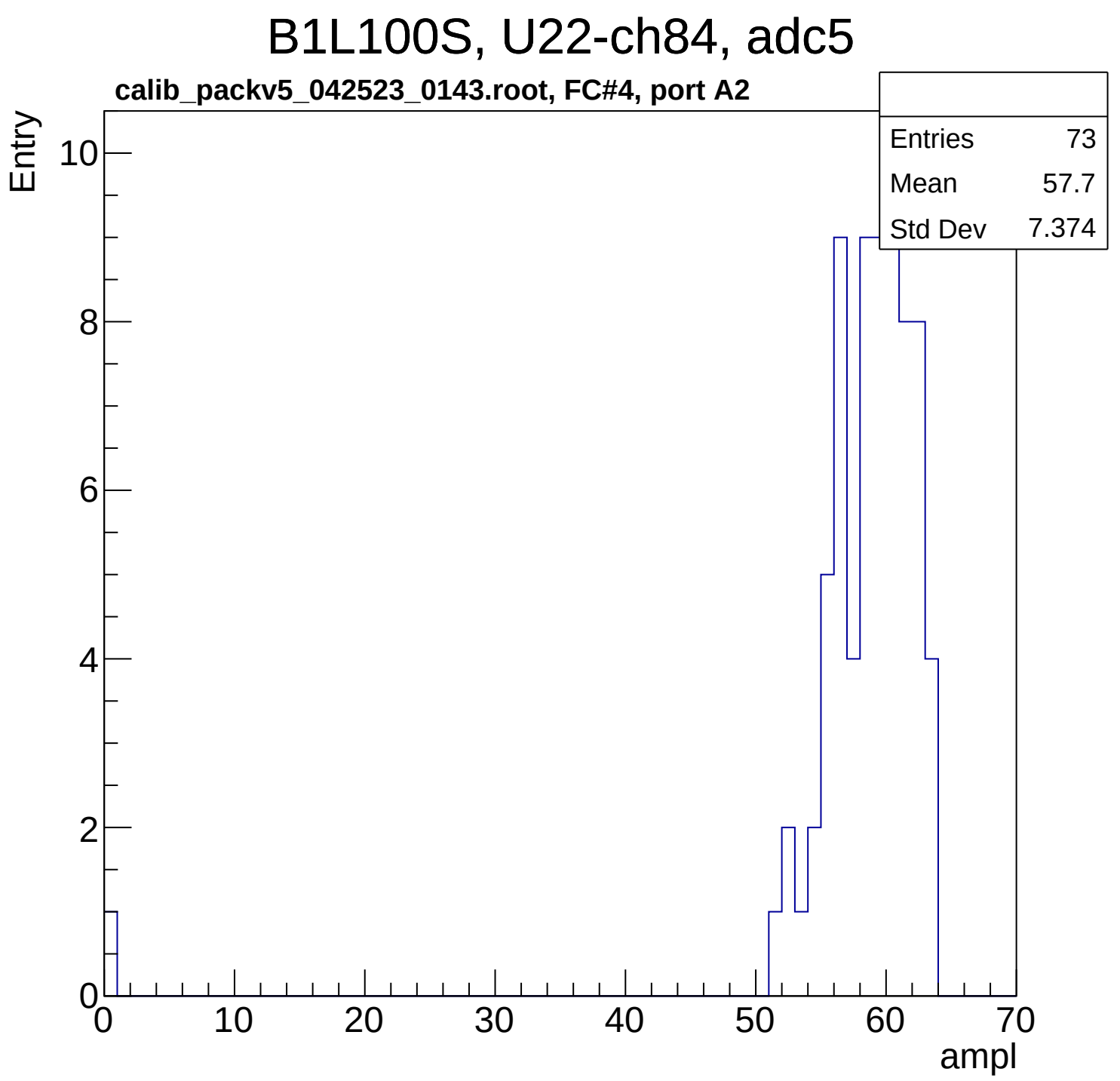

B1L100S, U22-ch84, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 52.55
Std Dev 3.121
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch84, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 57.7
Std Dev 7.374
Entry
ampl
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2
4
6
8
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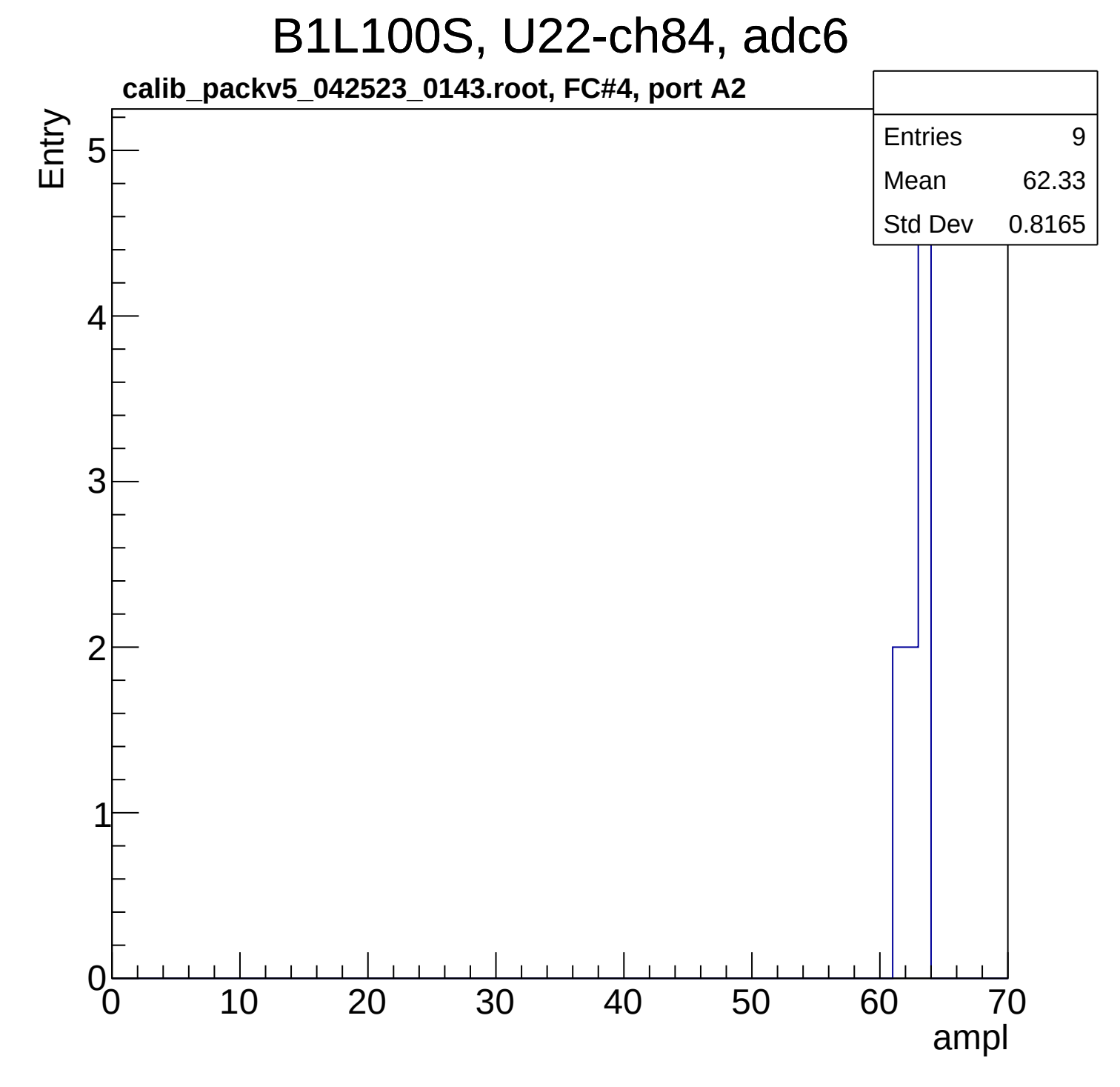
0
10
20
30
40
50
60
70


B1L100S, U22-ch84, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 62.33
Std Dev 0.8165
Entry
ampl
0
1
2
3
4
5
0
10
20
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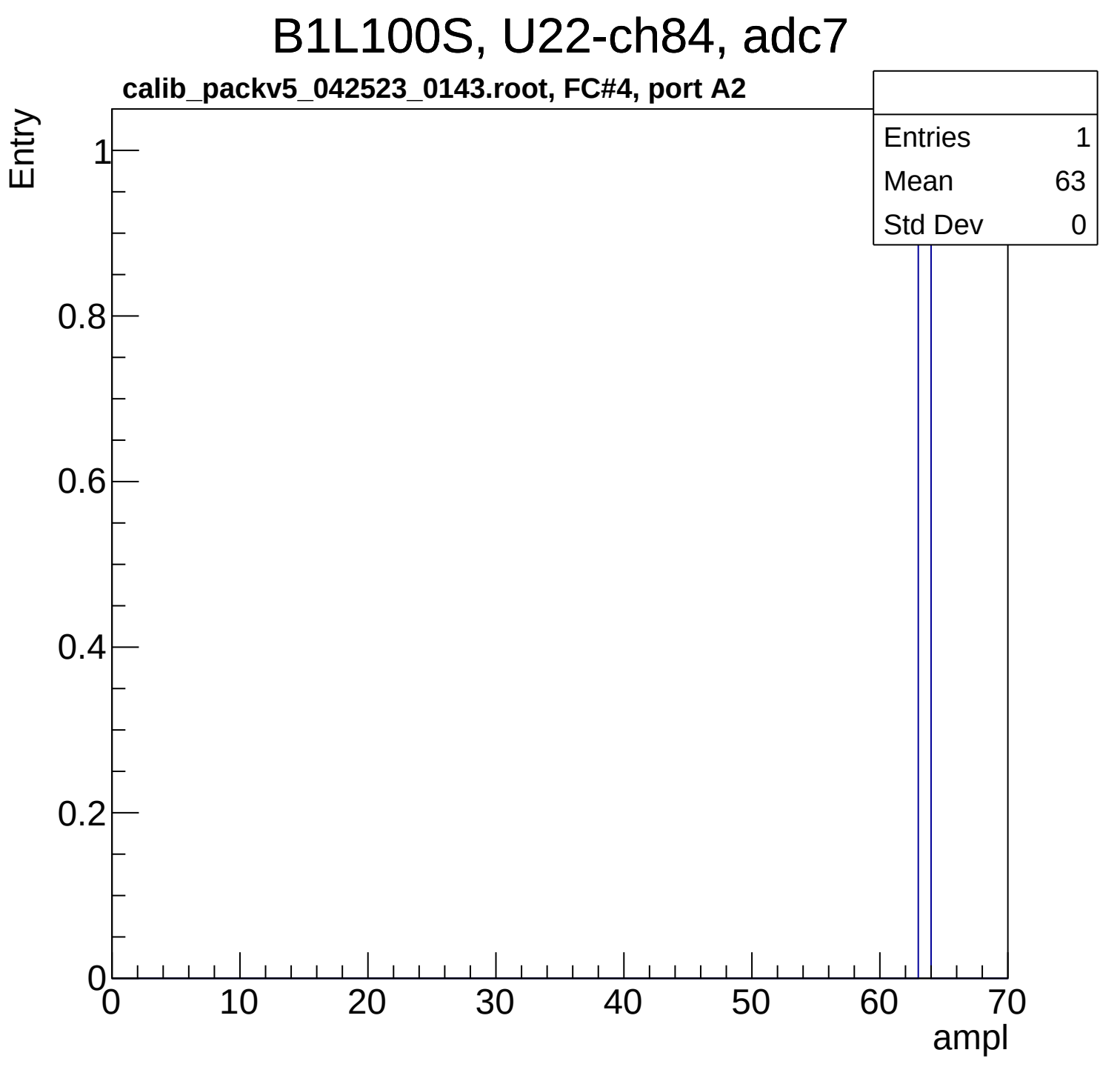
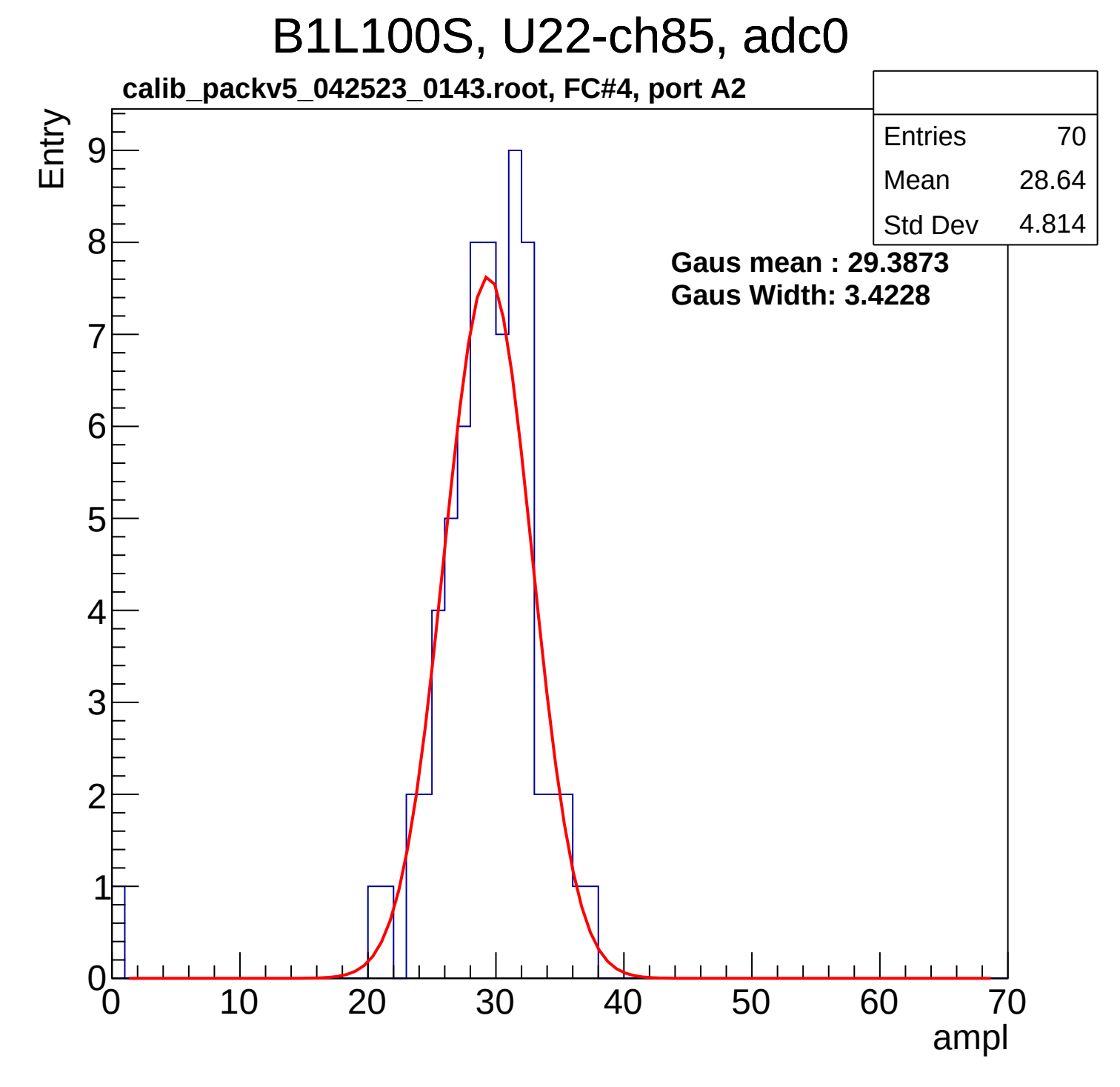
40
50
60
70


B1L100S, U22-ch84, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch85, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 28.64
Std Dev 4.814
Gaus mean : 29.3873
Gaus Width: 3.4228
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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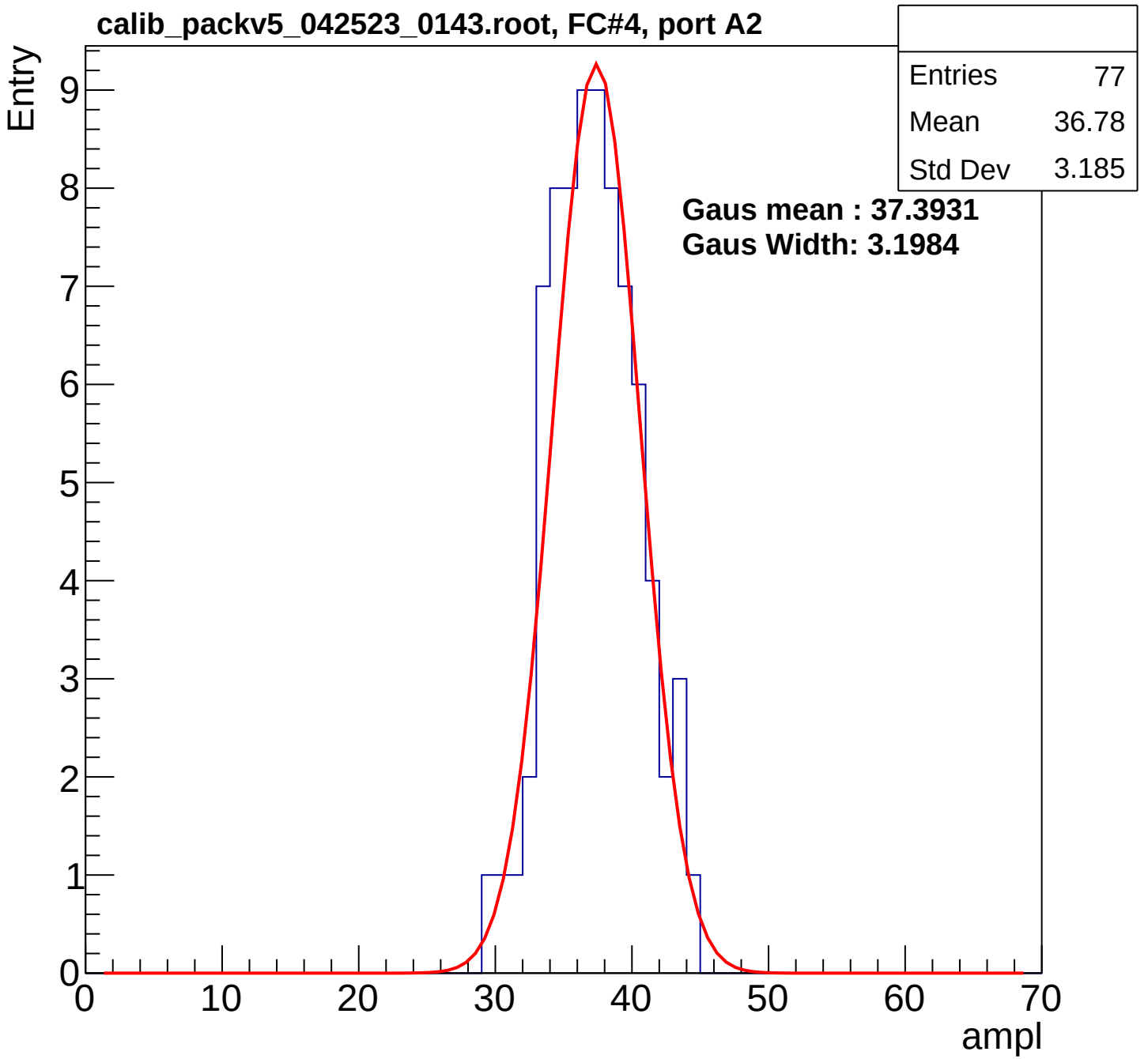
B1L100S, U22-ch85, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.78
Std Dev 3.185
Gaus mean : 37.3931
Gaus Width: 3.1984
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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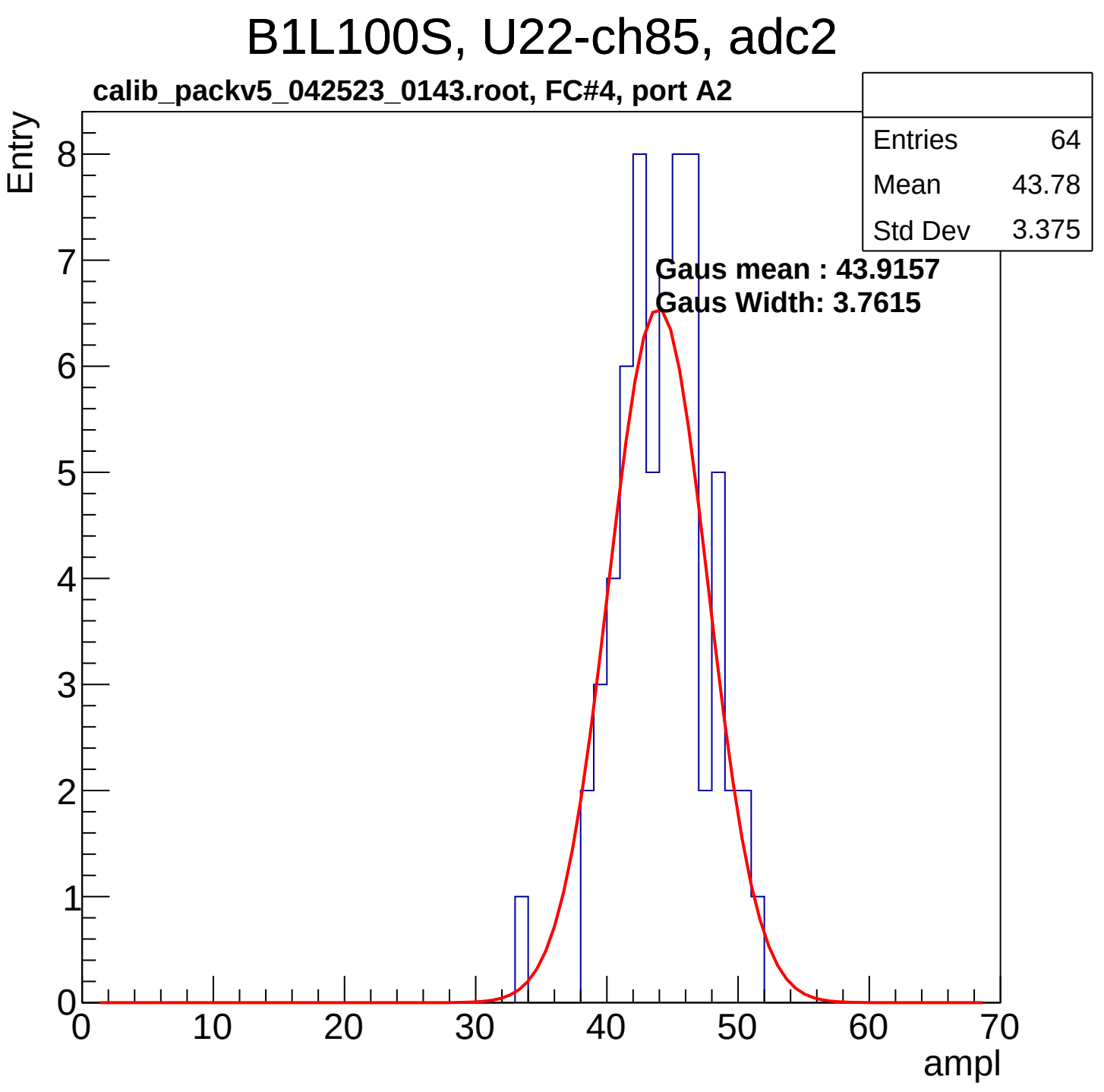
B1L100S, U22-ch85, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 43.78
Std Dev 3.375
Gaus mean : 43.9157
Gaus Width: 3.7615
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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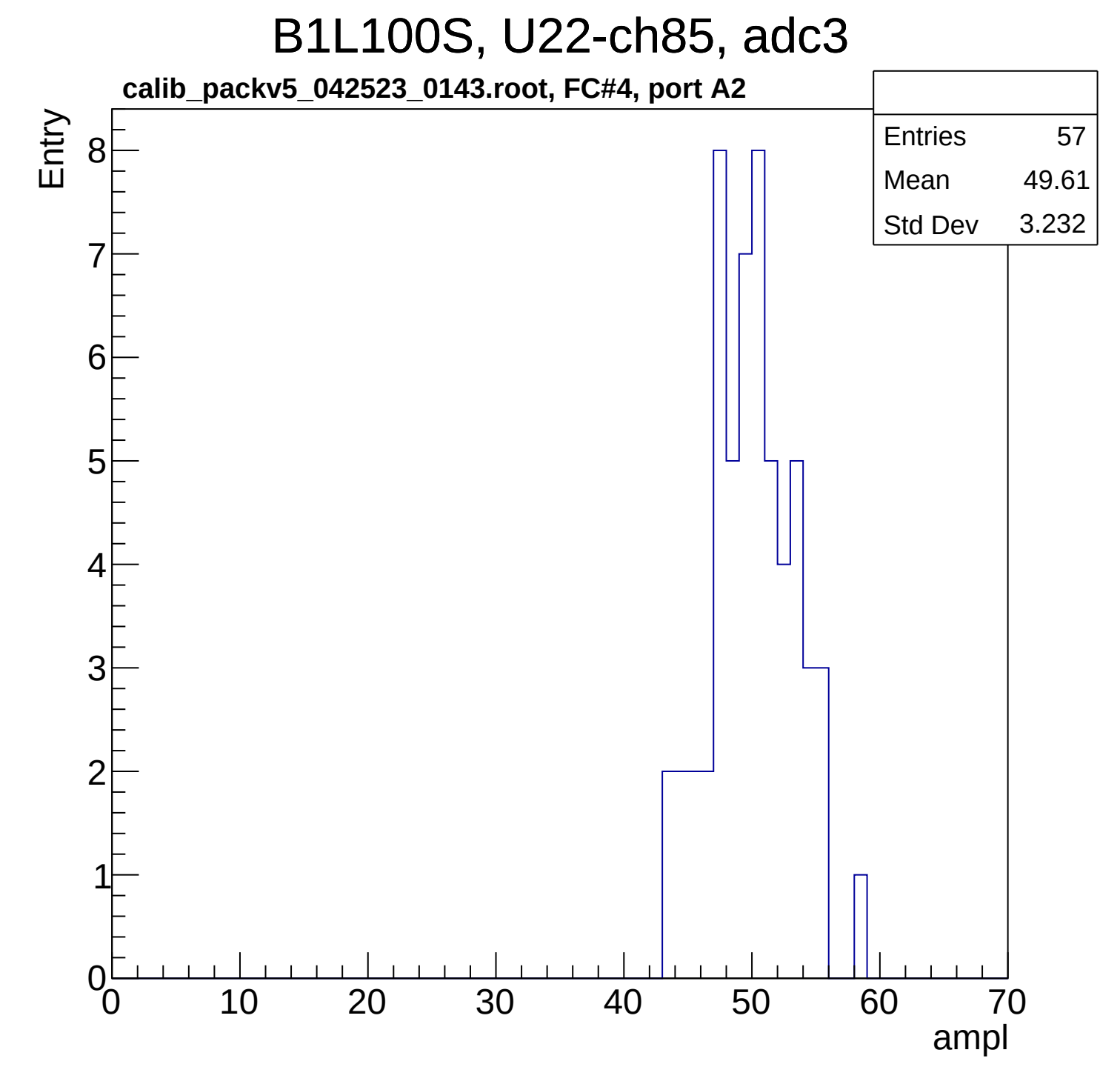
B1L100S, U22-ch85, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 49.61
Std Dev 3.232
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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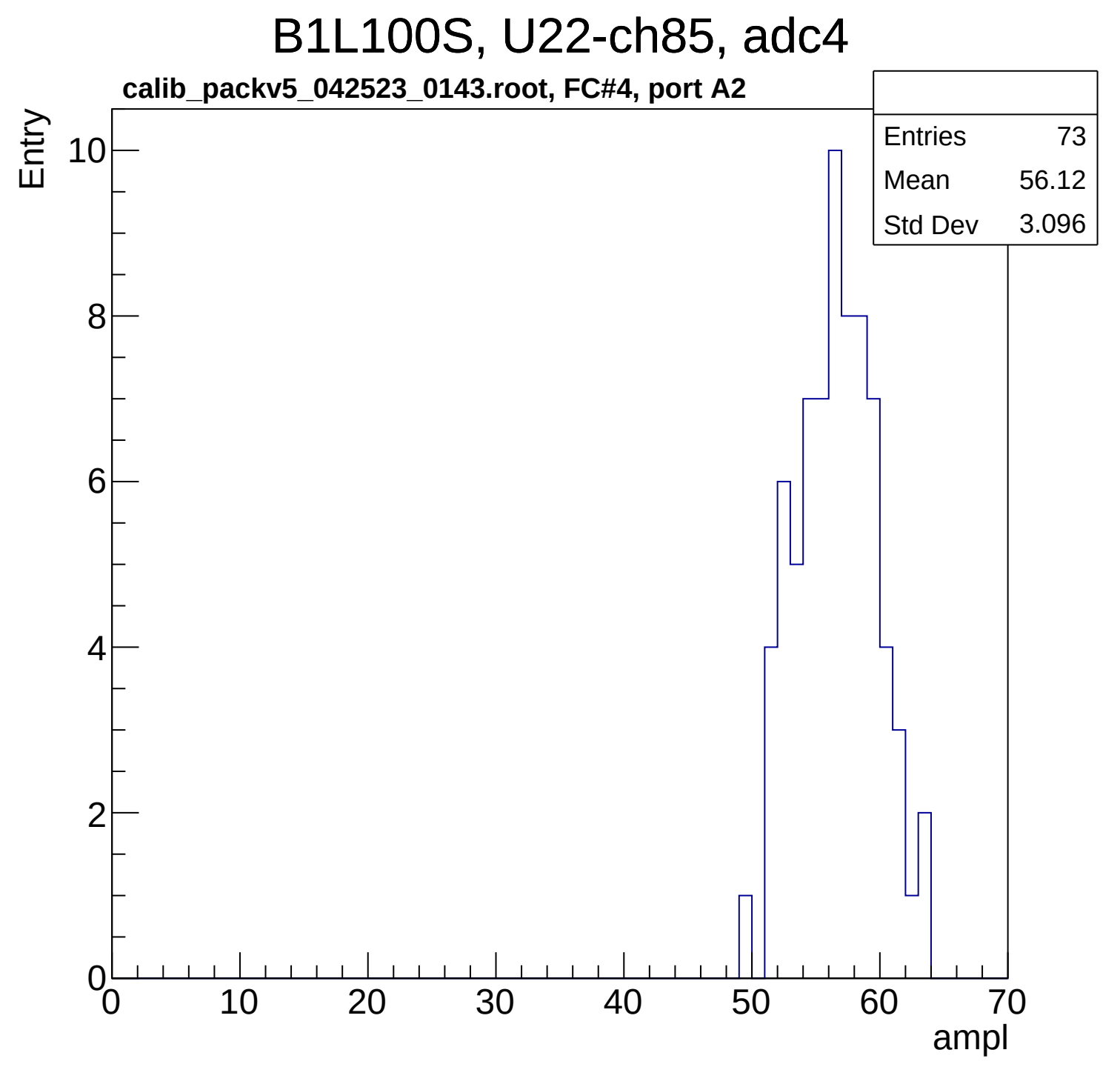
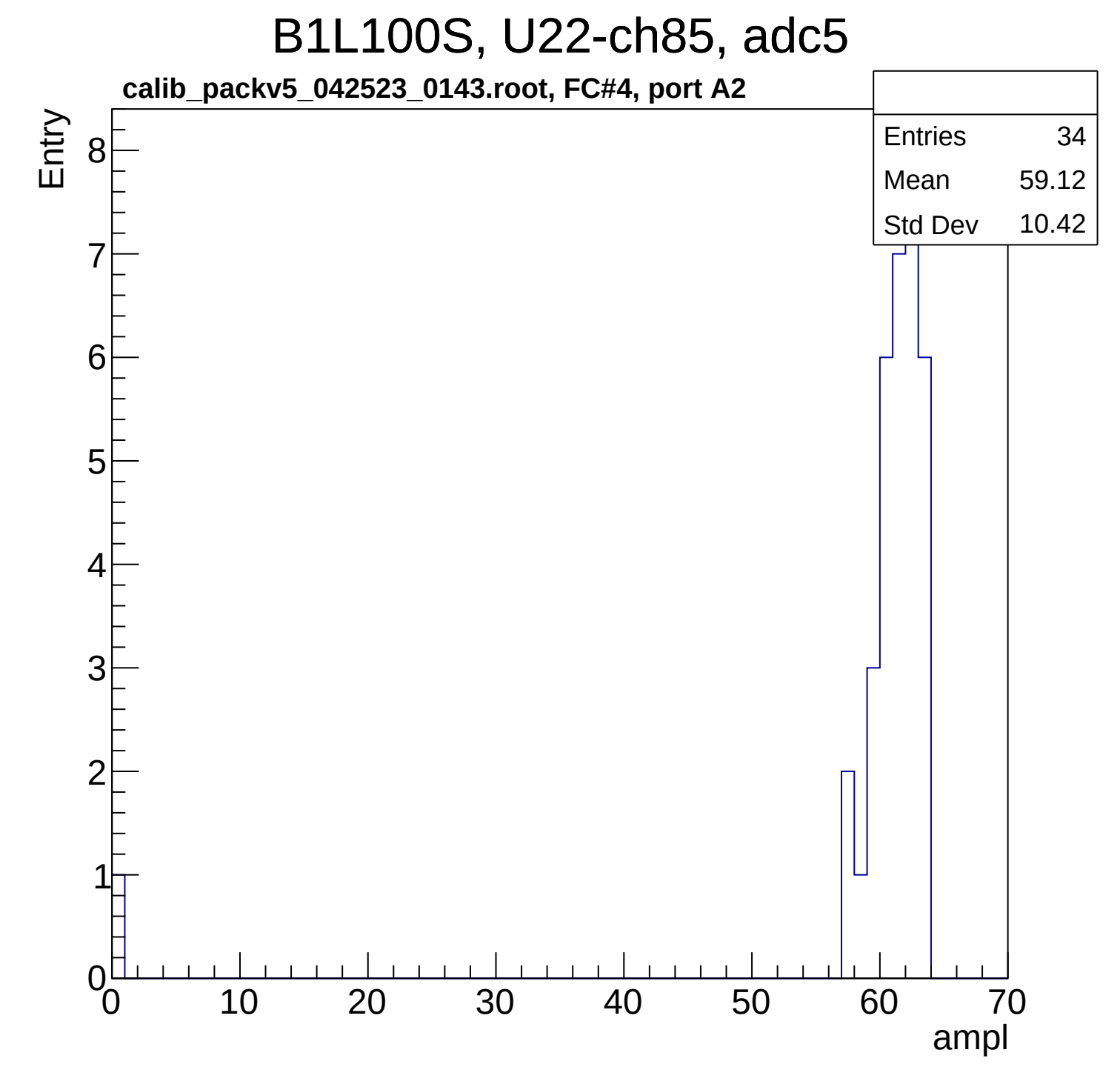
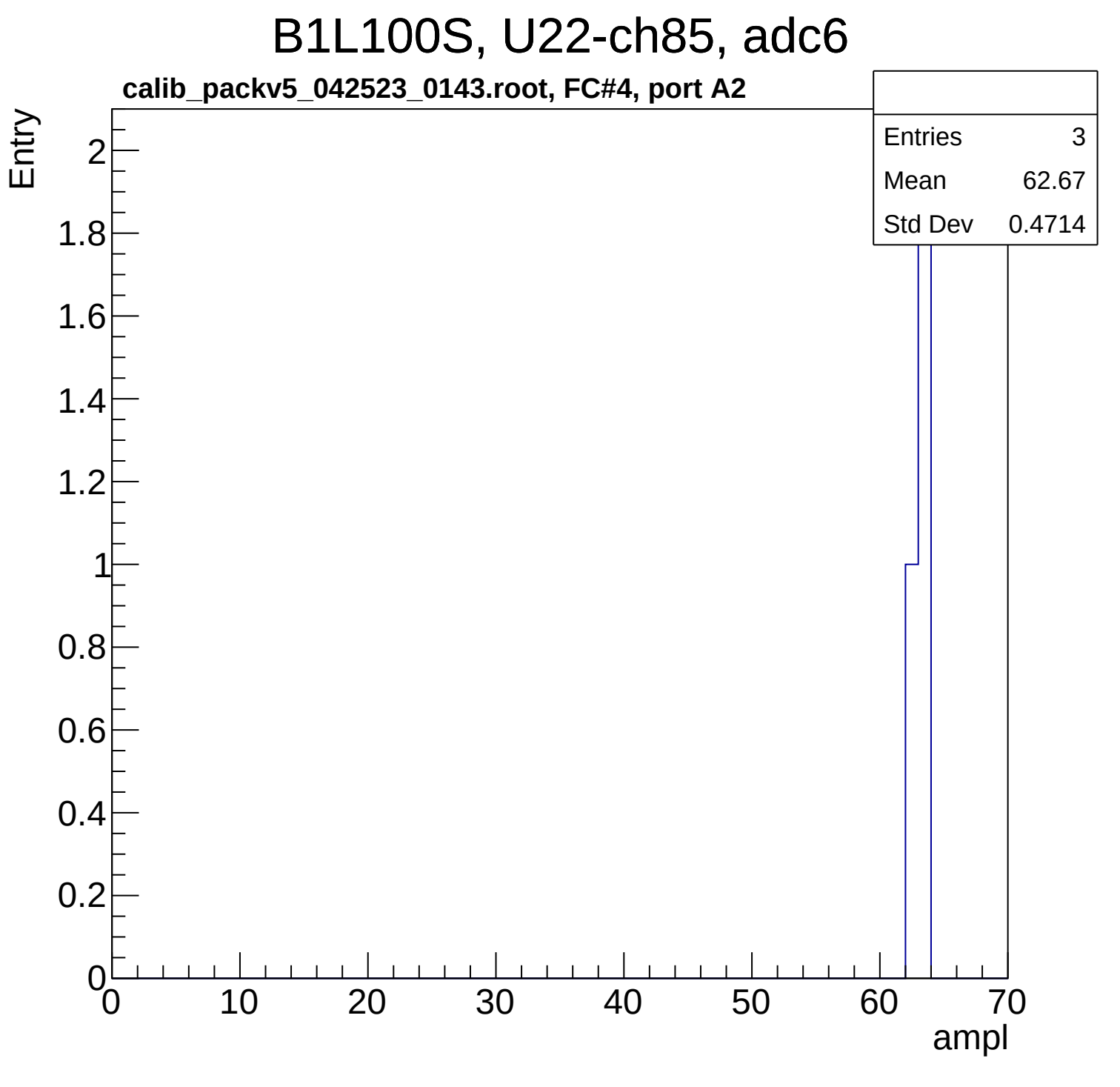

B1L100S, U22-ch85, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 56.12
Std Dev 3.096
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch85, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 34
Mean 59.12
Std Dev 10.42
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch85, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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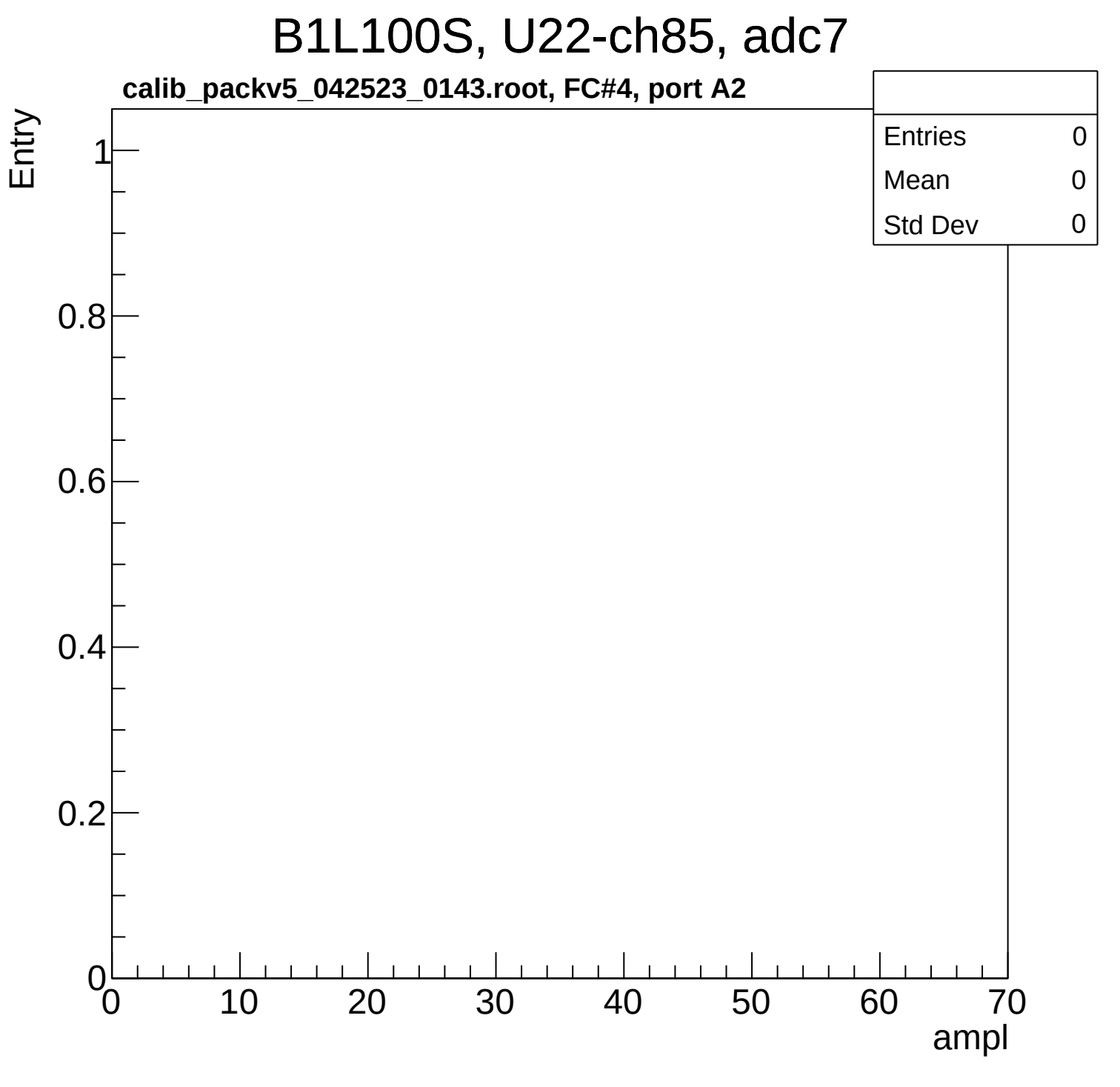
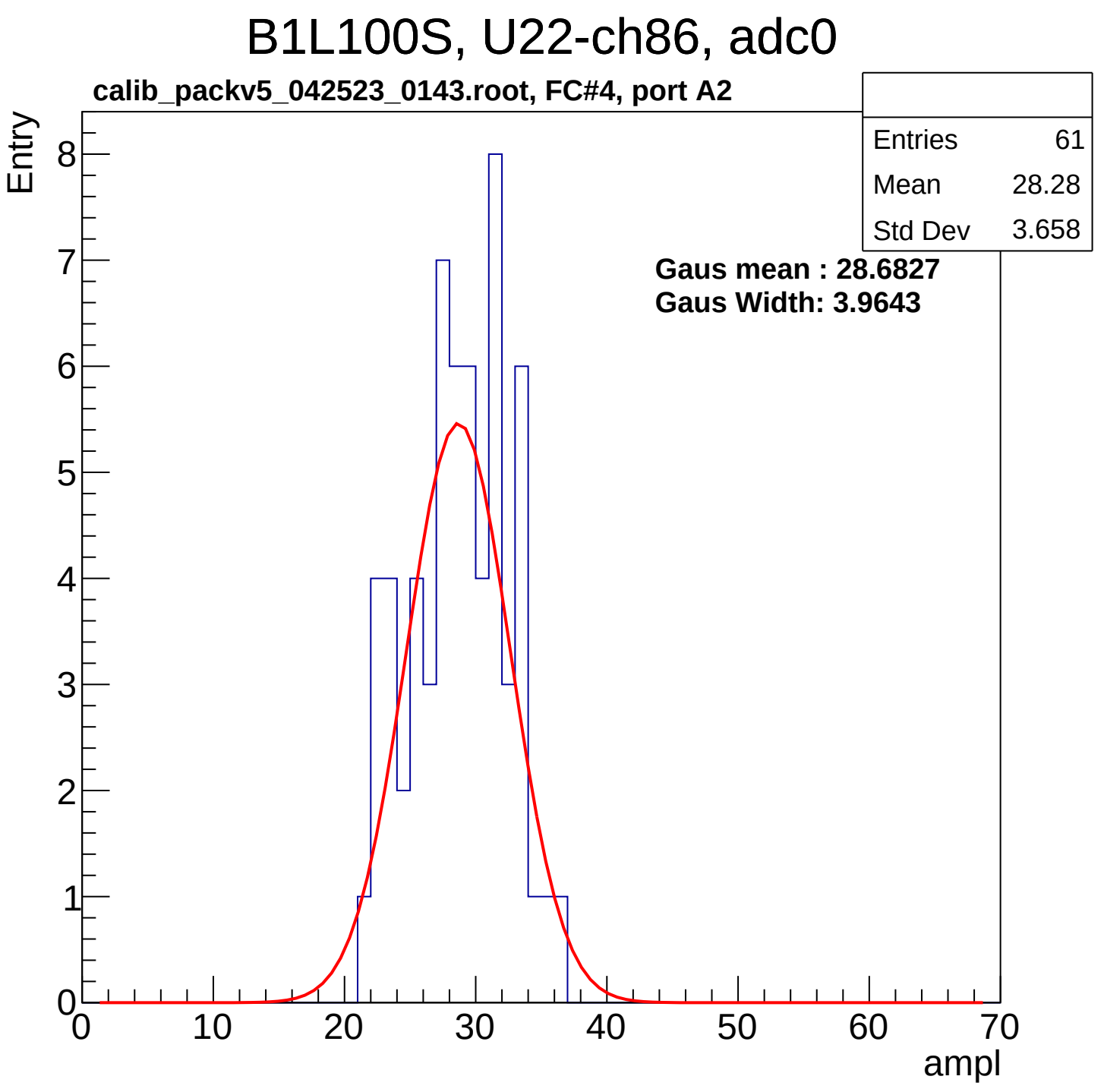
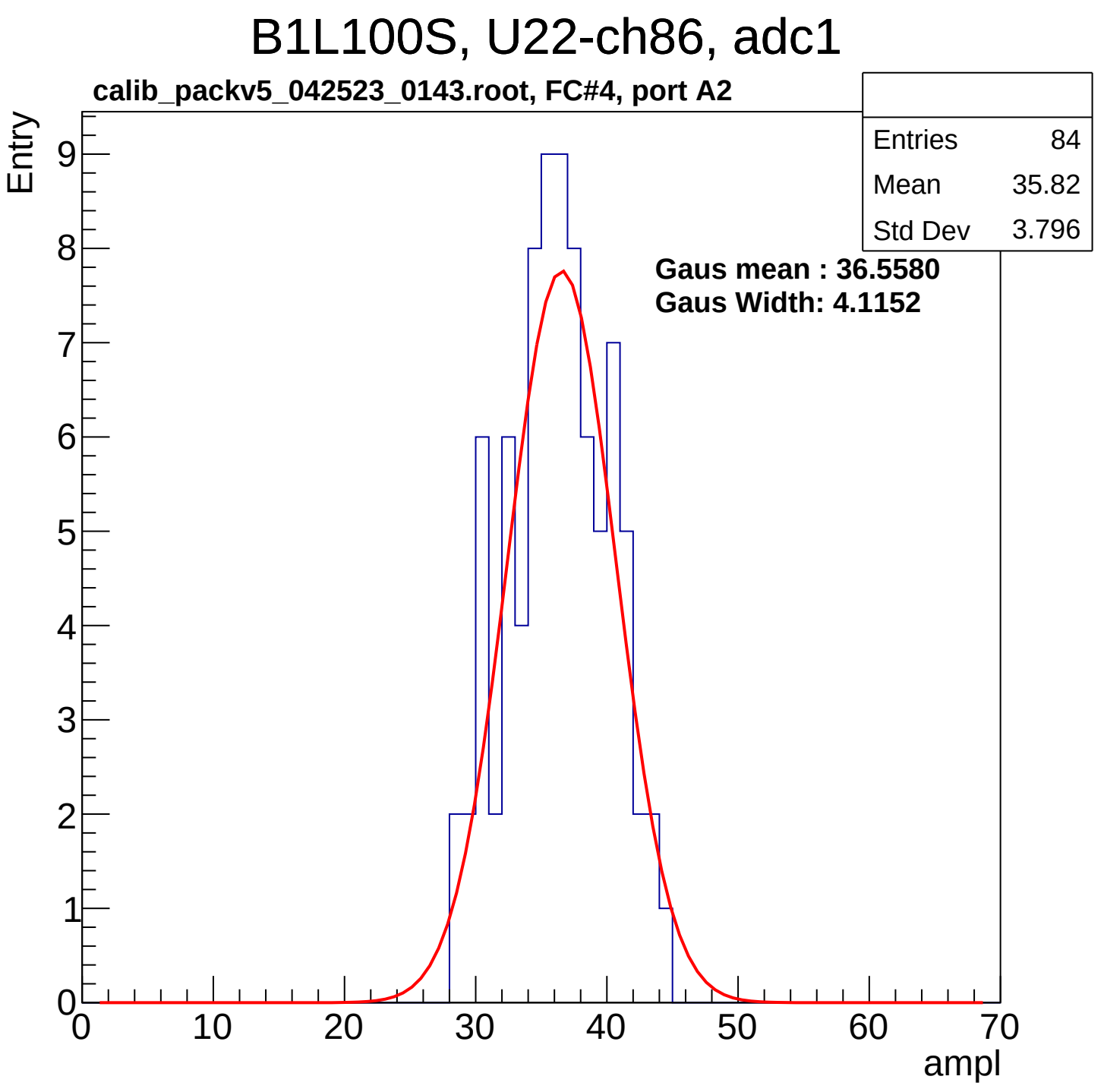
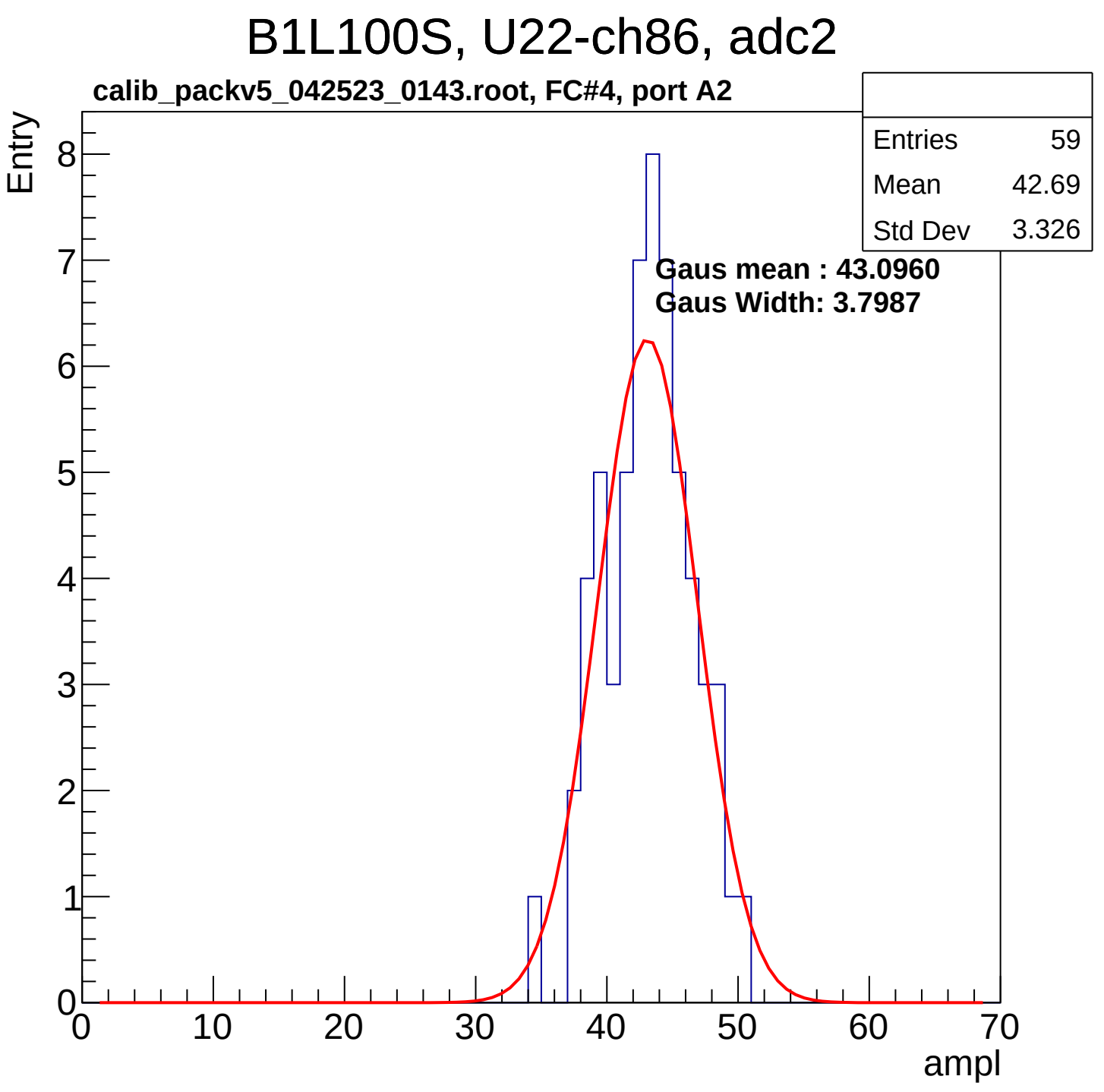
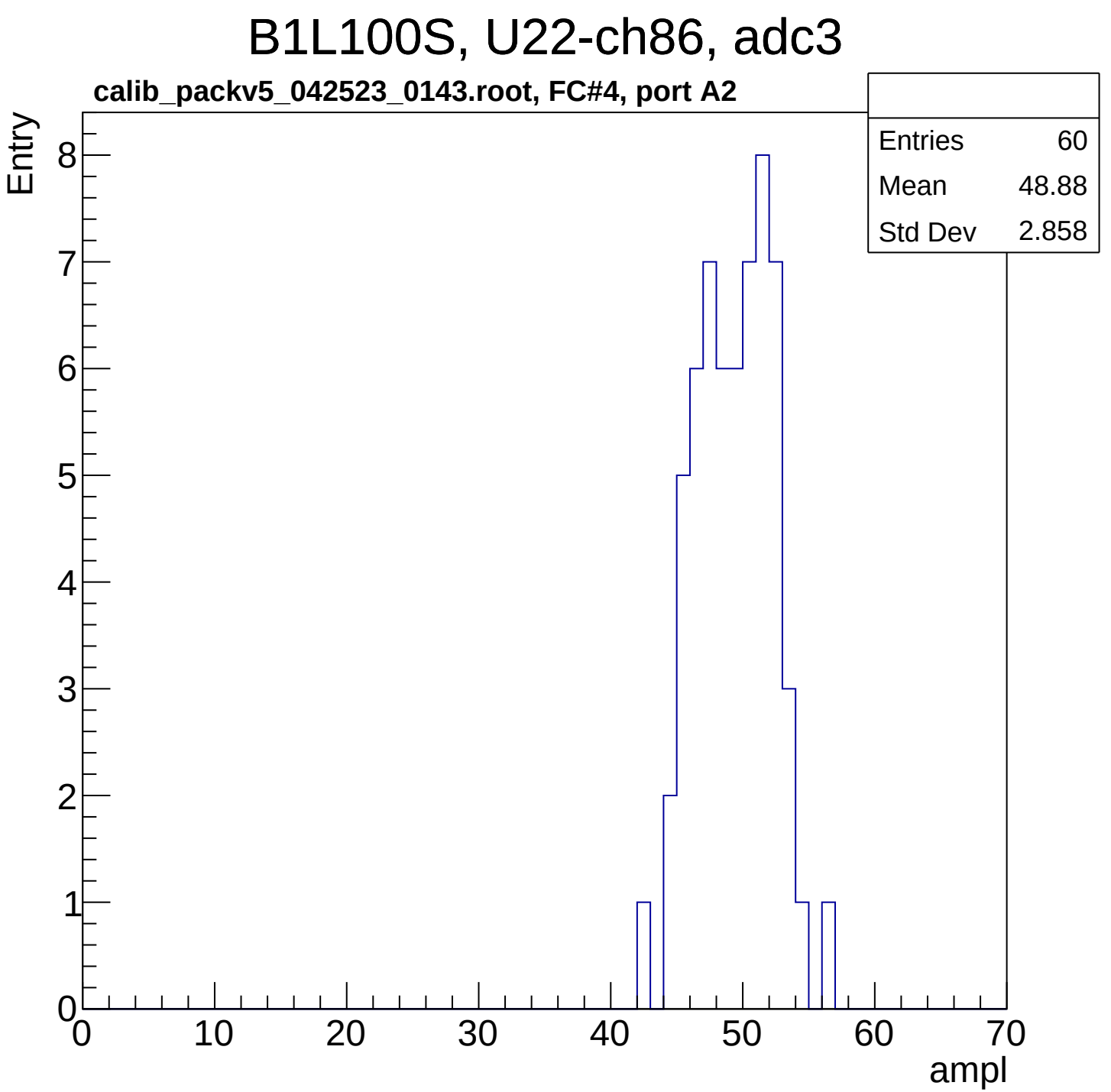
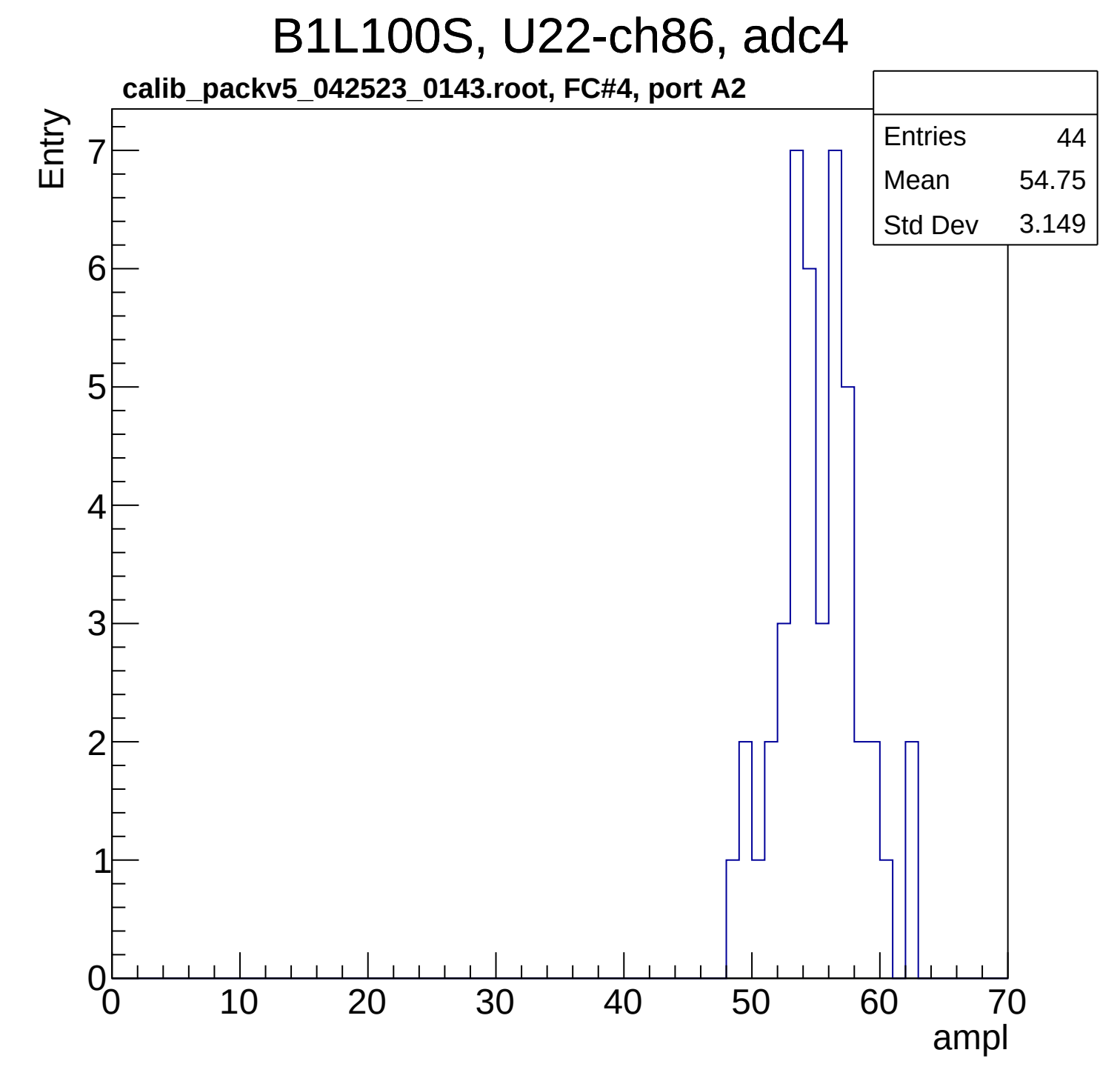

B1L100S, U22-ch85, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

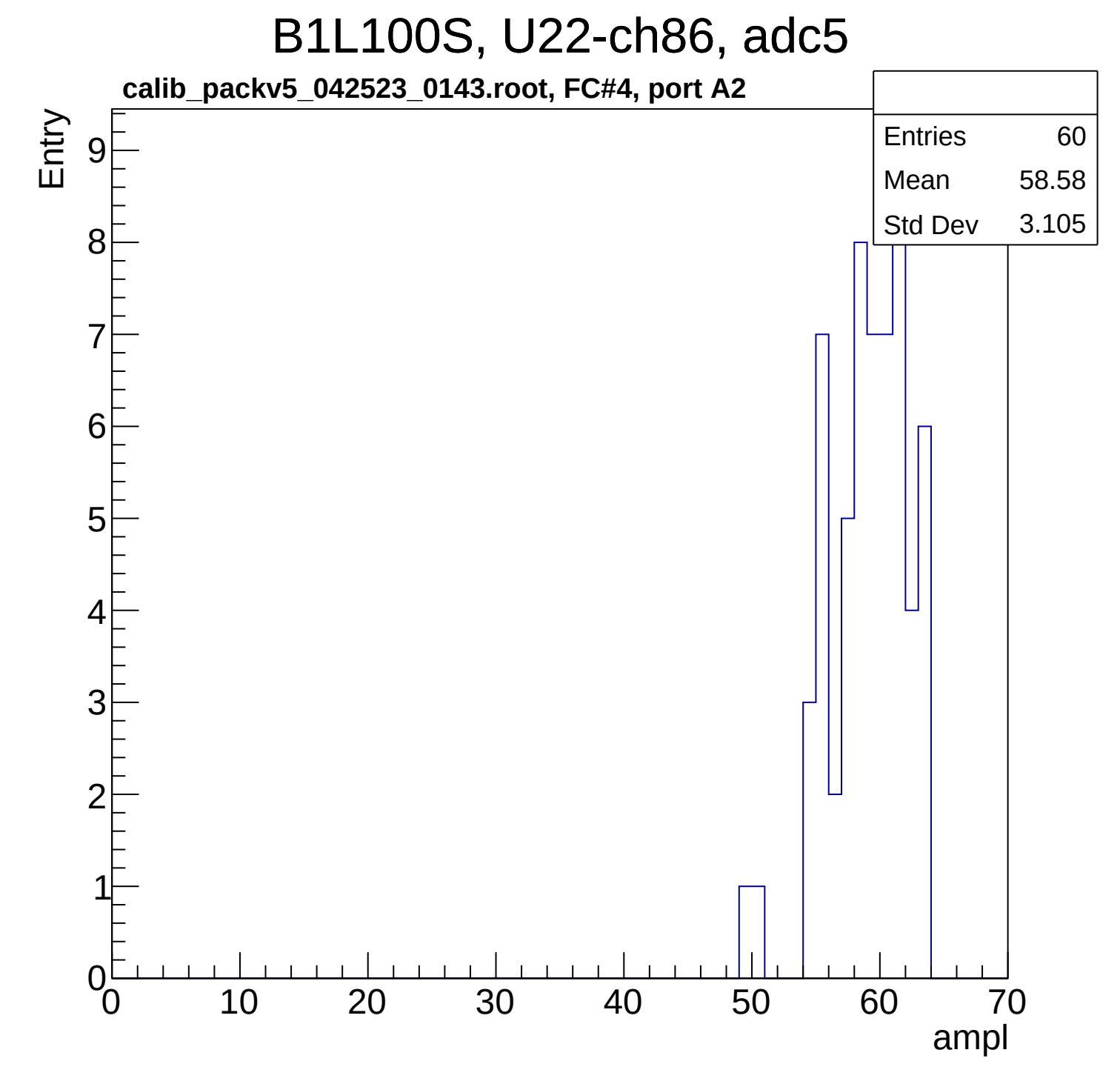

B1L100S, U22-ch86, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 28.28
Std Dev 3.658
Gaus mean : 28.6827
Gaus Width: 3.9643
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

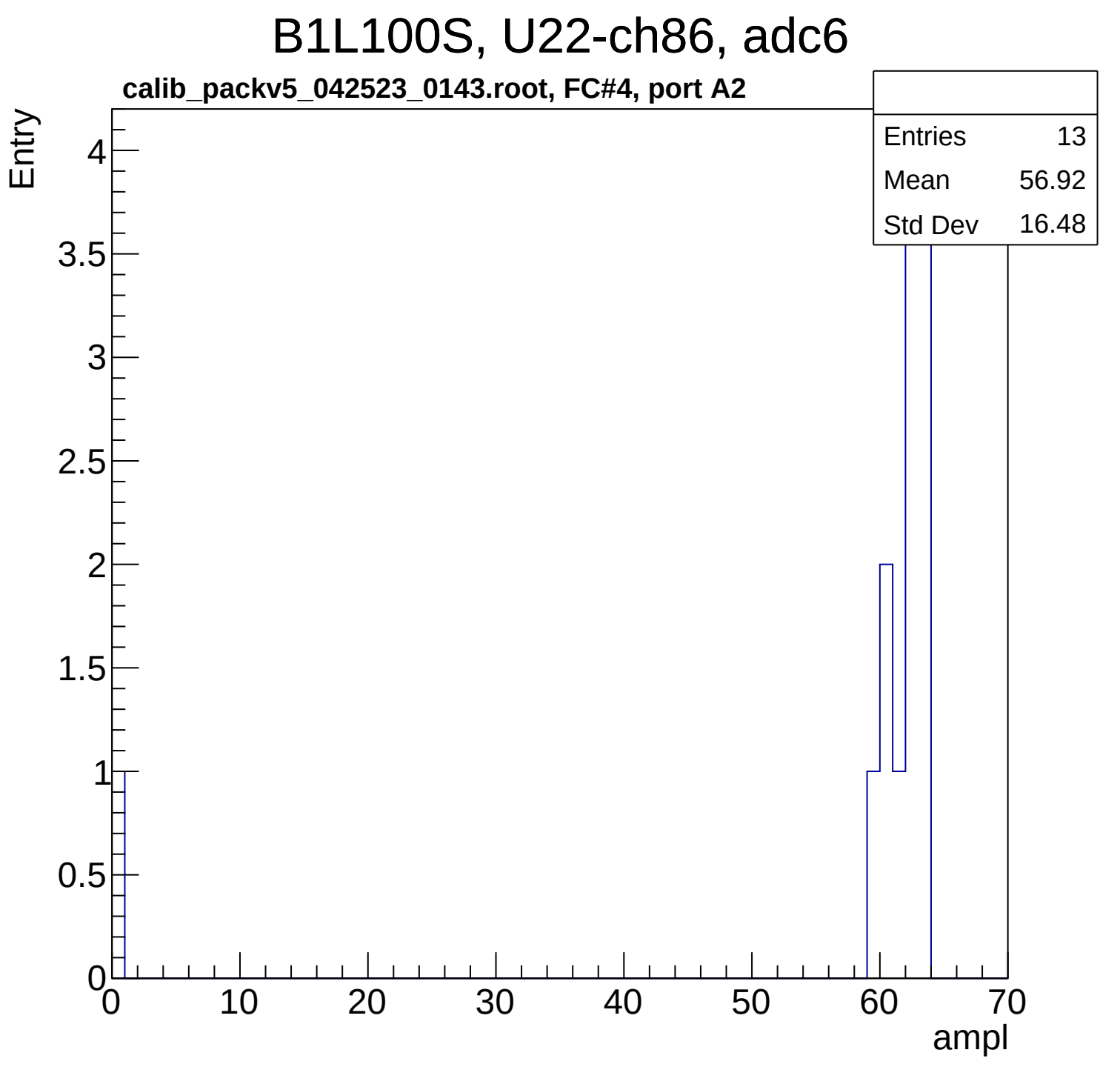

B1L100S, U22-ch86, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 35.82
Std Dev 3.796
Gaus mean : 36.5580
Gaus Width: 4.1152
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

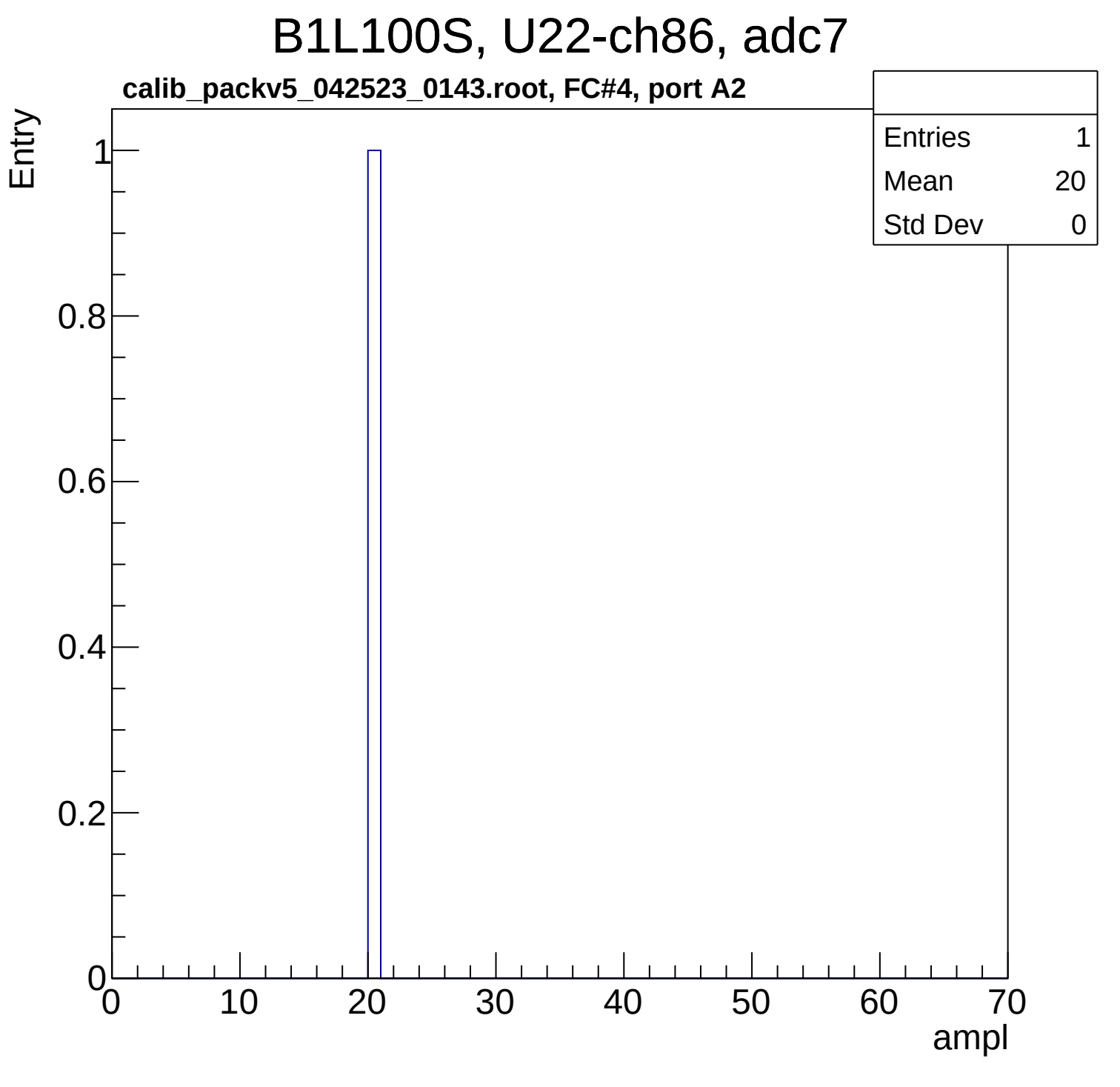

B1L100S, U22-ch86, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 42.69
Std Dev 3.326
Gaus mean : 43.0960
Gaus Width: 3.7987
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

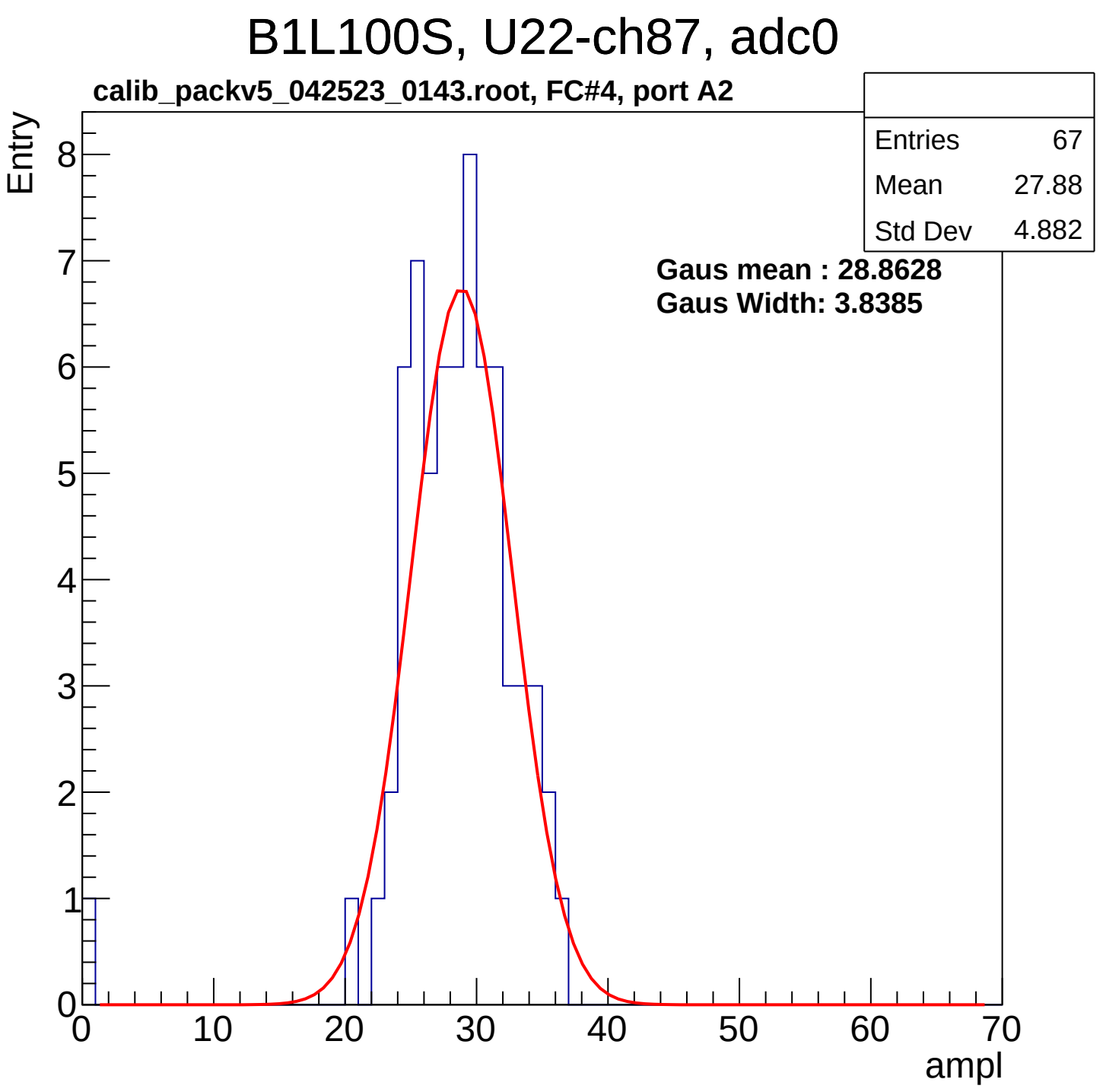

B1L100S, U22-ch86, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 48.88
Std Dev 2.858
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

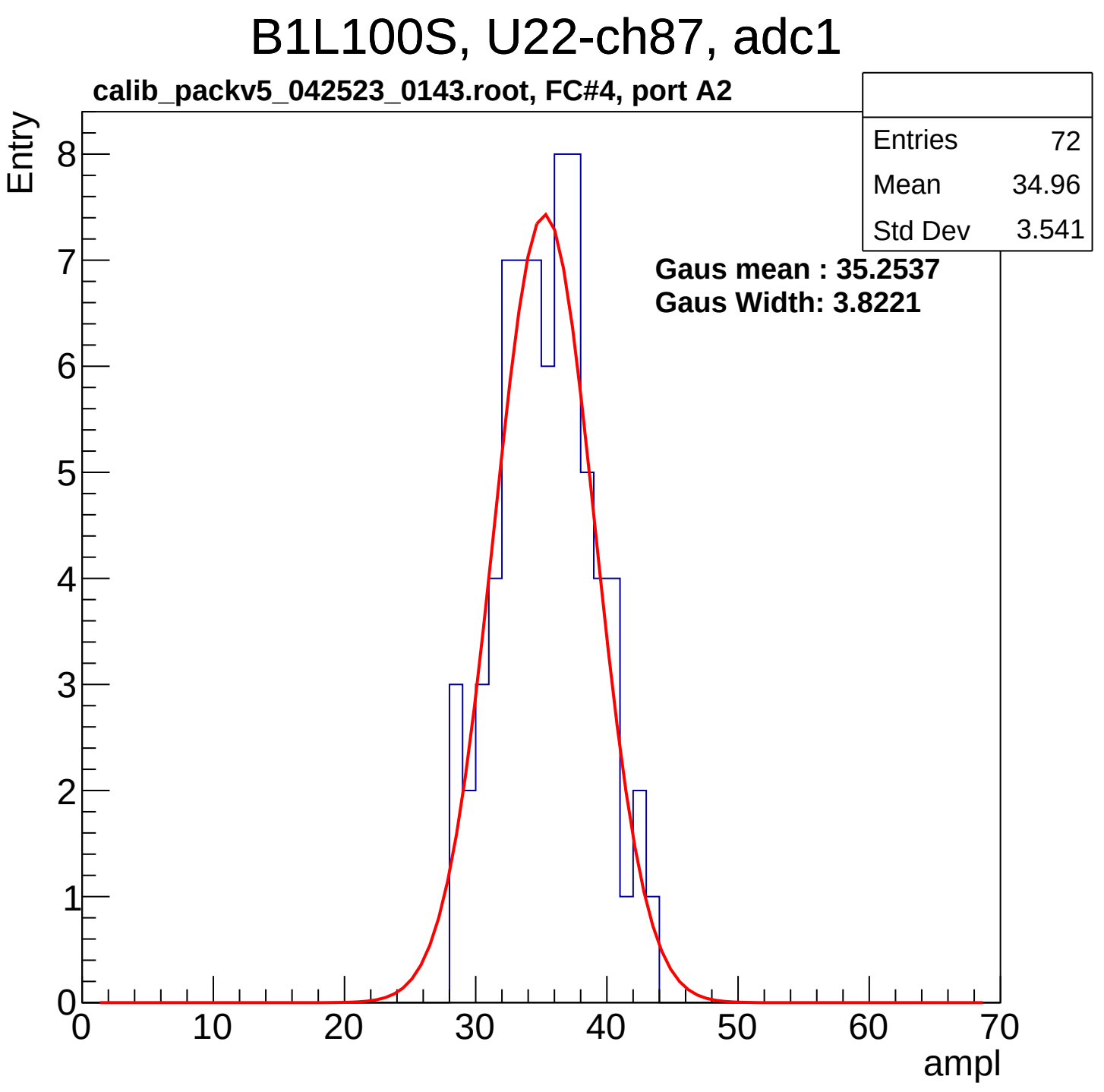

B1L100S, U22-ch86, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 54.75
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

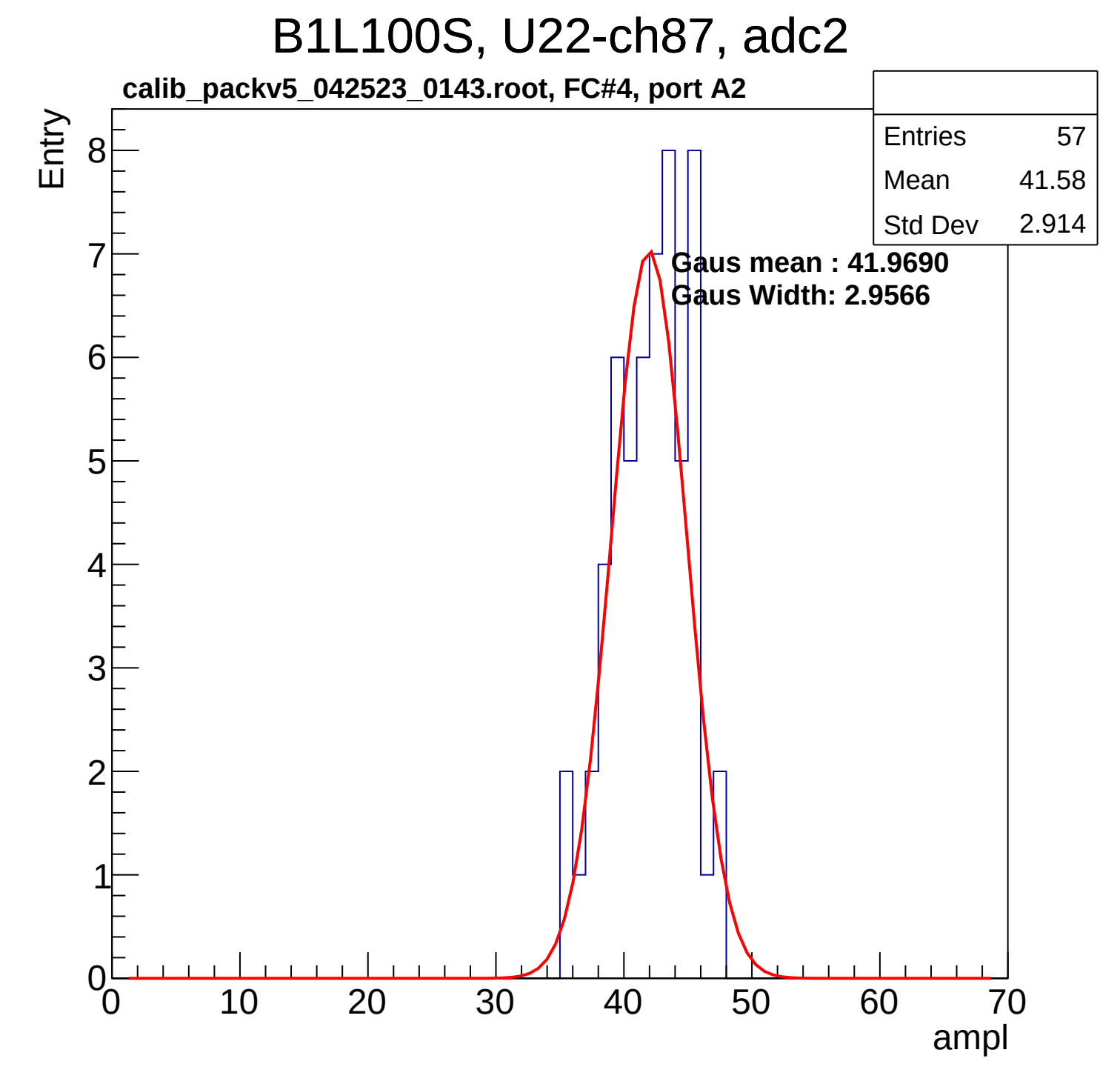

B1L100S, U22-ch86, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 58.58
Std Dev 3.105
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch86, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 56.92
Std Dev 16.48
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch86, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 20
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch87, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 27.88
Std Dev 4.882
Gaus mean : 28.8628
Gaus Width: 3.8385
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch87, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 34.96
Std Dev 3.541
Gaus mean : 35.2537
Gaus Width: 3.8221
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch87, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 41.58
Std Dev 2.914
Gaus mean : 41.9690
Gaus Width: 2.9566
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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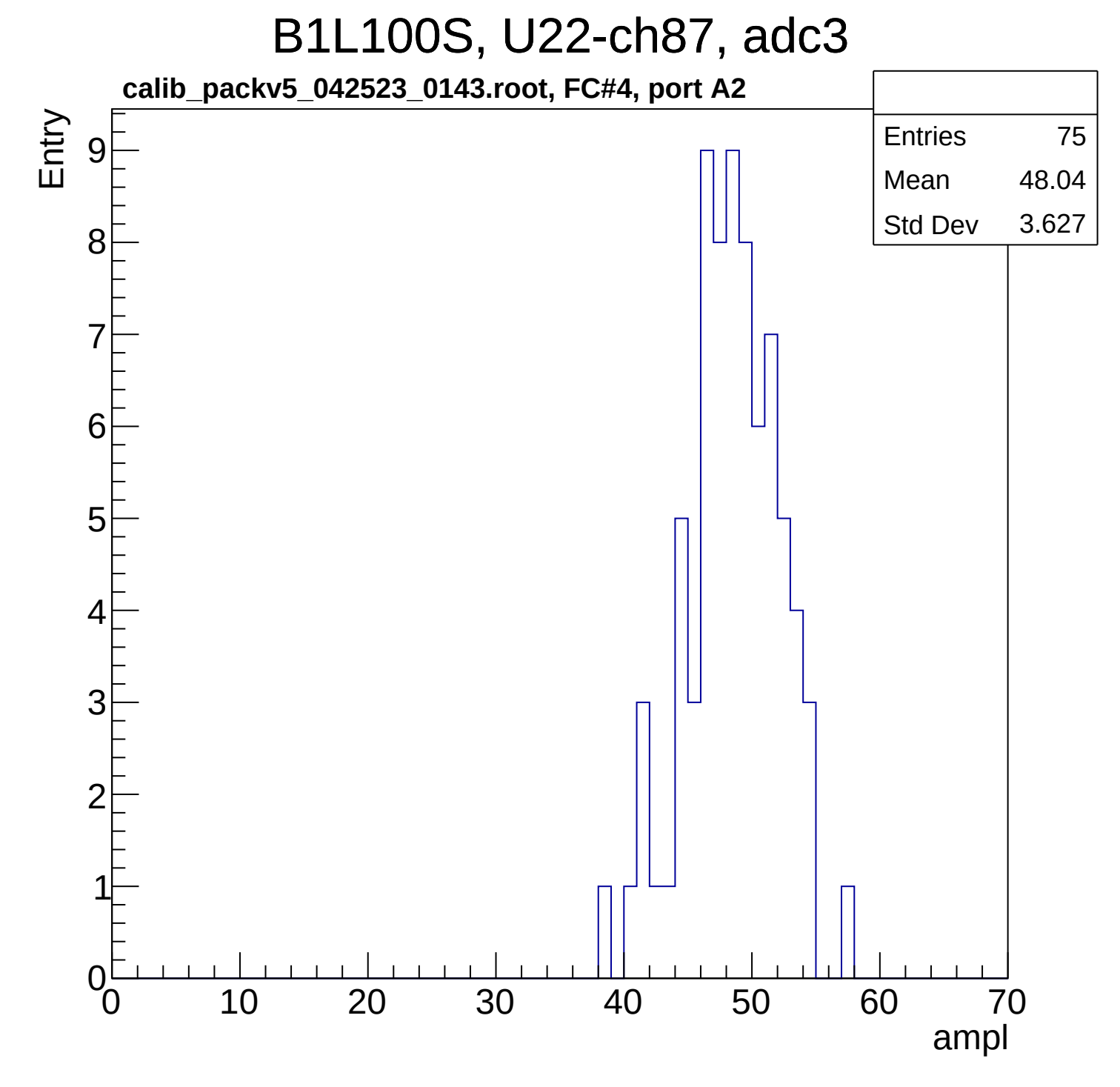
B1L100S, U22-ch87, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 48.04
Std Dev 3.627
Entry
ampl
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3
4
5
6
7
8
9
0
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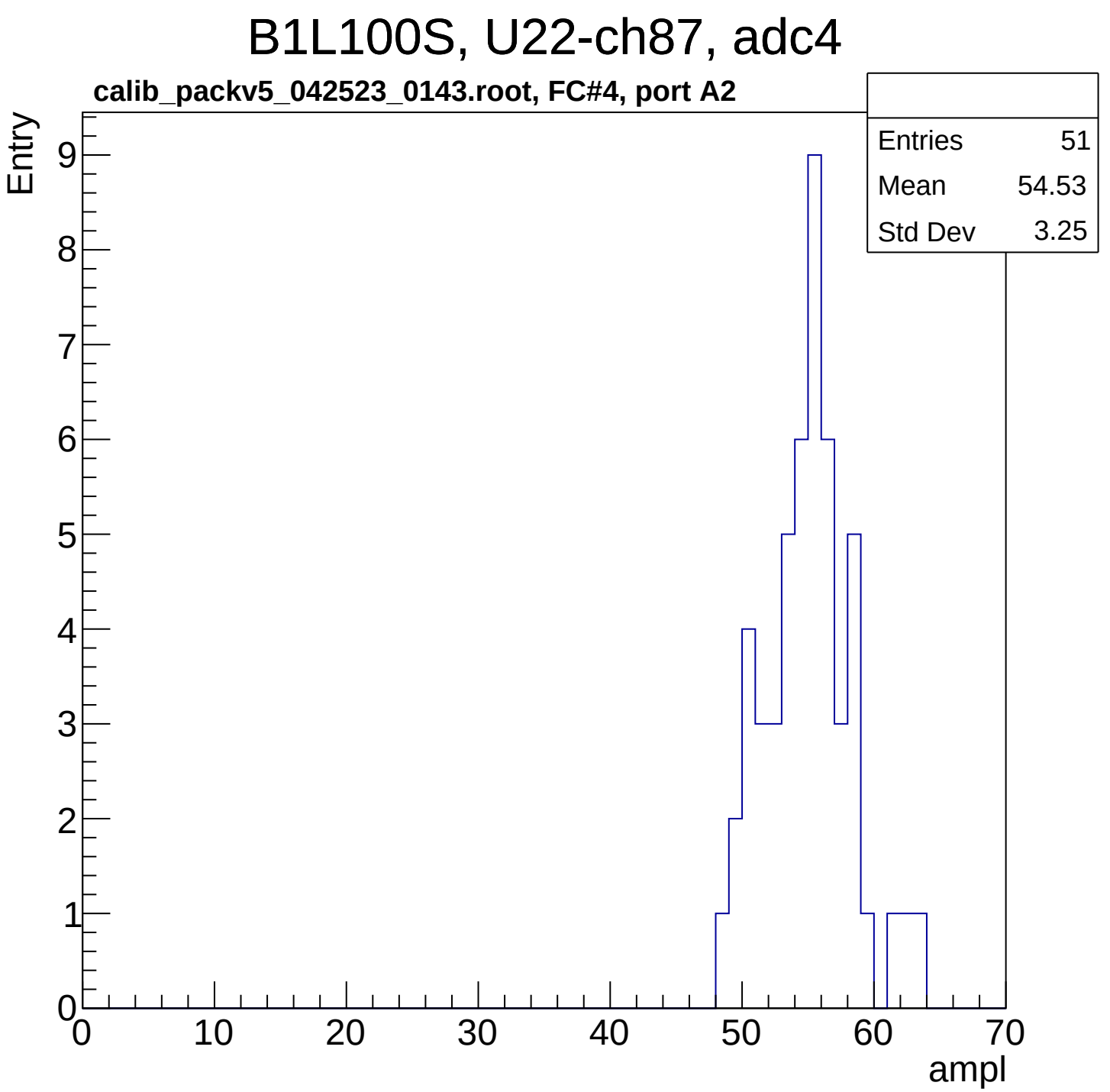
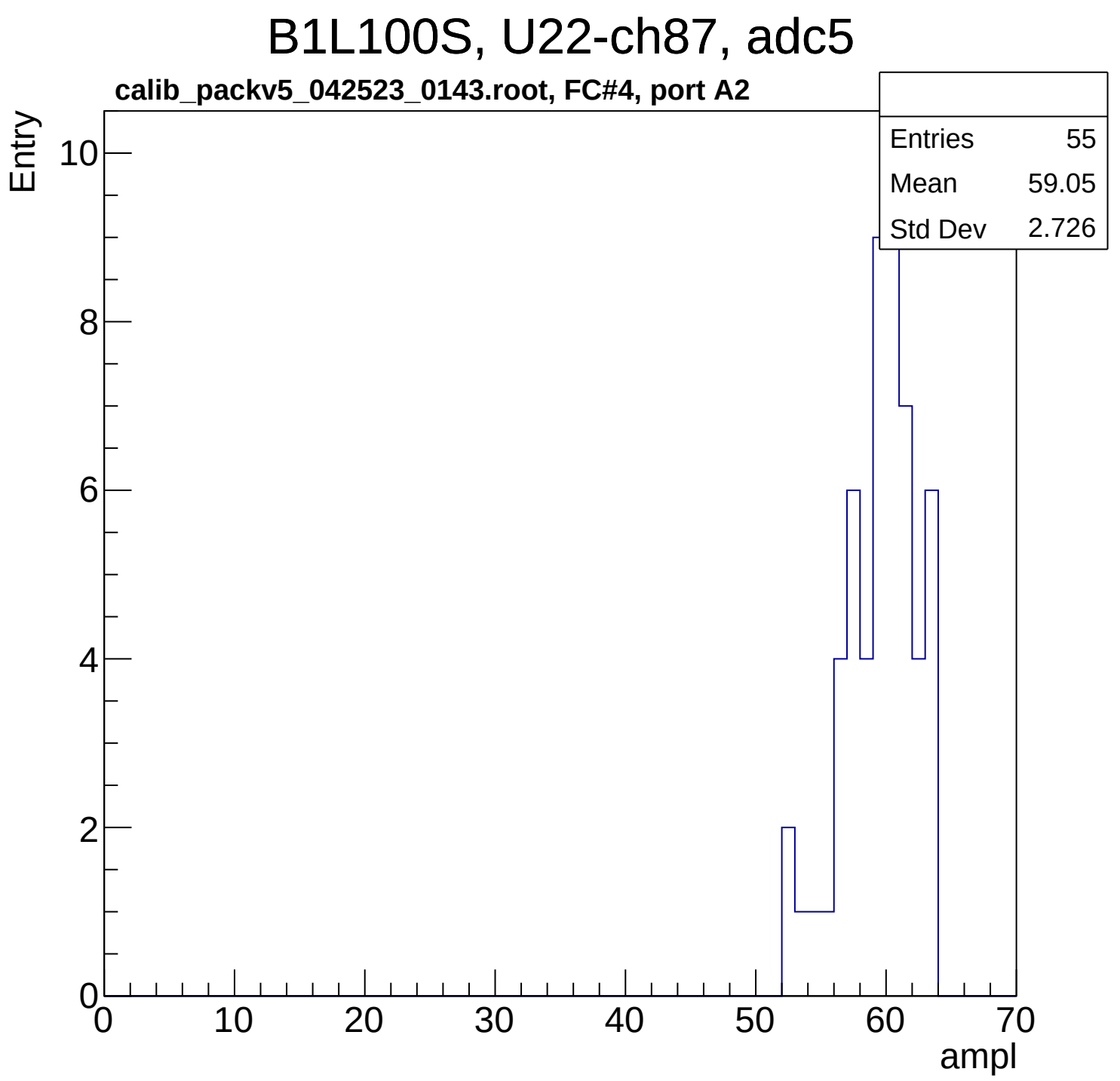
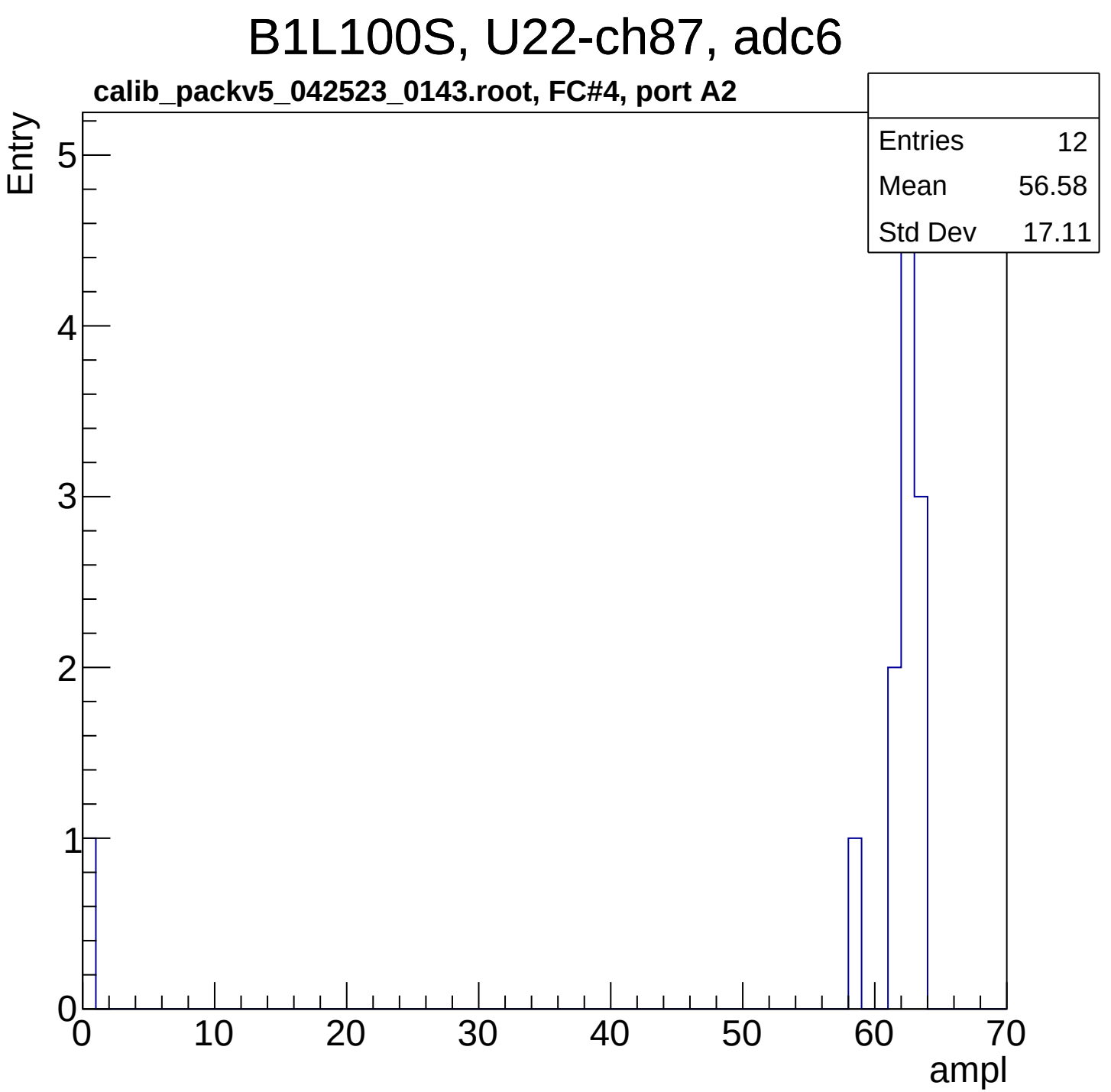
60
70


B1L100S, U22-ch87, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.53
Std Dev 3.25
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch87, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 59.05
Std Dev 2.726
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch87, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 12
Mean 56.58
Std Dev 17.11
Entry
ampl
0
1
2
3
4
5
0
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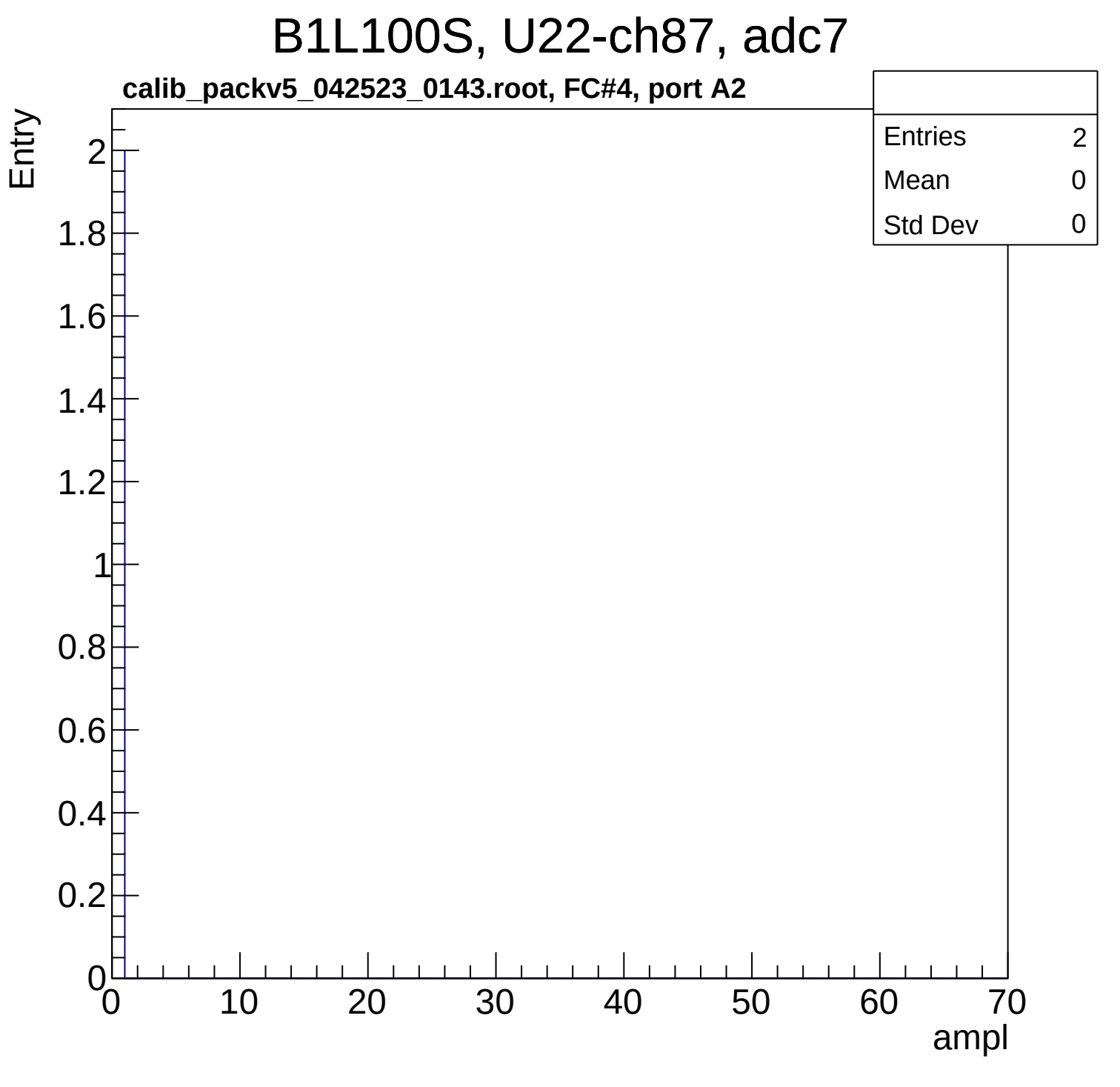
20
30
40
50
60
70


B1L100S, U22-ch87, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
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30
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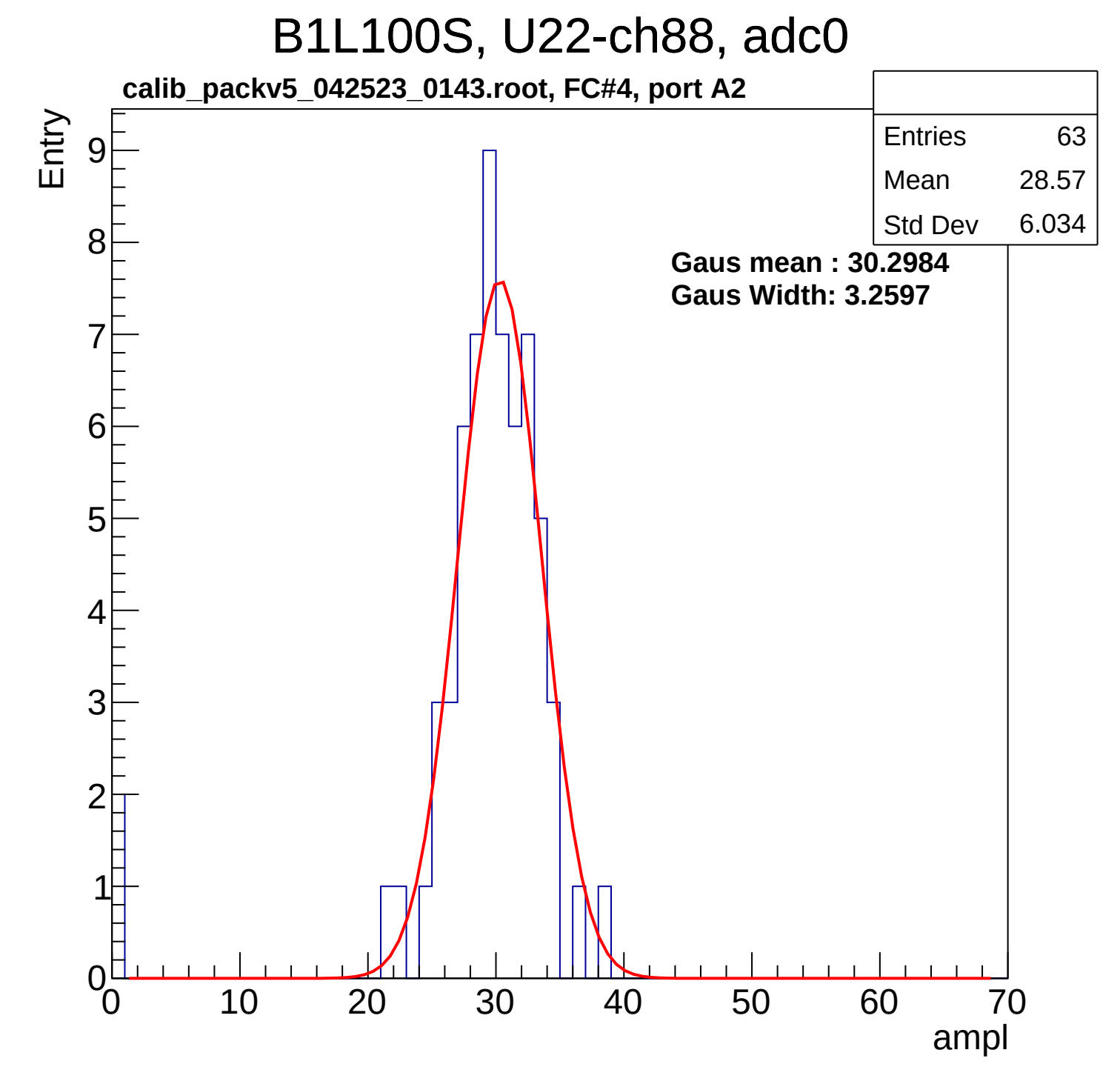
50
60
70
ampl


B1L100S, U22-ch88, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 28.57
Std Dev 6.034
Gaus mean : 30.2984
Gaus Width: 3.2597
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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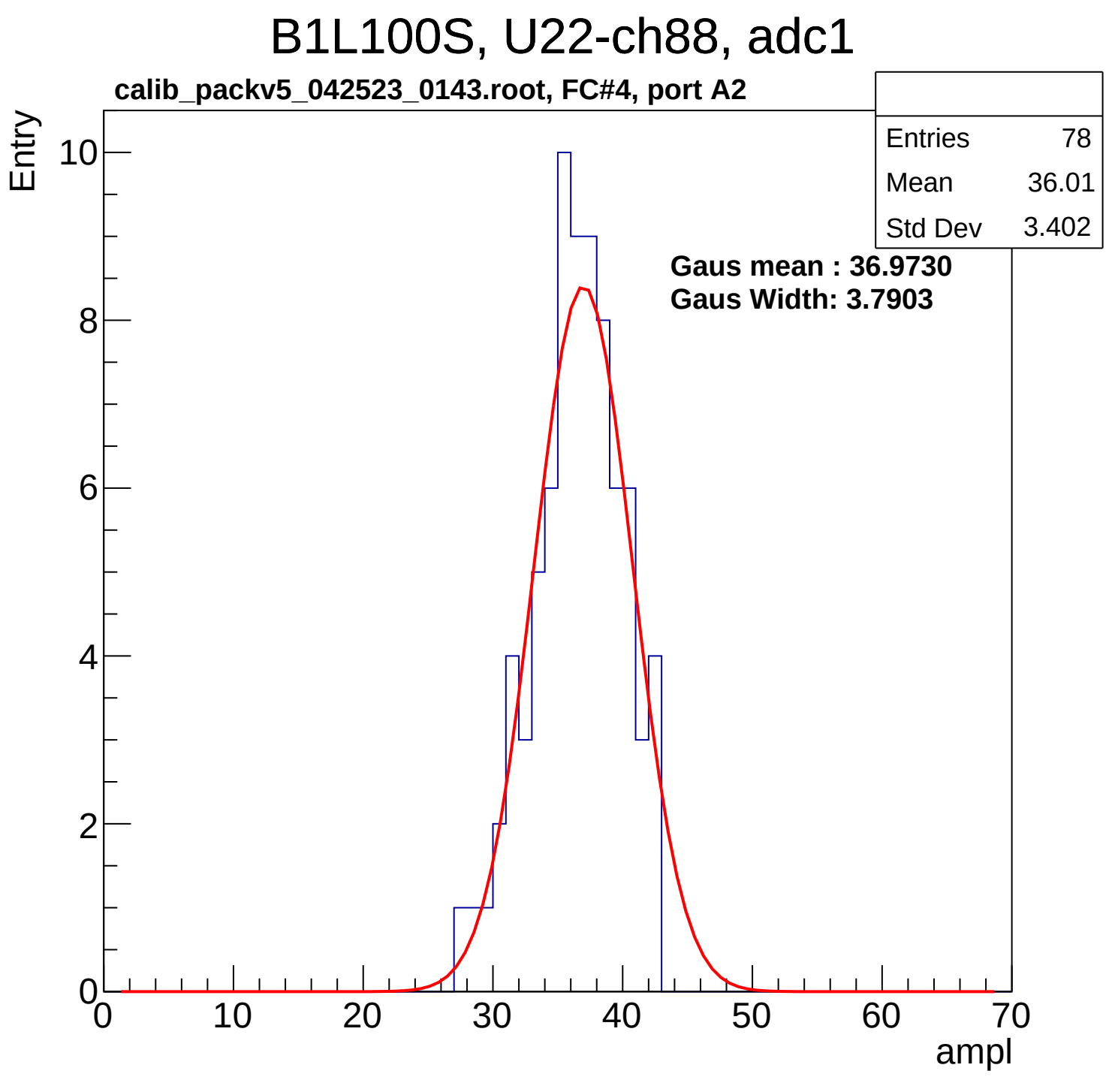
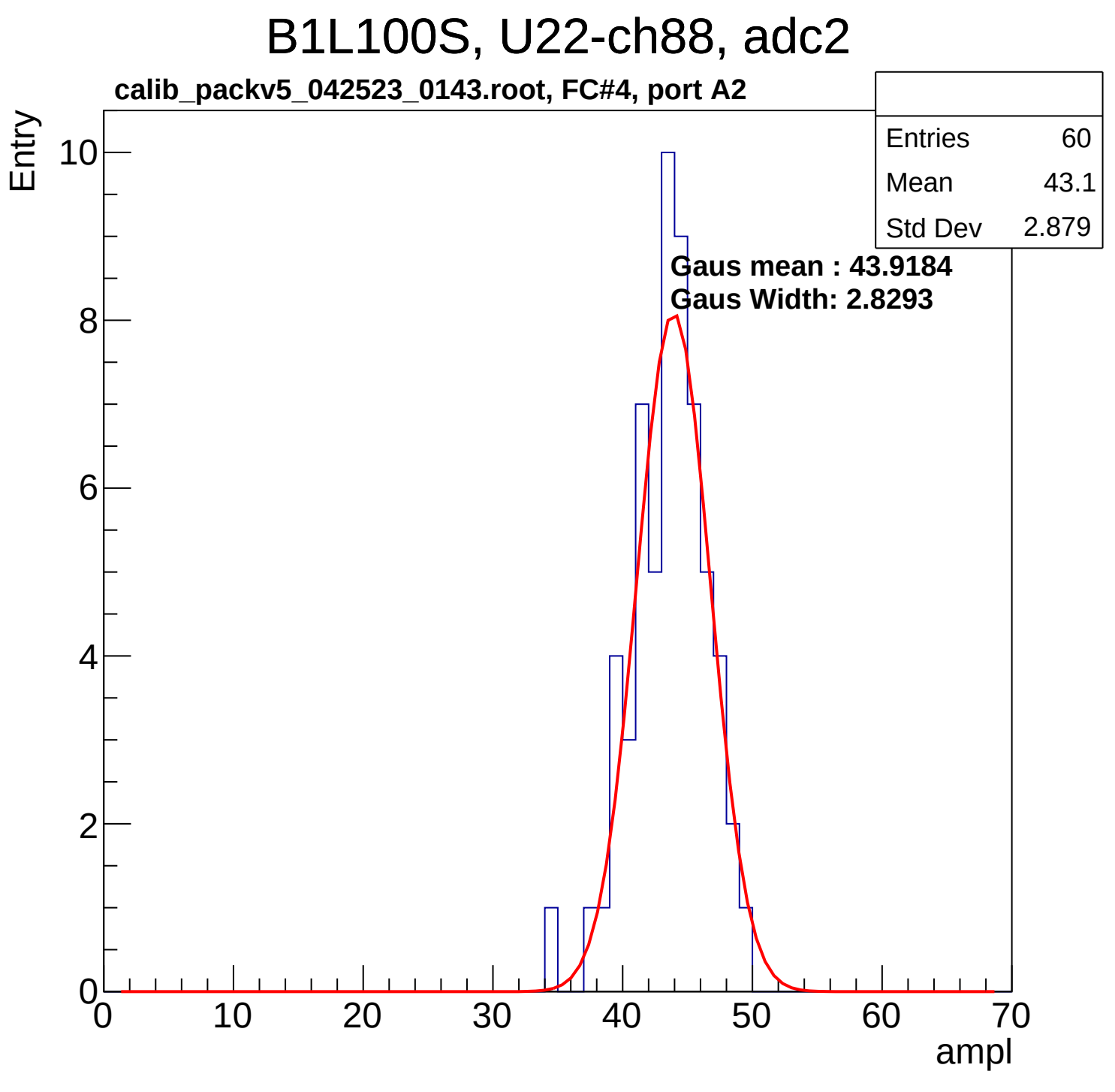

B1L100S, U22-ch88, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 36.01
Std Dev 3.402
Gaus mean : 36.9730
Gaus Width: 3.7903
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch88, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 43.1
Std Dev 2.879
Gaus mean : 43.9184
Gaus Width: 2.8293
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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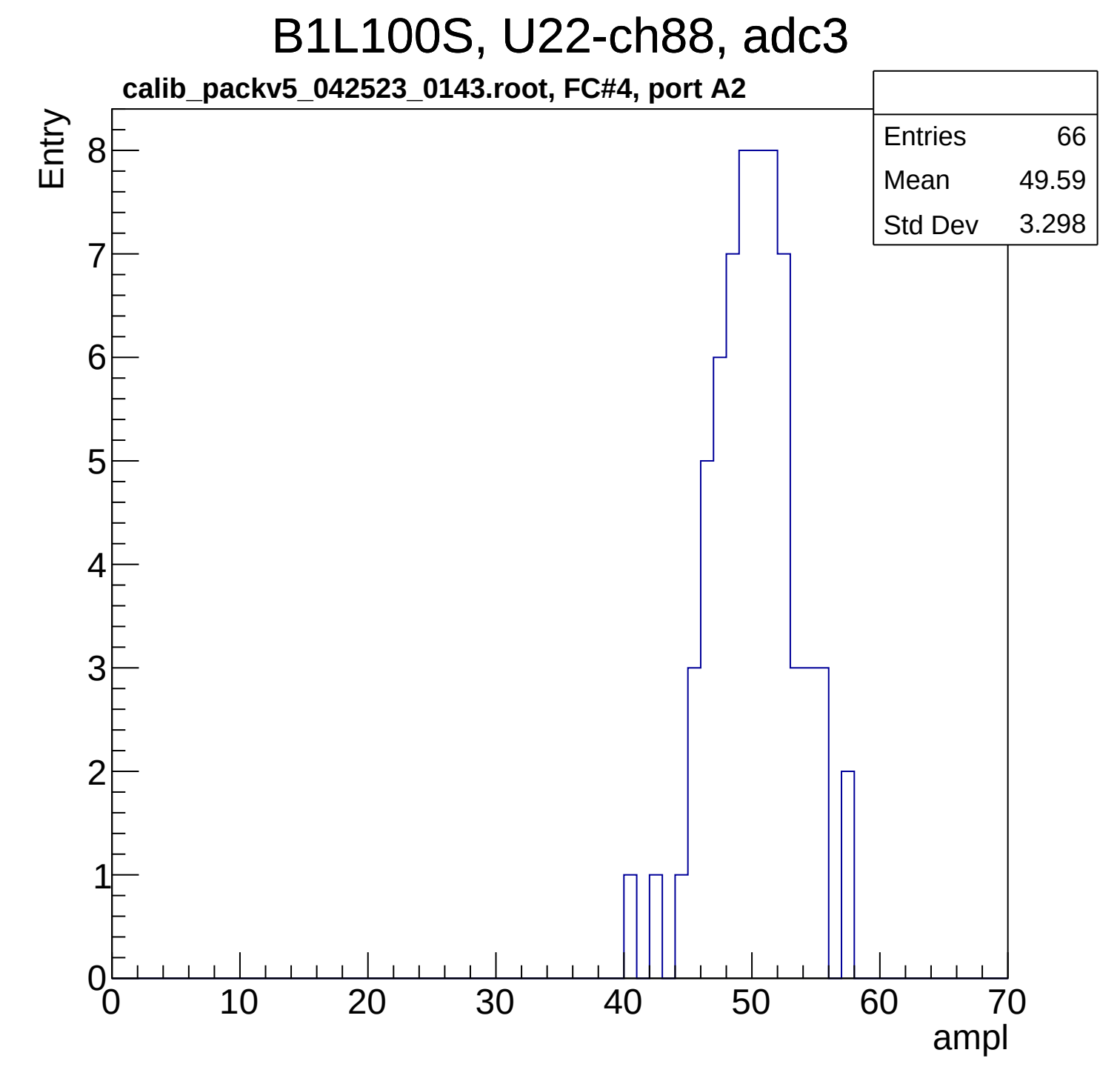
B1L100S, U22-ch88, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 49.59
Std Dev 3.298
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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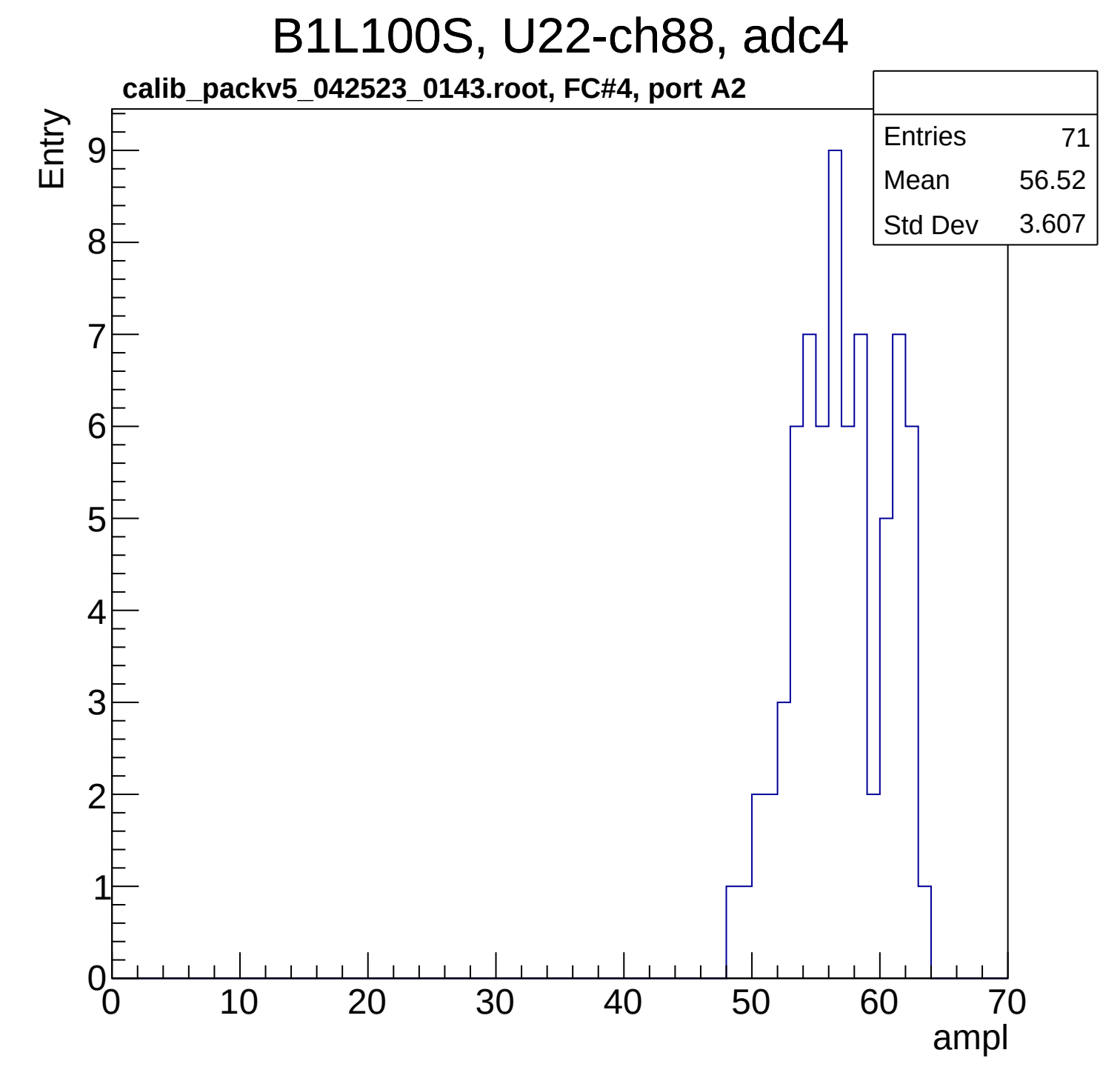
B1L100S, U22-ch88, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 56.52
Std Dev 3.607
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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60
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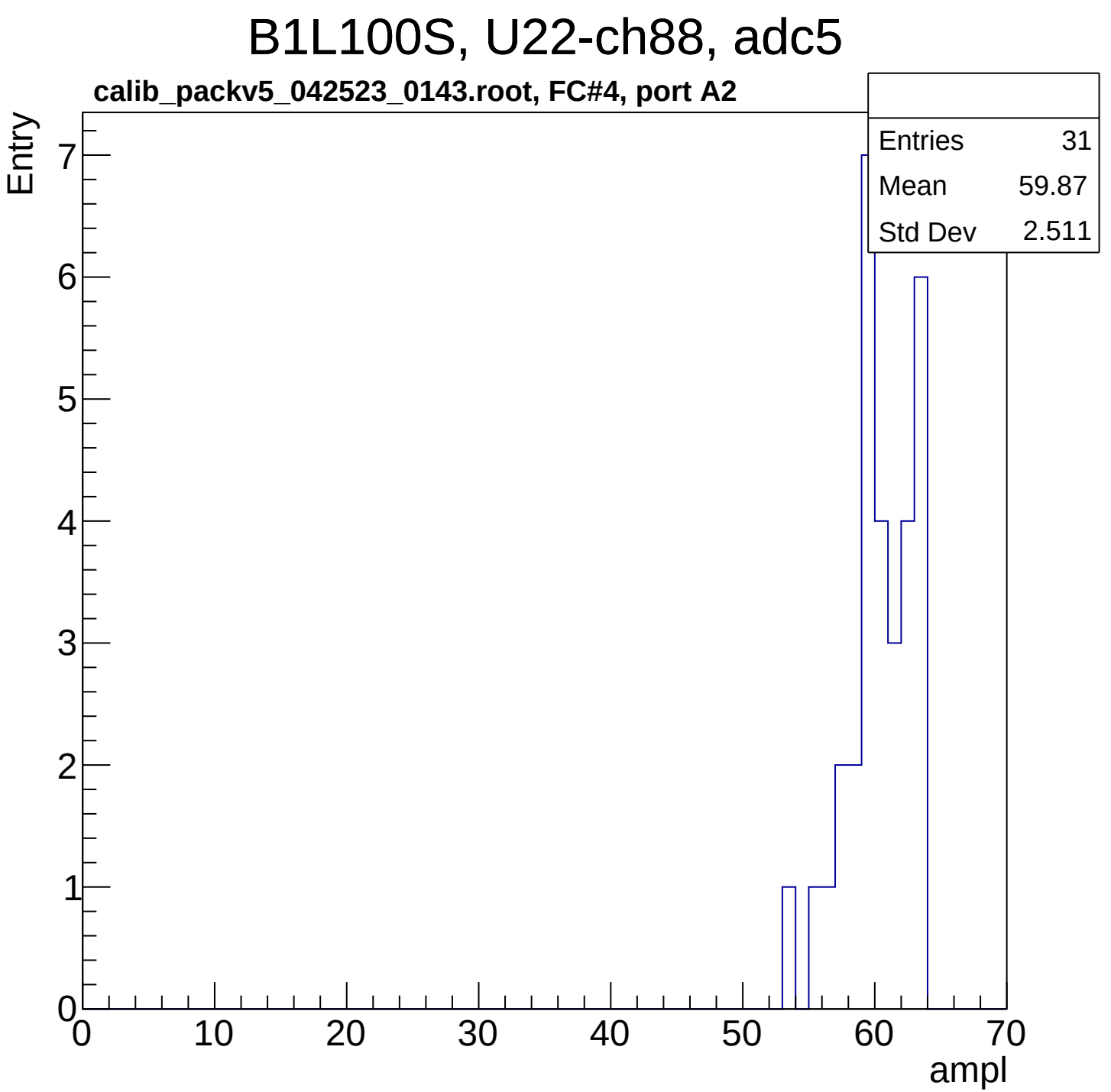
B1L100S, U22-ch88, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 31
Mean 59.87
Std Dev 2.511
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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40
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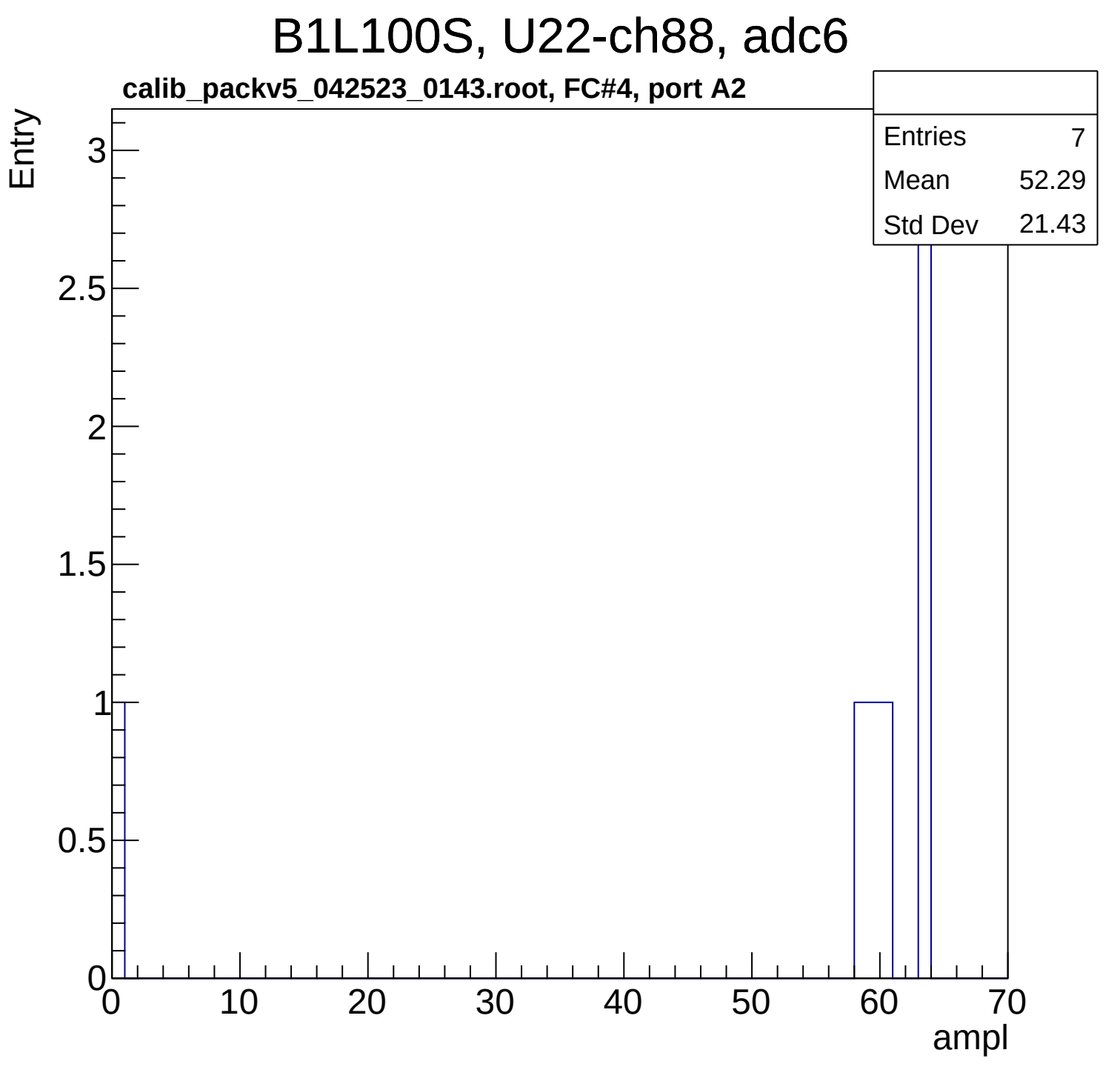
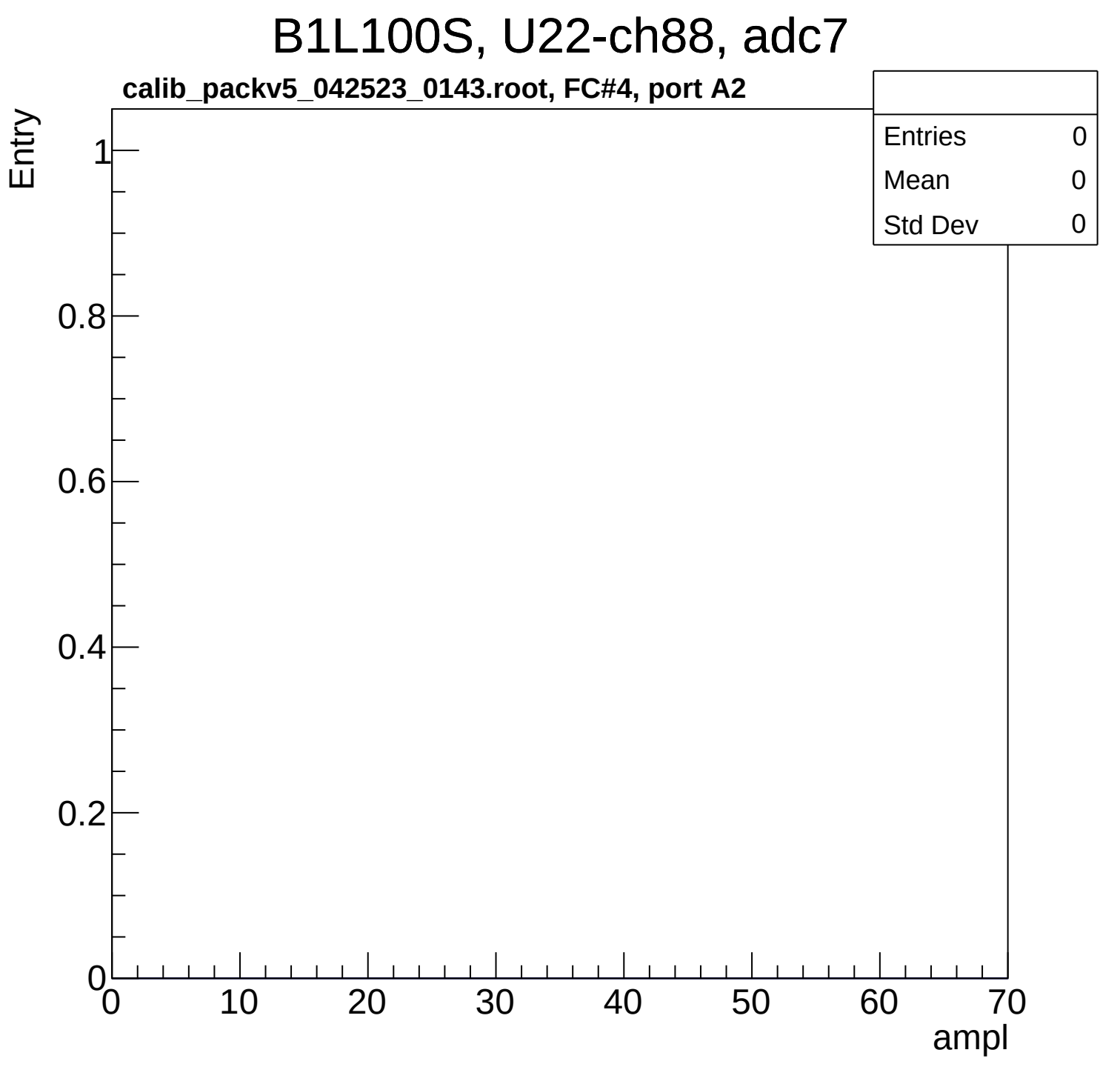
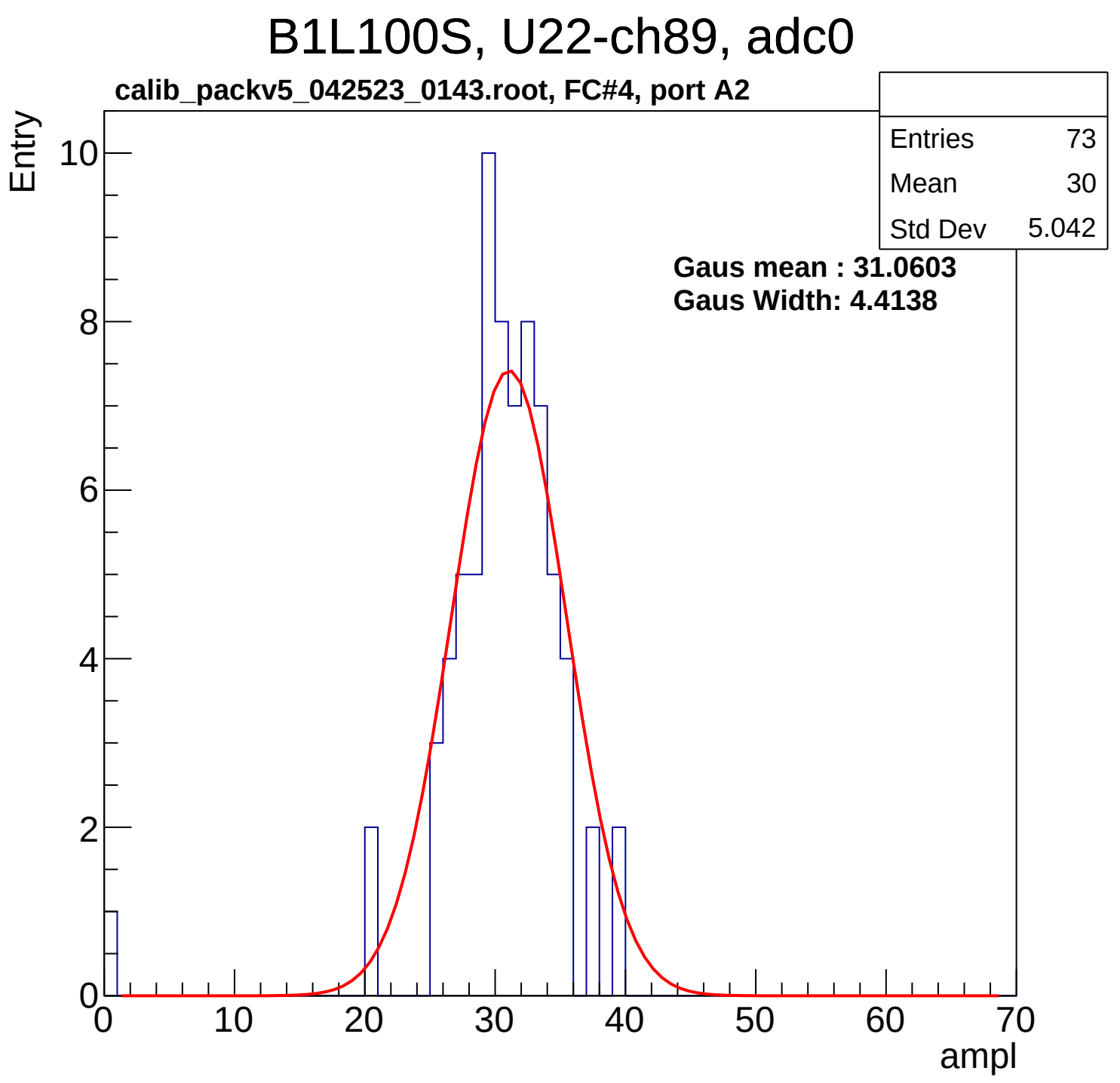
60
70


B1L100S, U22-ch88, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 52.29
Std Dev 21.43
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch88, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch89, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 30
Std Dev 5.042
Gaus mean : 31.0603
Gaus Width: 4.4138
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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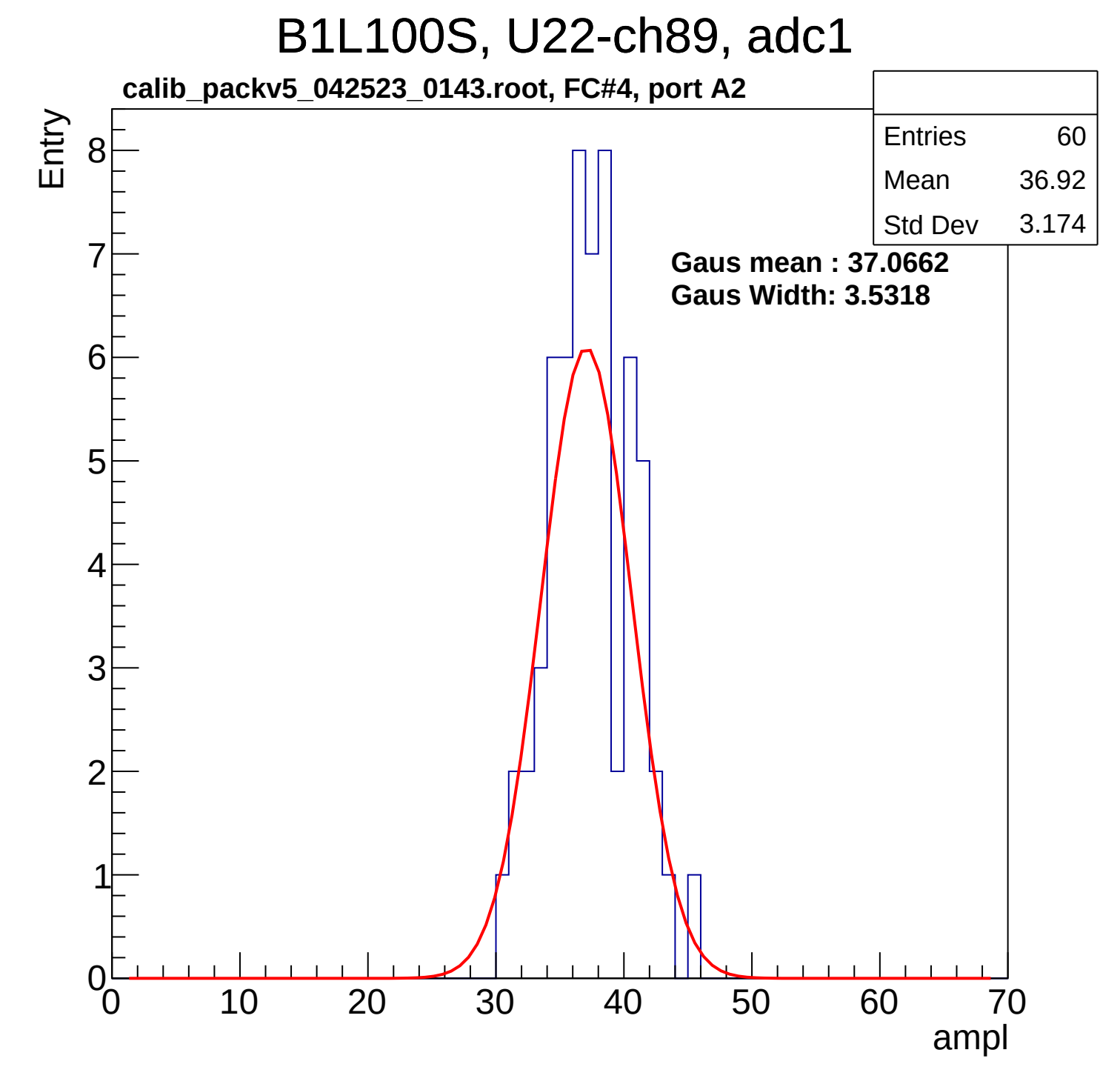
B1L100S, U22-ch89, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 36.92
Std Dev 3.174
Gaus mean : 37.0662
Gaus Width: 3.5318
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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40
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60
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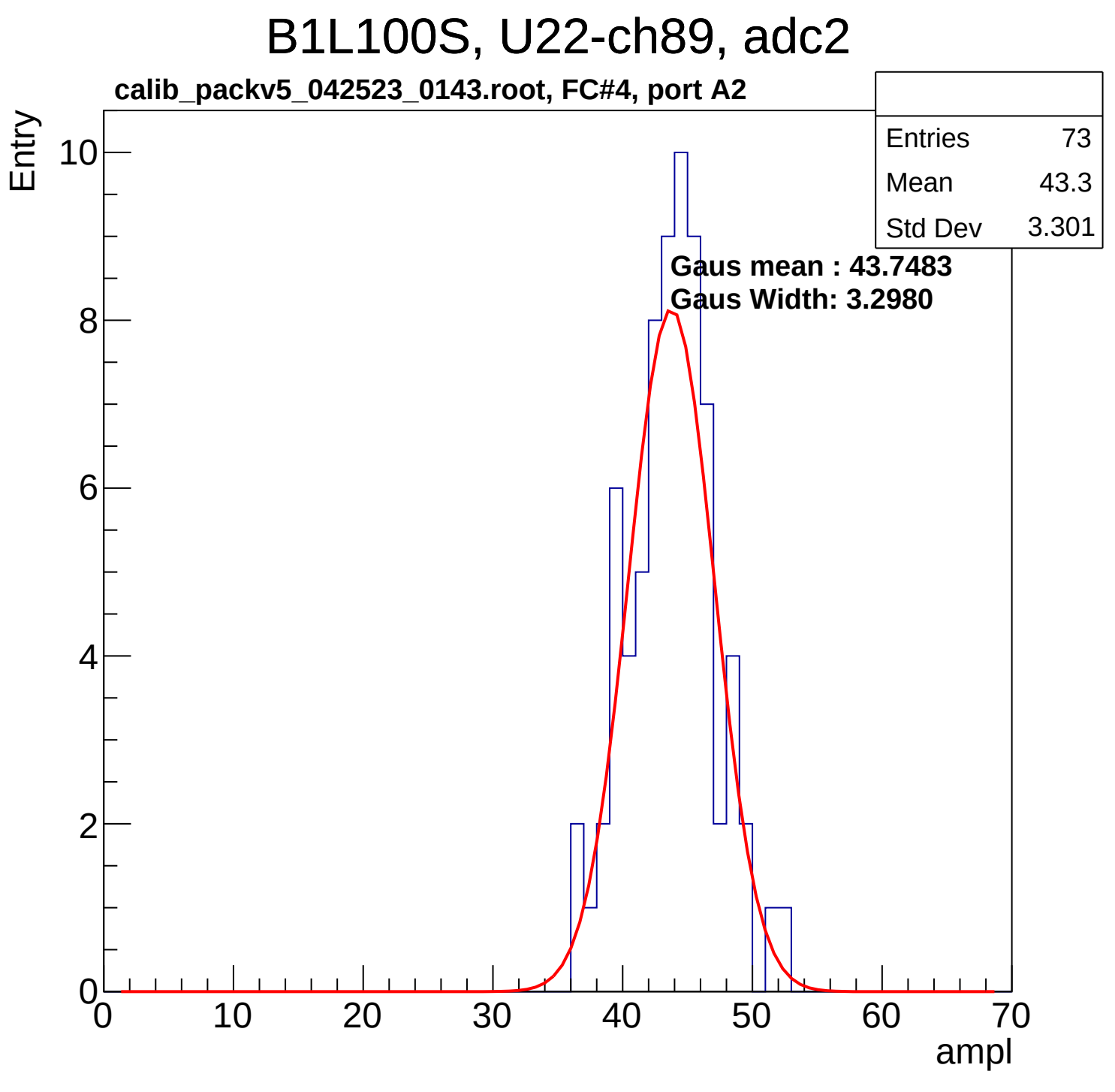
B1L100S, U22-ch89, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.3
Std Dev 3.301
Gaus mean : 43.7483
Gaus Width: 3.2980
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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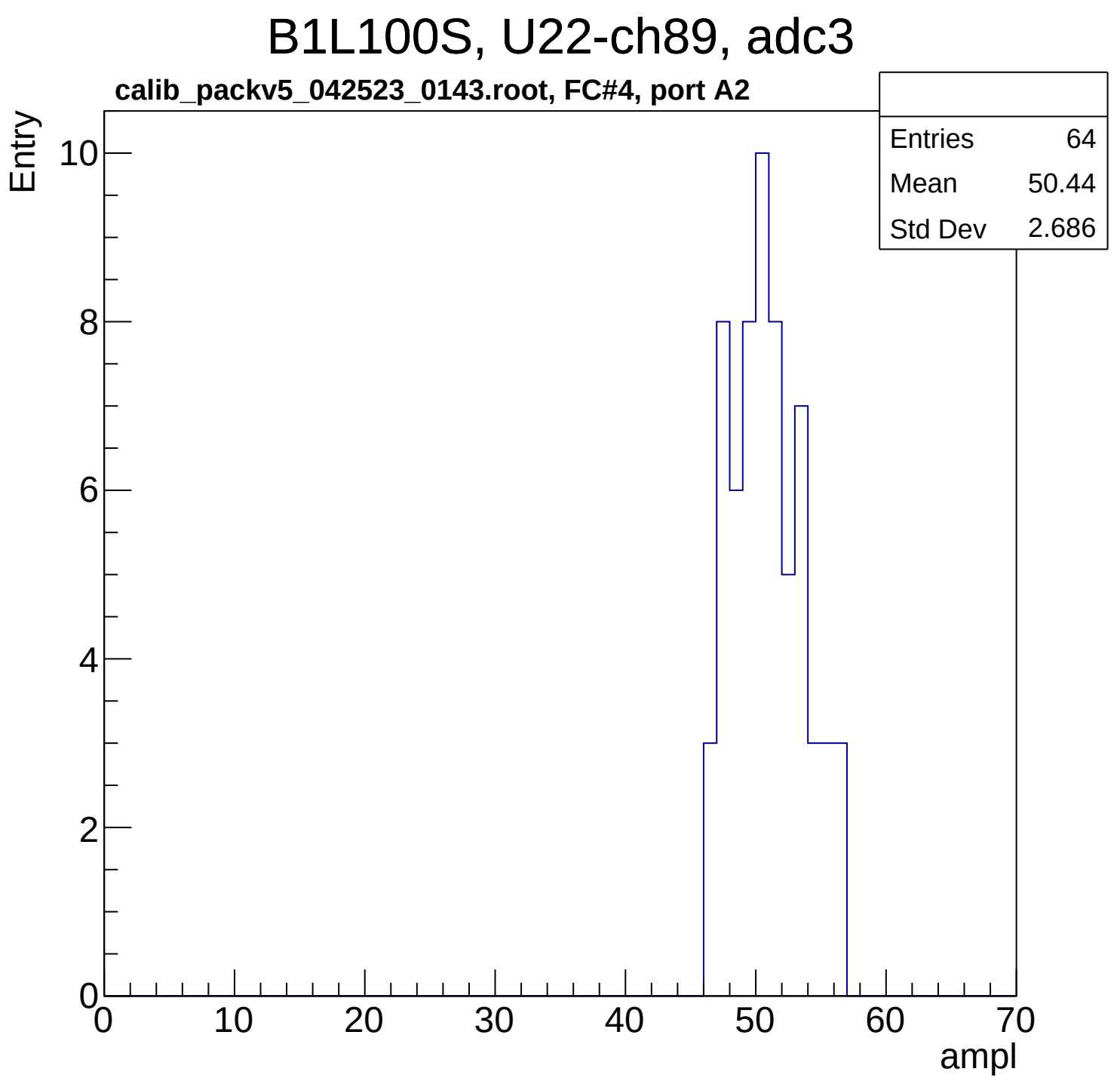
B1L100S, U22-ch89, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.44
Std Dev 2.686
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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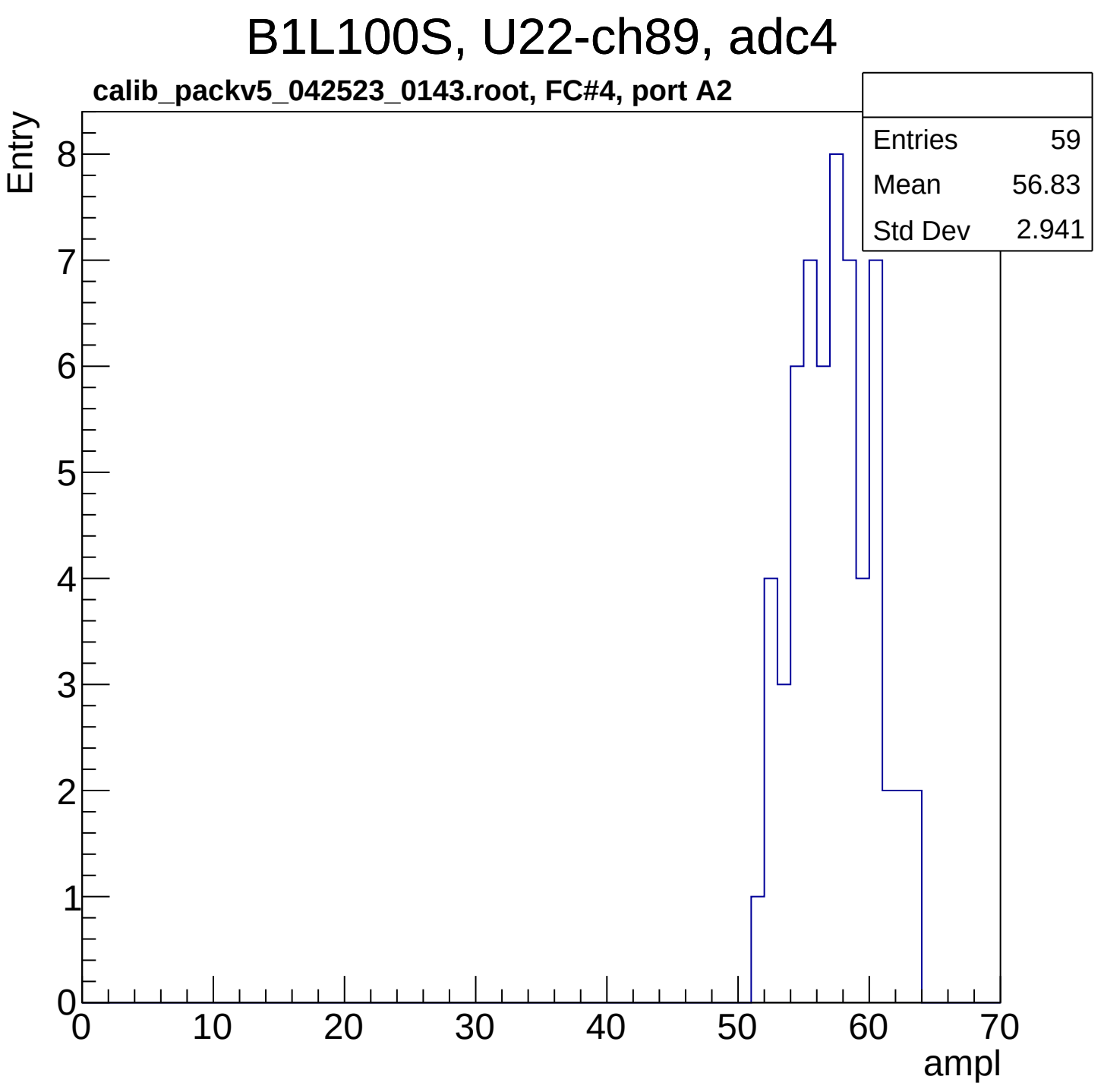
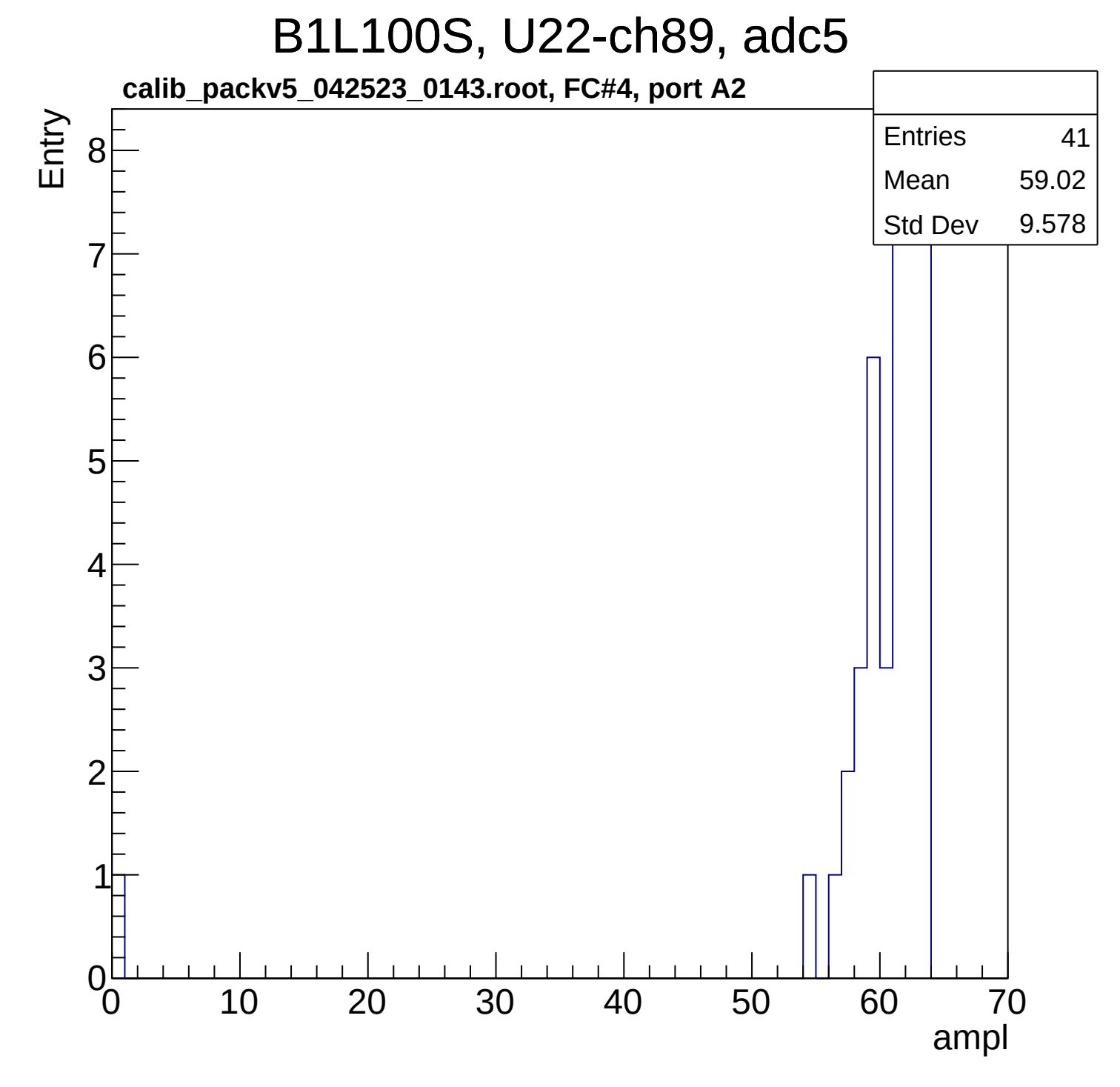

B1L100S, U22-ch89, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 56.83
Std Dev 2.941
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch89, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 59.02
Std Dev 9.578
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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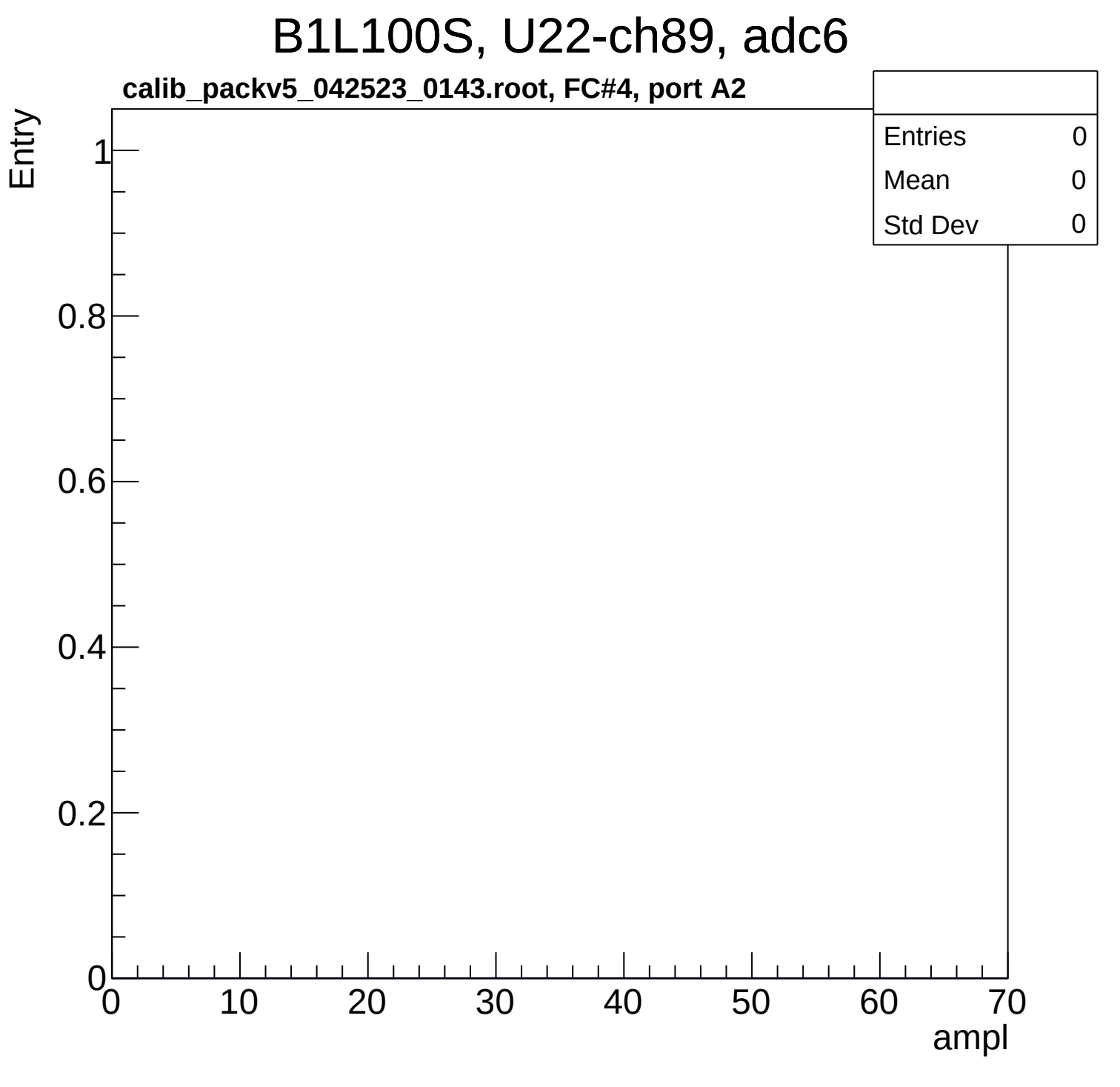
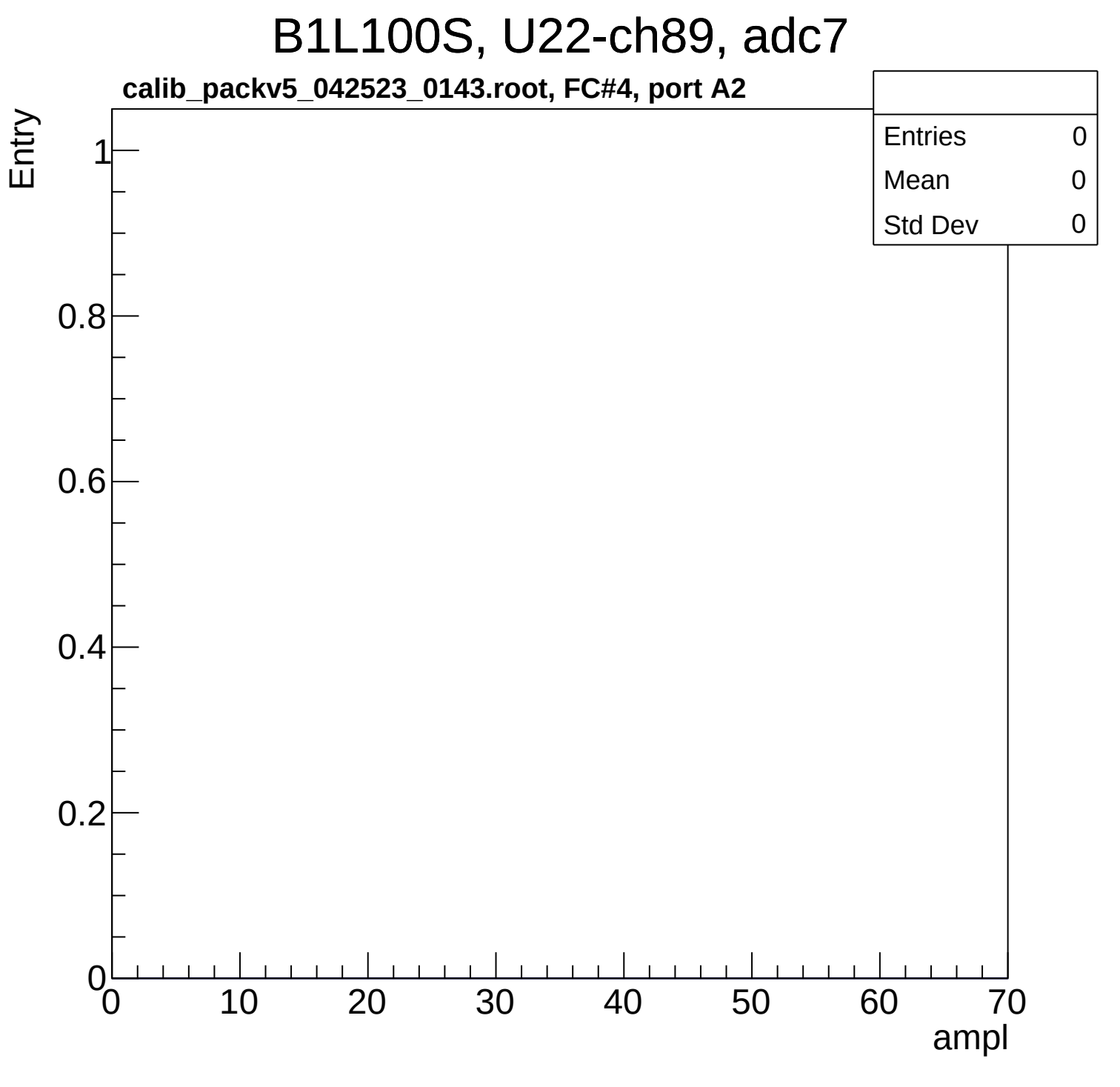
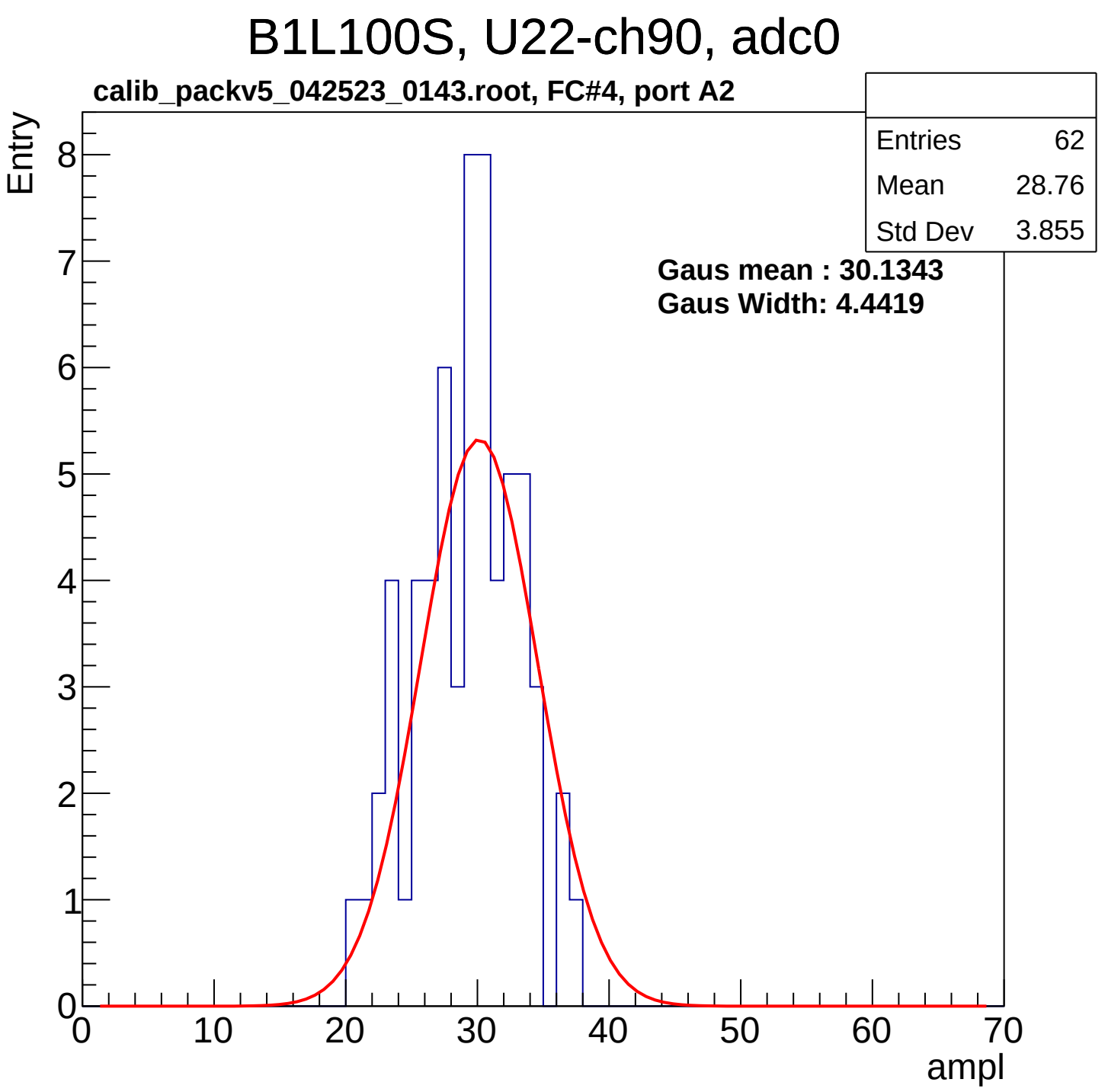
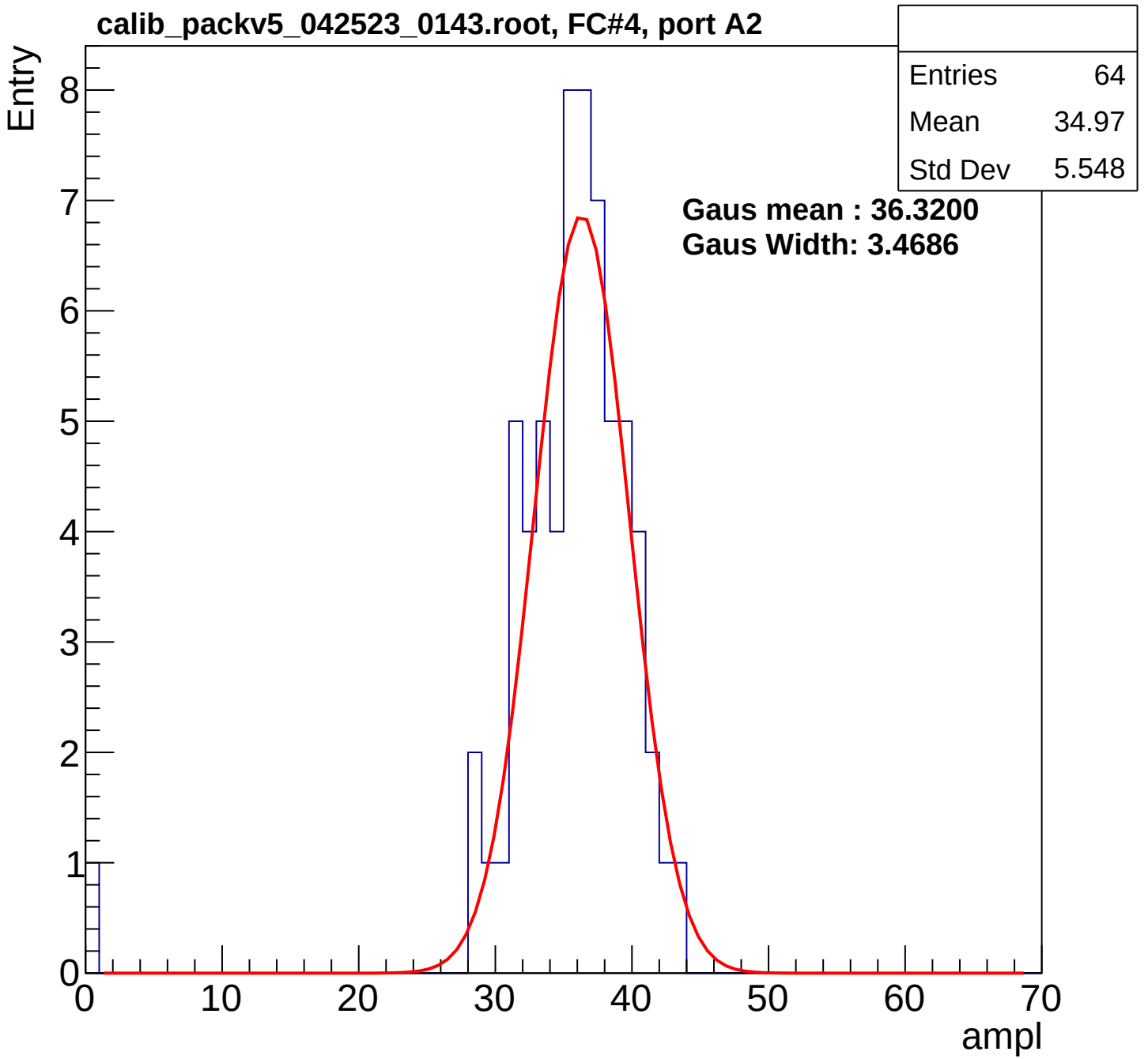
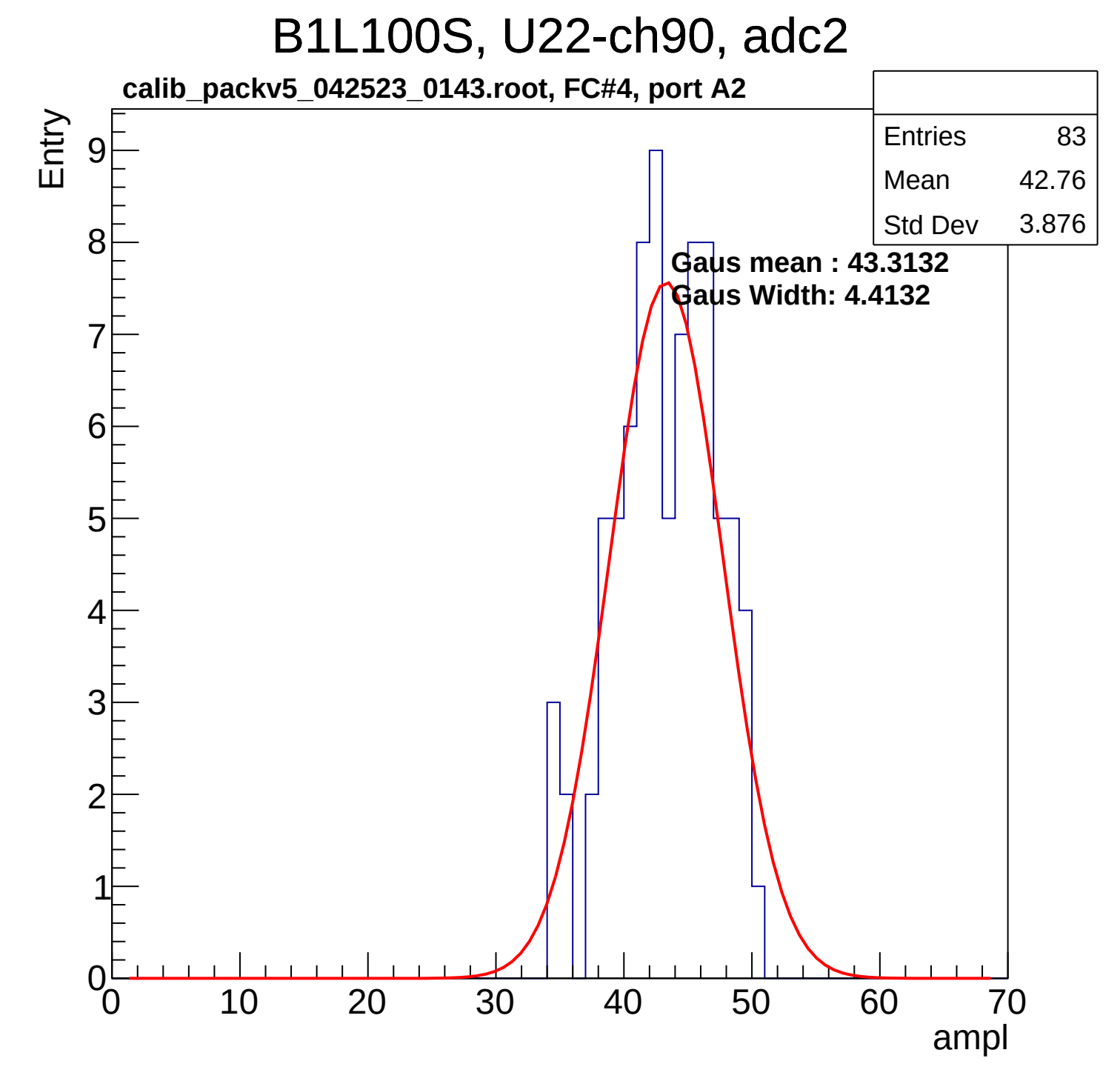
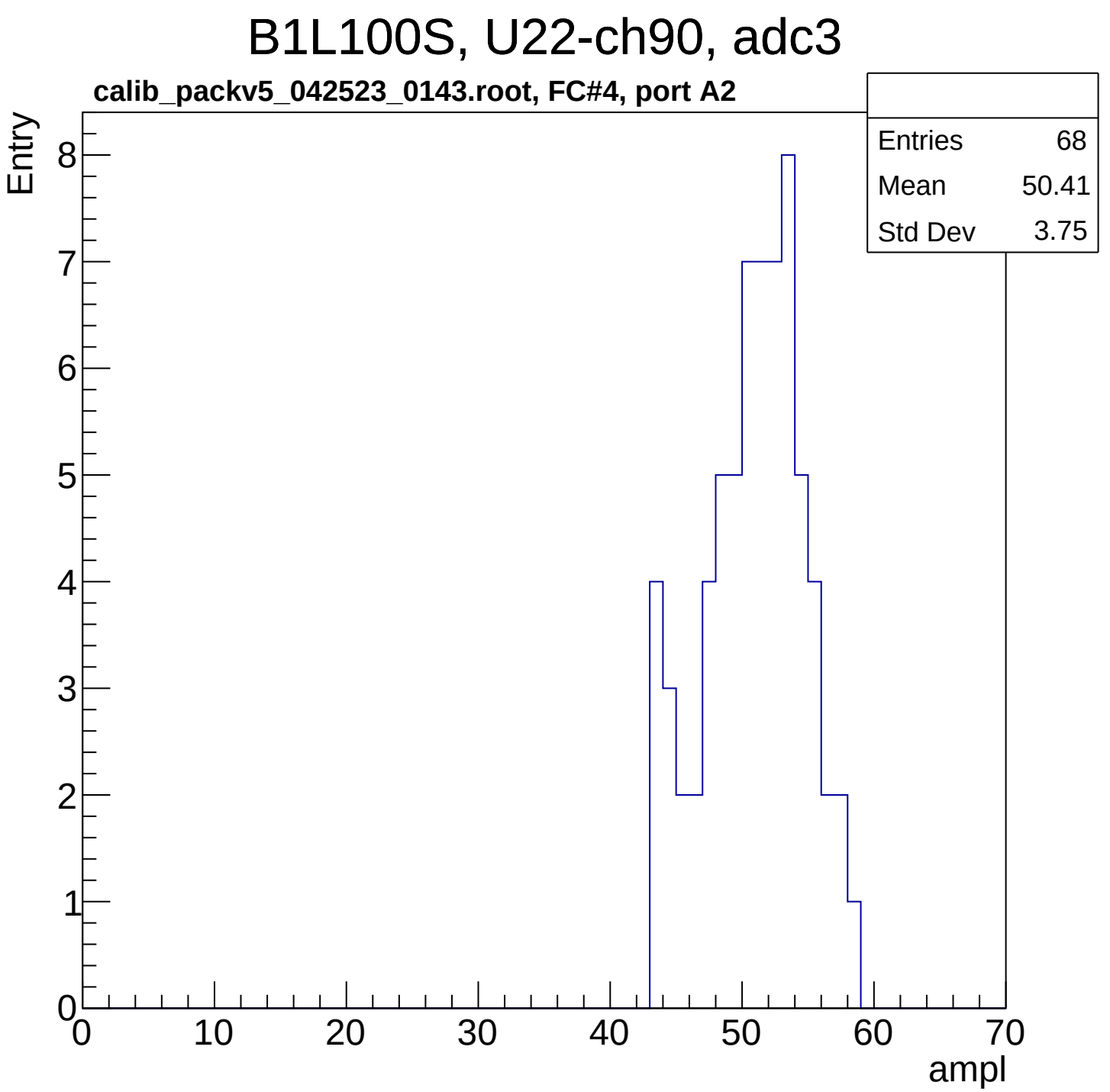

B1L100S, U22-ch89, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

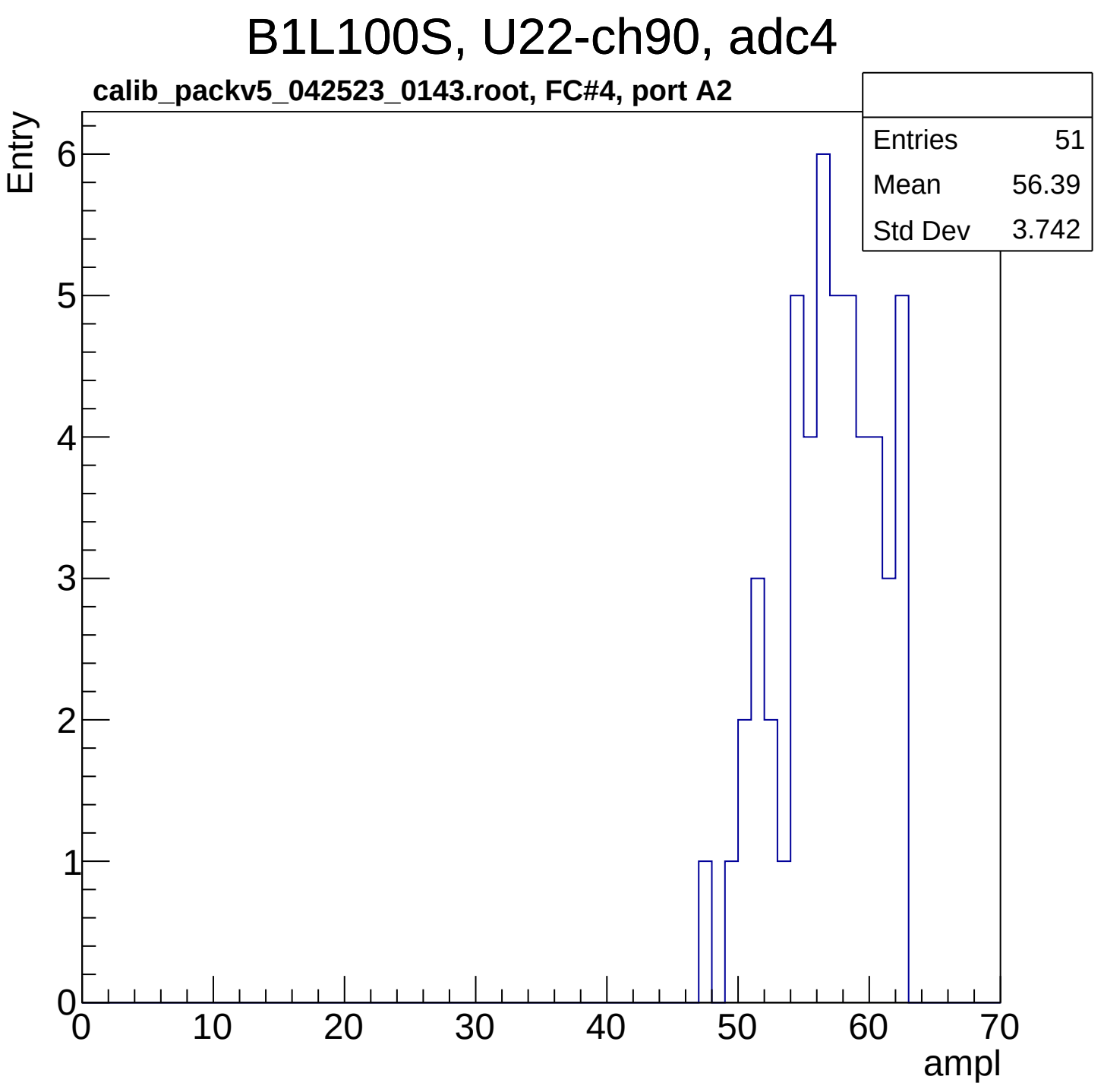

B1L100S, U22-ch89, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

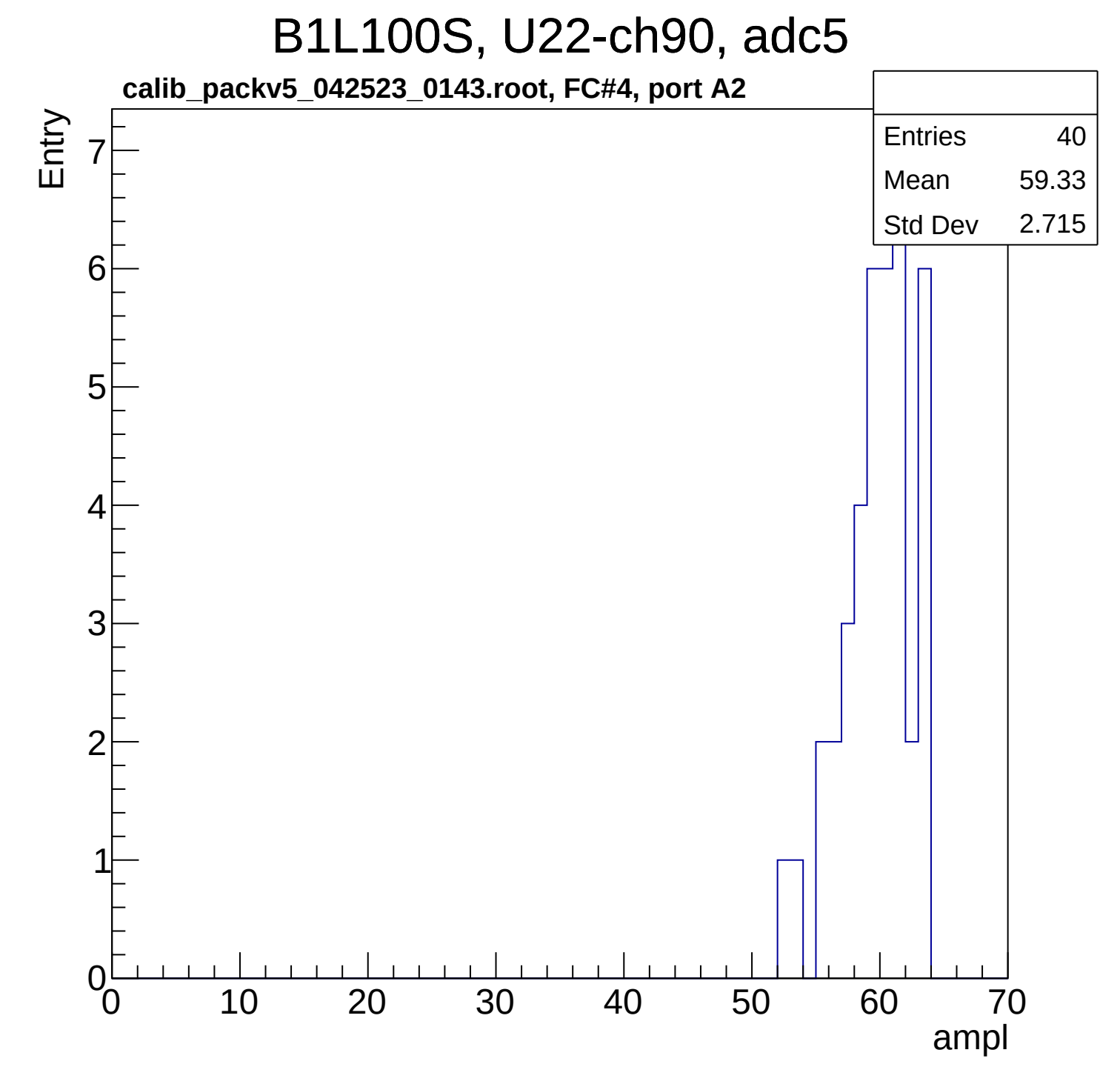

B1L100S, U22-ch90, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 28.76
Std Dev 3.855
Gaus mean : 30.1343
Gaus Width: 4.4419
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

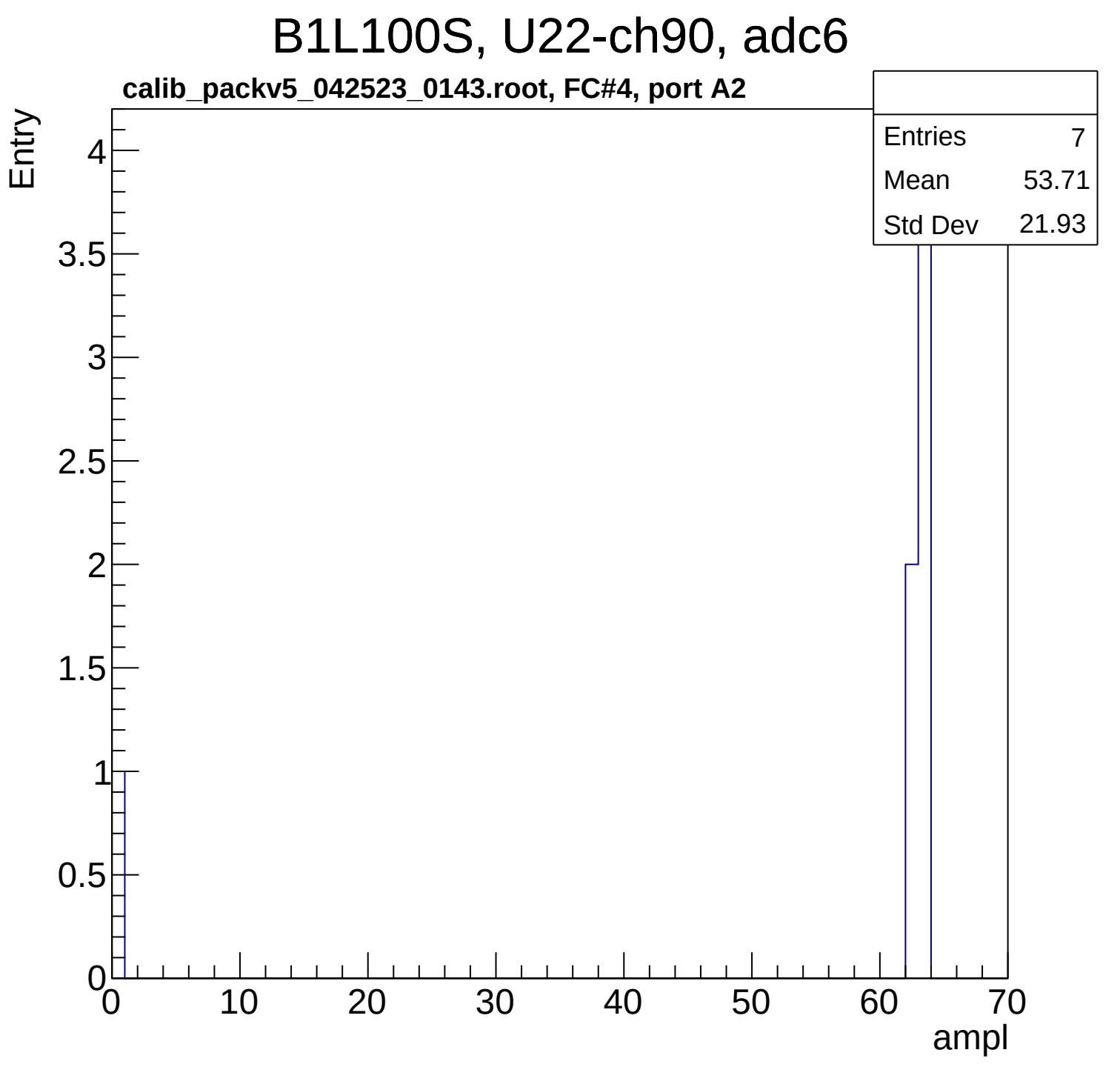

B1L100S, U22-ch90, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 34.97
Std Dev 5.548
Gaus mean : 36.3200
Gaus Width: 3.4686
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

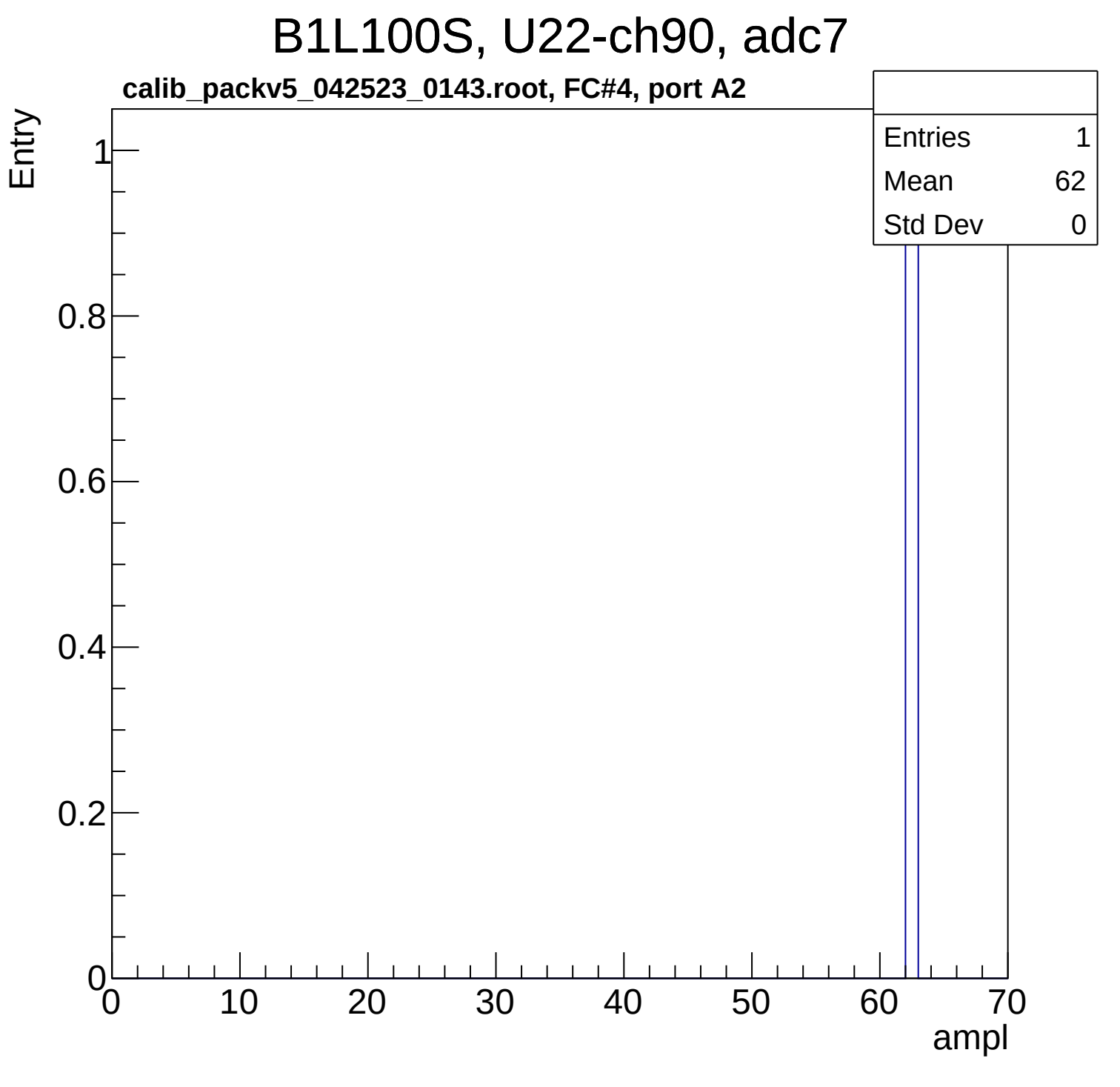

B1L100S, U22-ch90, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 42.76
Std Dev 3.876
Gaus mean : 43.3132
Gaus Width: 4.4132
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70

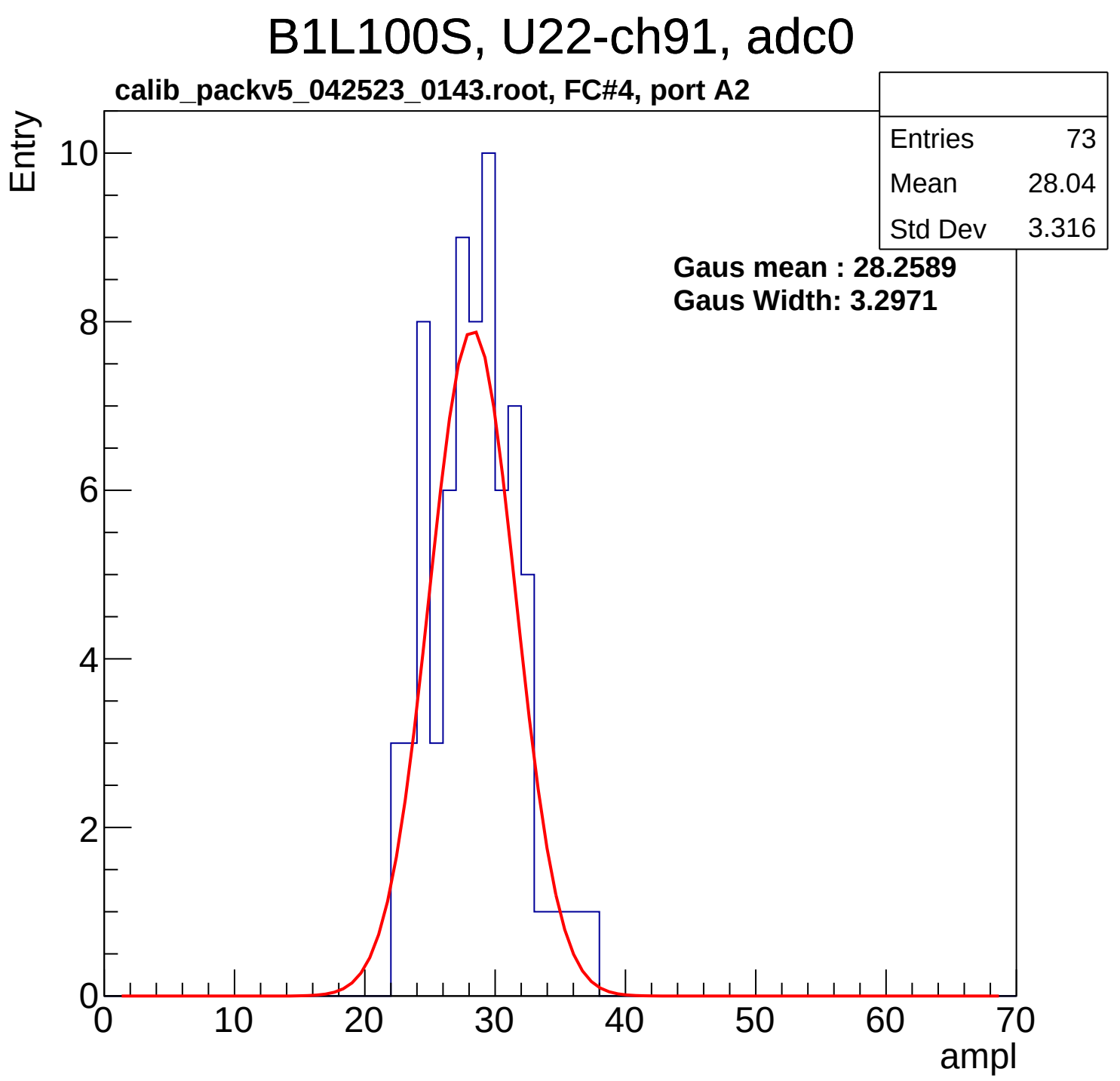

B1L100S, U22-ch90, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.41
Std Dev 3.75
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

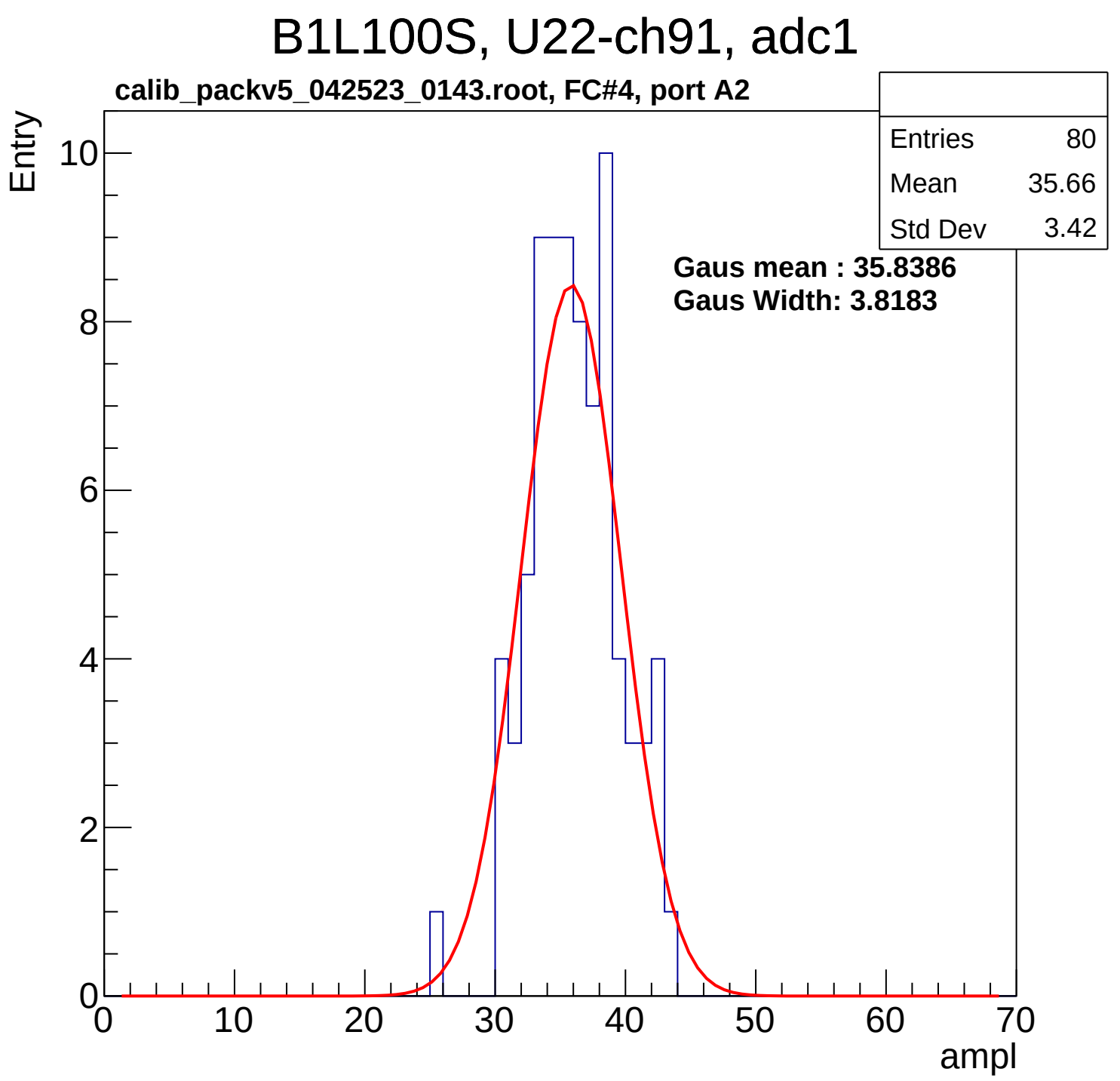

B1L100S, U22-ch90, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 56.39
Std Dev 3.742
Entry
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70
ampl

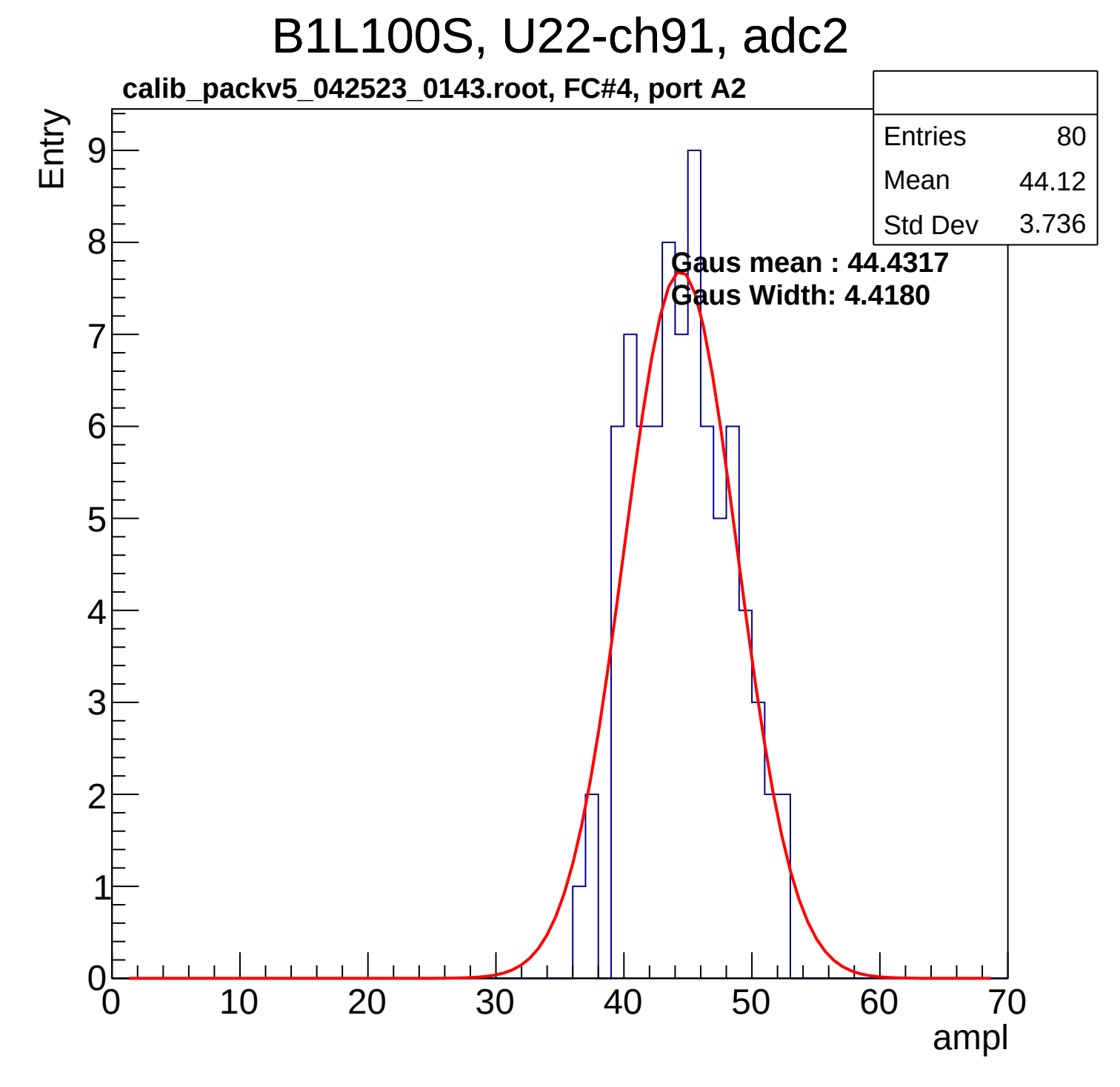

B1L100S, U22-ch90, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 59.33
Std Dev 2.715
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

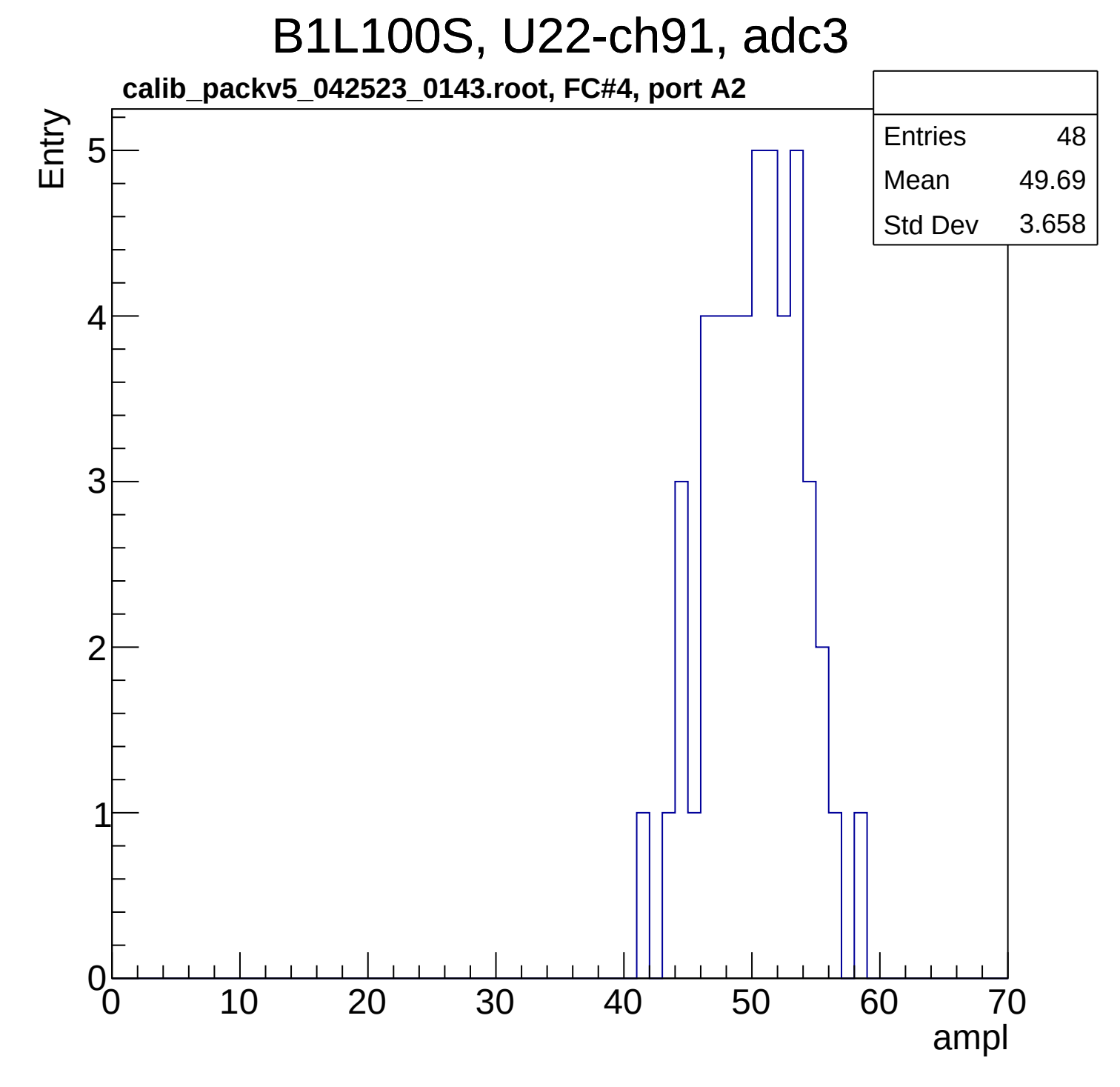

B1L100S, U22-ch90, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.71
Std Dev 21.93
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

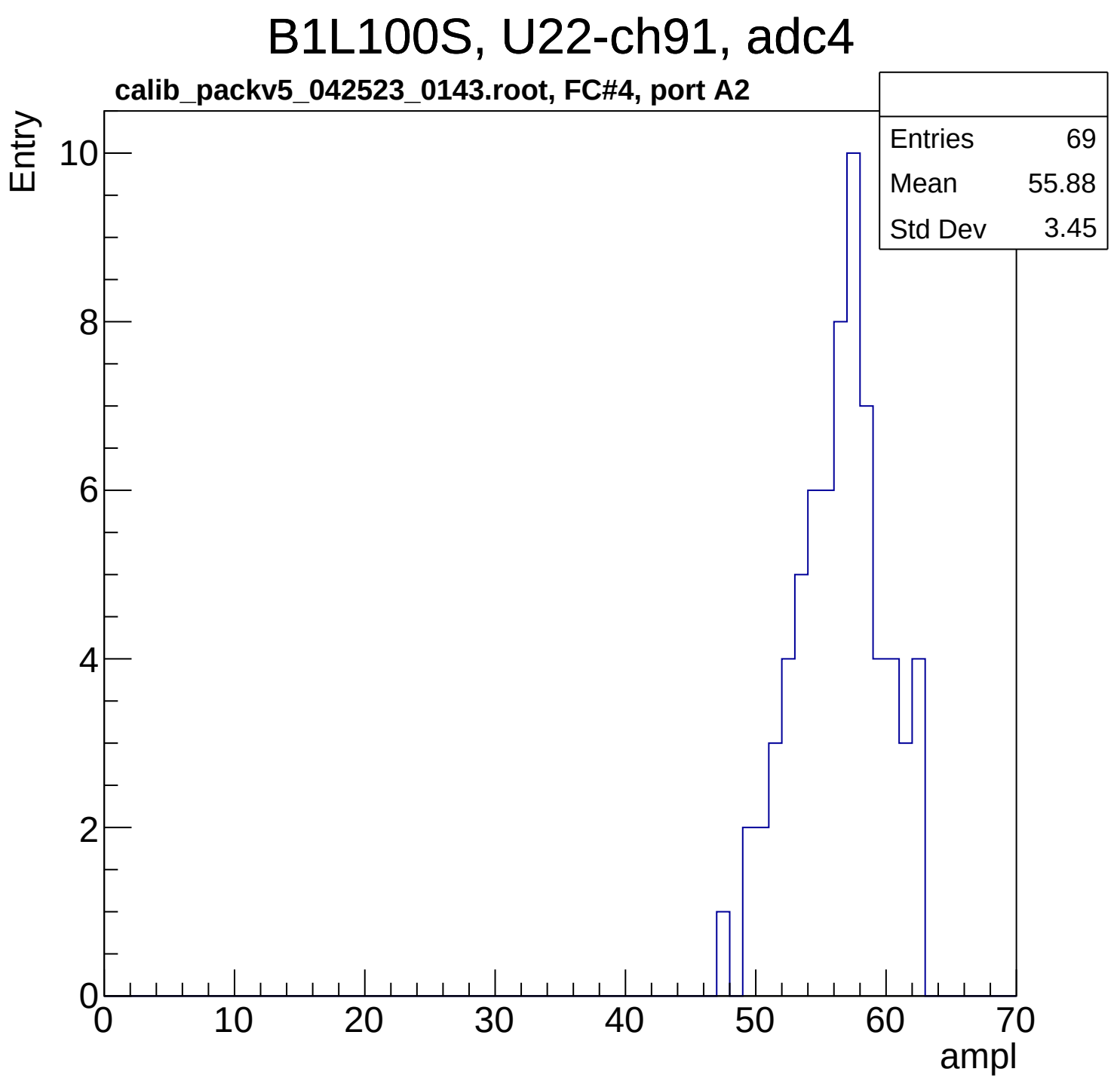

B1L100S, U22-ch90, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

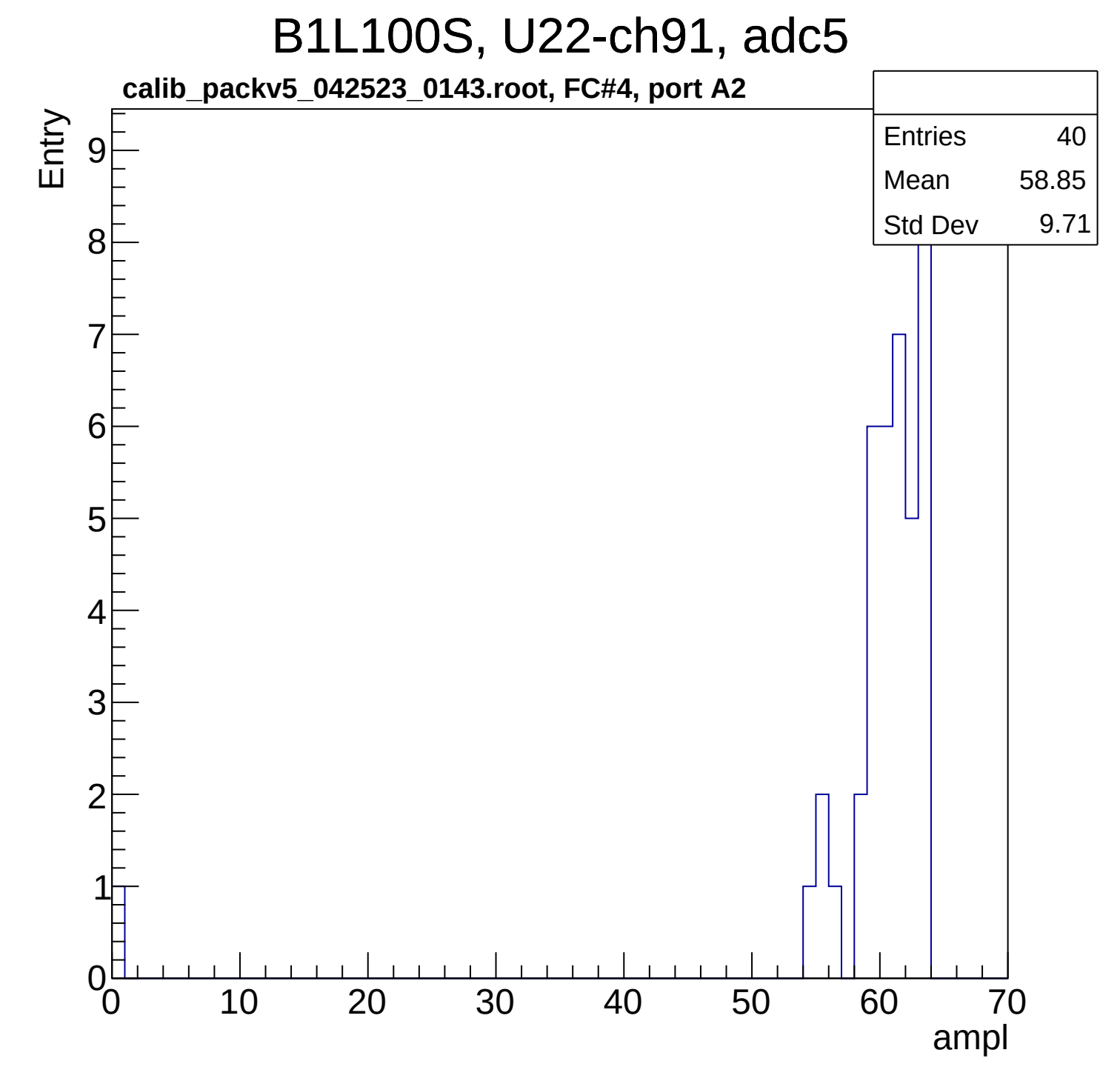

B1L100S, U22-ch91, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 28.04
Std Dev 3.316
Gaus mean : 28.2589
Gaus Width: 3.2971
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch91, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.66
Std Dev 3.42
Gaus mean : 35.8386
Gaus Width: 3.8183
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch91, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 44.12
Std Dev 3.736
Gaus mean : 44.4317
Gaus Width: 4.4180
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch91, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 49.69
Std Dev 3.658
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U22-ch91, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 55.88
Std Dev 3.45
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch91, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.85
Std Dev 9.71
Entry
ampl
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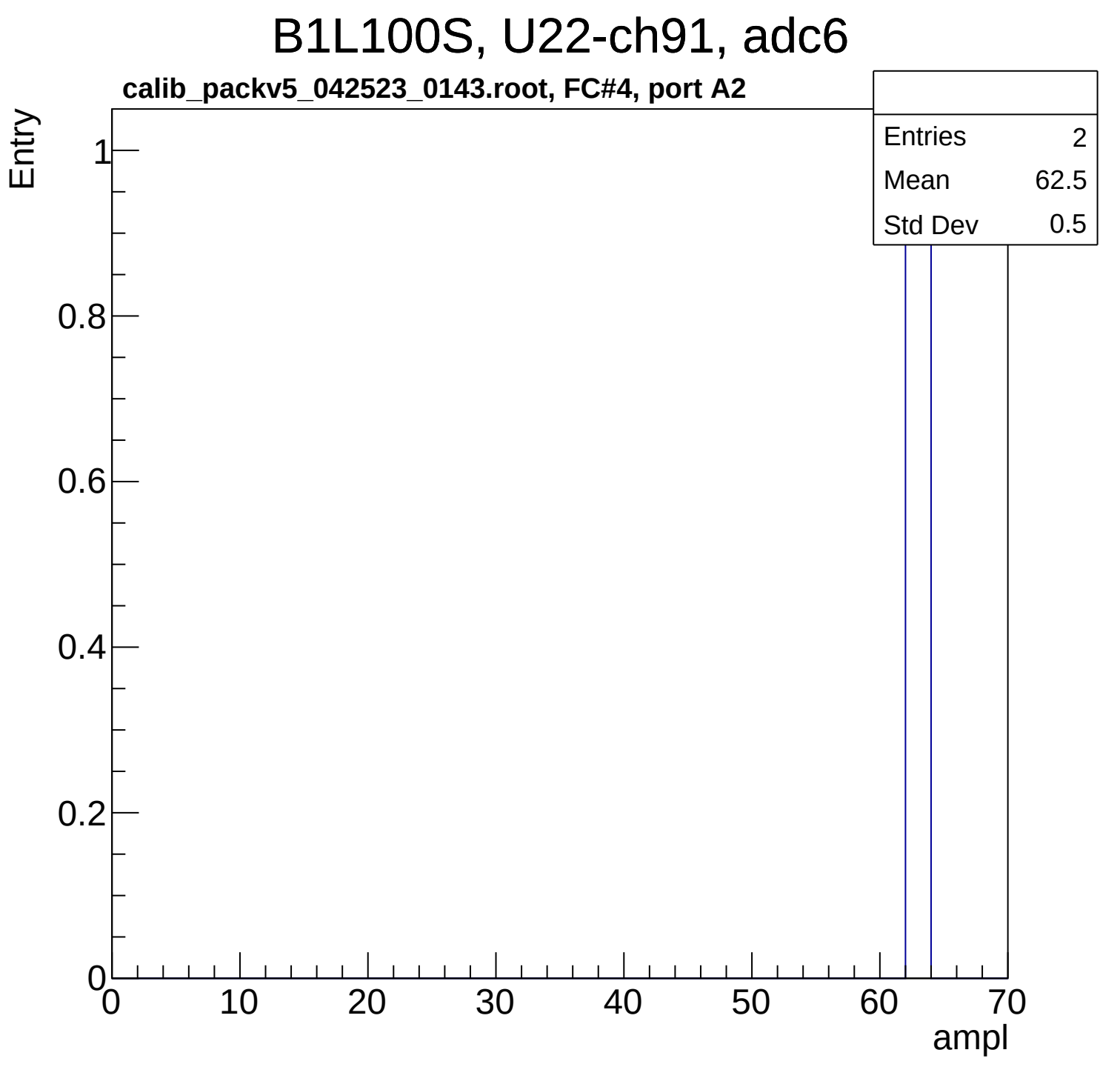
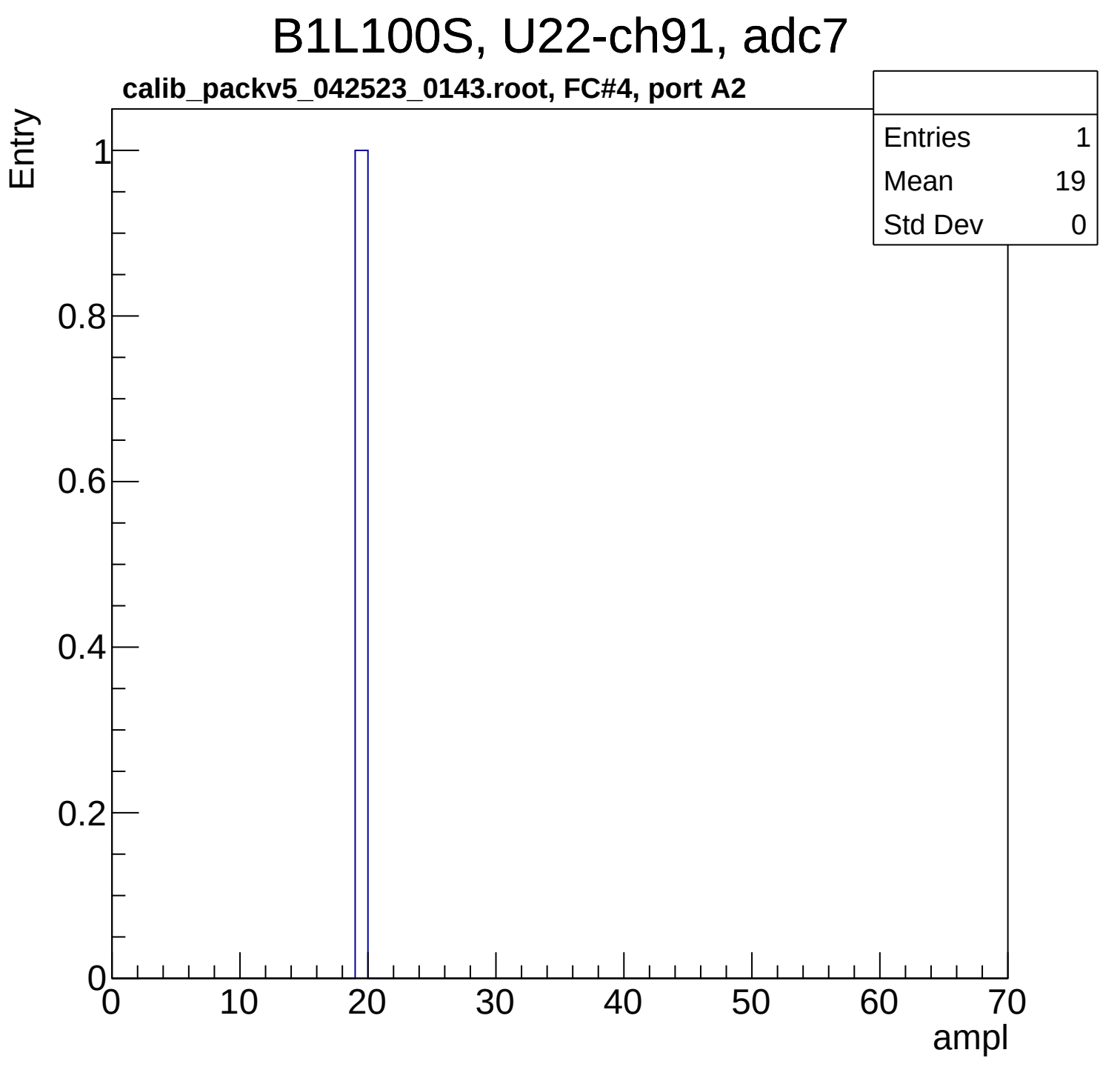
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch91, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62.5
Std Dev 0.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch91, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 19
Std Dev 0
Entry
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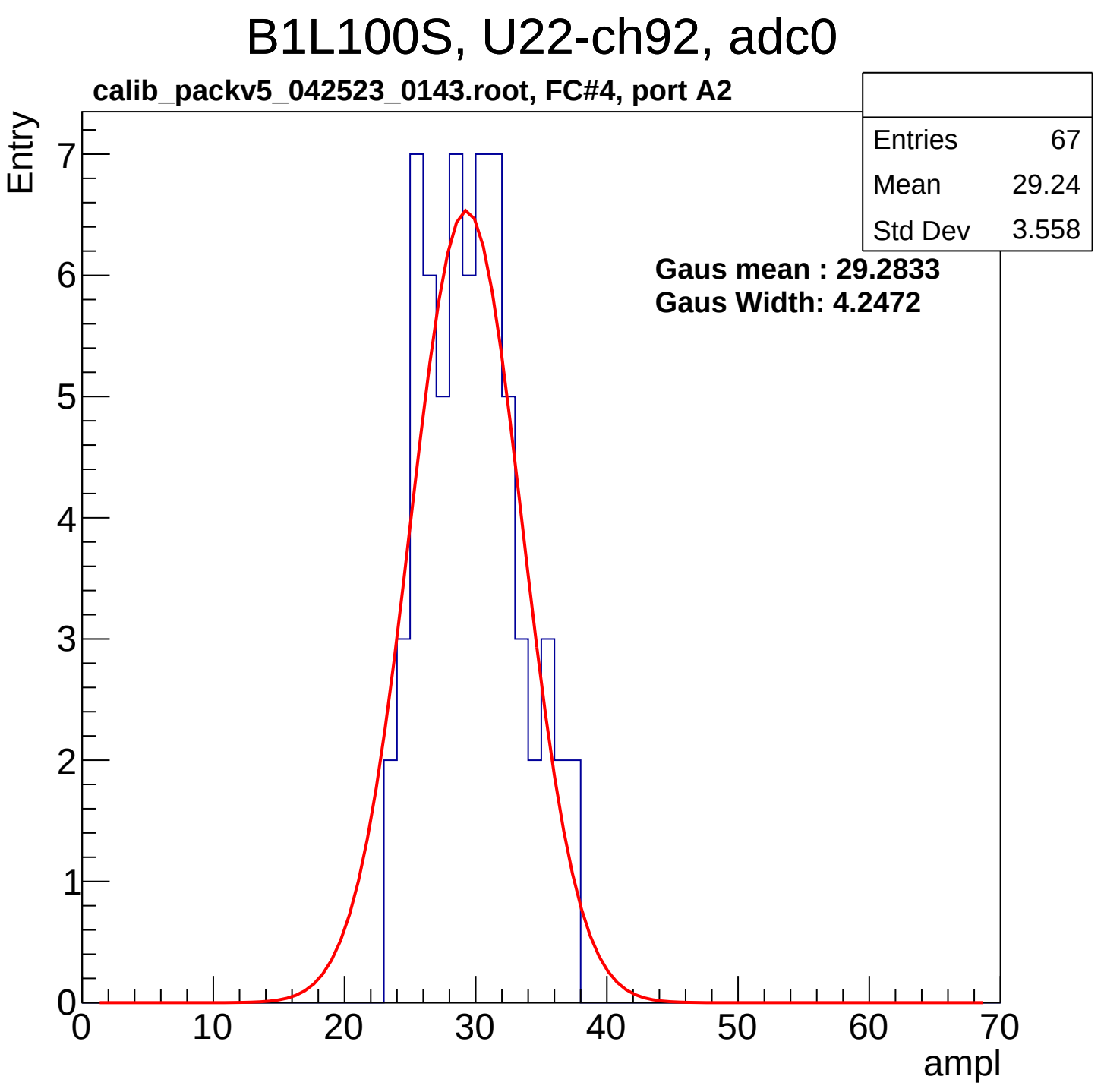
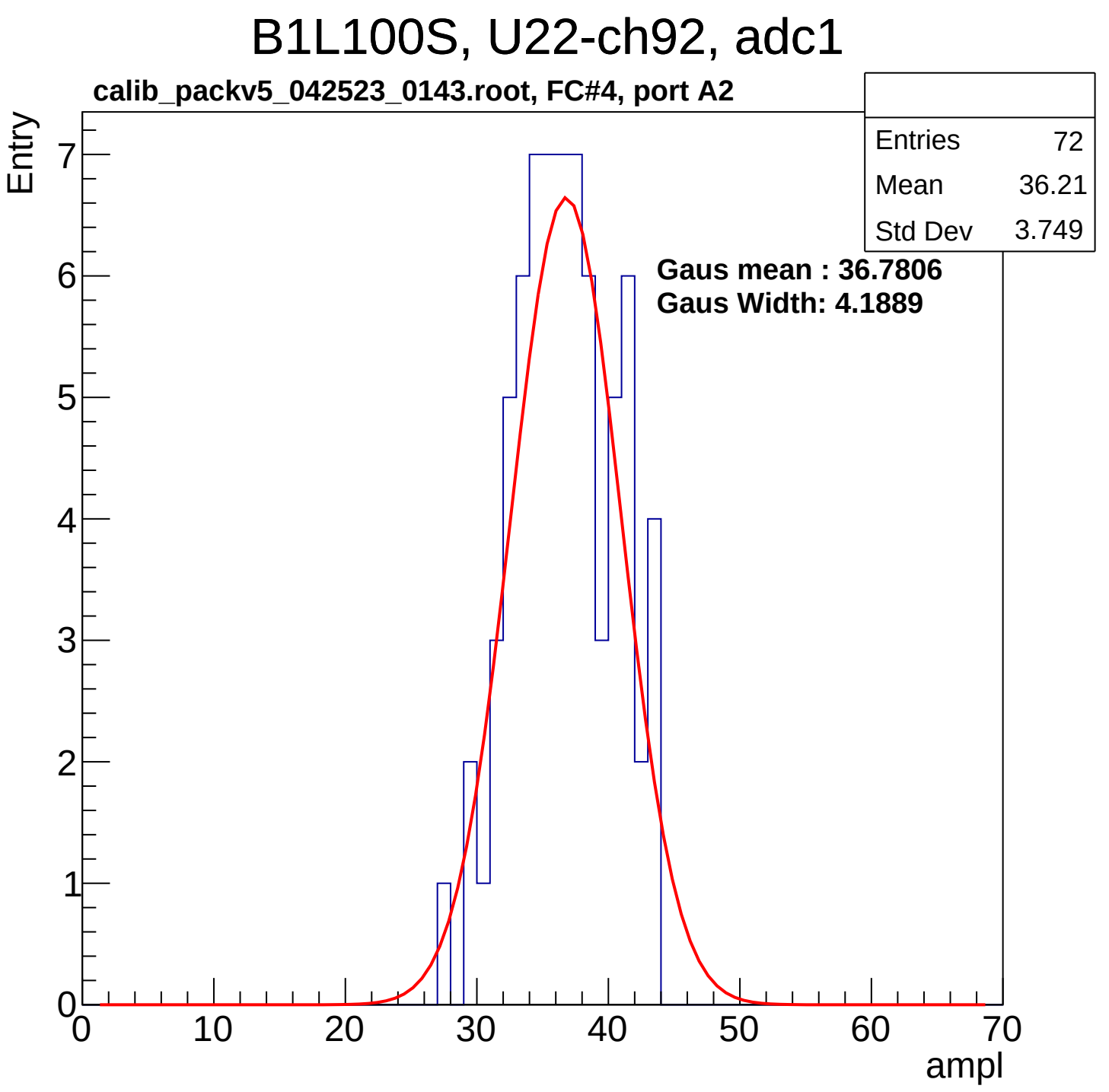
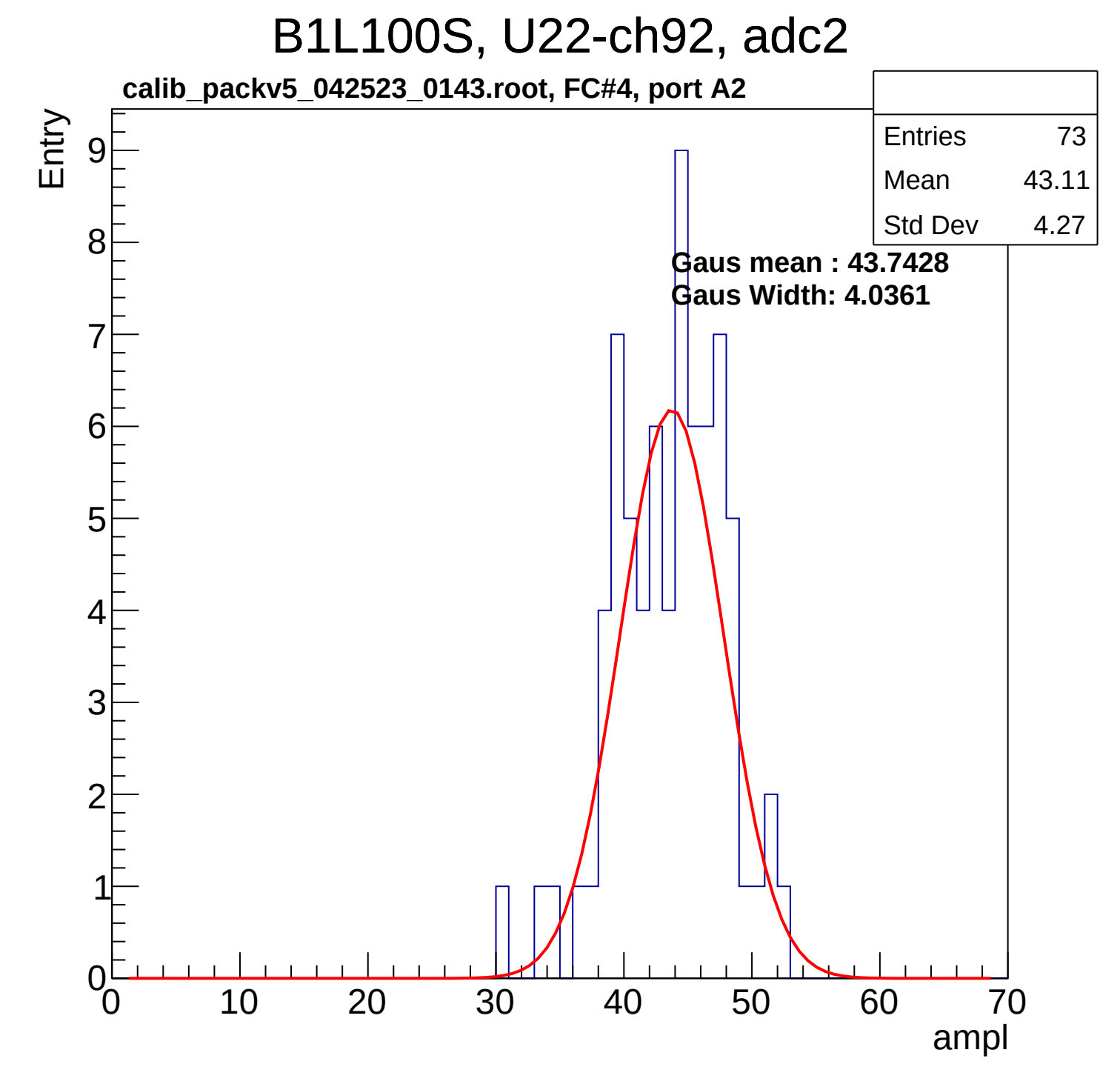
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch92, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 29.24
Std Dev 3.558
Gaus mean : 29.2833
Gaus Width: 4.2472
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch92, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 36.21
Std Dev 3.749
Gaus mean : 36.7806
Gaus Width: 4.1889
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch92, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 43.11
Std Dev 4.27
Gaus mean : 43.7428
Gaus Width: 4.0361
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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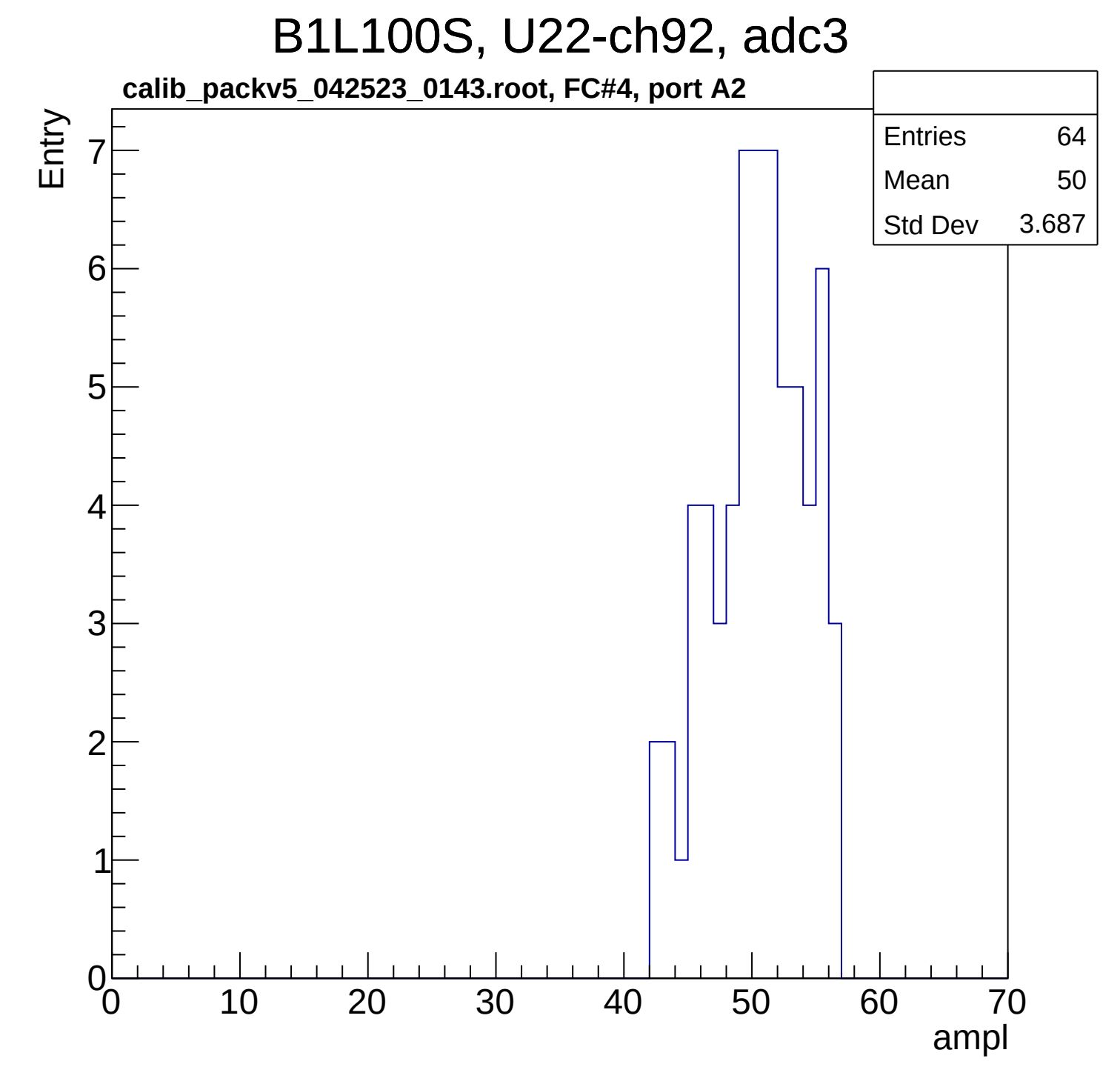
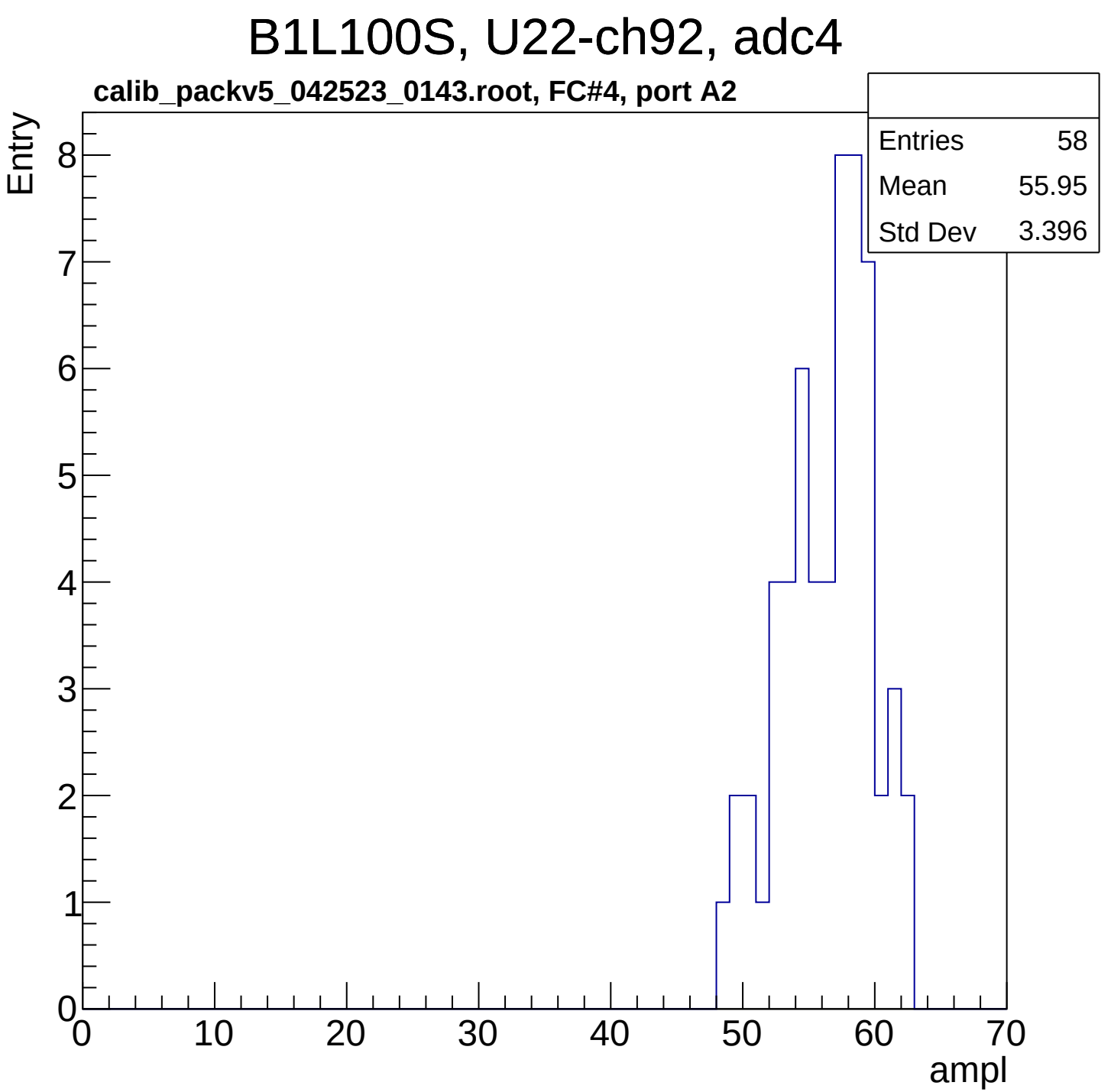
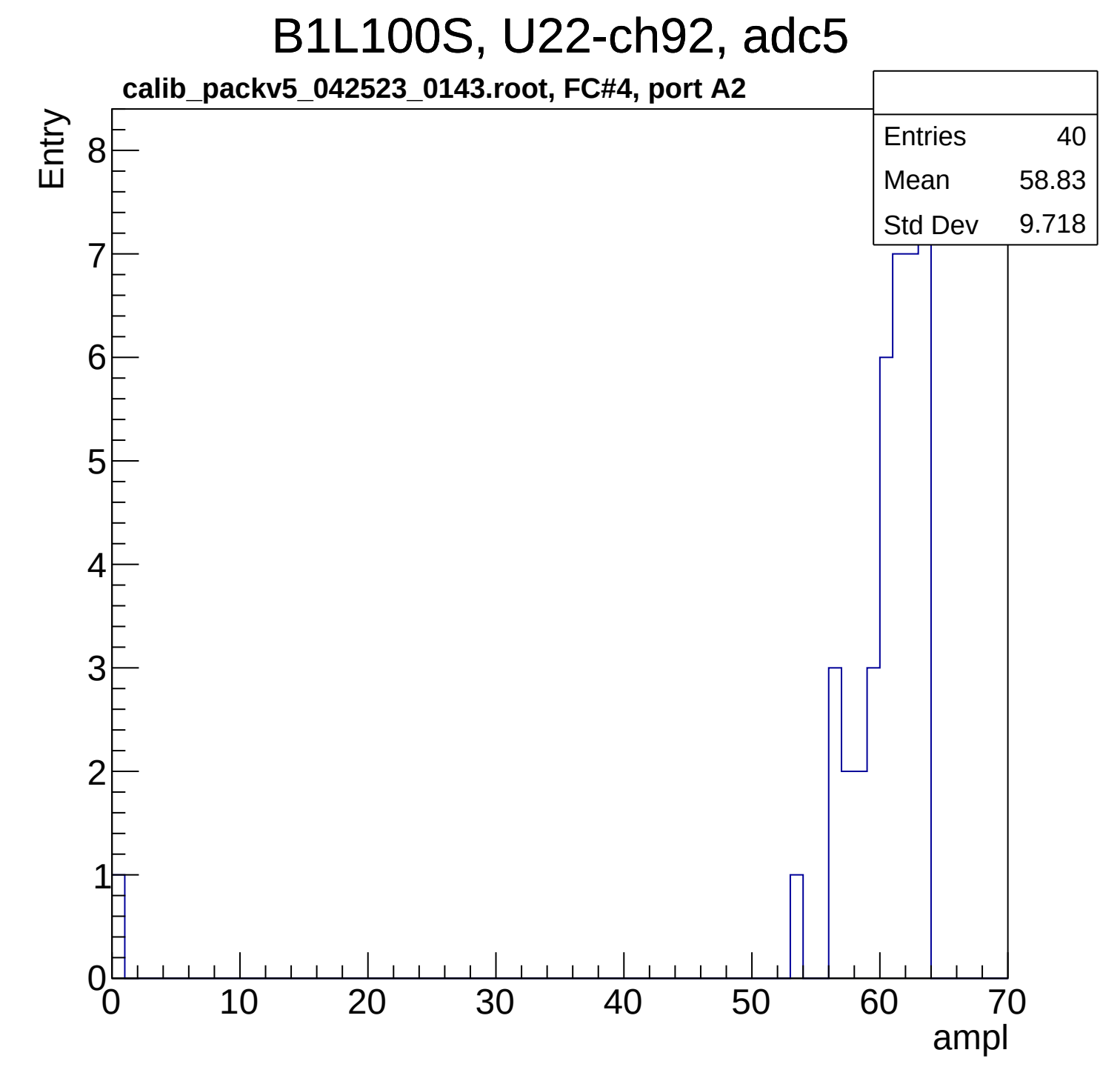

B1L100S, U22-ch92, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50
Std Dev 3.687
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch92, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.95
Std Dev 3.396
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch92, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 58.83
Std Dev 9.718
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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60
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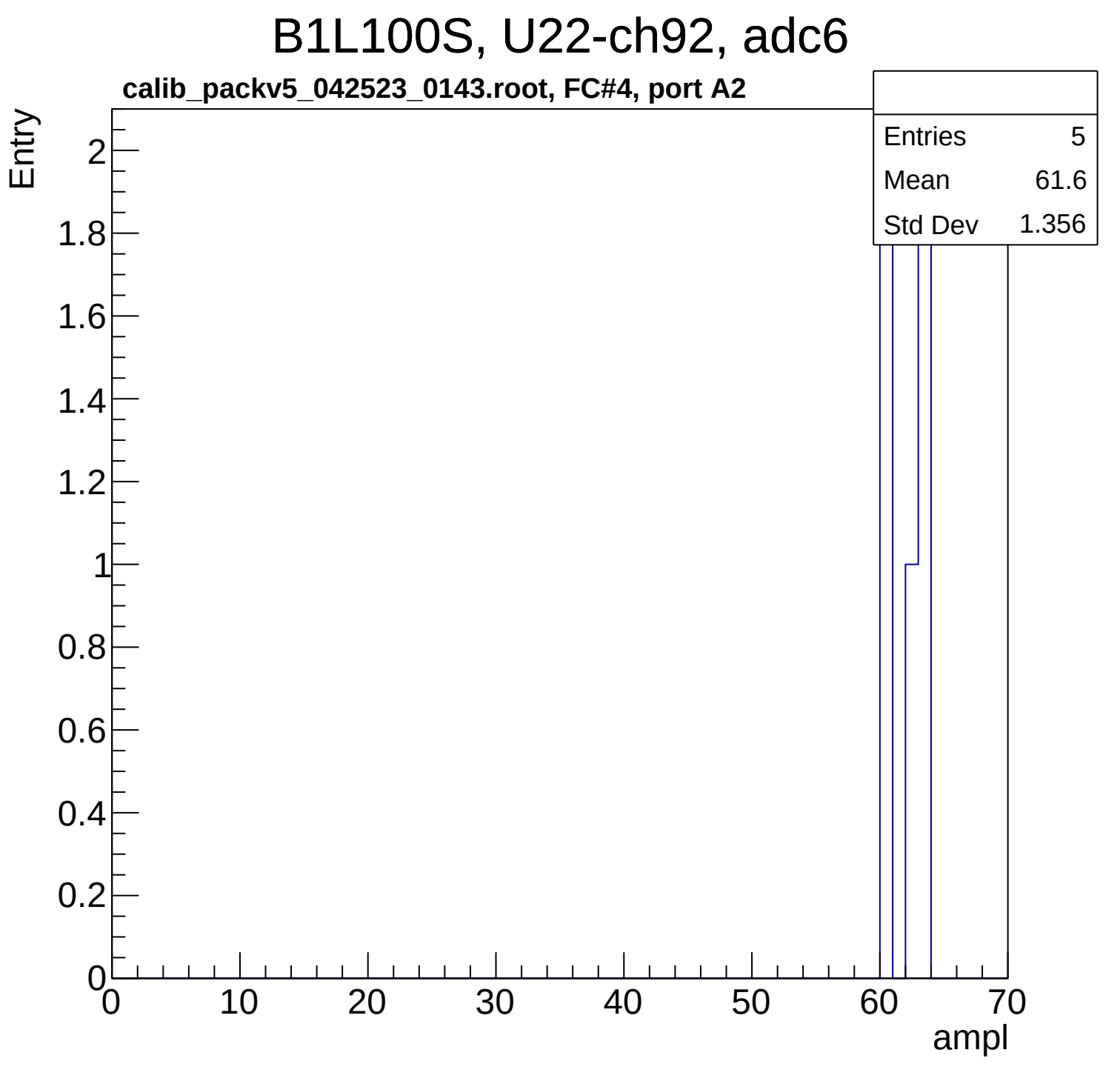
B1L100S, U22-ch92, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.6
Std Dev 1.356
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
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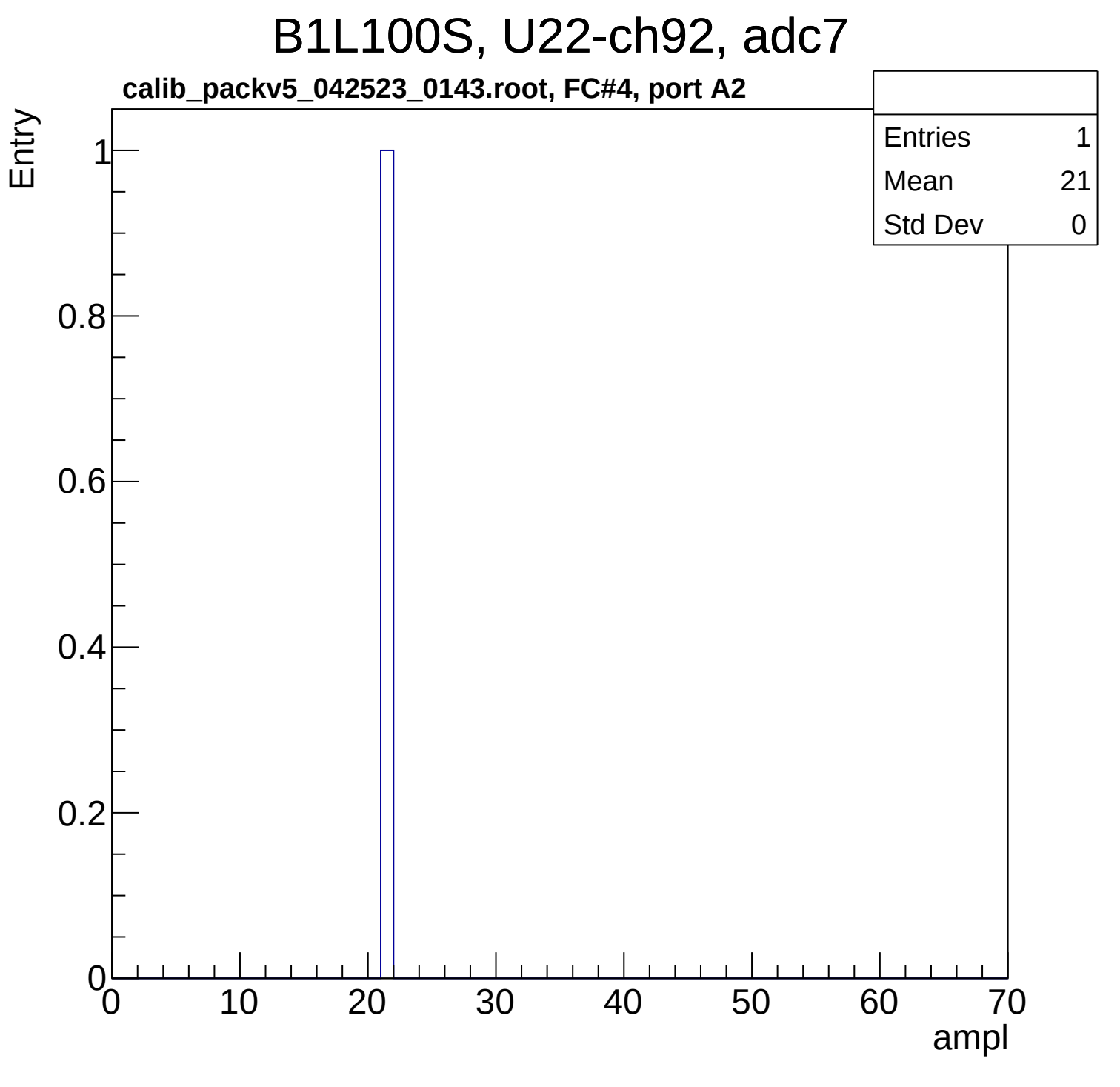
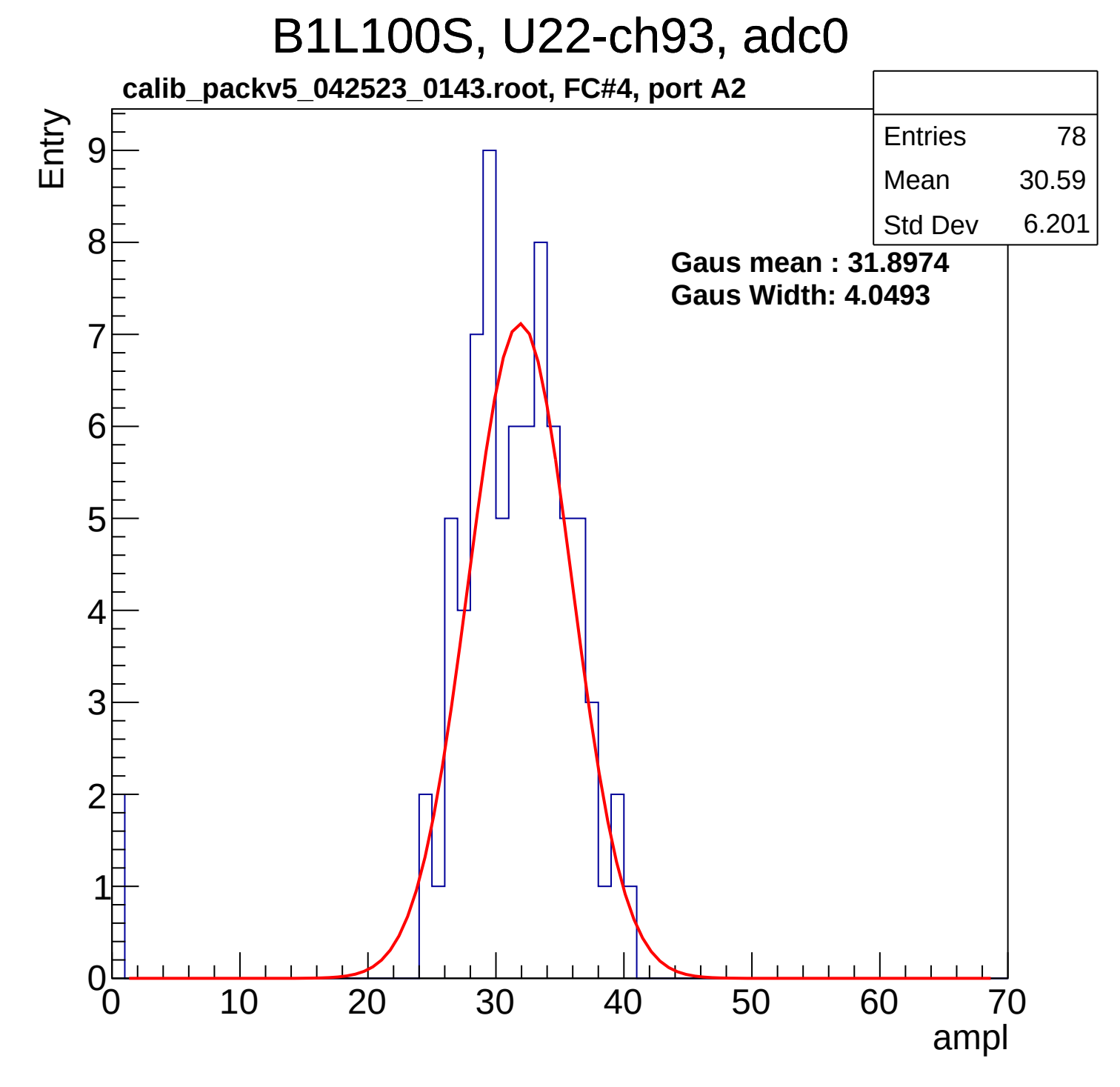
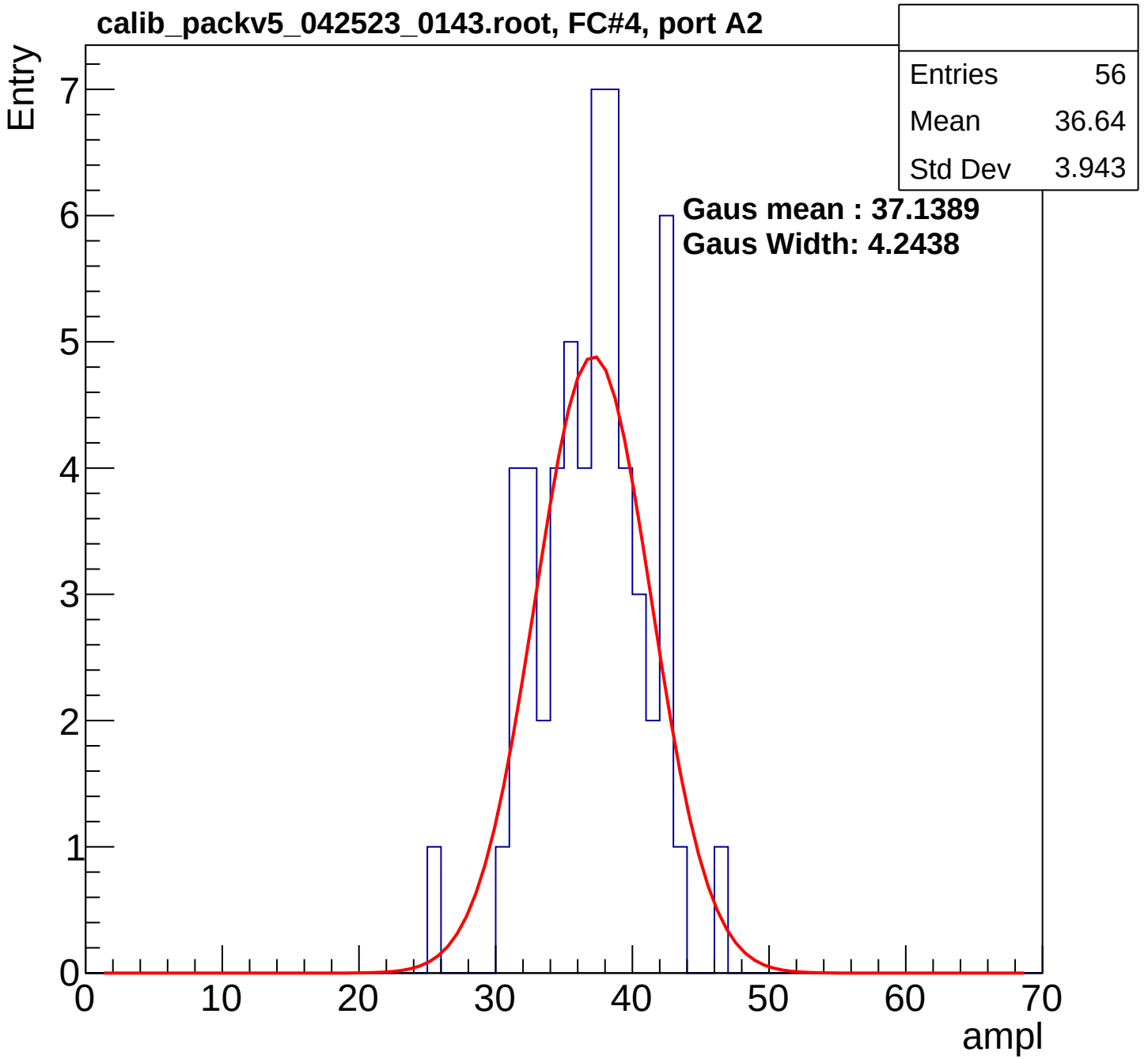
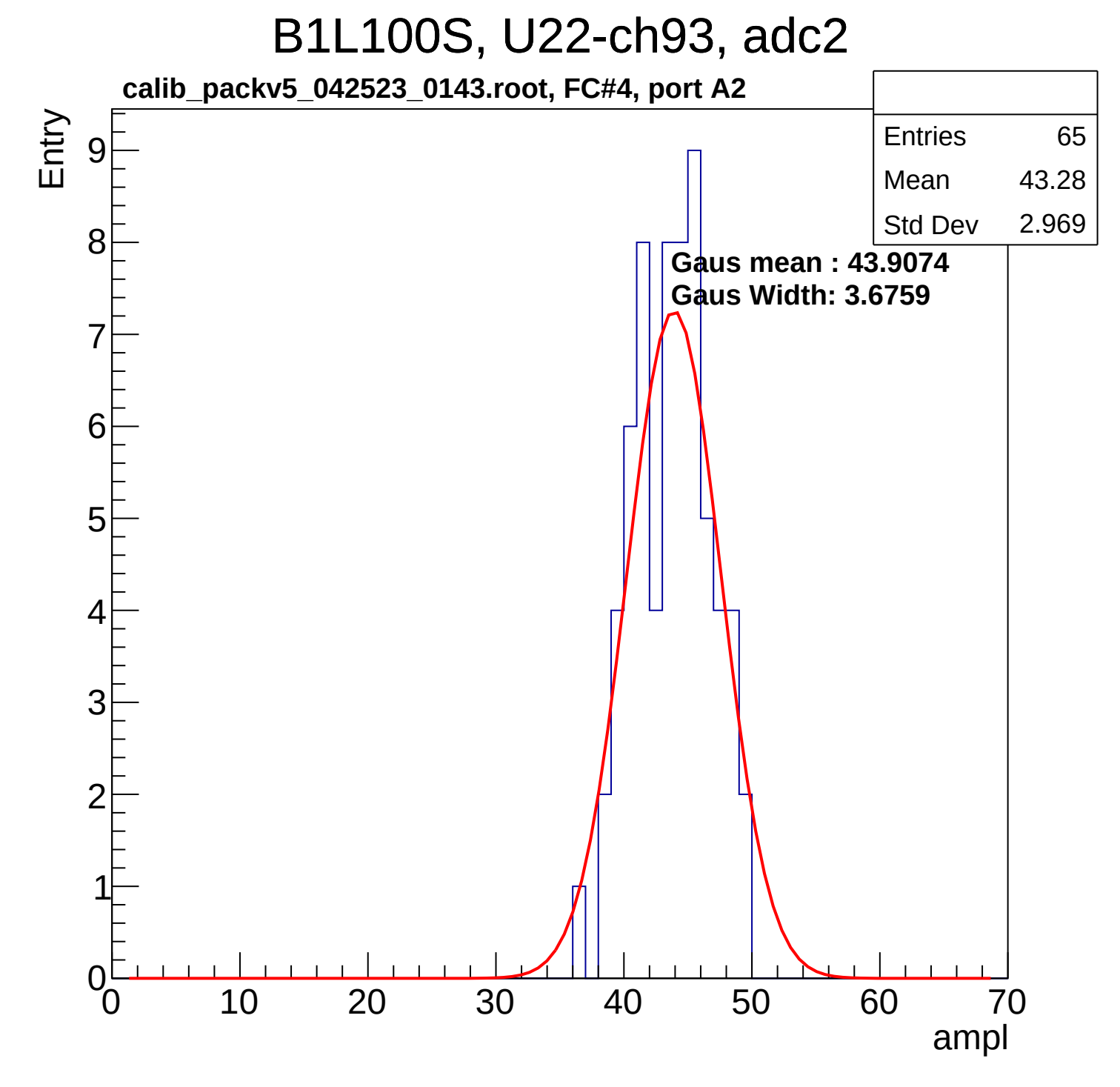

B1L100S, U22-ch92, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch93, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 78
Mean 30.59
Std Dev 6.201
Gaus mean : 31.8974
Gaus Width: 4.0493
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch93, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 36.64
Std Dev 3.943
Gaus mean : 37.1389
Gaus Width: 4.2438
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch93, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 43.28
Std Dev 2.969
Gaus mean : 43.9074
Gaus Width: 3.6759
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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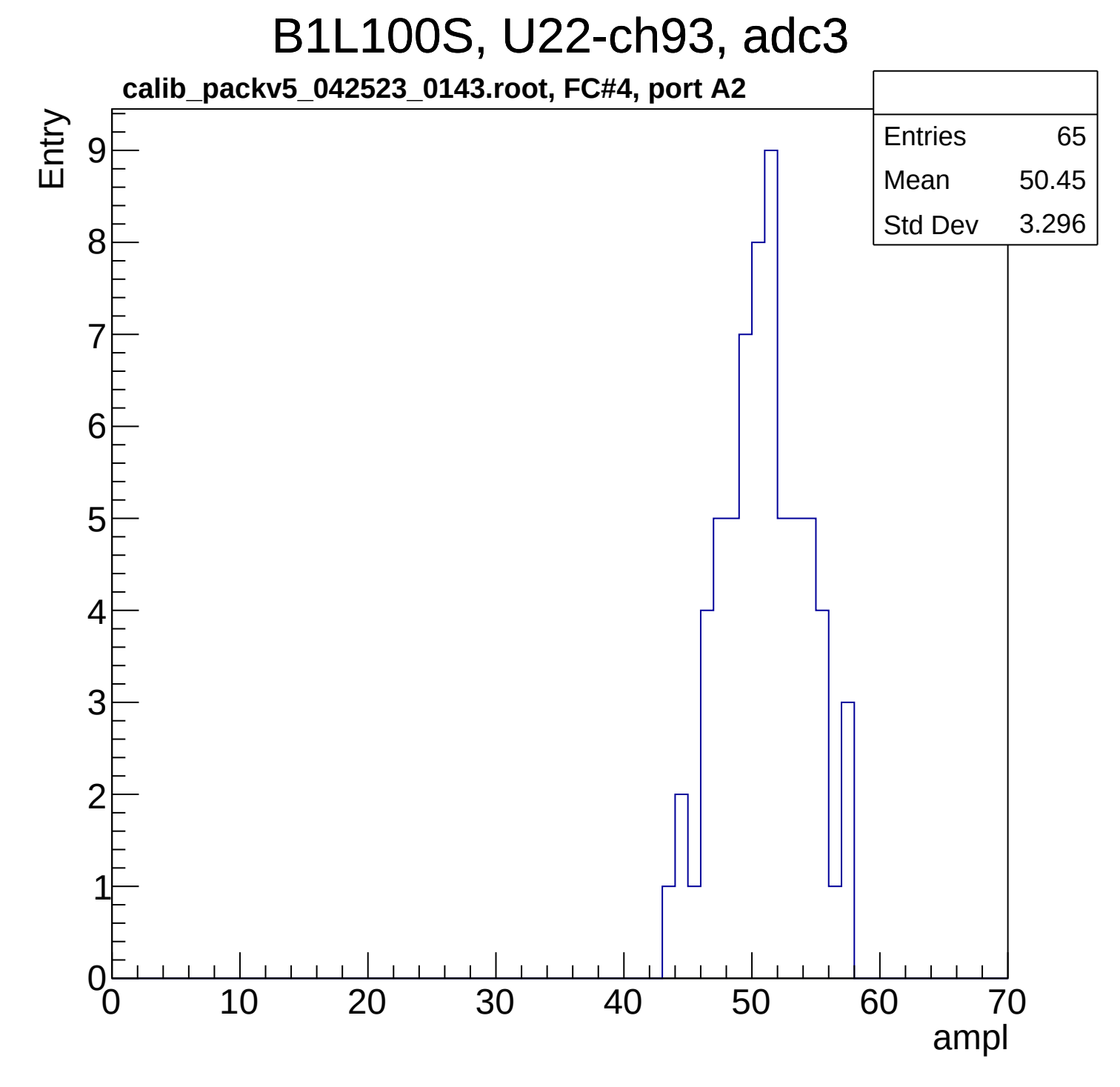
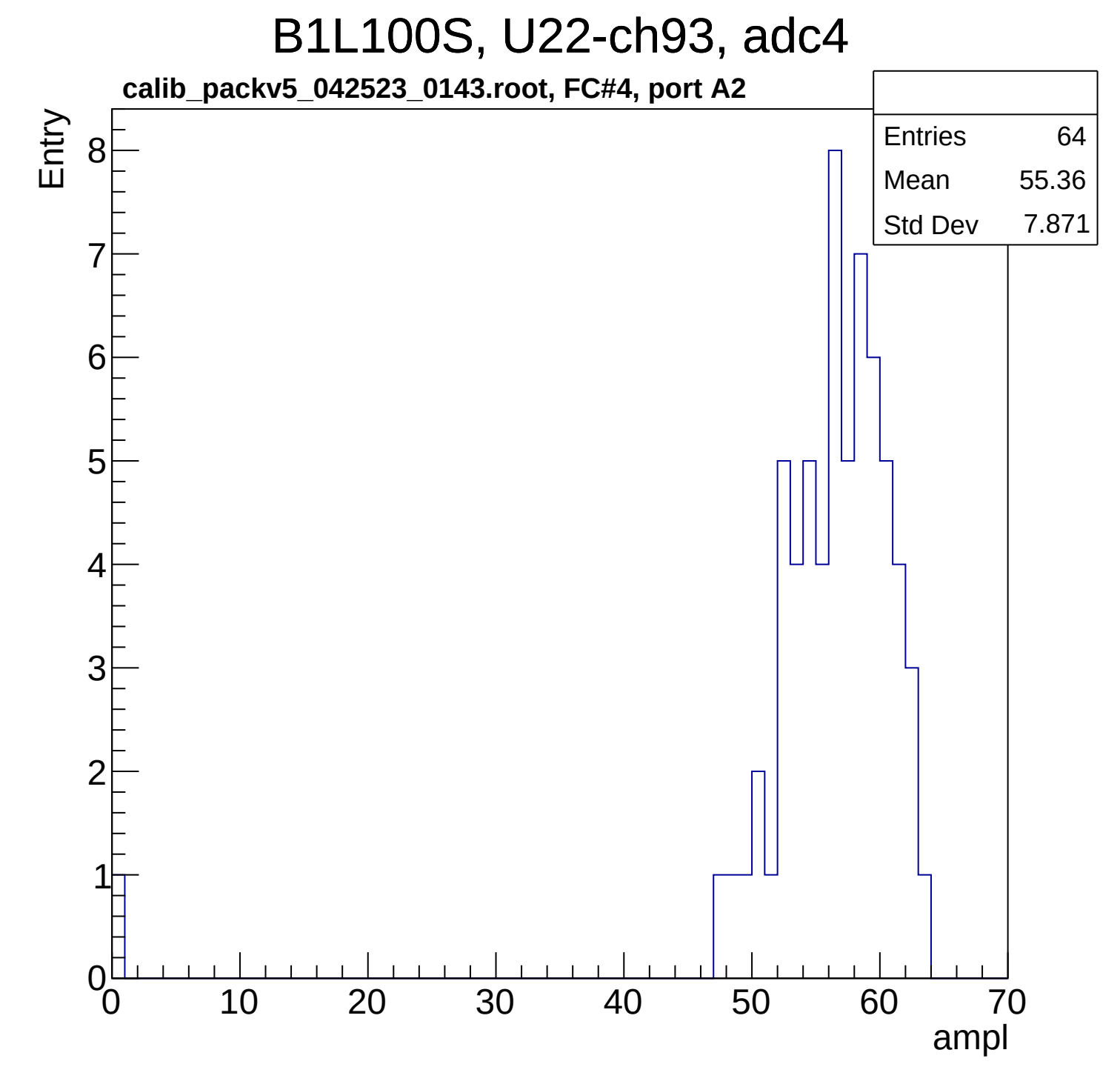

B1L100S, U22-ch93, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 65
Mean 50.45
Std Dev 3.296
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch93, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 55.36
Std Dev 7.871
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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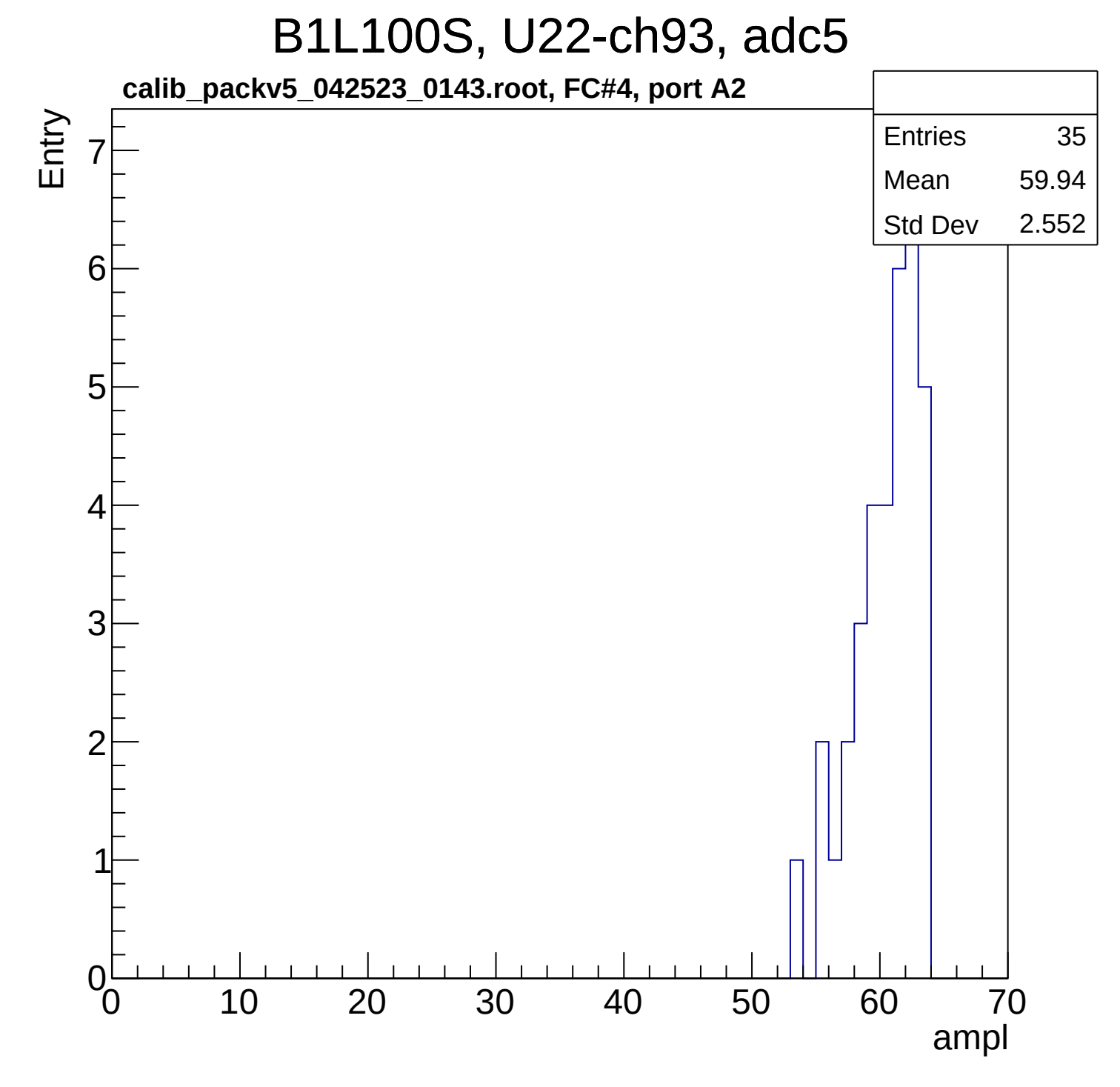
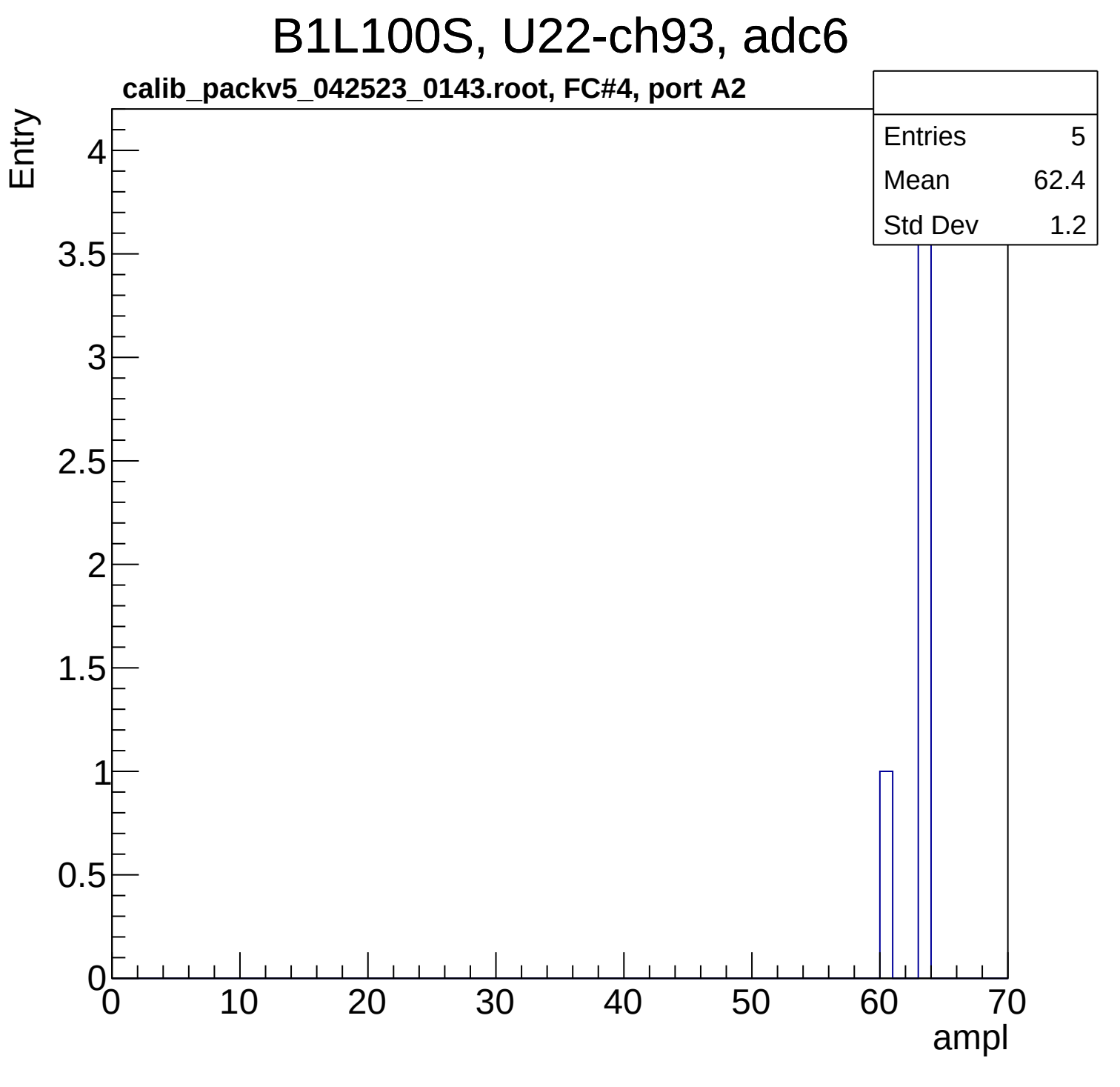
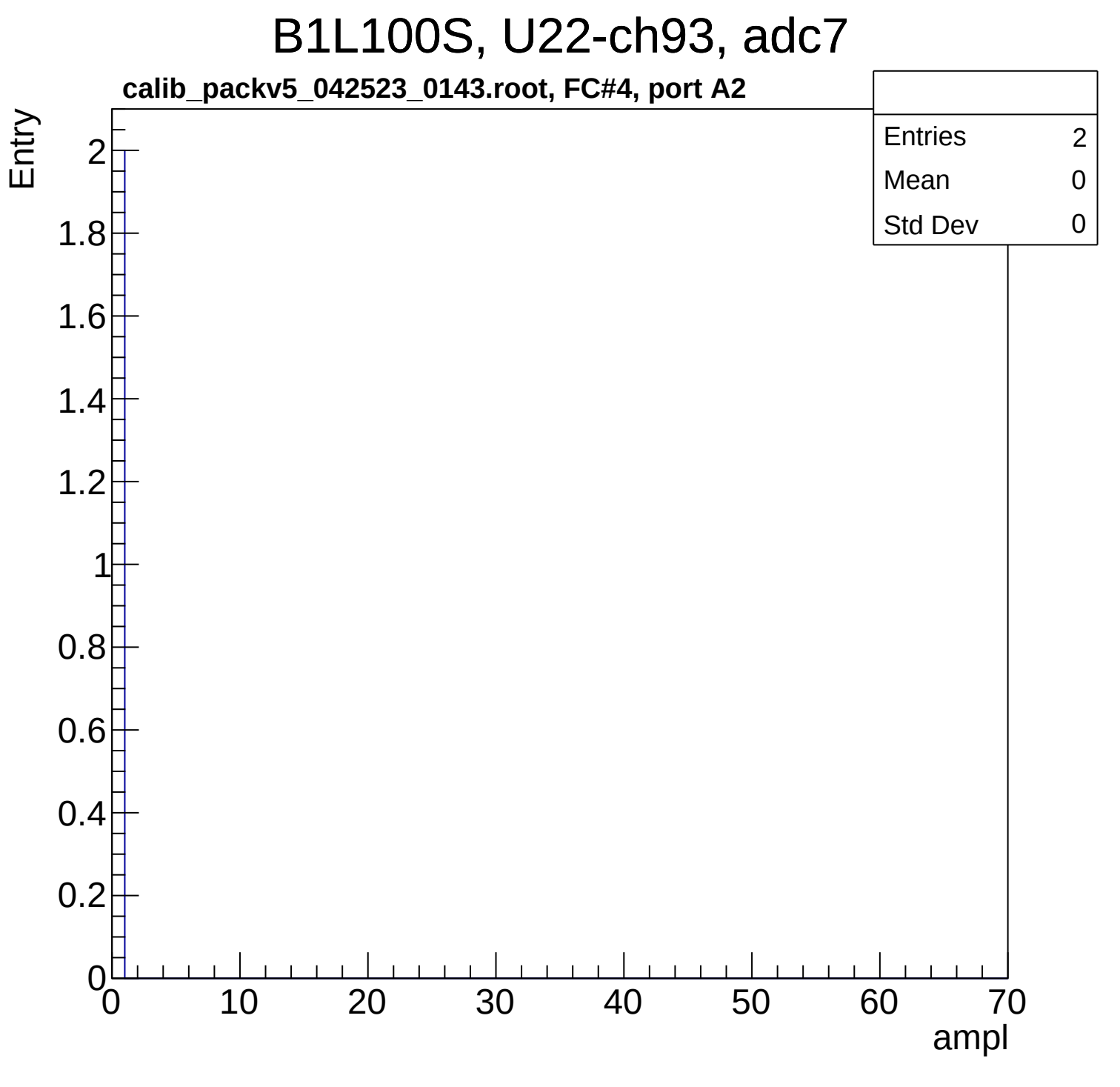
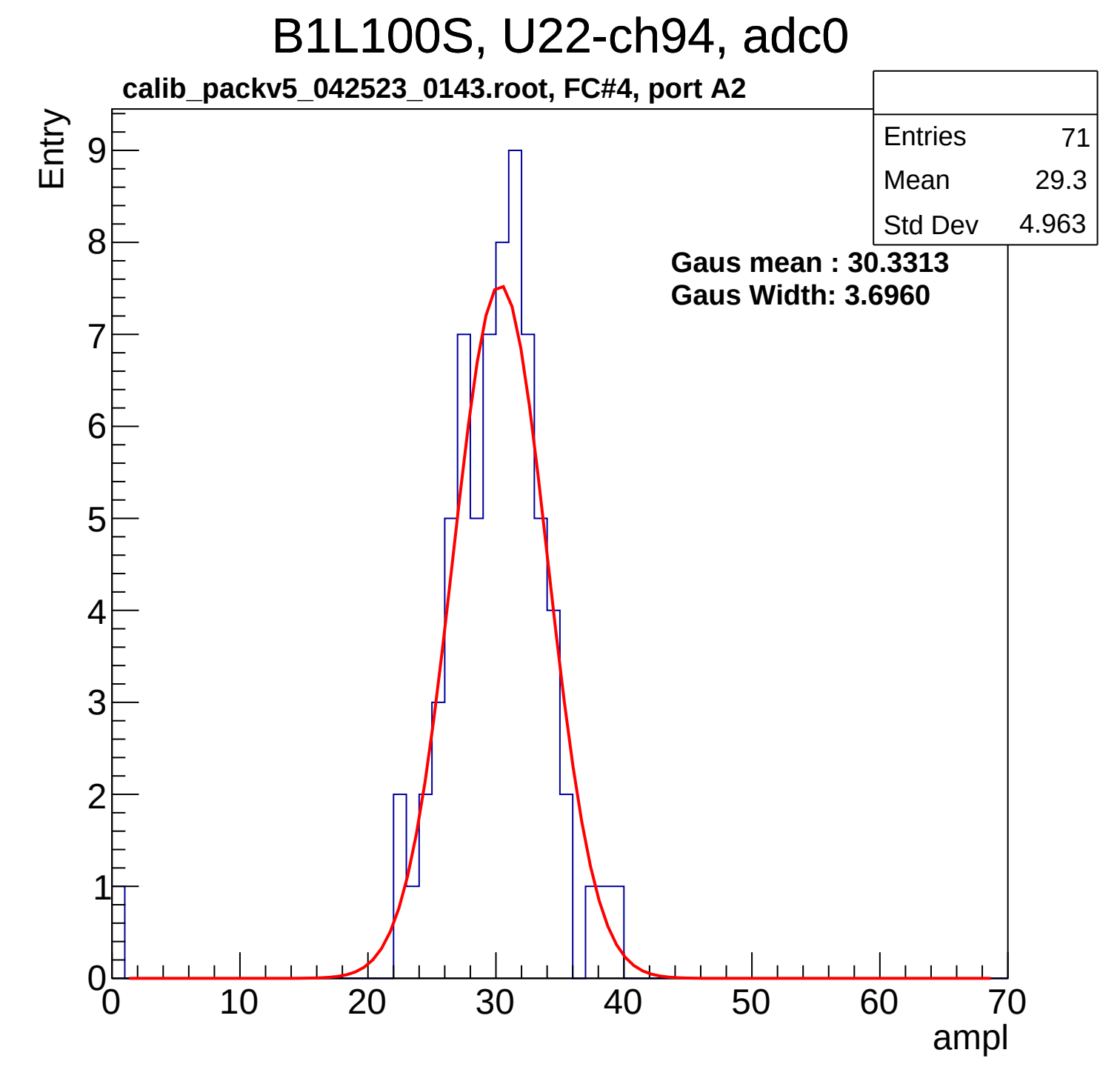
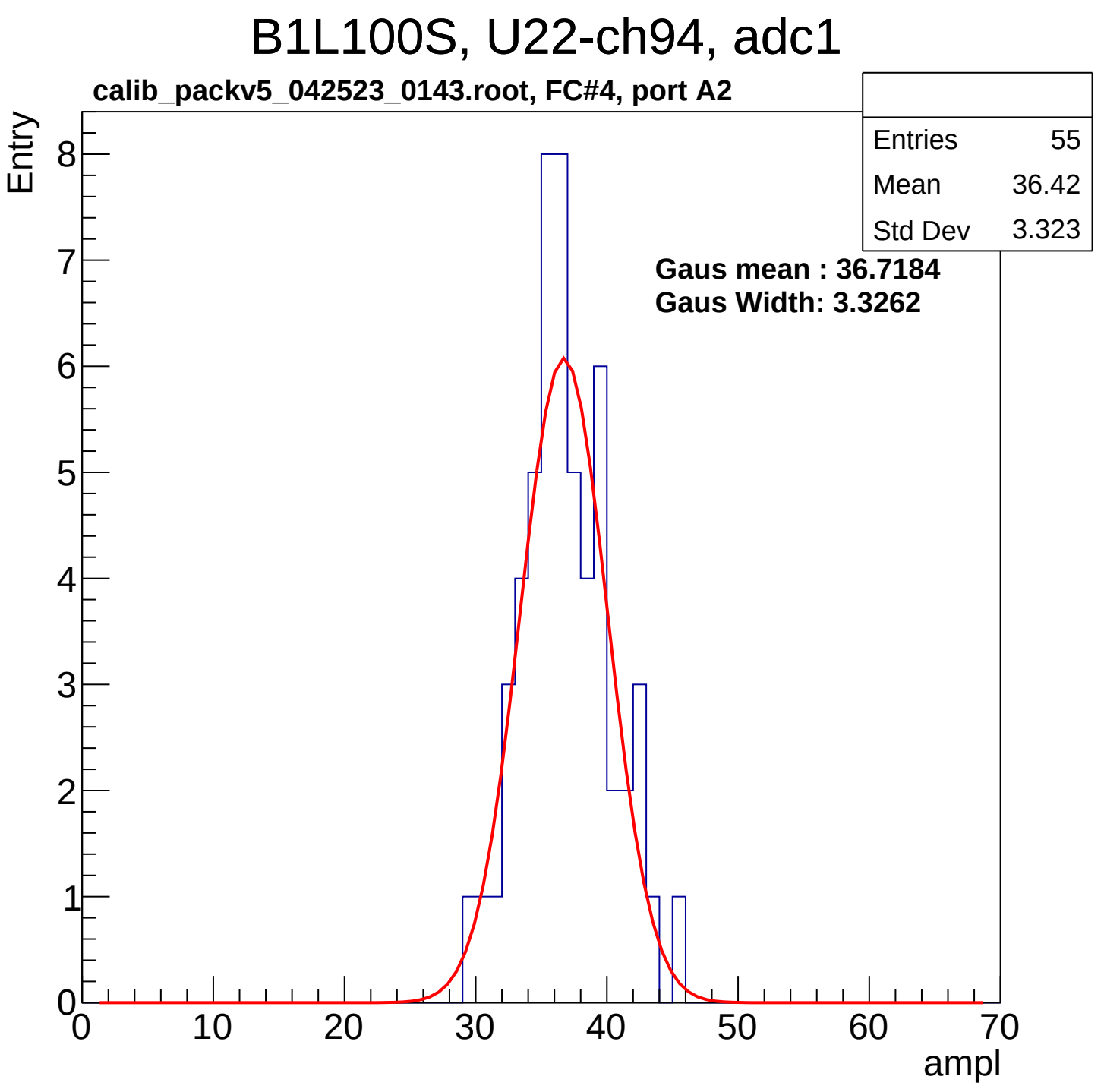

B1L100S, U22-ch93, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 59.94
Std Dev 2.552
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch93, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.4
Std Dev 1.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch93, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch94, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 29.3
Std Dev 4.963
Gaus mean : 30.3313
Gaus Width: 3.6960
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch94, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 36.42
Std Dev 3.323
Gaus mean : 36.7184
Gaus Width: 3.3262
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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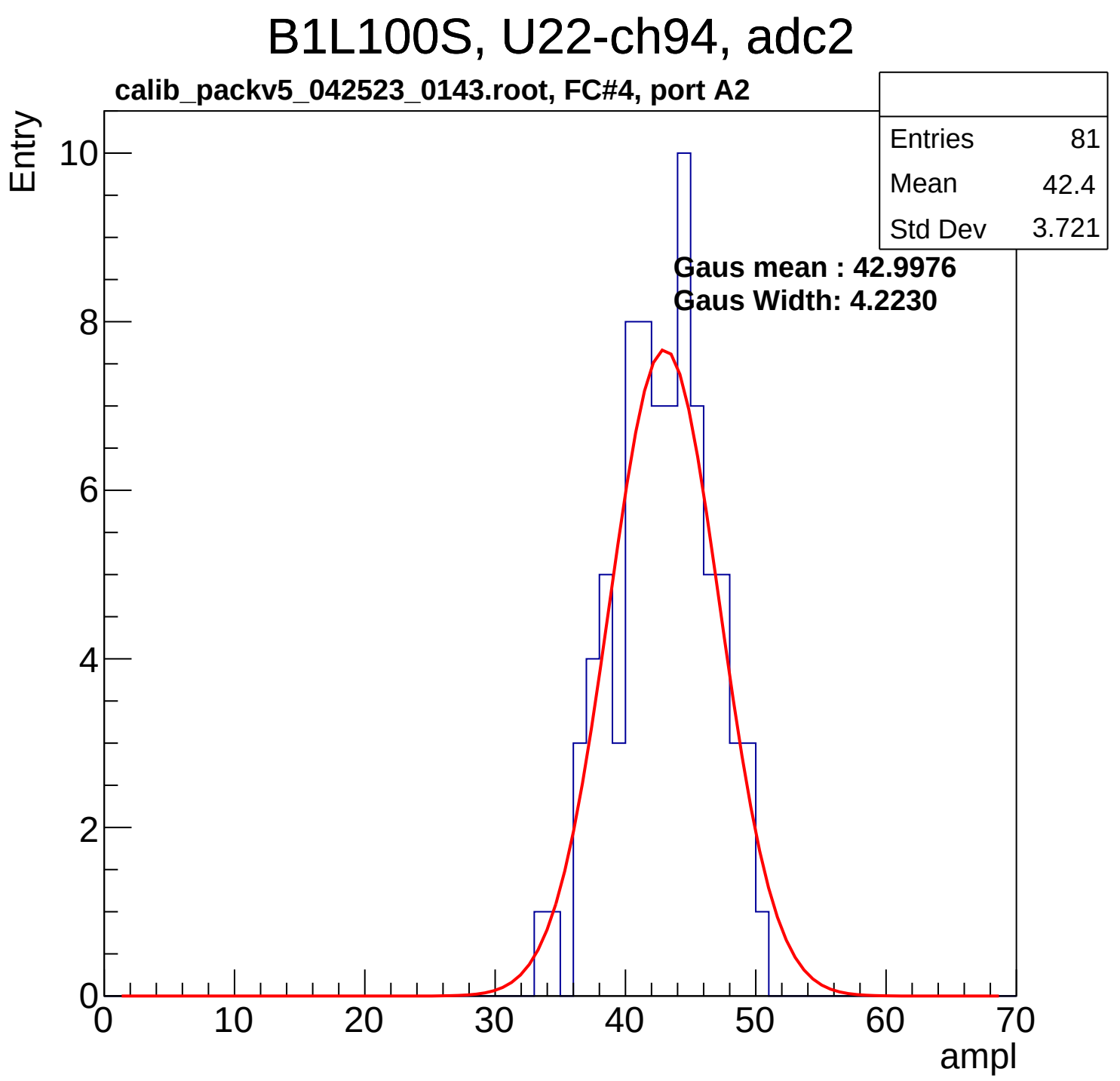
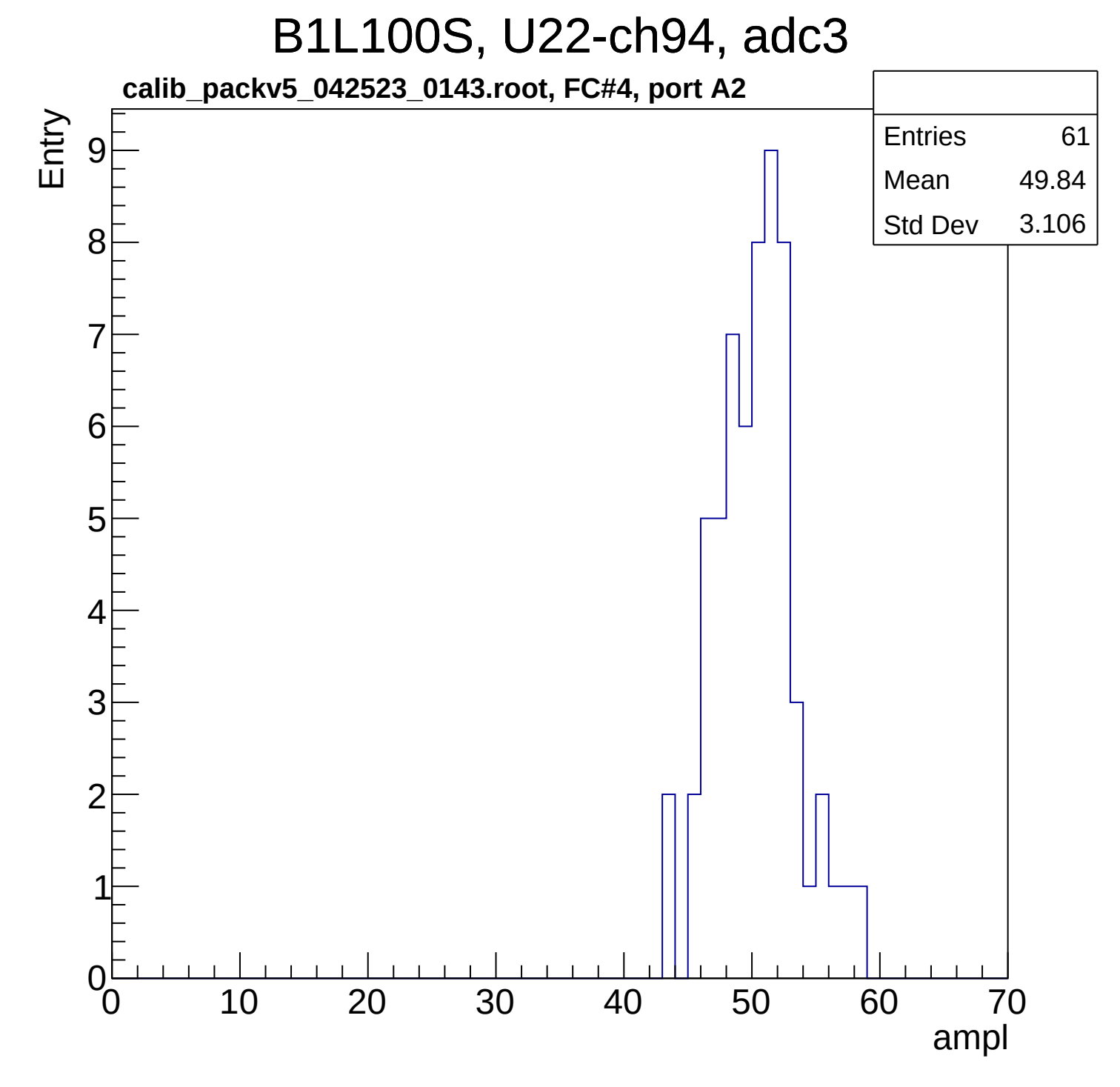
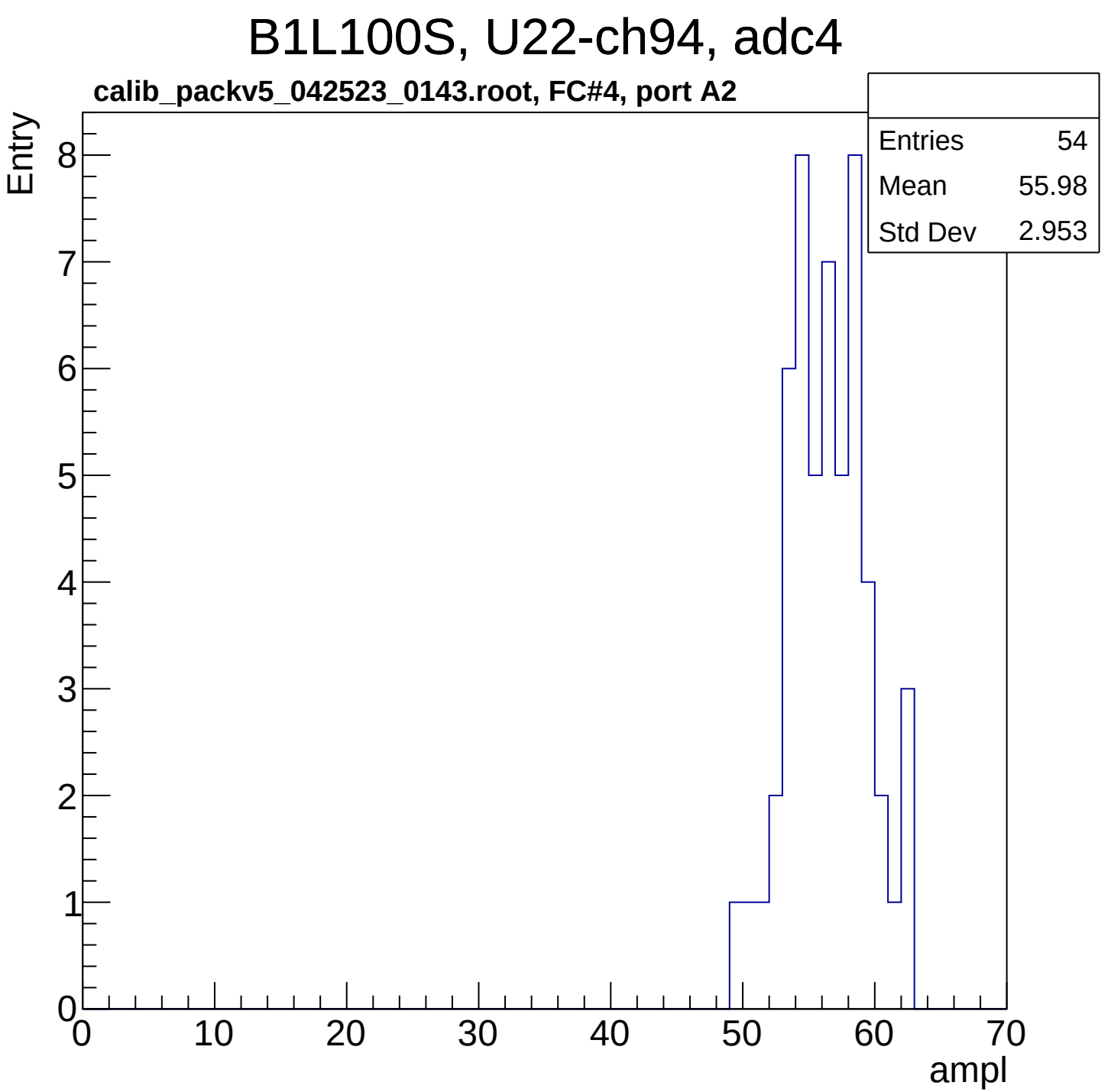
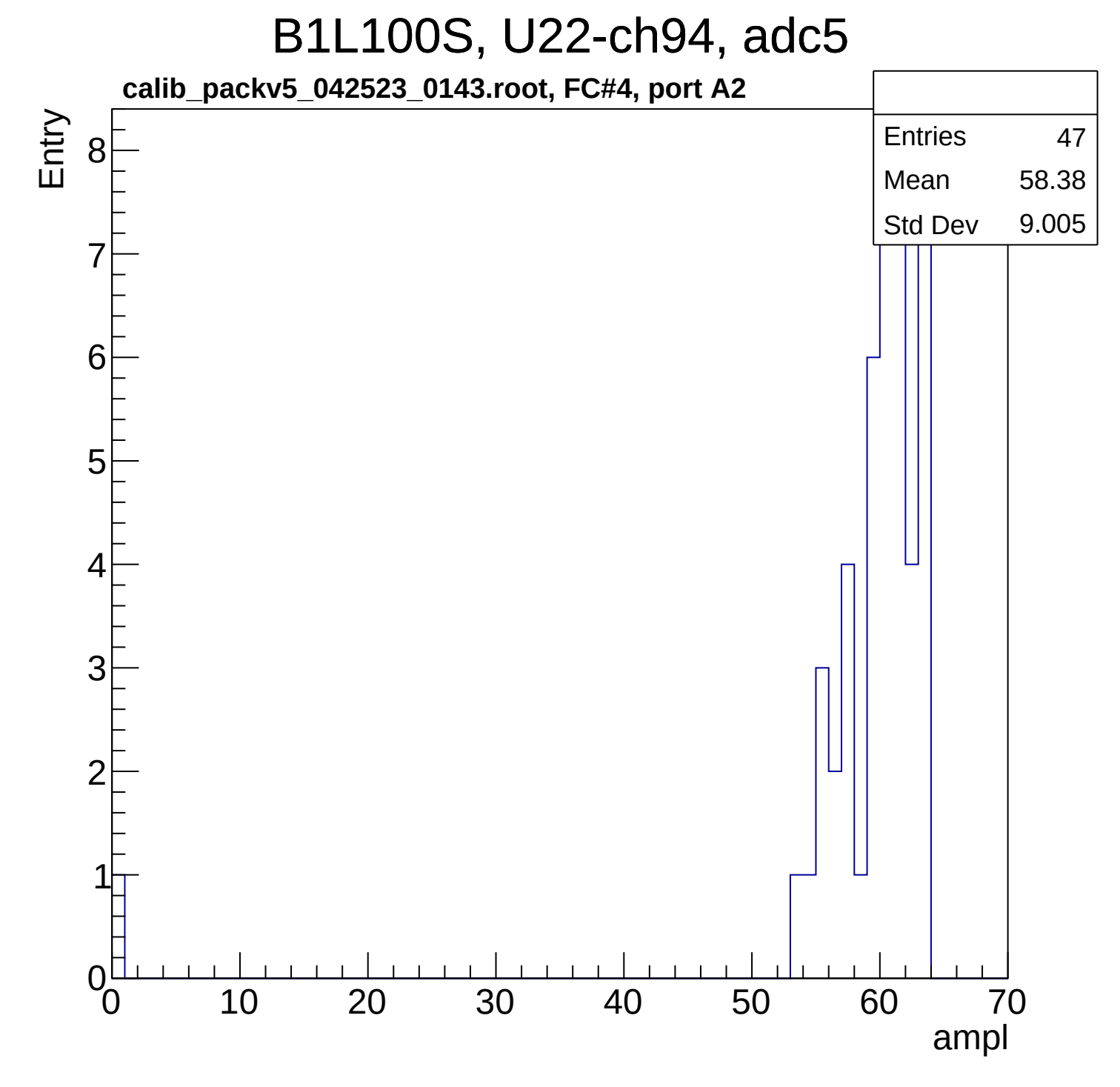

B1L100S, U22-ch94, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 81
Mean 42.4
Std Dev 3.721
Gaus mean : 42.9976
Gaus Width: 4.2230
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch94, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 49.84
Std Dev 3.106
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch94, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 55.98
Std Dev 2.953
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch94, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58.38
Std Dev 9.005
Entry
ampl
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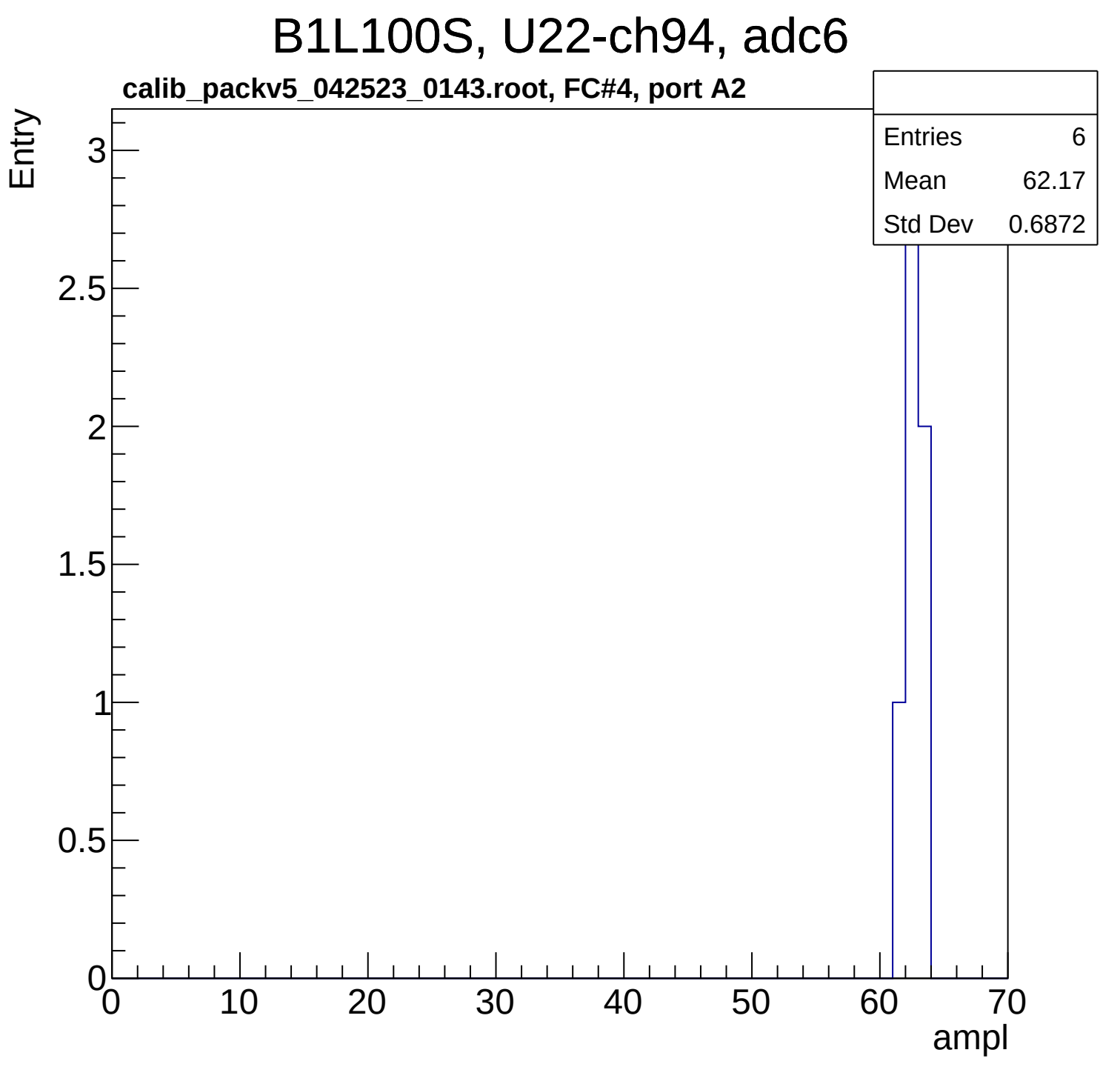
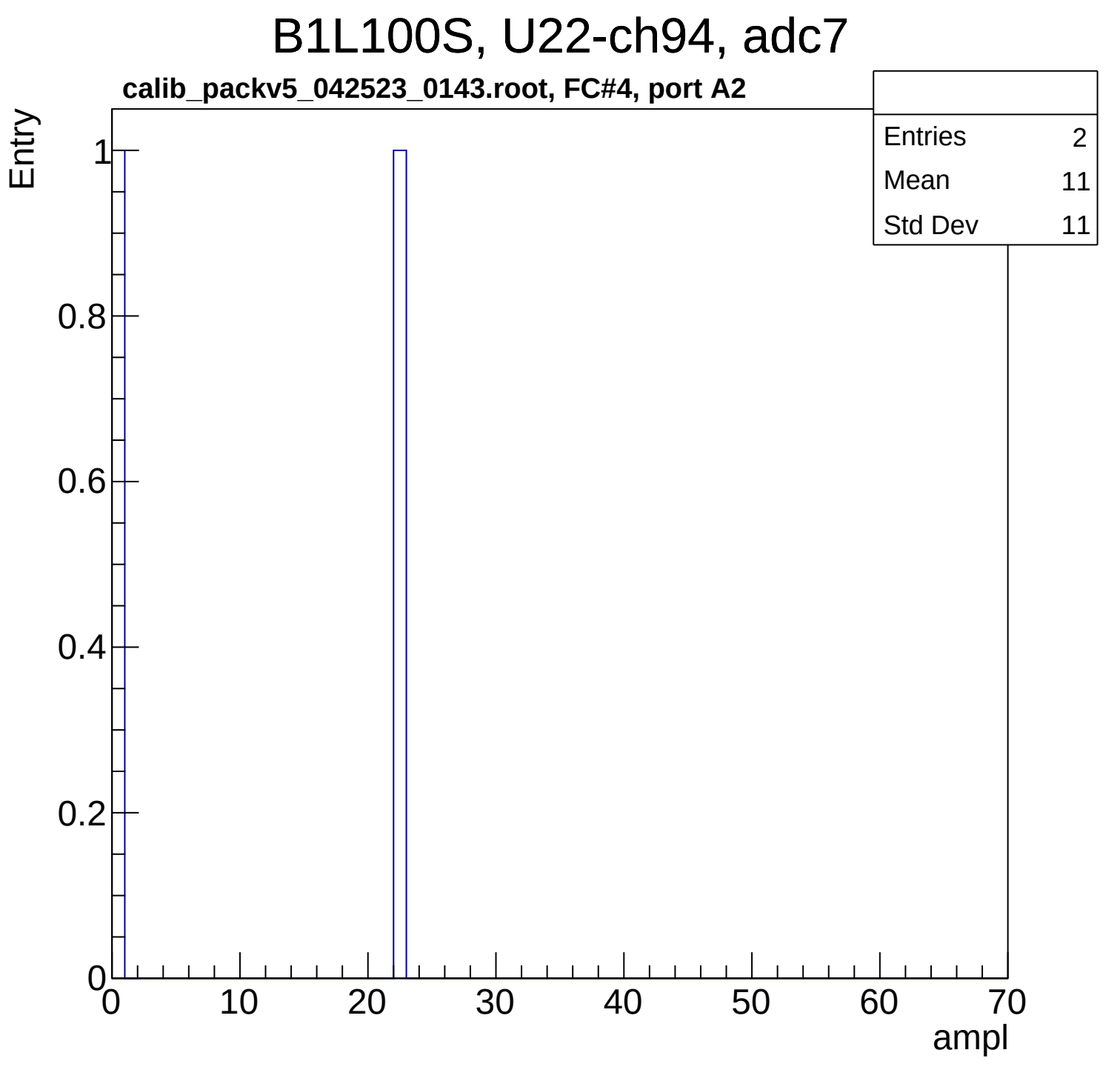
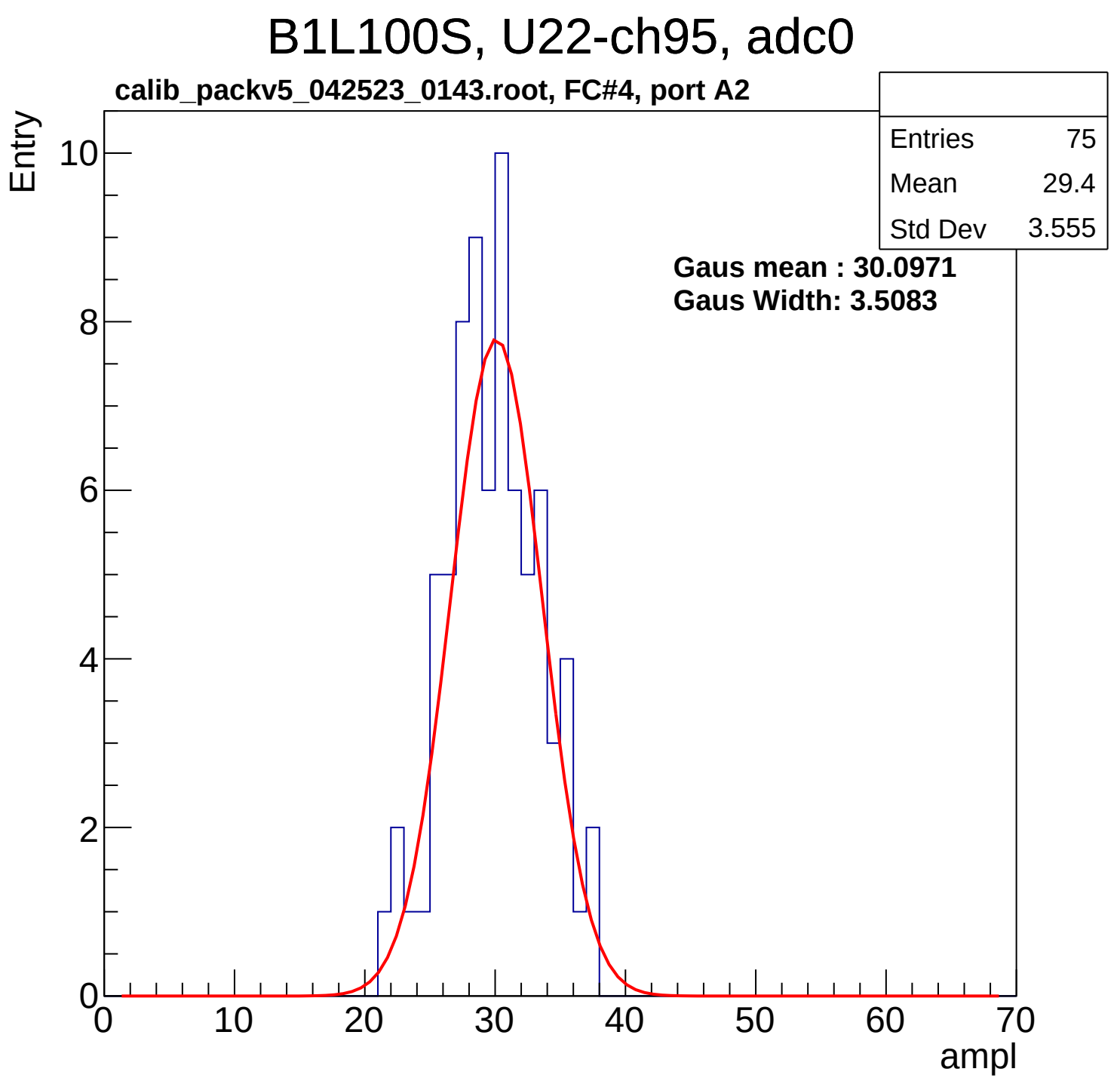
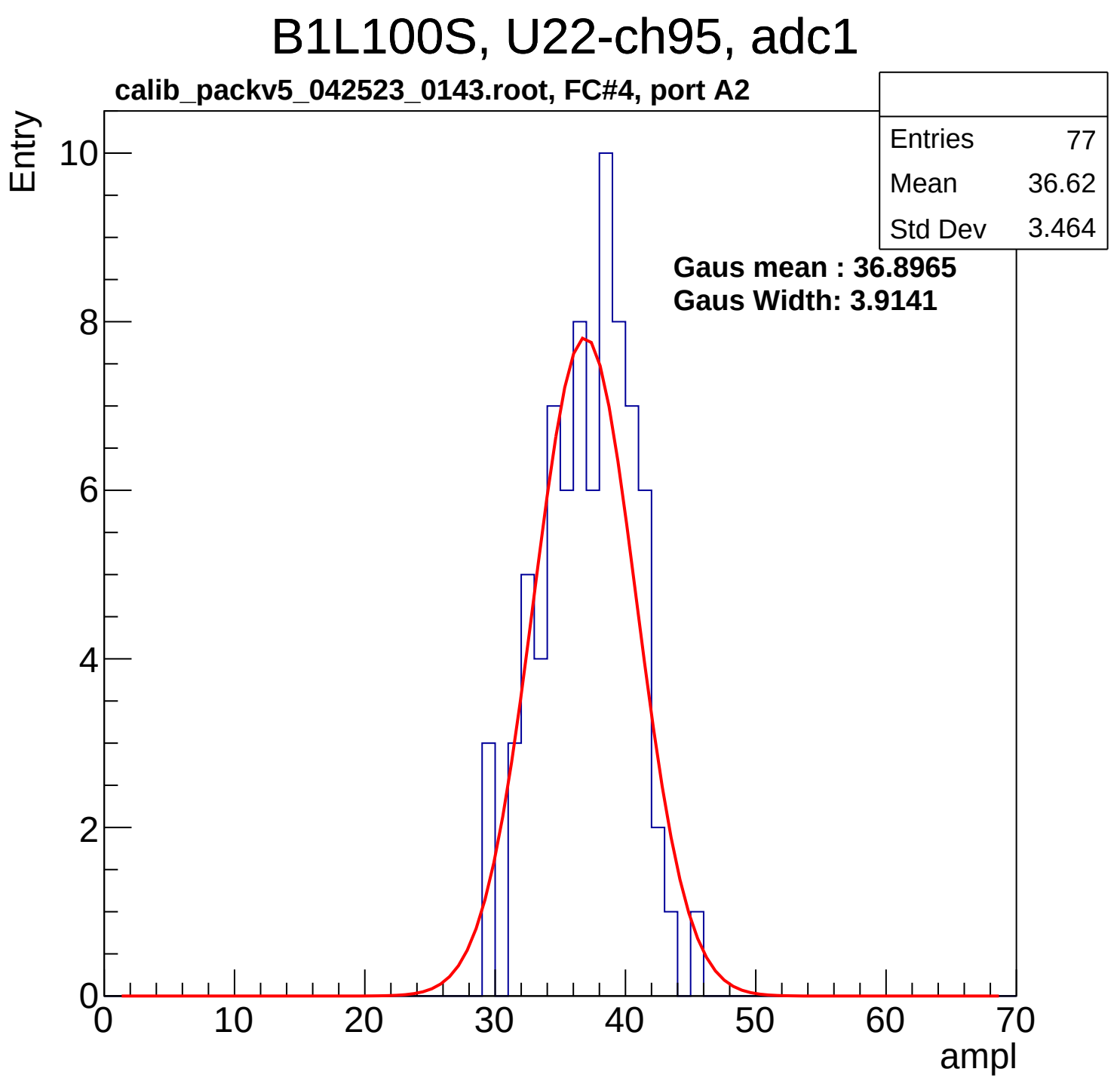
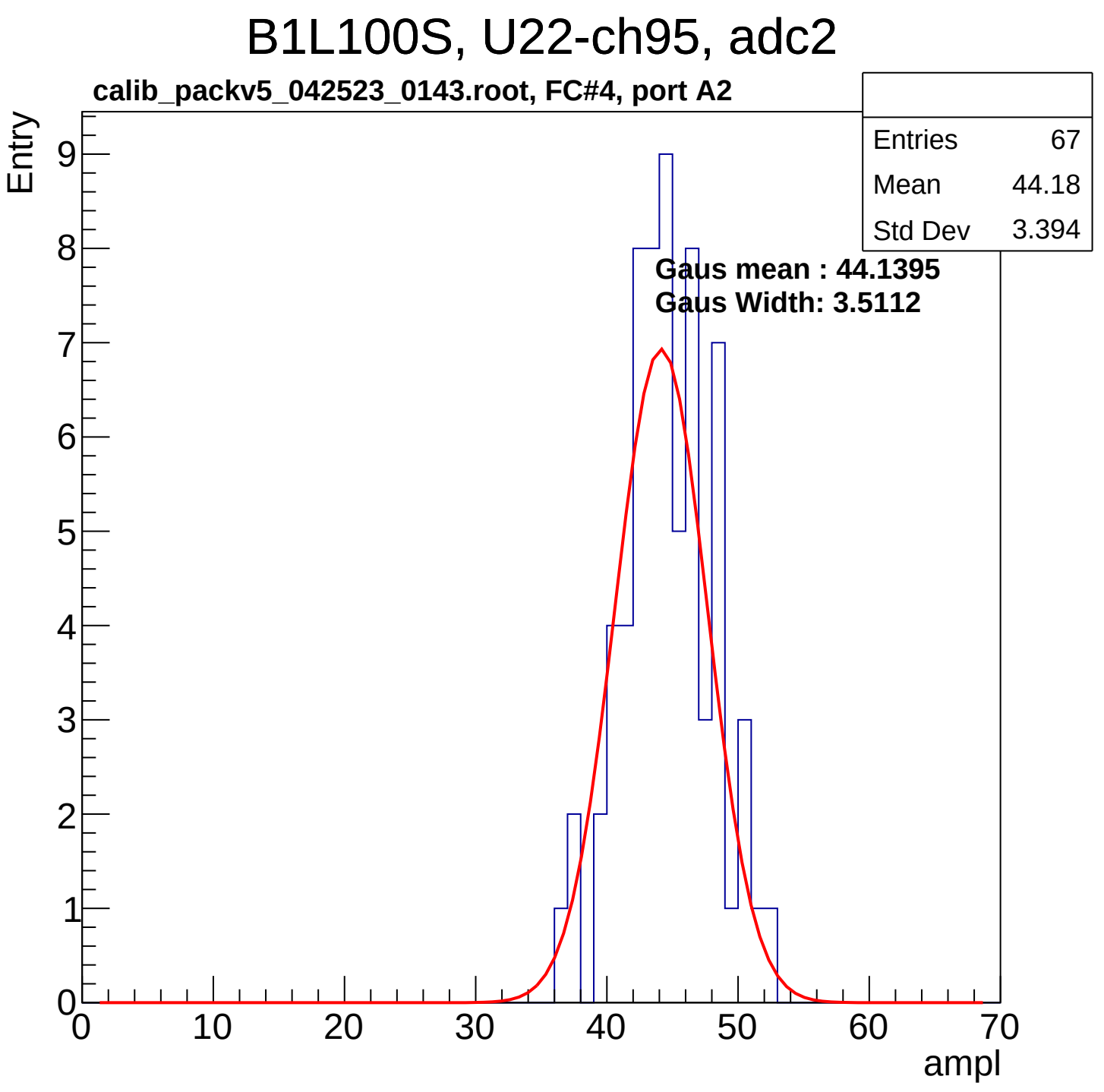
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

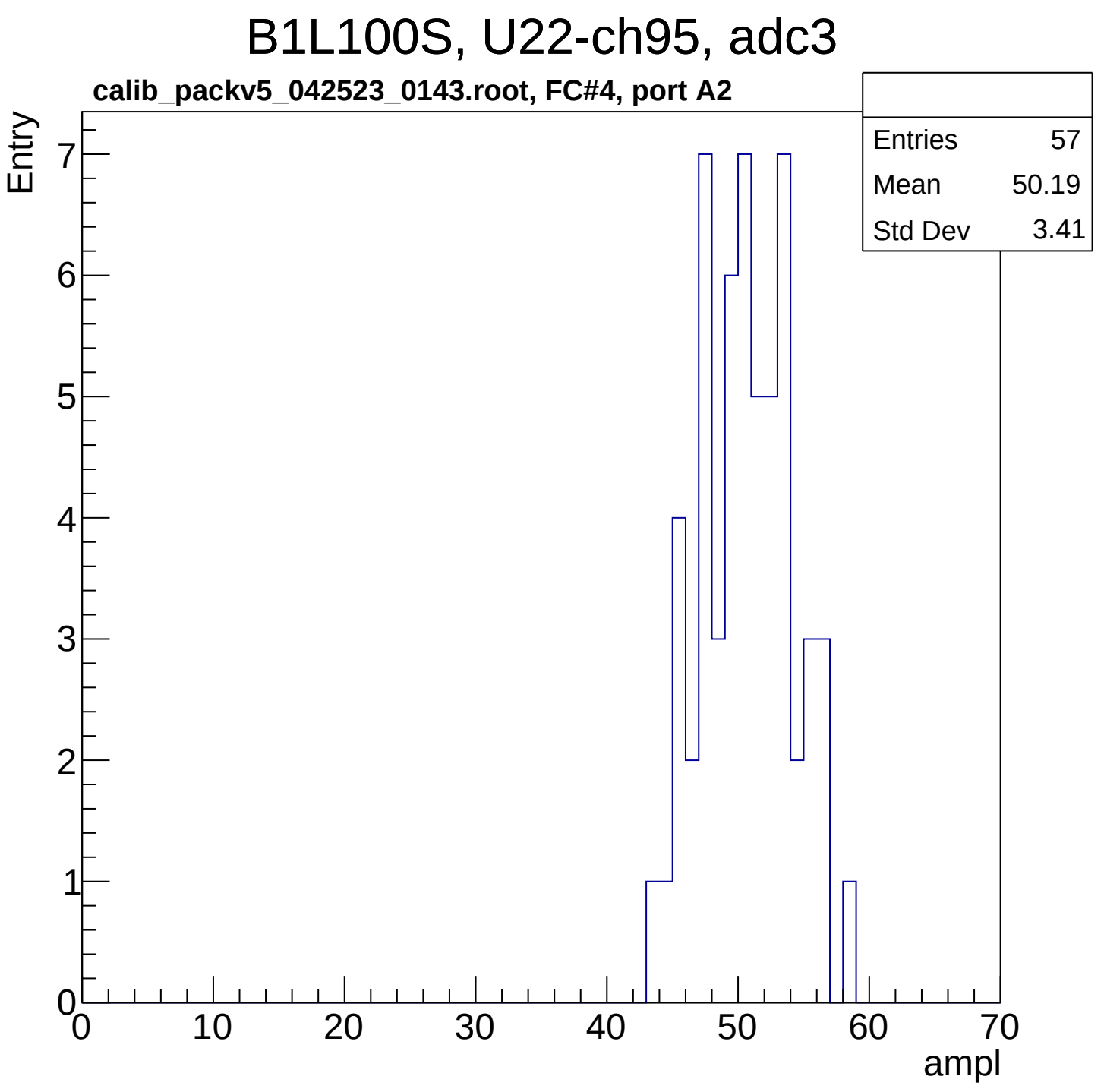

B1L100S, U22-ch94, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 6
Mean 62.17
Std Dev 0.6872
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch94, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 11
Std Dev 11
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch95, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.4
Std Dev 3.555
Gaus mean : 30.0971
Gaus Width: 3.5083
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch95, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 36.62
Std Dev 3.464
Gaus mean : 36.8965
Gaus Width: 3.9141
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch95, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 44.18
Std Dev 3.394
Gaus mean : 44.1395
Gaus Width: 3.5112
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch95, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50.19
Std Dev 3.41
Entry
ampl
0
1
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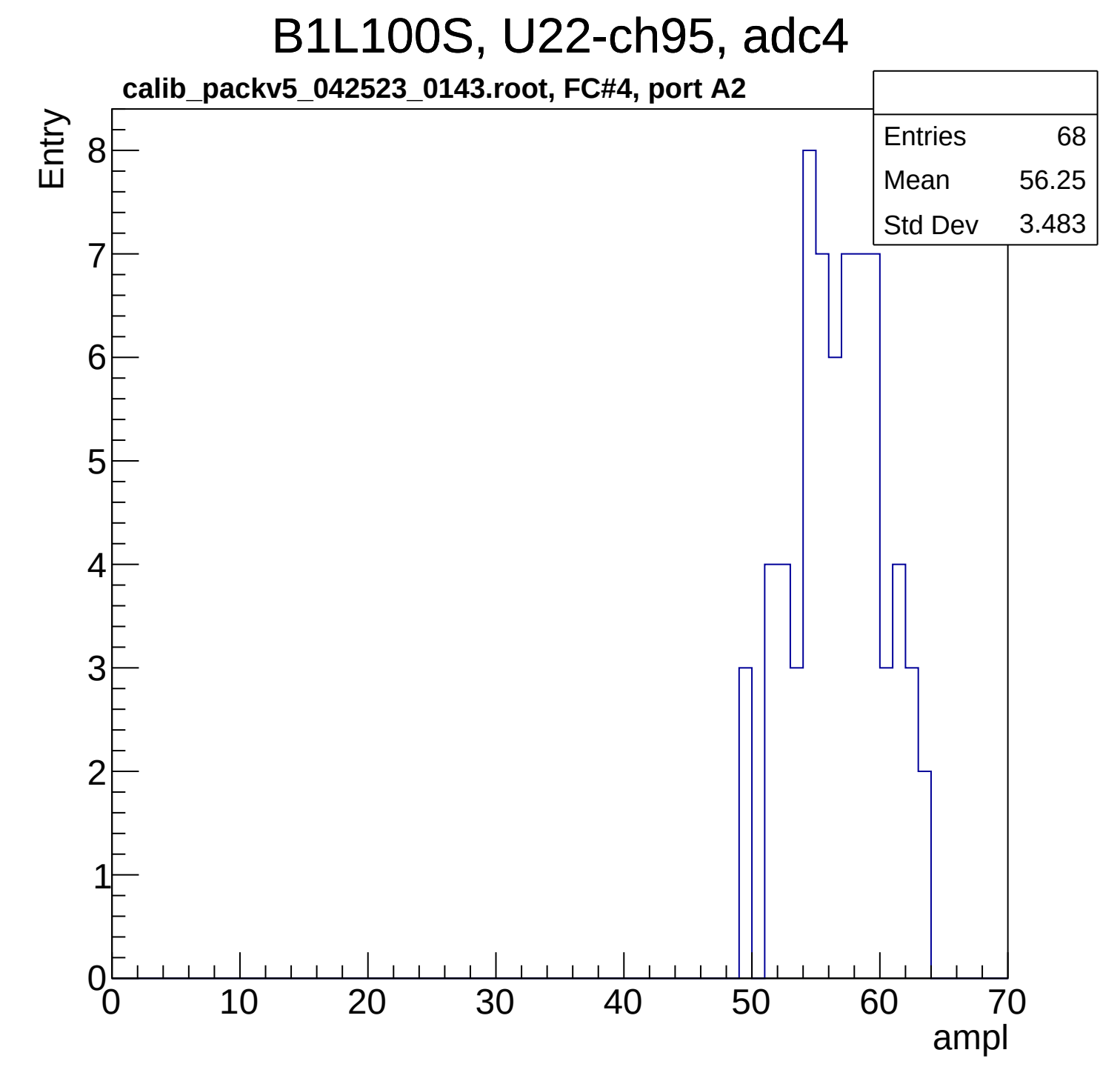
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch95, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 56.25
Std Dev 3.483
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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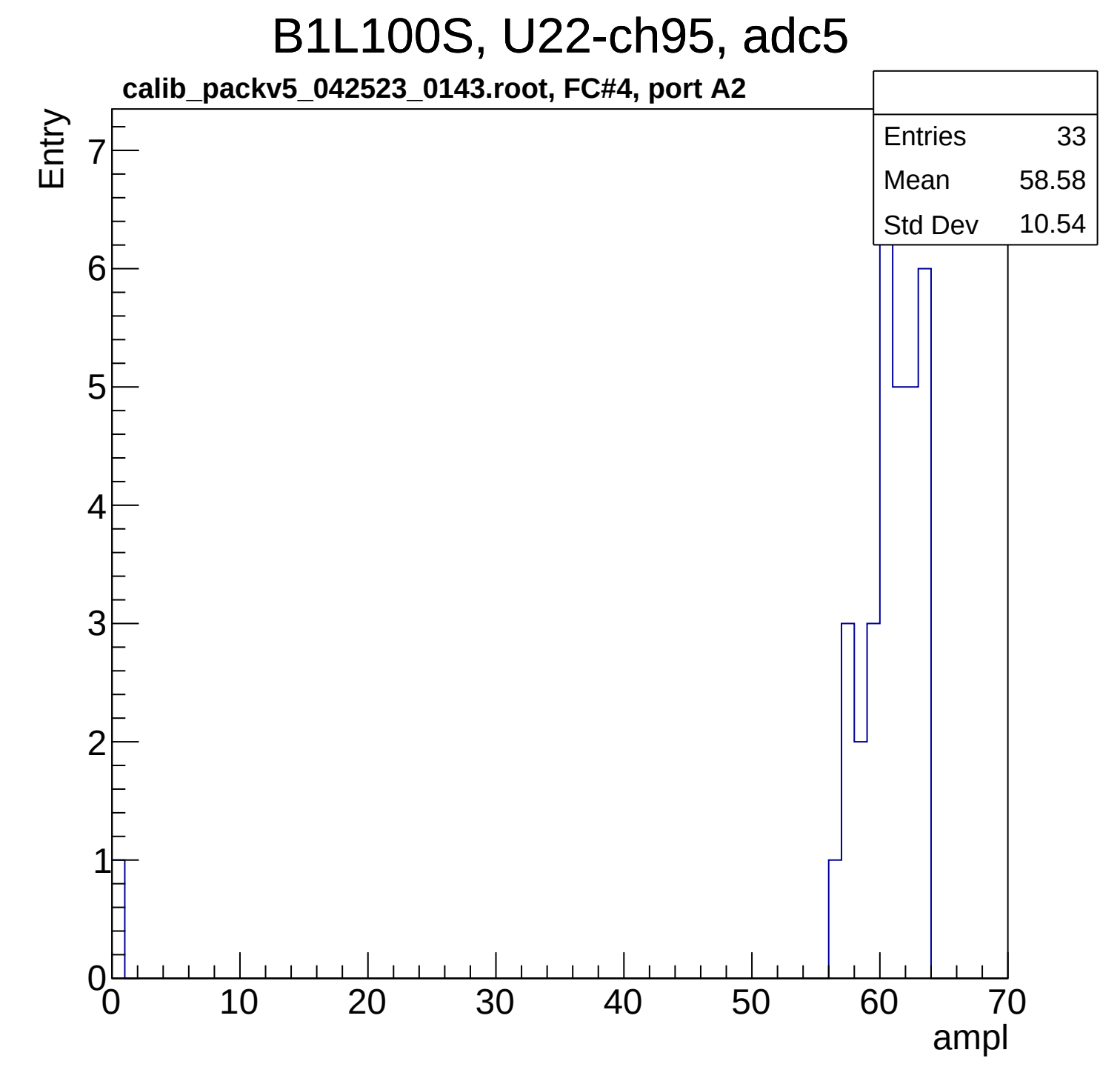
40
50
60
70


B1L100S, U22-ch95, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.58
Std Dev 10.54
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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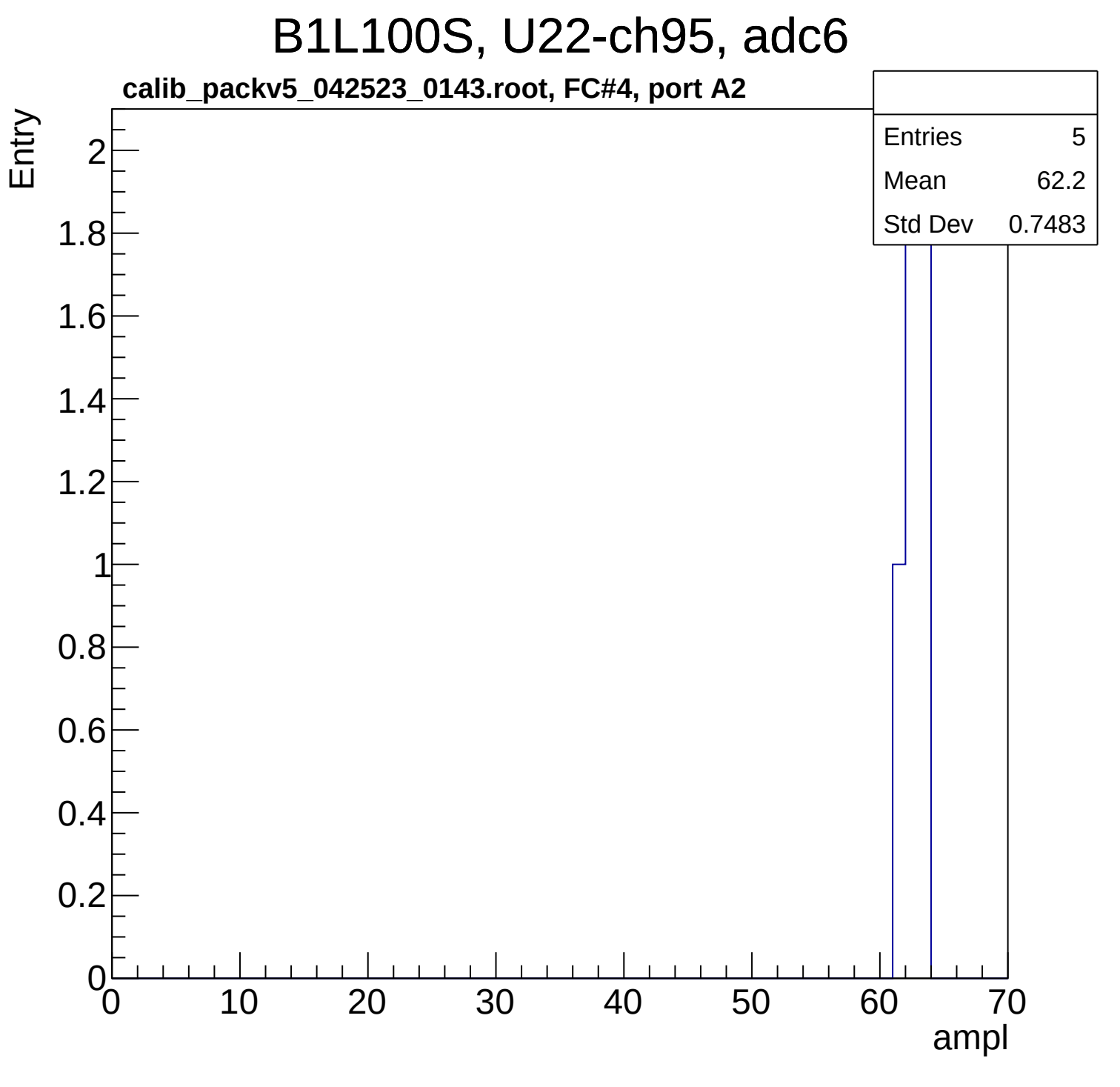
B1L100S, U22-ch95, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 62.2
Std Dev 0.7483
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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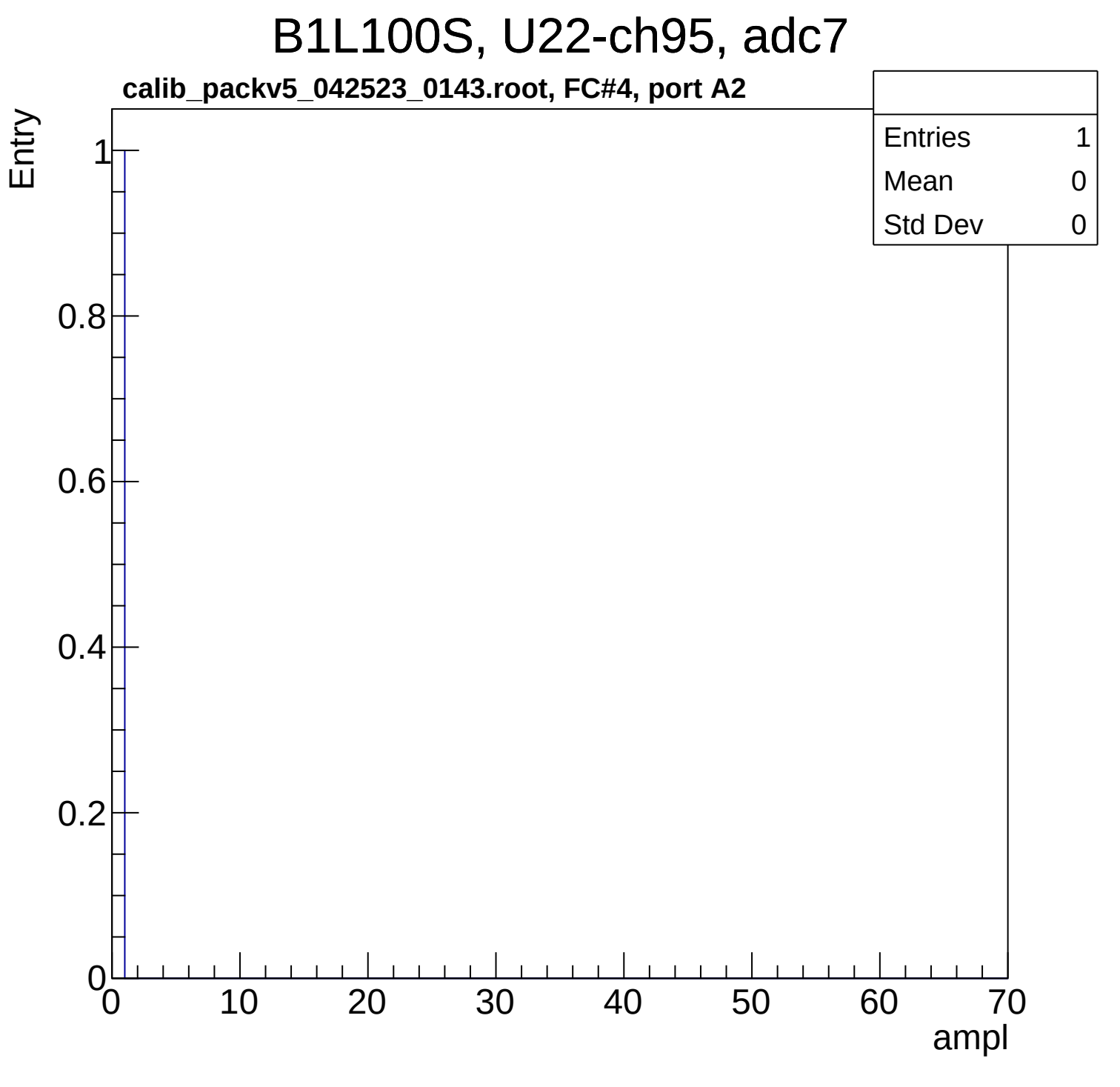
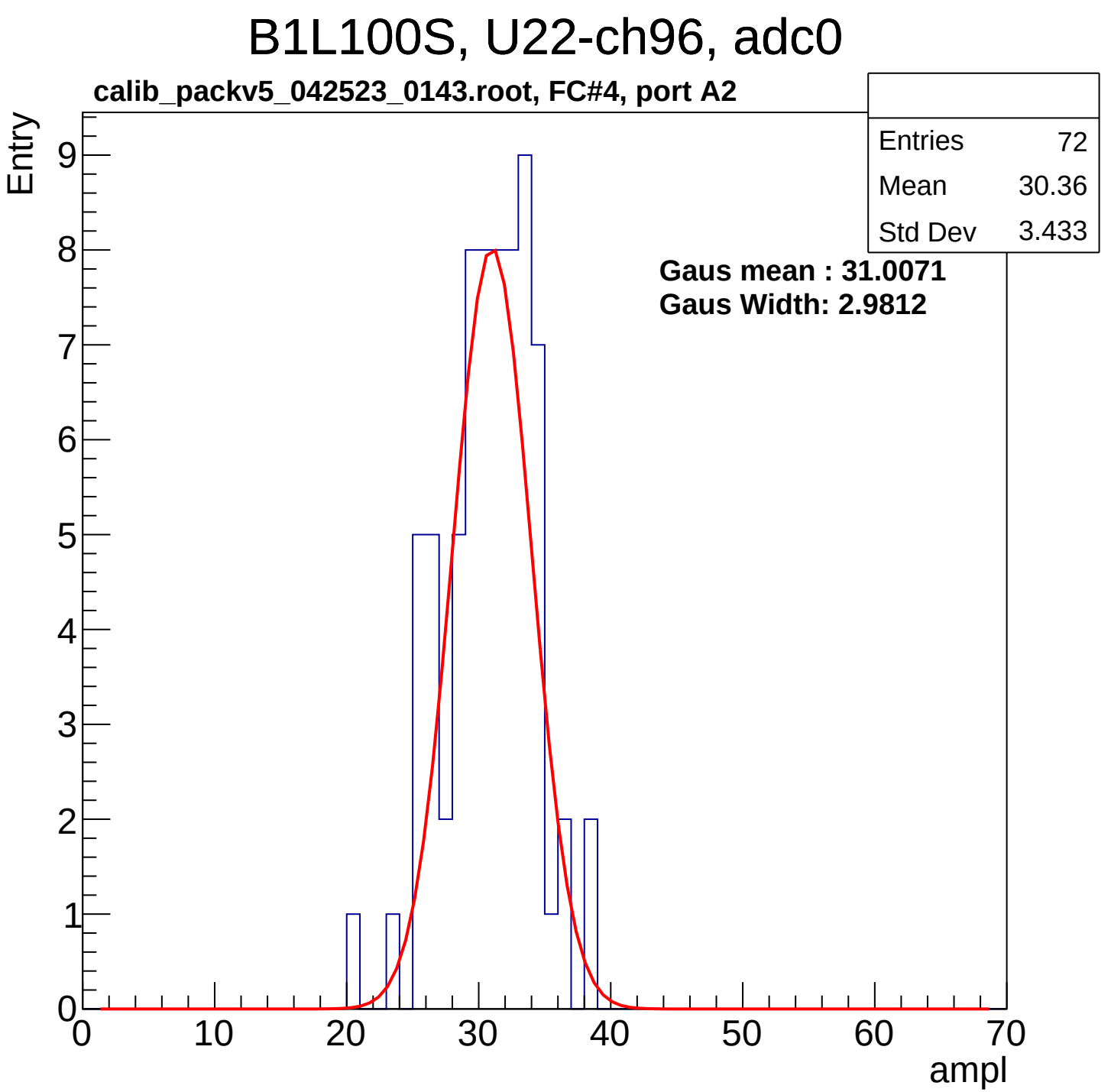
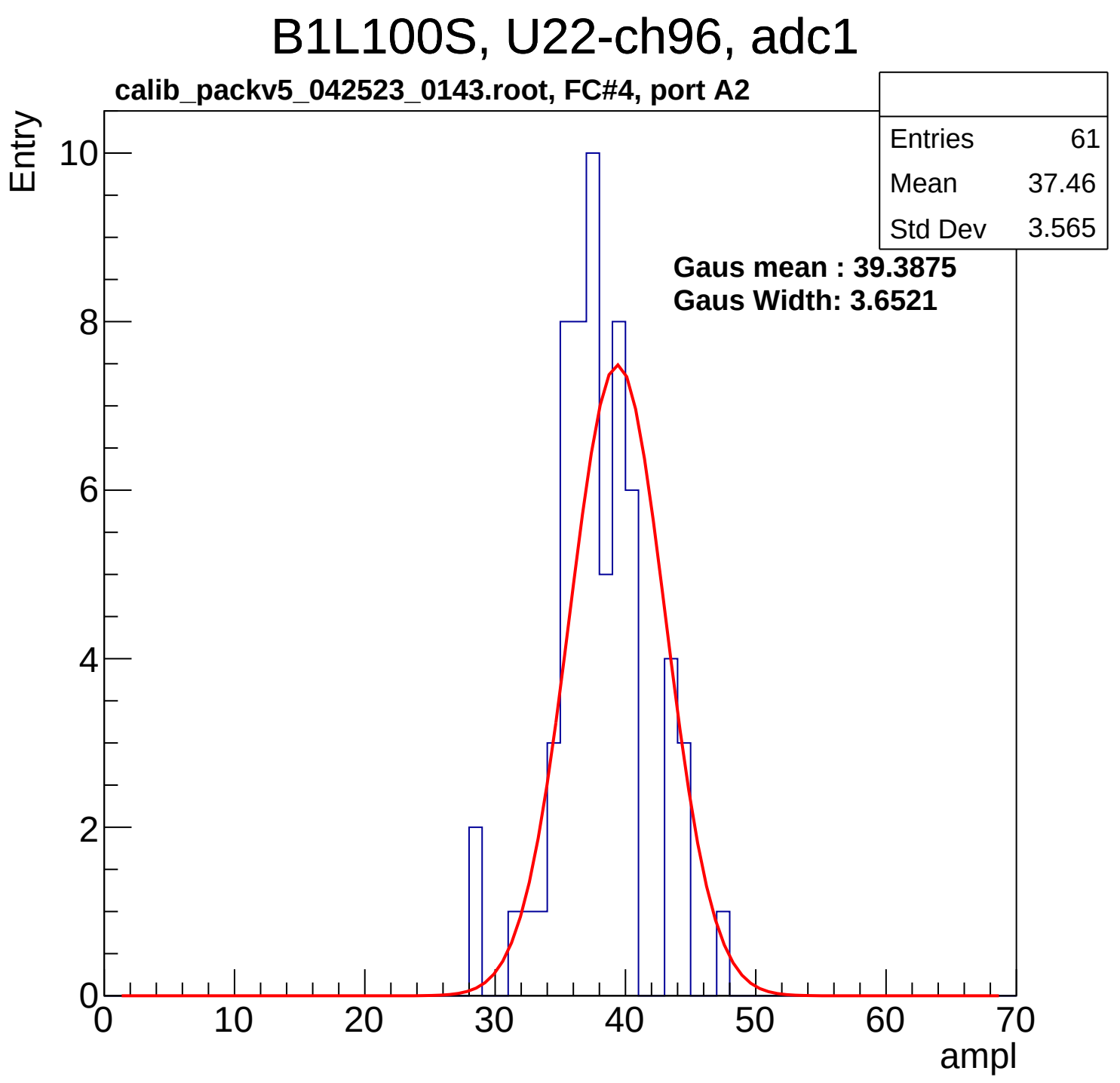
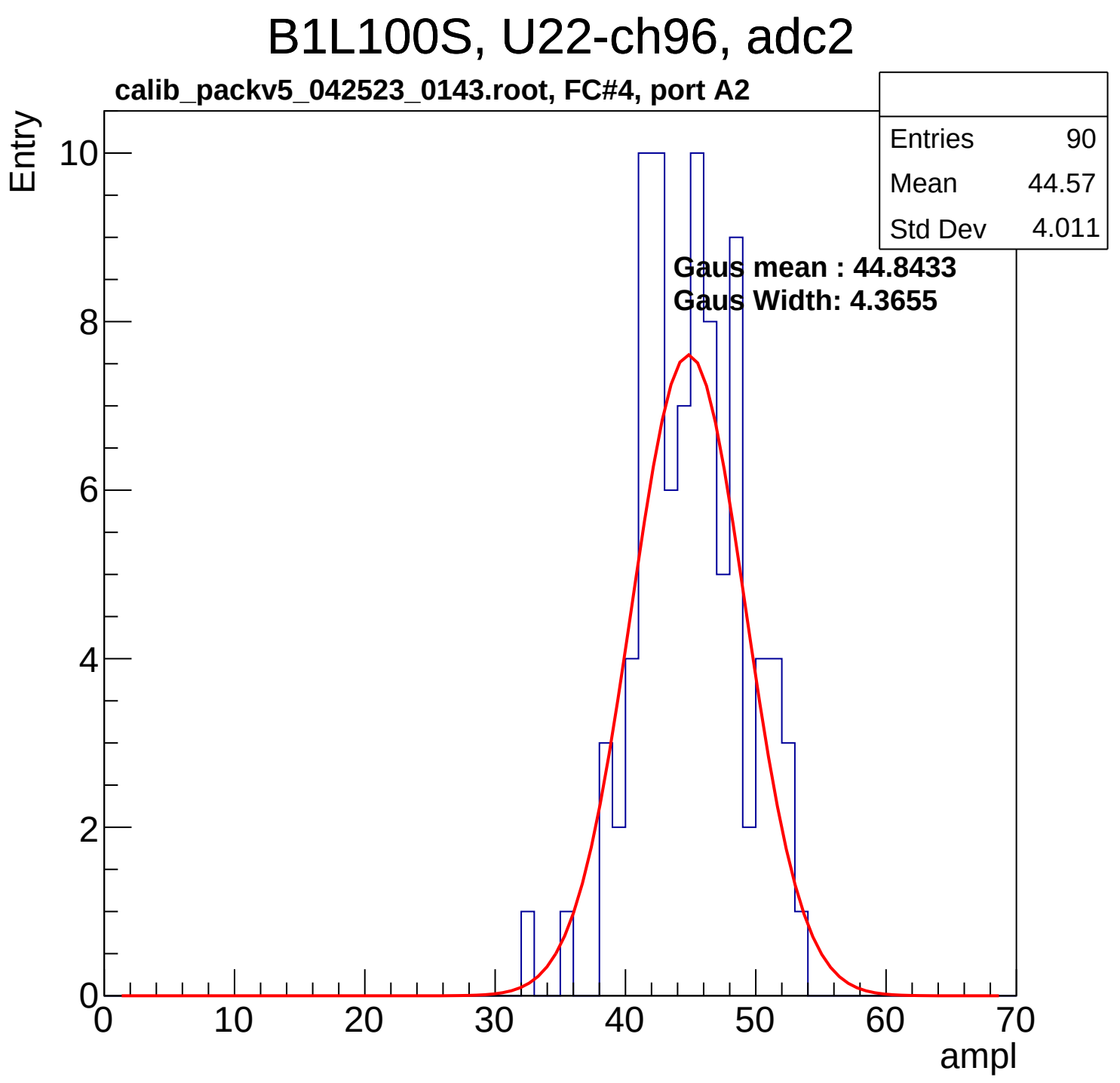
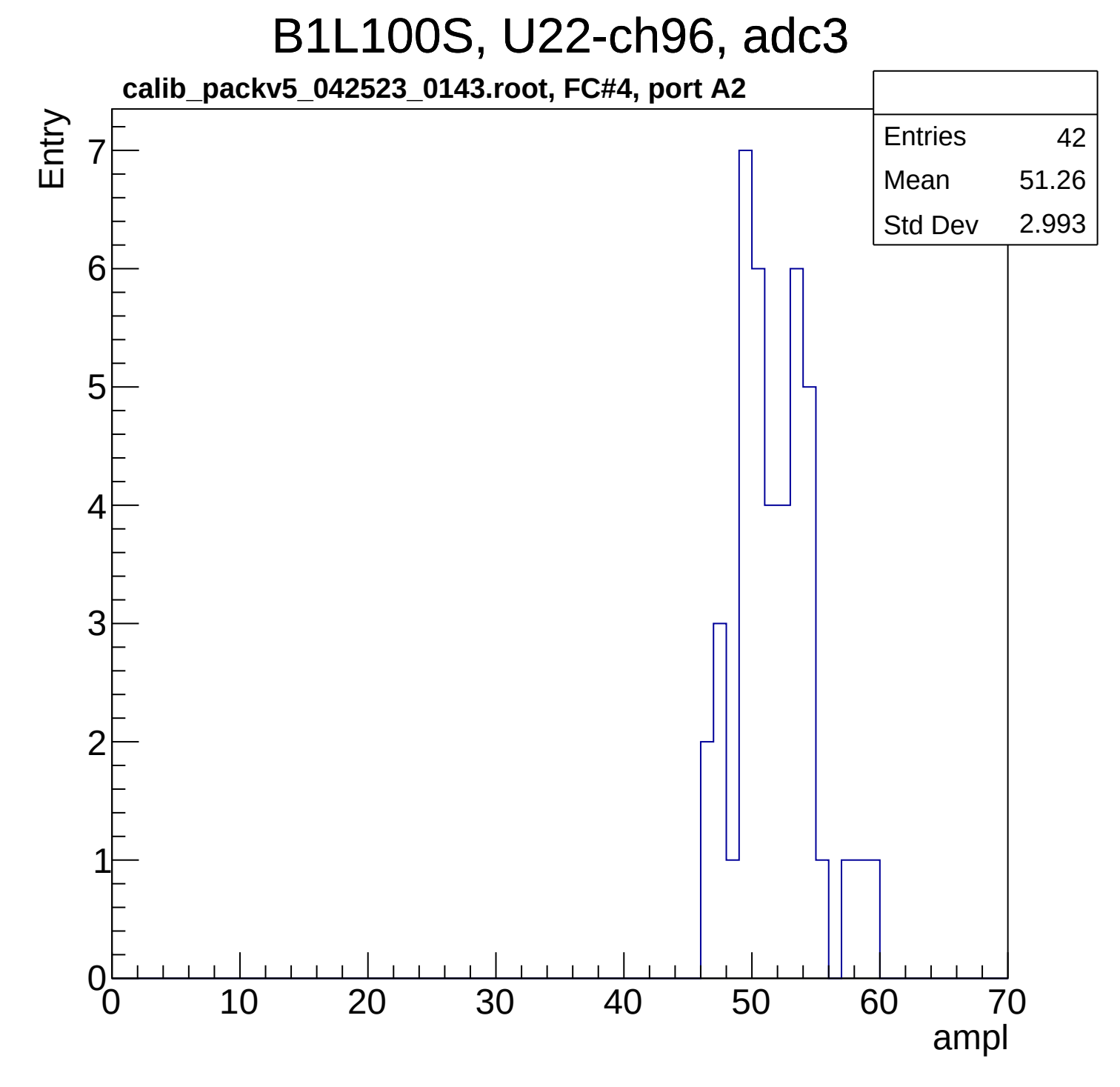
20
30
40
50
60
70


B1L100S, U22-ch95, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch96, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.36
Std Dev 3.433
Gaus mean : 31.0071
Gaus Width: 2.9812
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch96, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 37.46
Std Dev 3.565
Gaus mean : 39.3875
Gaus Width: 3.6521
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch96, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 44.57
Std Dev 4.011
Gaus mean : 44.8433
Gaus Width: 4.3655
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch96, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 51.26
Std Dev 2.993
Entry
ampl
0
1
2
3
4
5
6
7
0
10
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60
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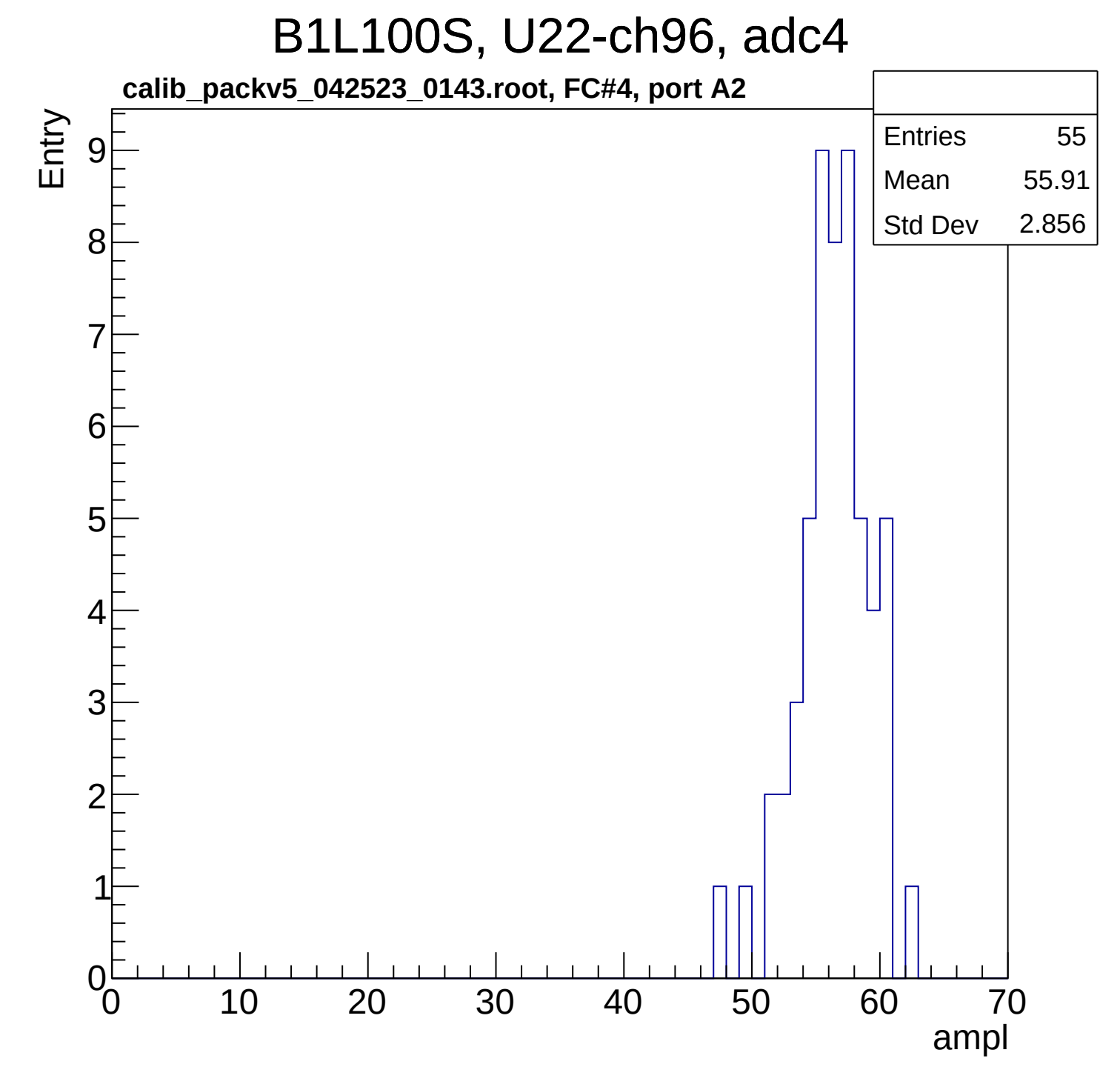
B1L100S, U22-ch96, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.91
Std Dev 2.856
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
50
60
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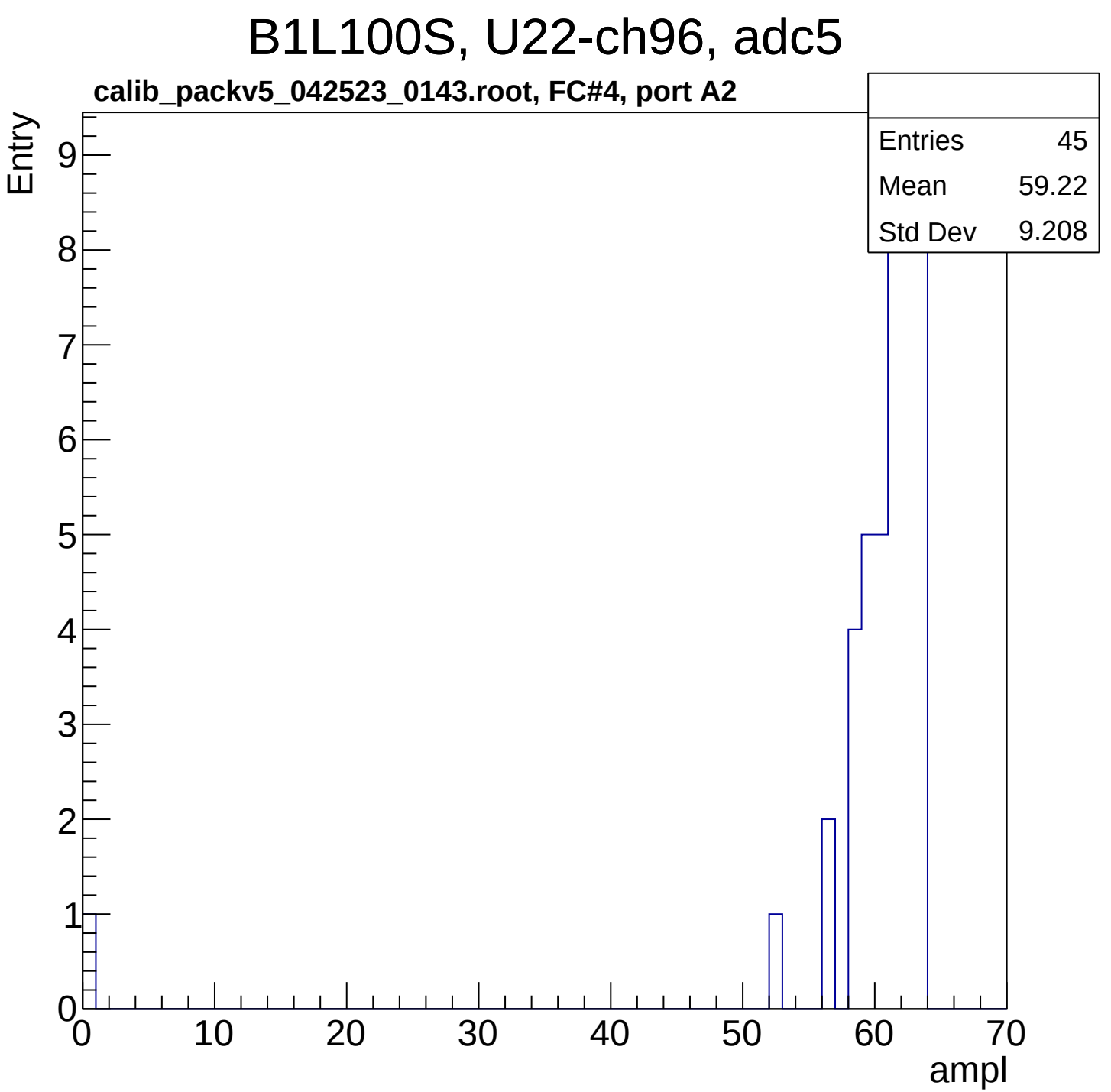
B1L100S, U22-ch96, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.22
Std Dev 9.208
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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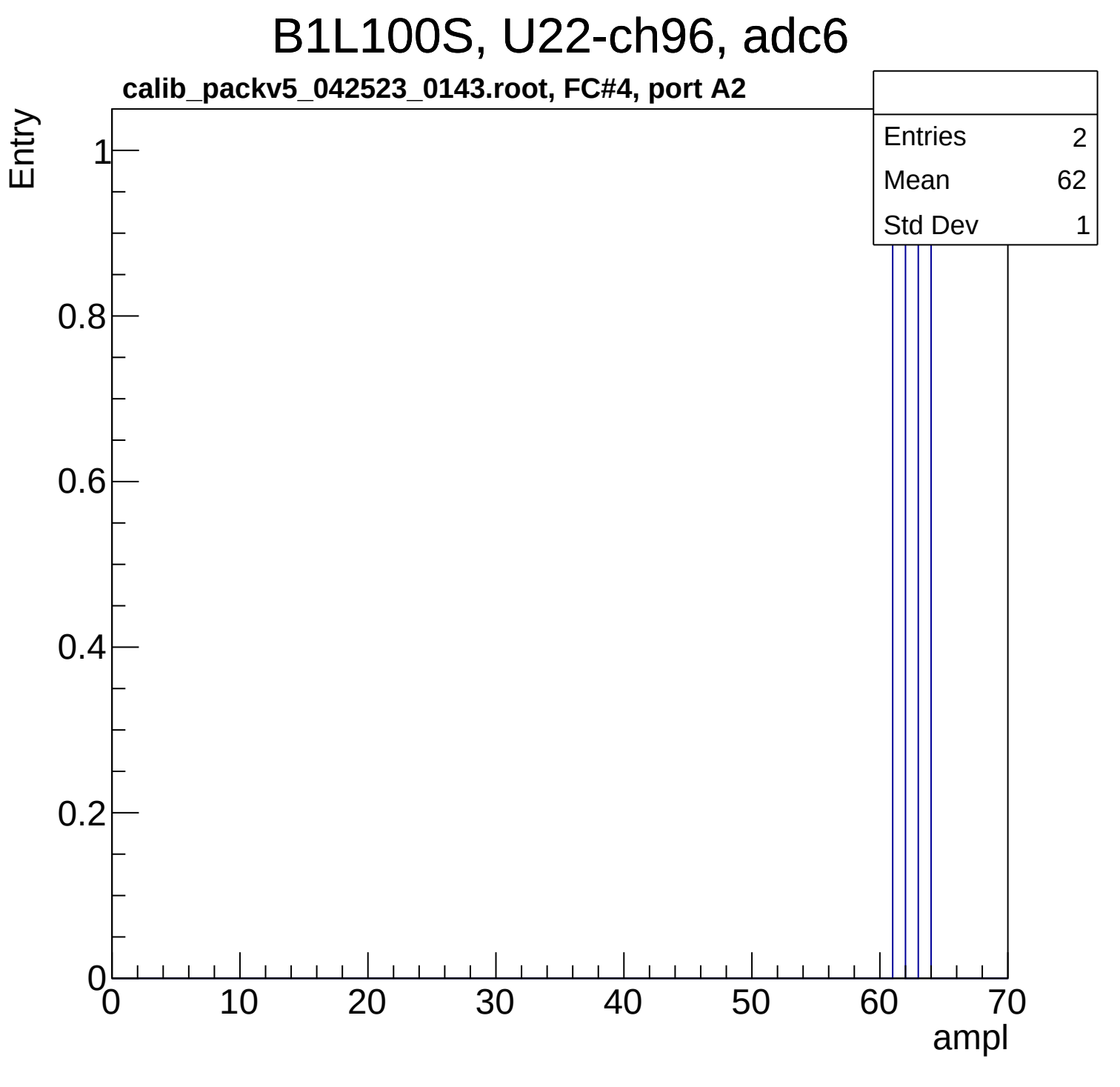
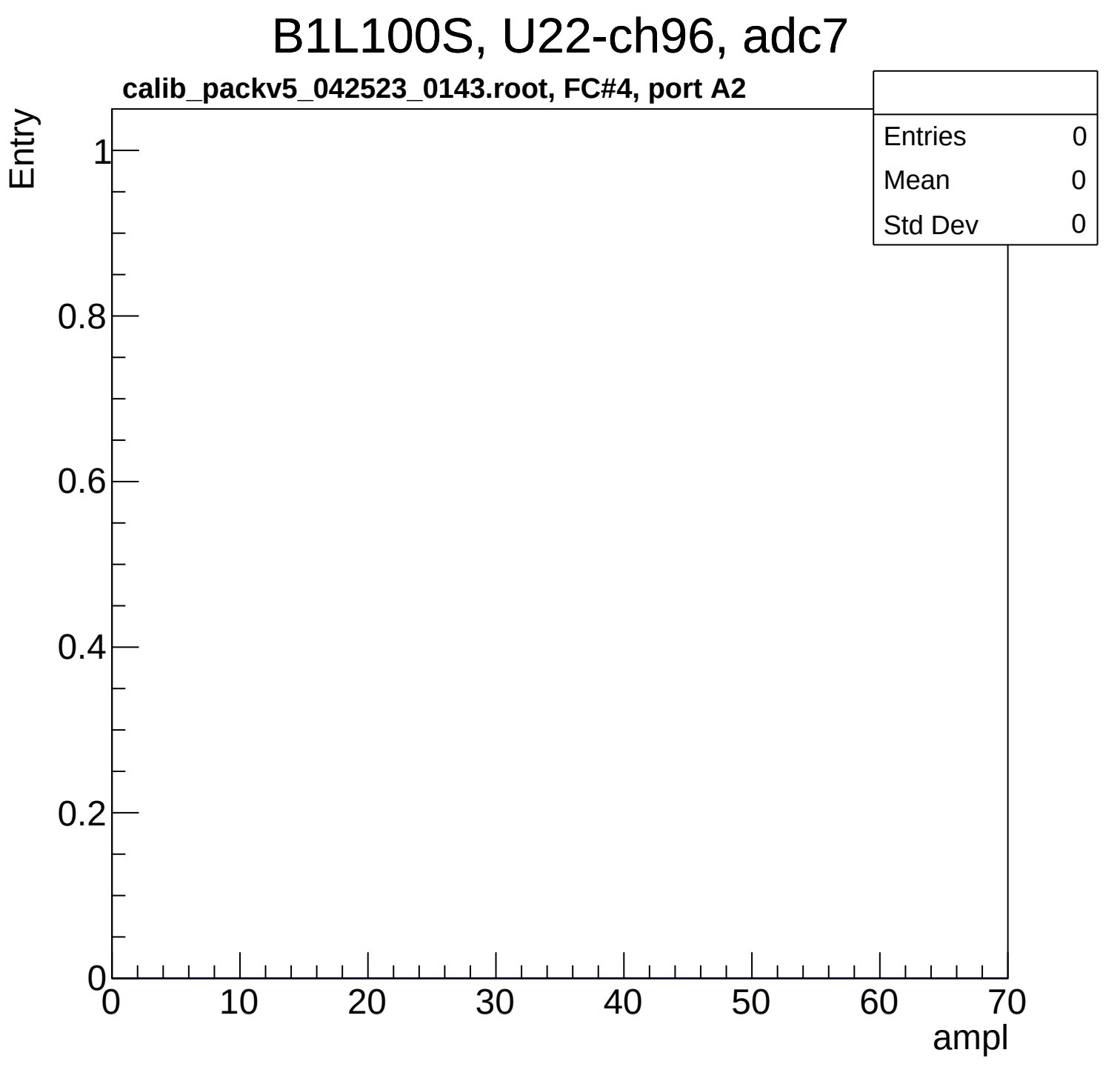
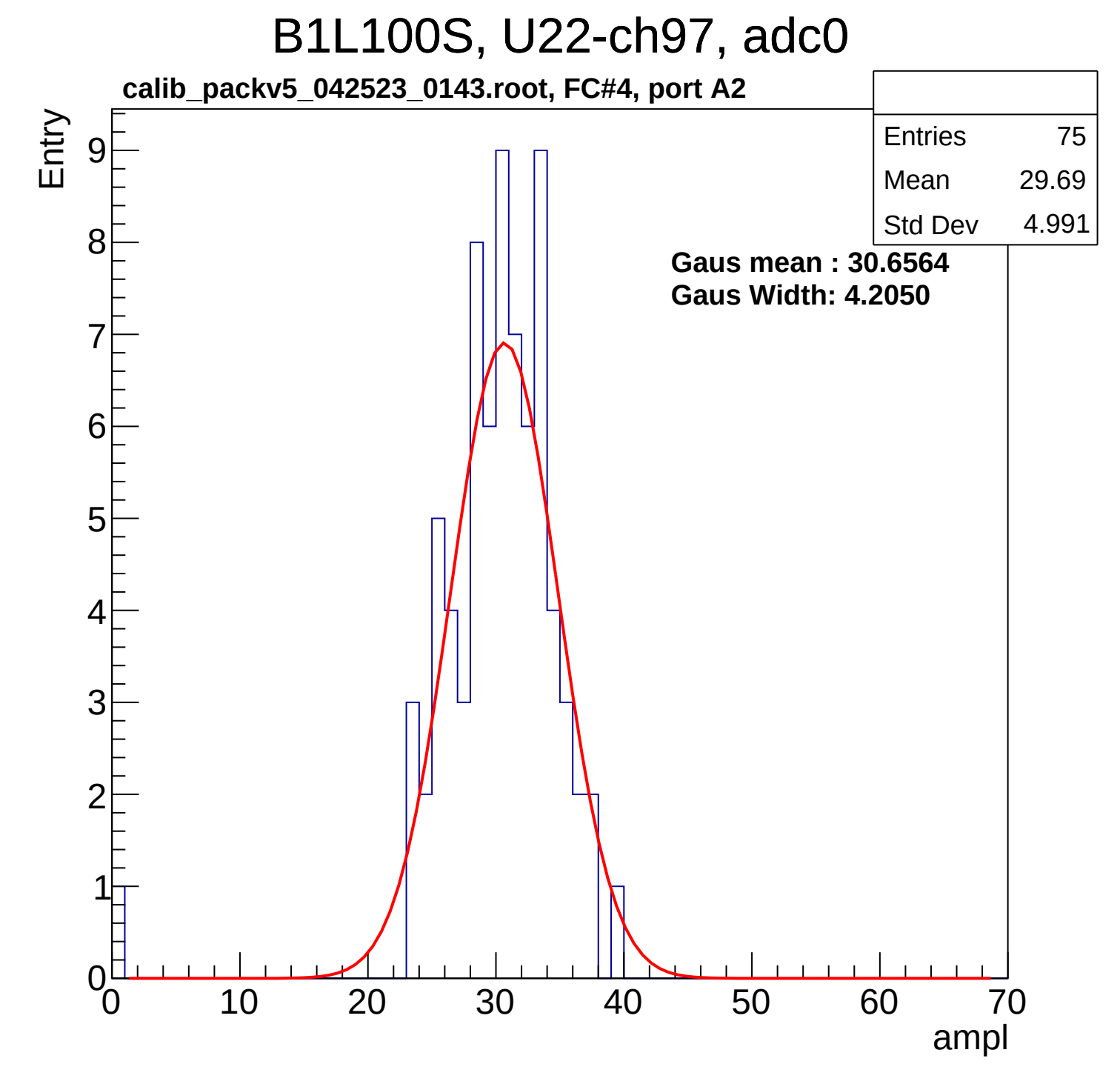
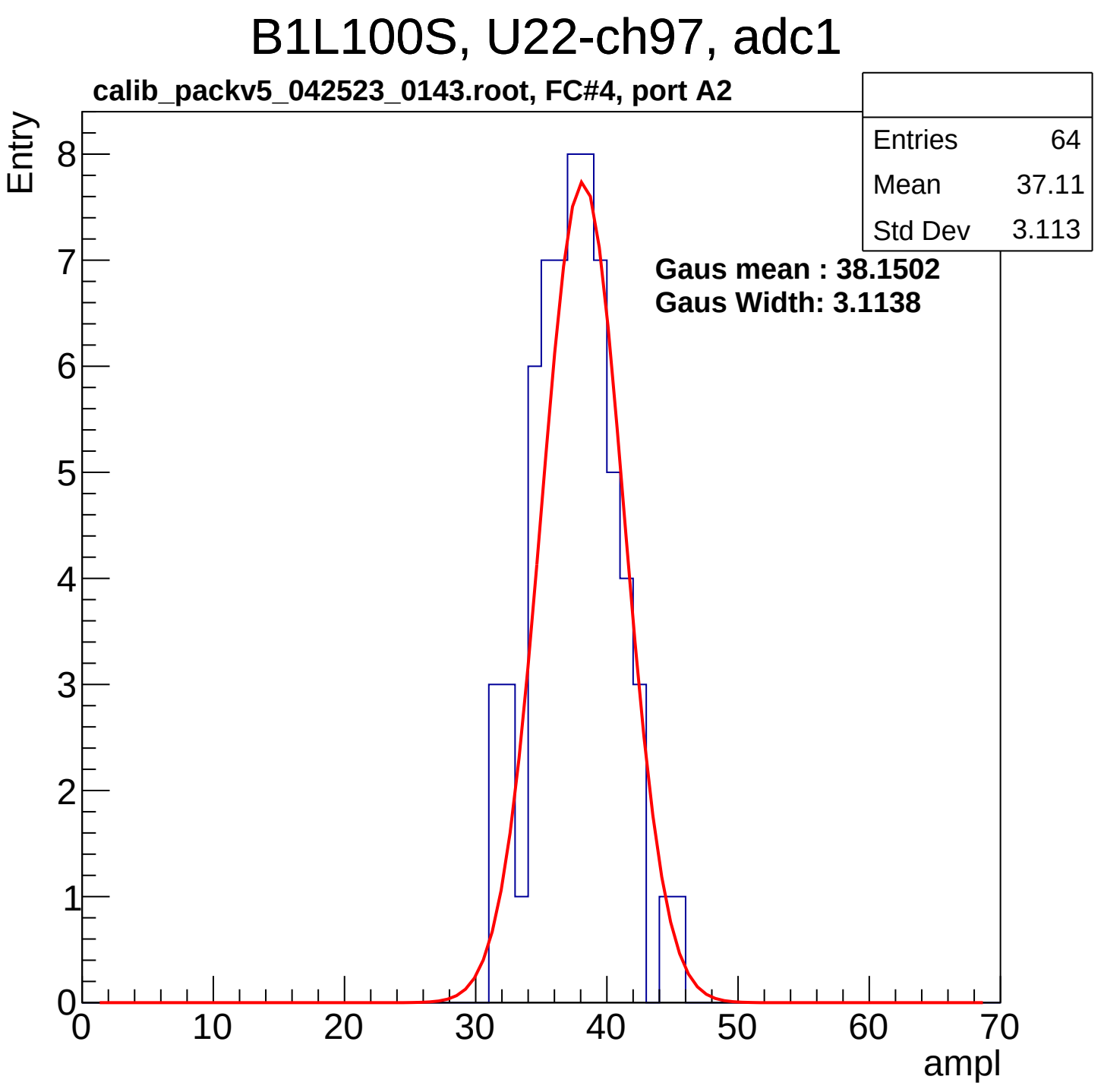
40
50
60
70


B1L100S, U22-ch96, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 62
Std Dev 1
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch96, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch97, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.69
Std Dev 4.991
Gaus mean : 30.6564
Gaus Width: 4.2050
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch97, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 37.11
Std Dev 3.113
Gaus mean : 38.1502
Gaus Width: 3.1138
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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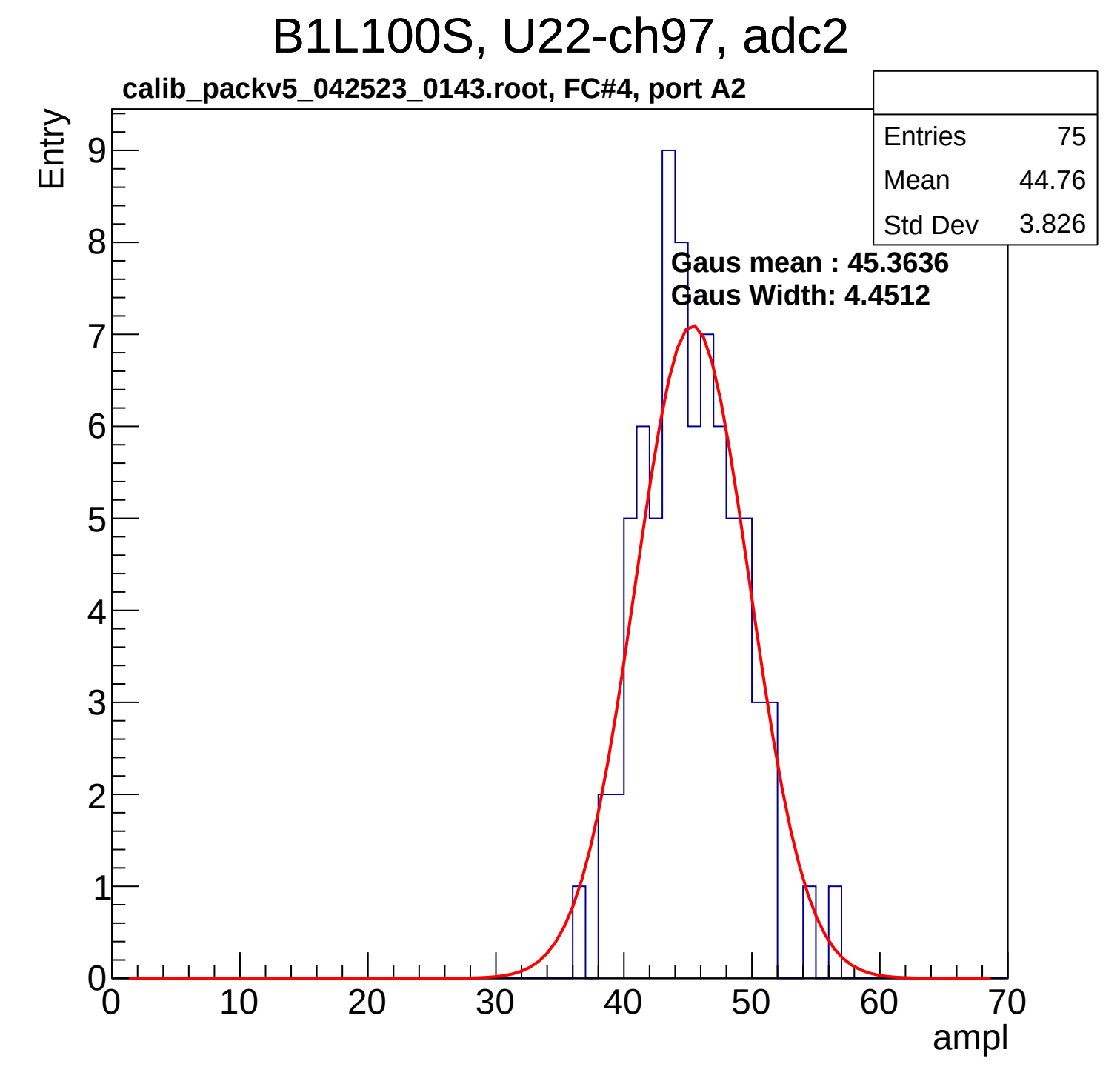
B1L100S, U22-ch97, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.76
Std Dev 3.826
Gaus mean : 45.3636
Gaus Width: 4.4512
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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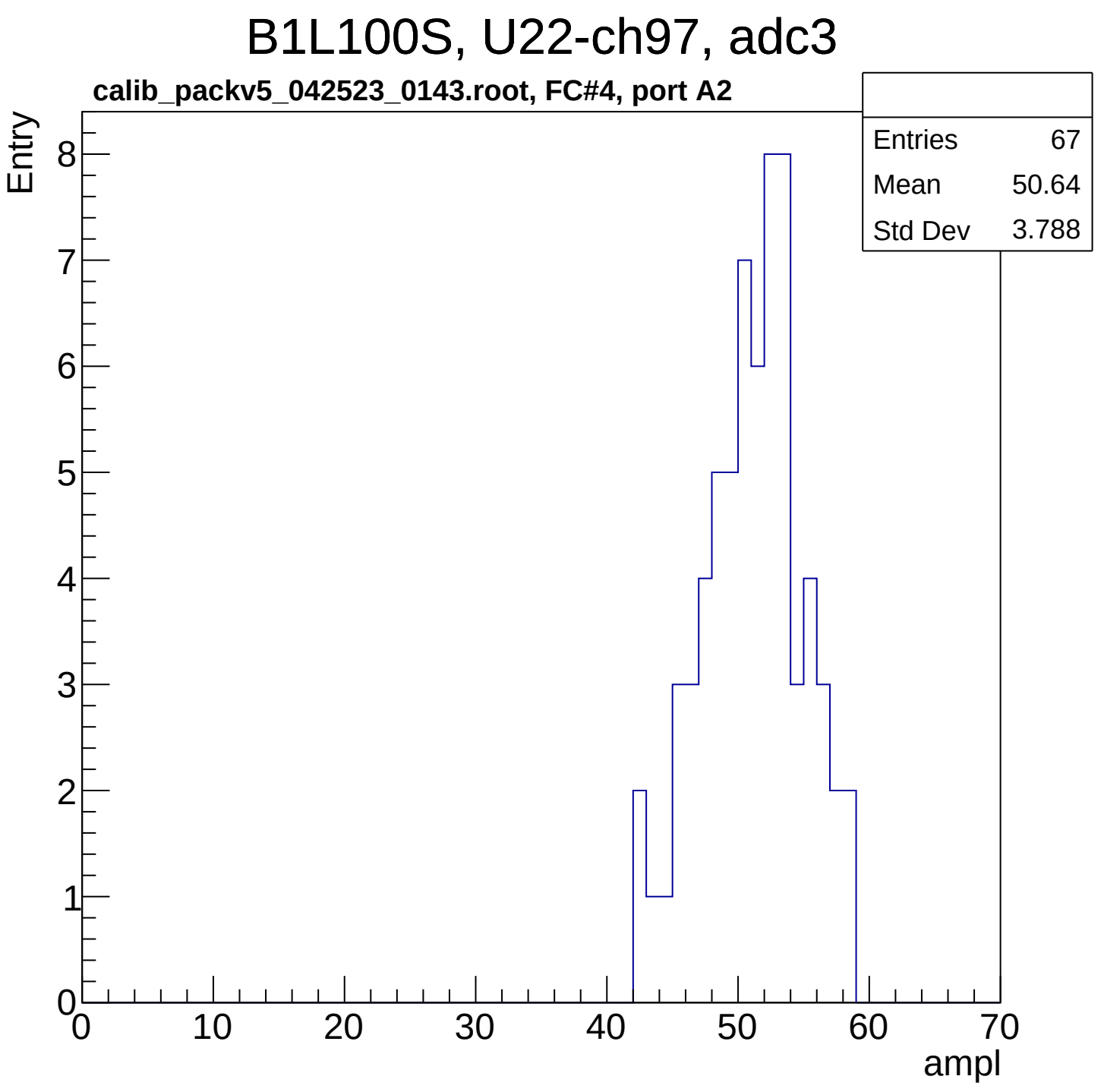
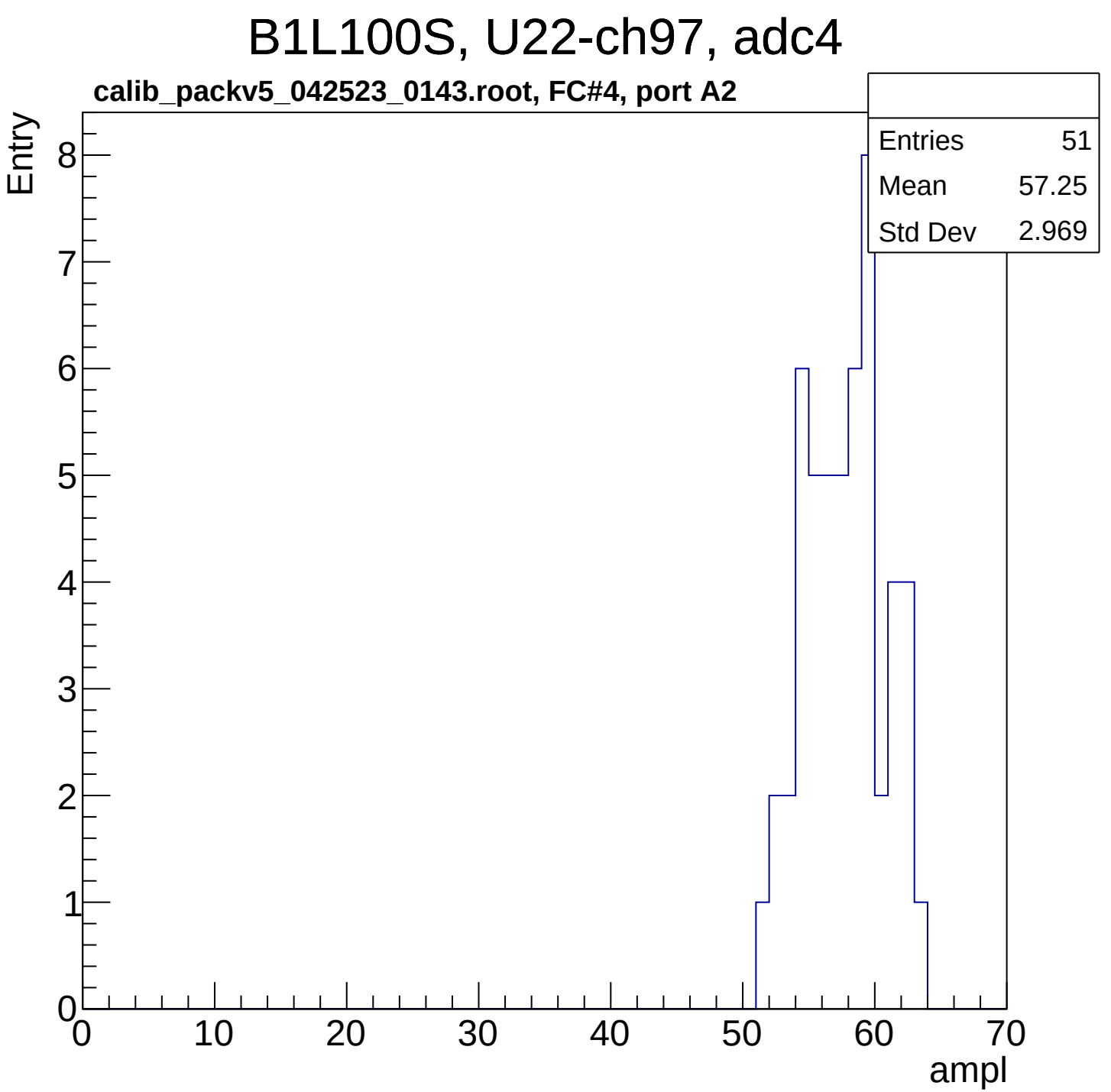
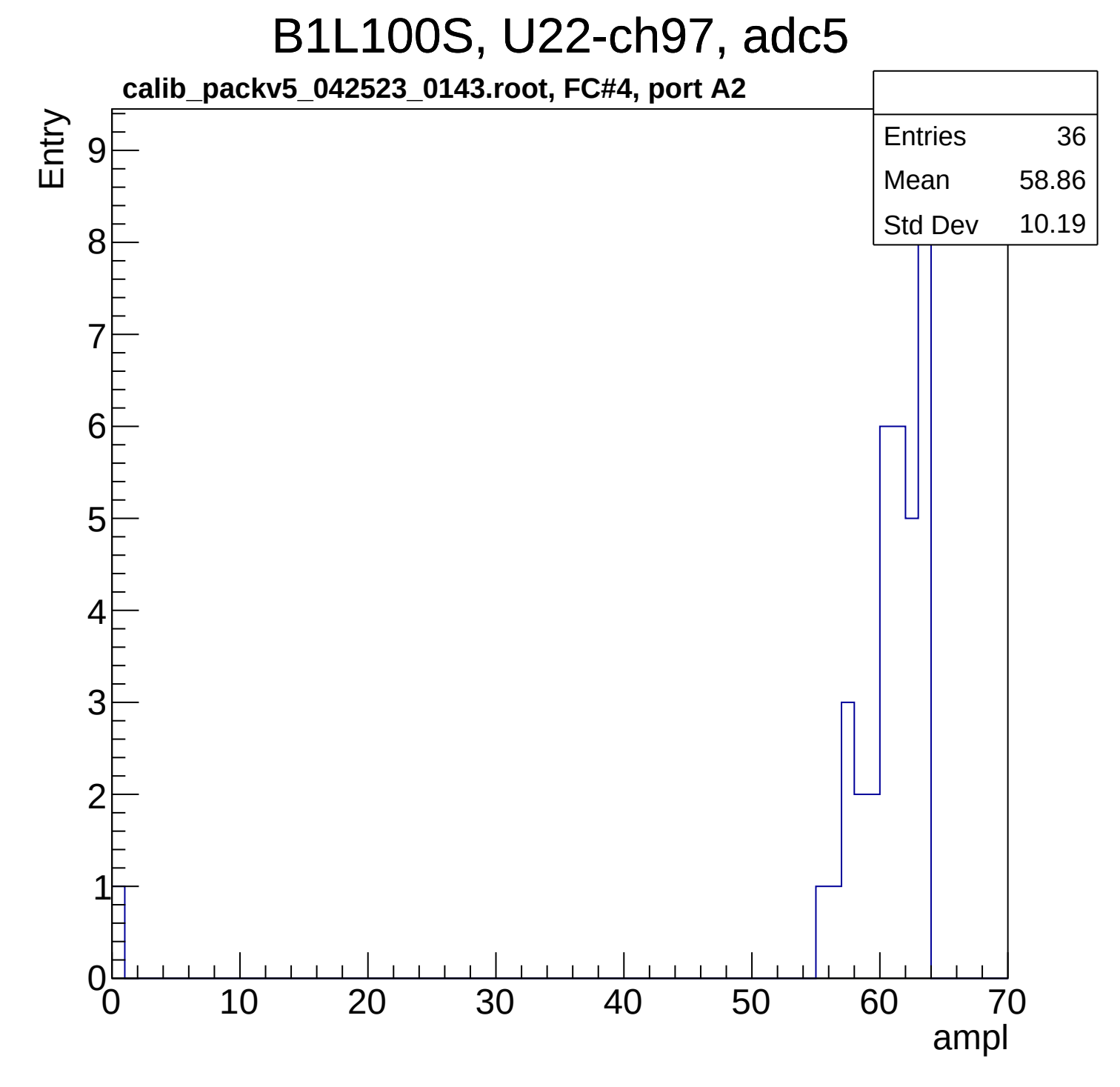

B1L100S, U22-ch97, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.64
Std Dev 3.788
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch97, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 57.25
Std Dev 2.969
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch97, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 58.86
Std Dev 10.19
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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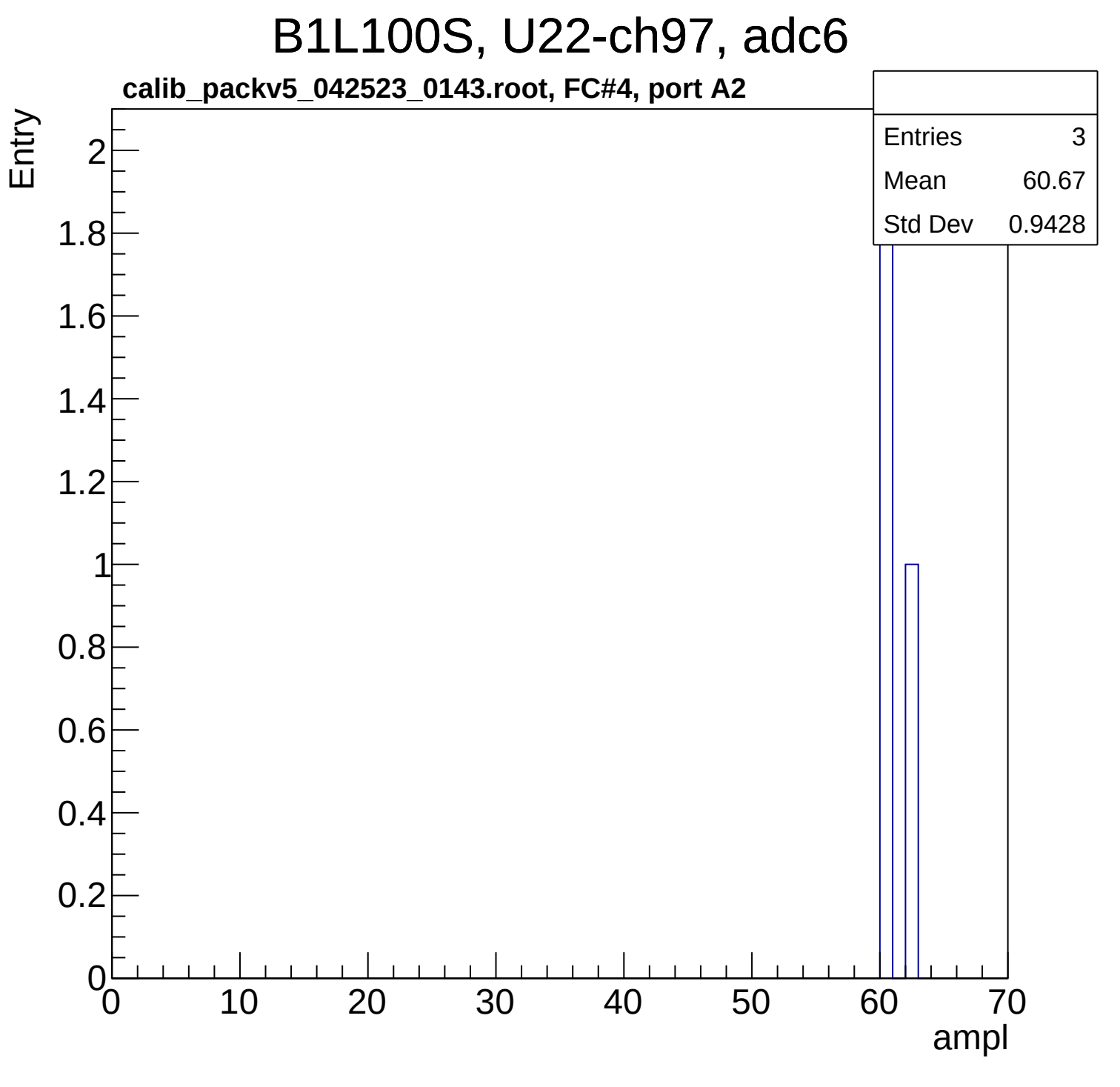
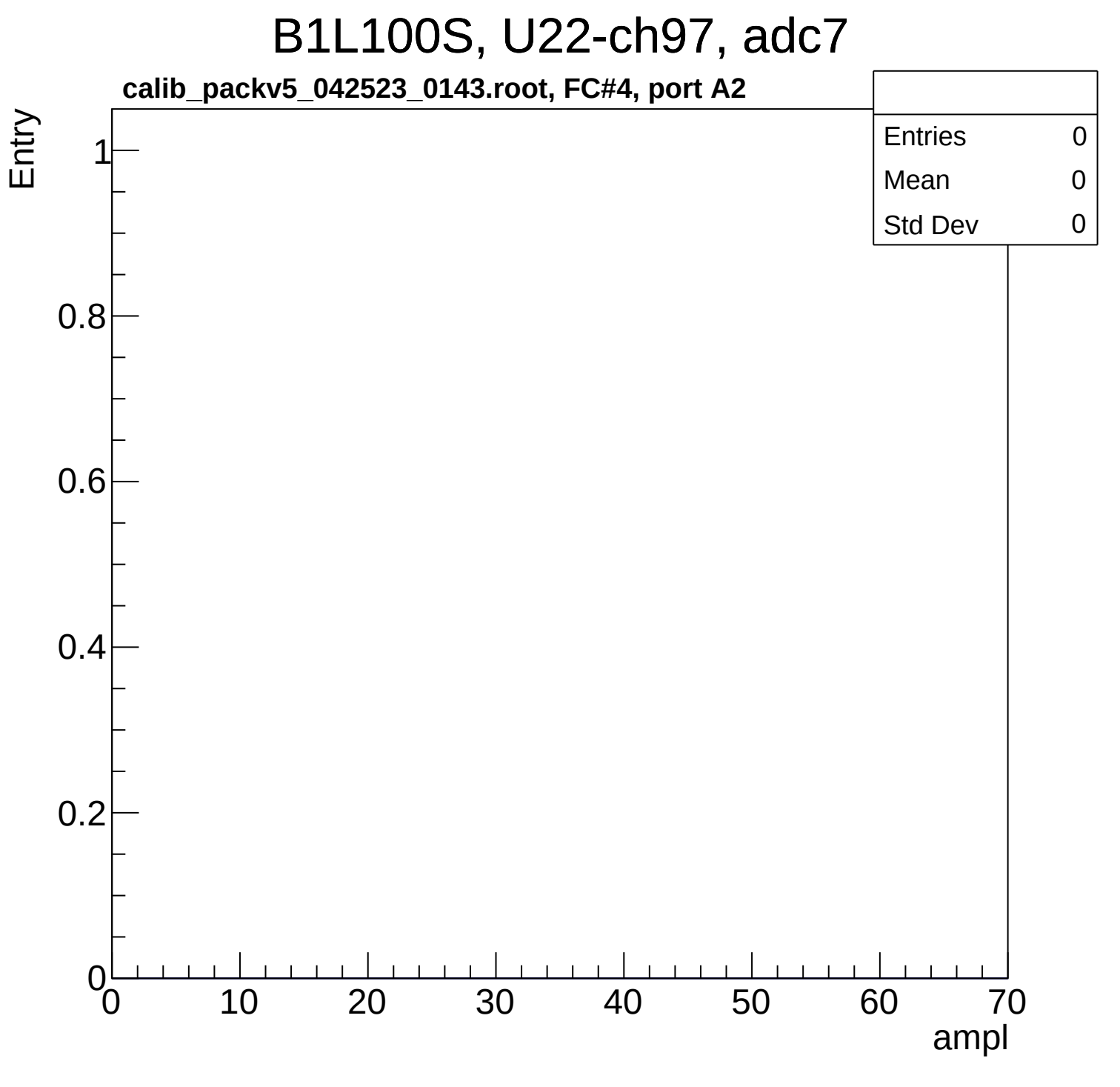
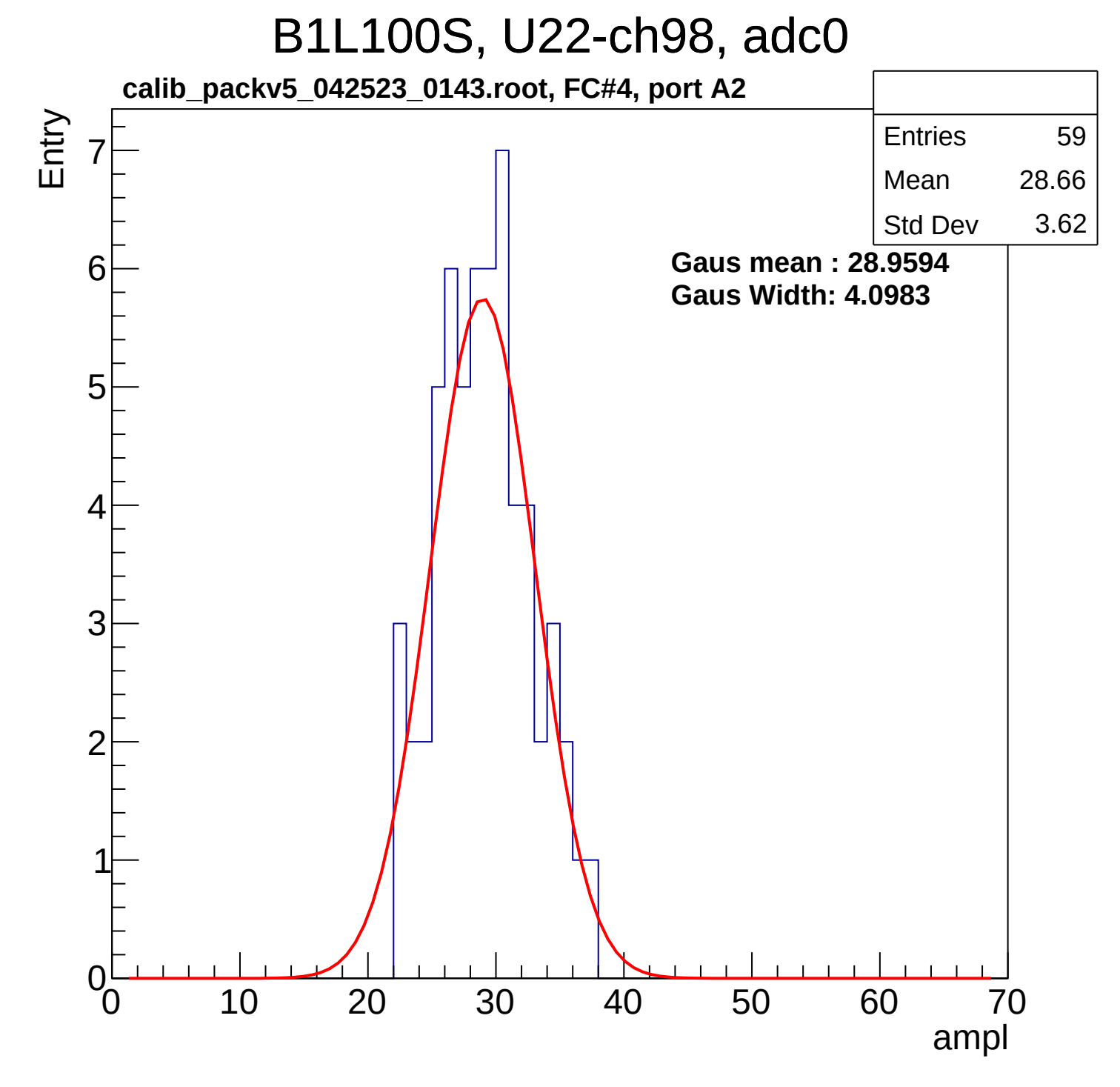
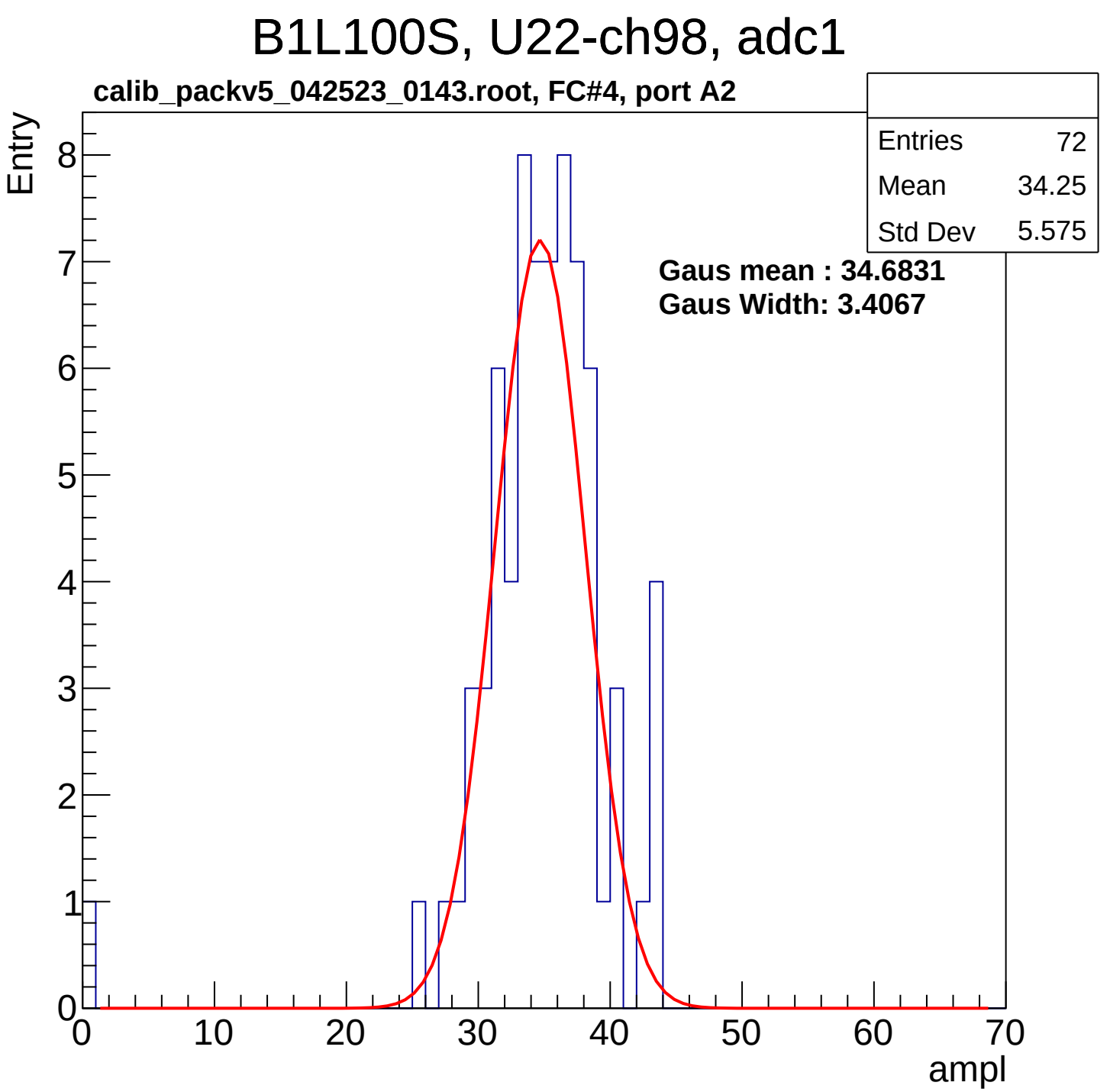
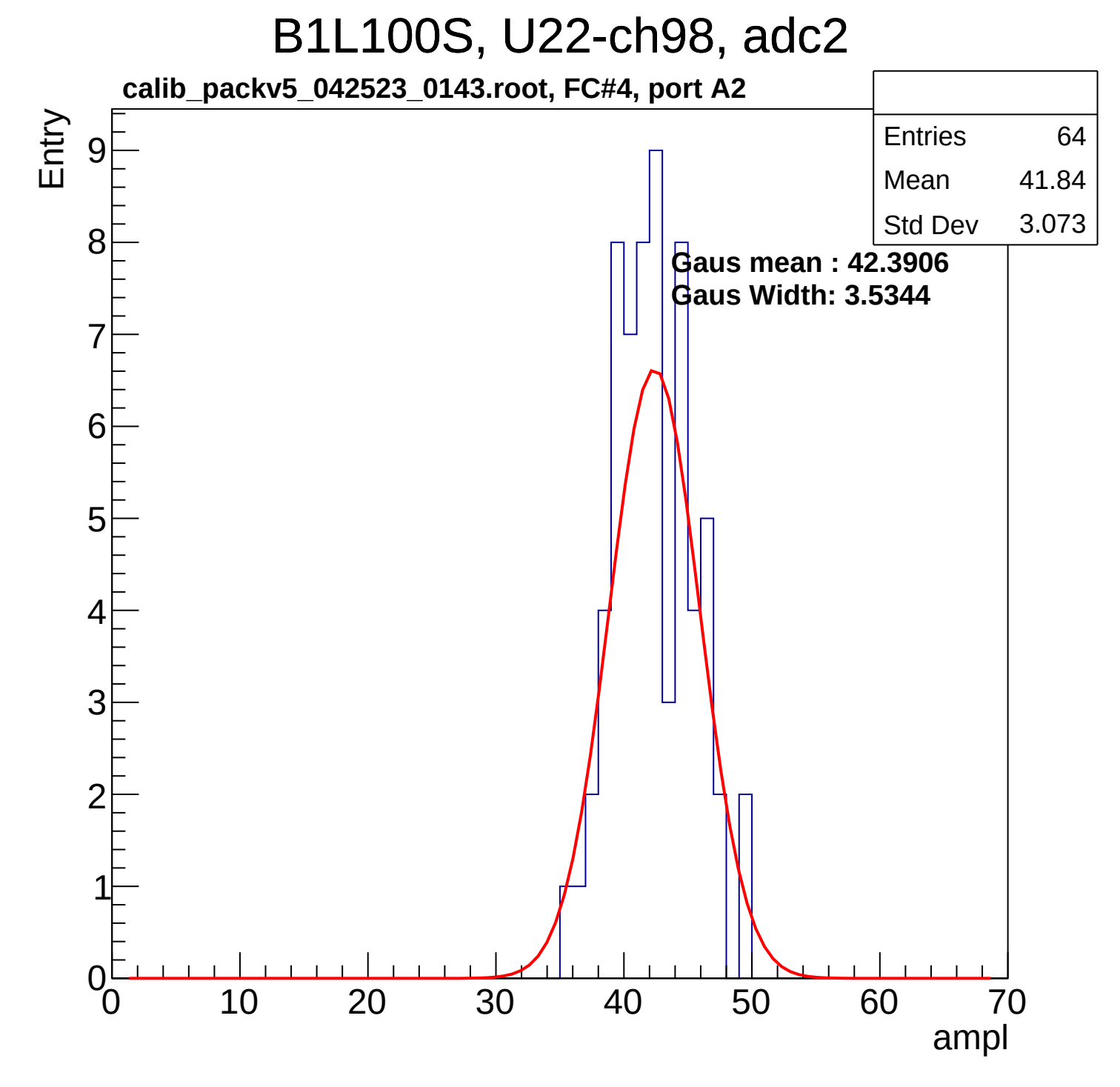
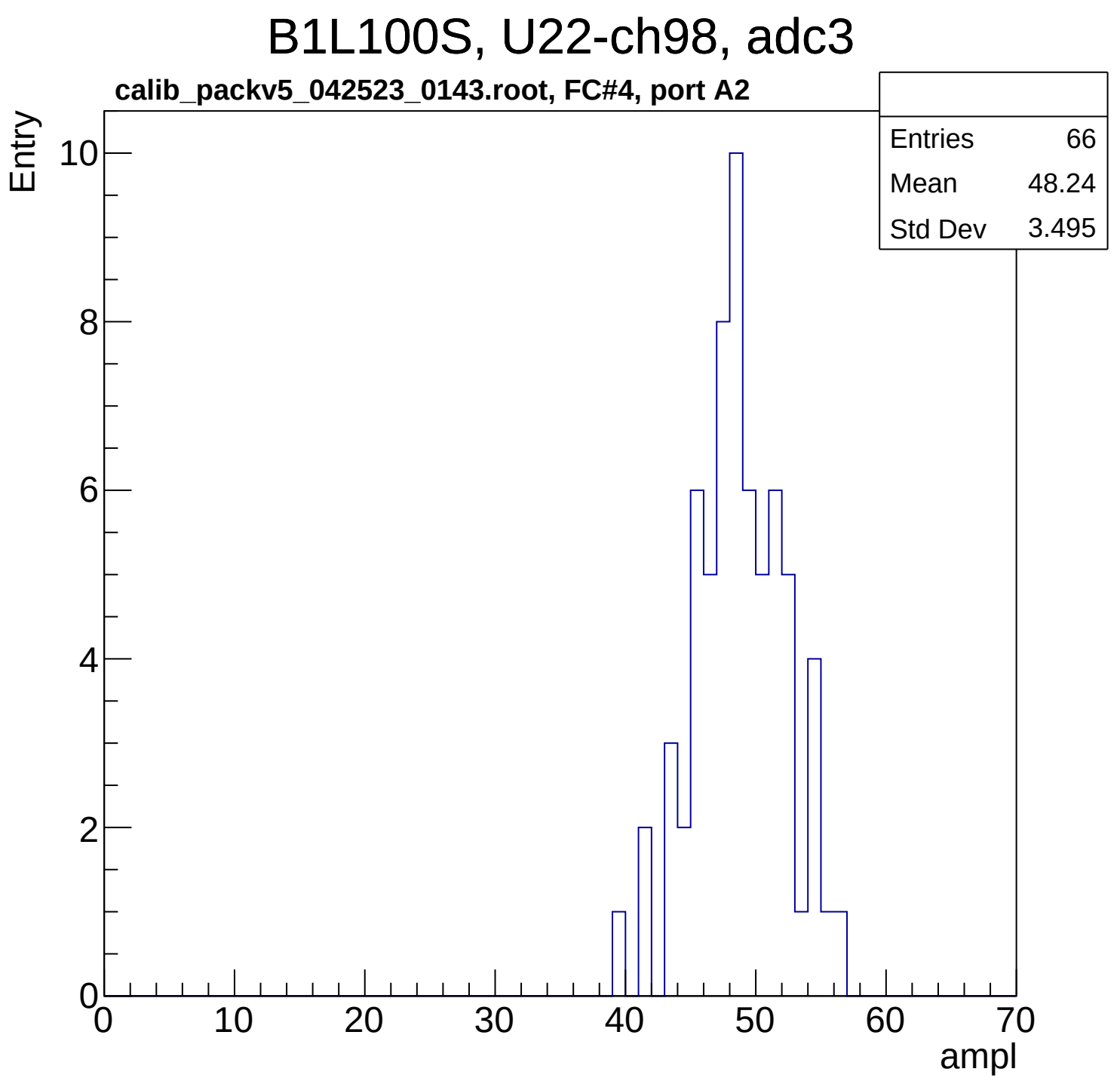

B1L100S, U22-ch97, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 60.67
Std Dev 0.9428
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70

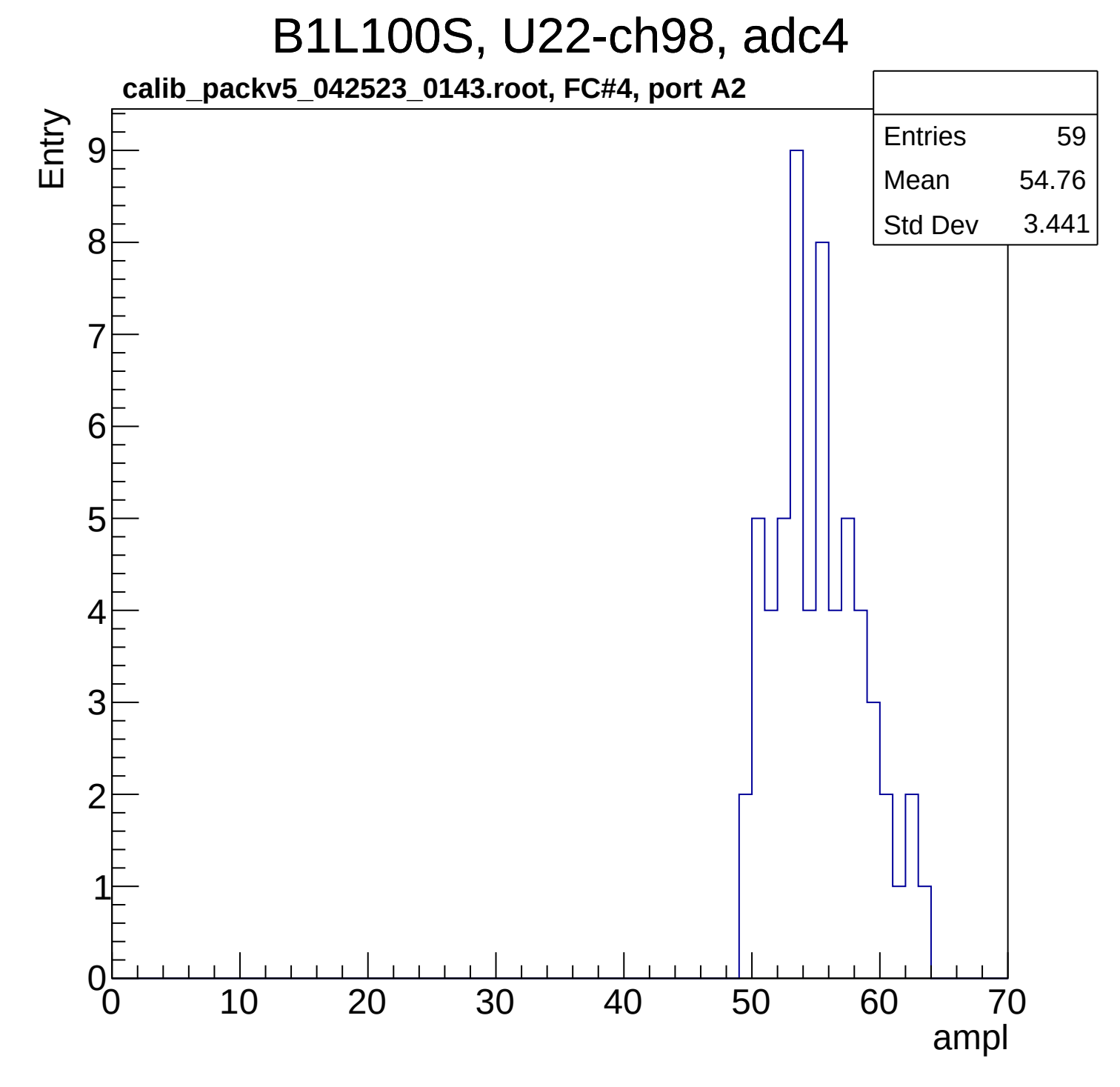

B1L100S, U22-ch97, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl

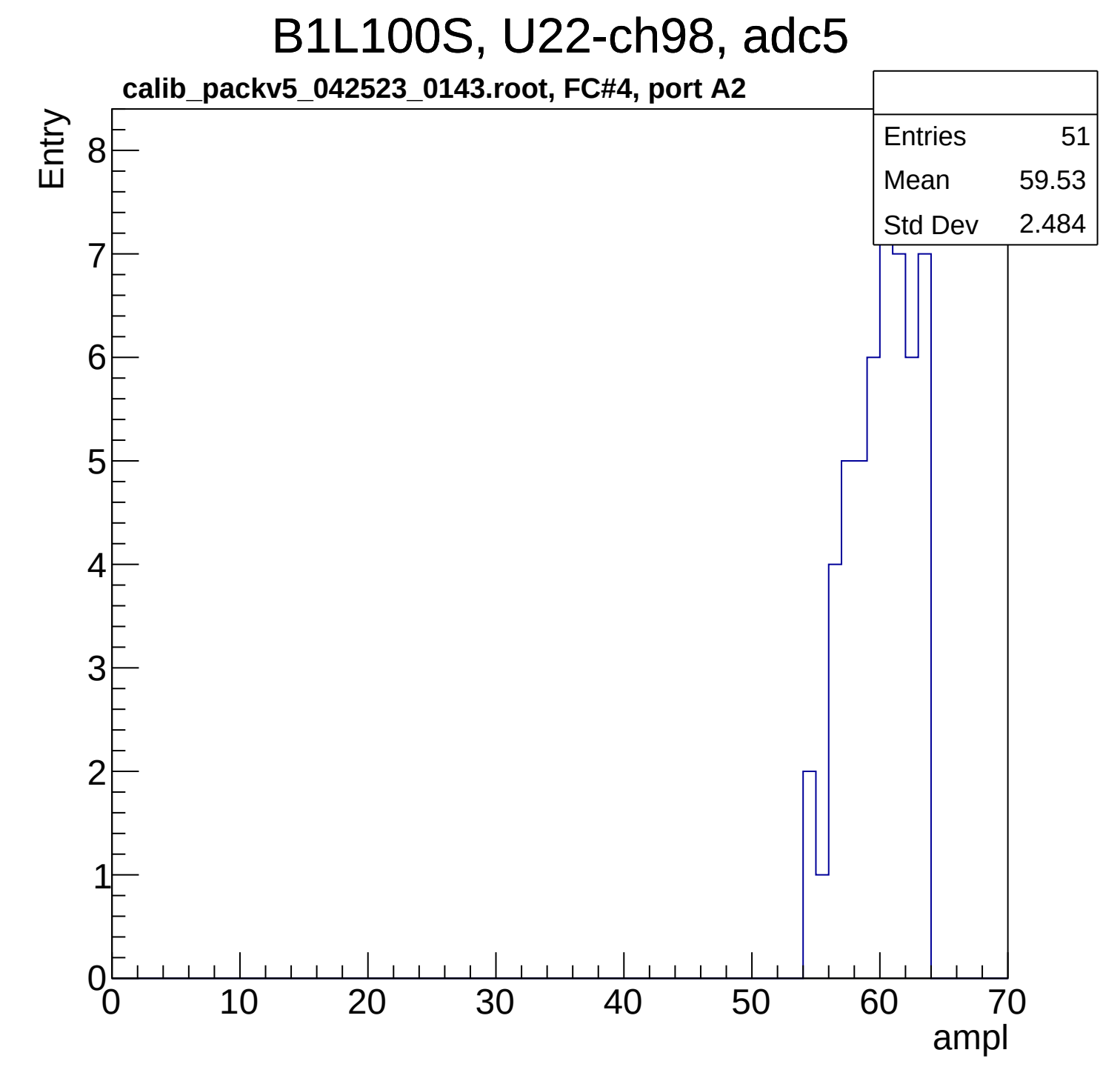

B1L100S, U22-ch98, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 28.66
Std Dev 3.62
Gaus mean : 28.9594
Gaus Width: 4.0983
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch98, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 34.25
Std Dev 5.575
Gaus mean : 34.6831
Gaus Width: 3.4067
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch98, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 41.84
Std Dev 3.073
Gaus mean : 42.3906
Gaus Width: 3.5344
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch98, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 48.24
Std Dev 3.495
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch98, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.76
Std Dev 3.441
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch98, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 59.53
Std Dev 2.484
Entry
ampl
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1
2
3
4
5
6
7
8
0
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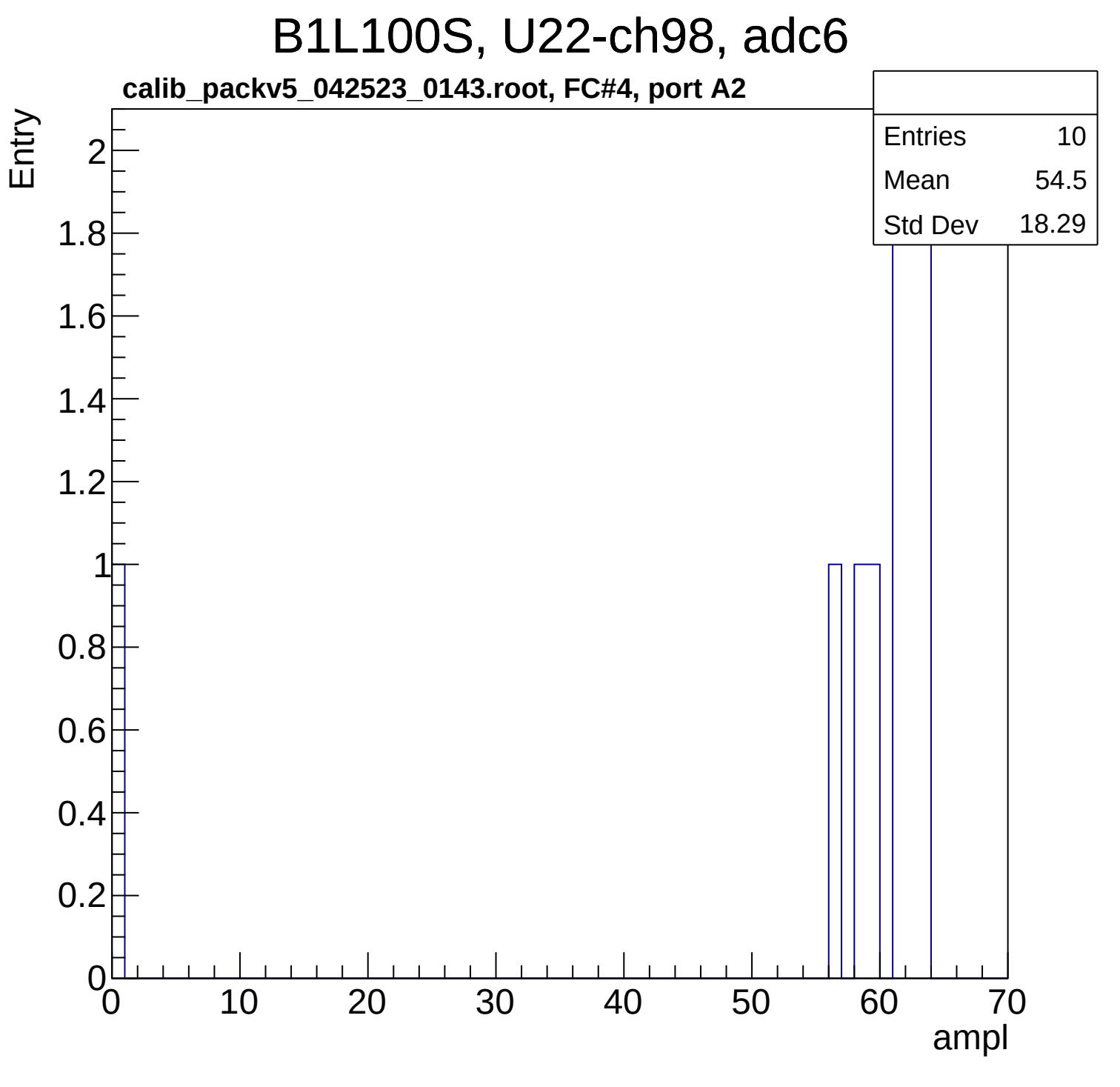
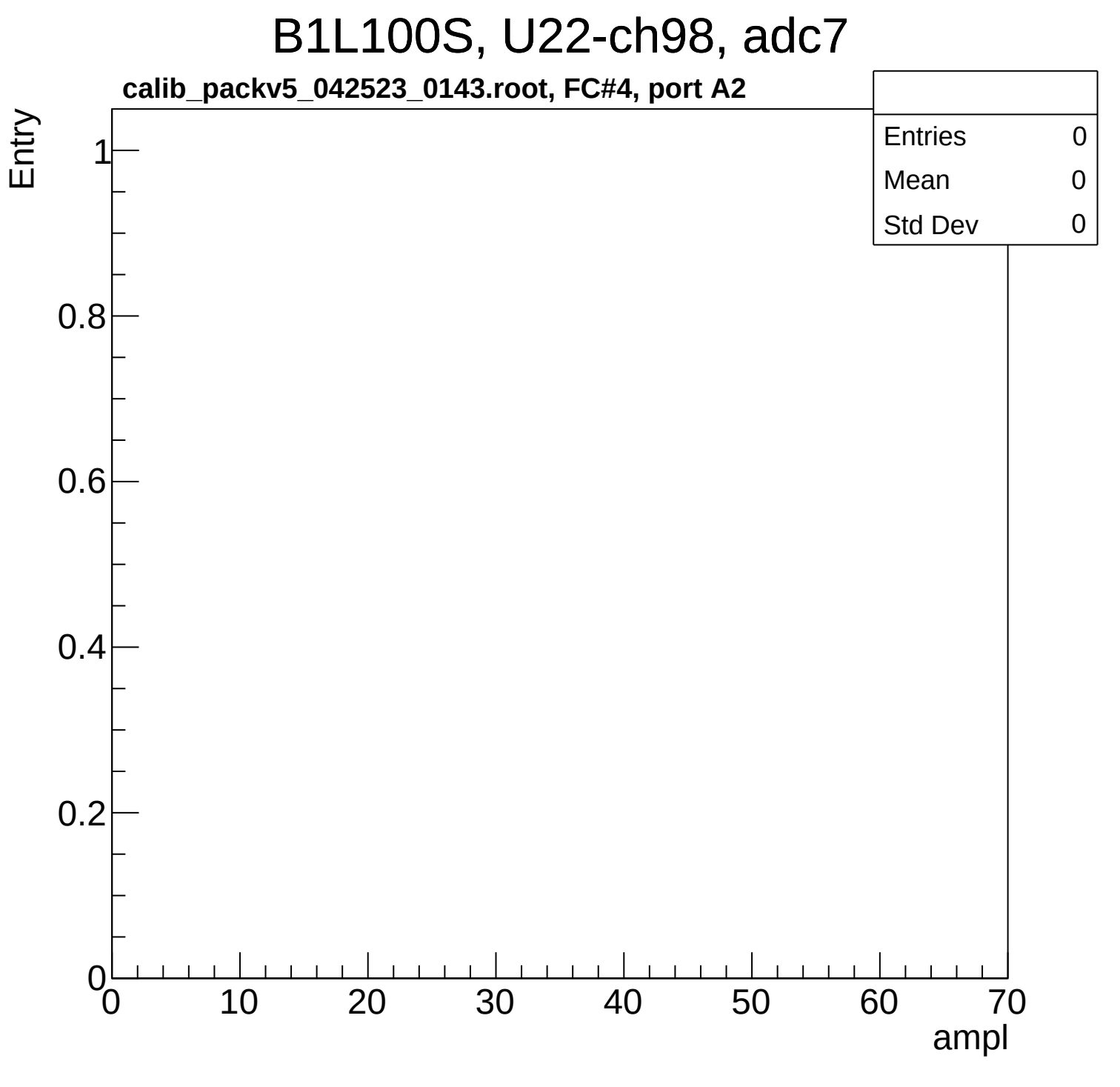
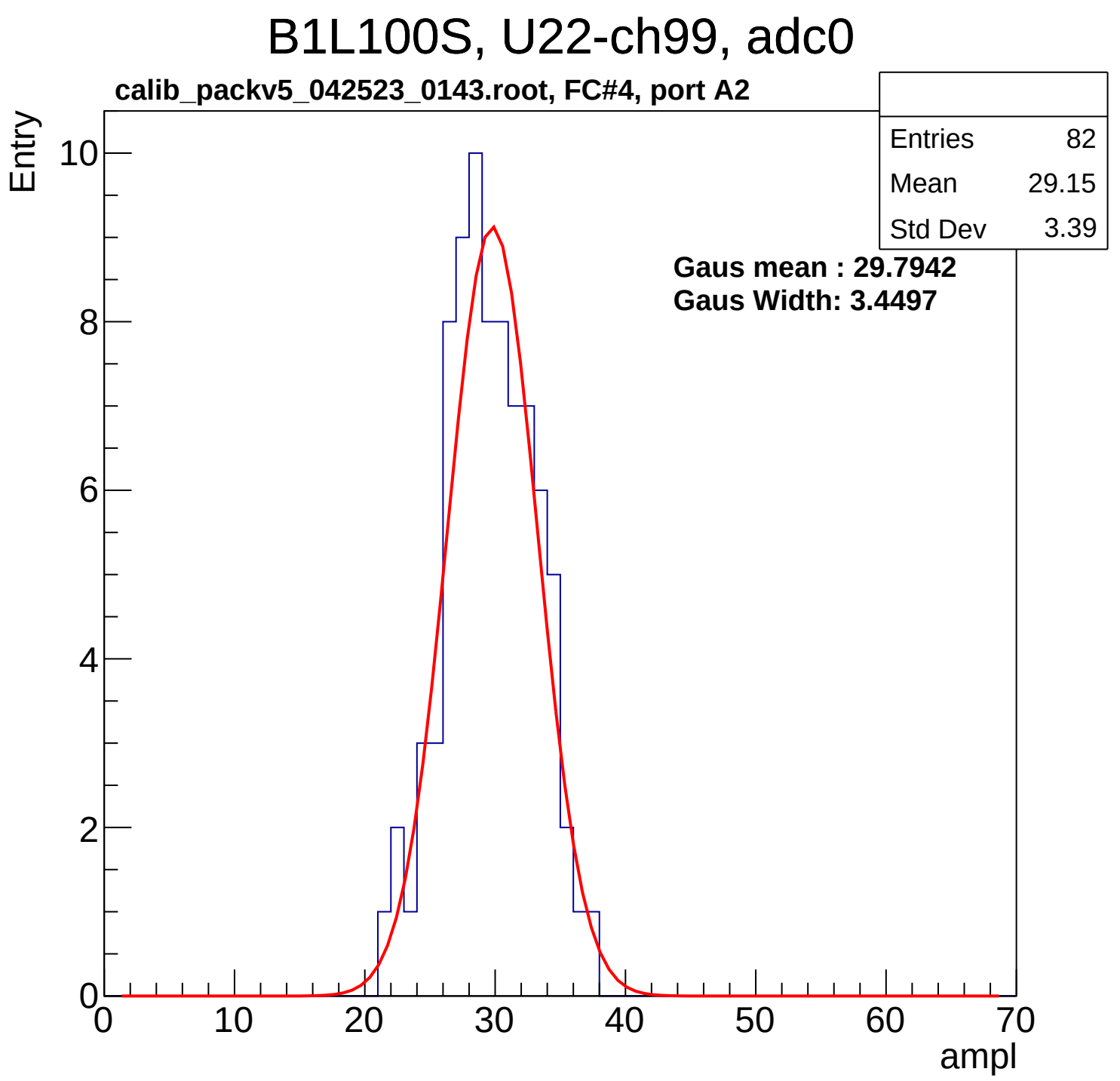
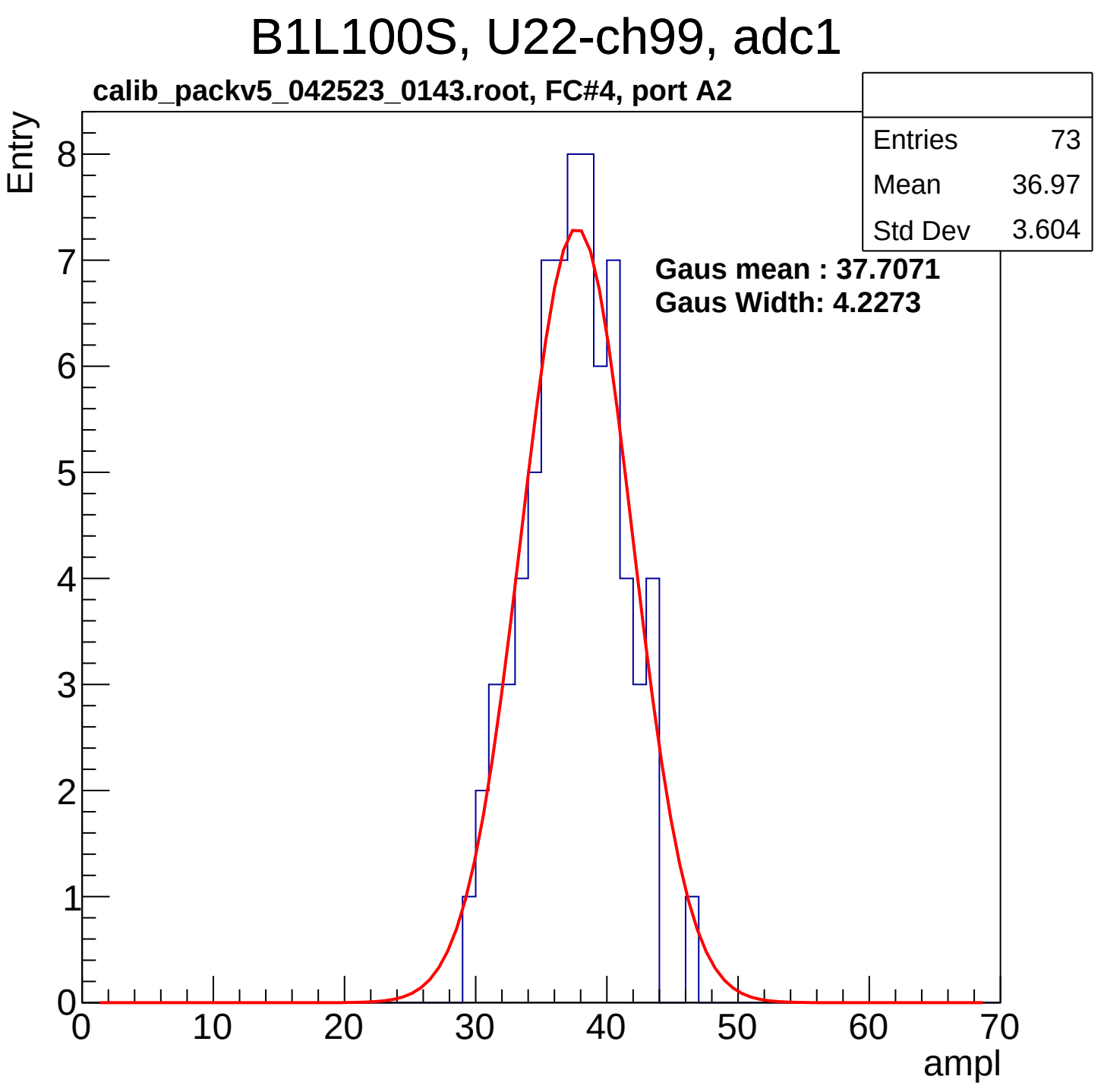
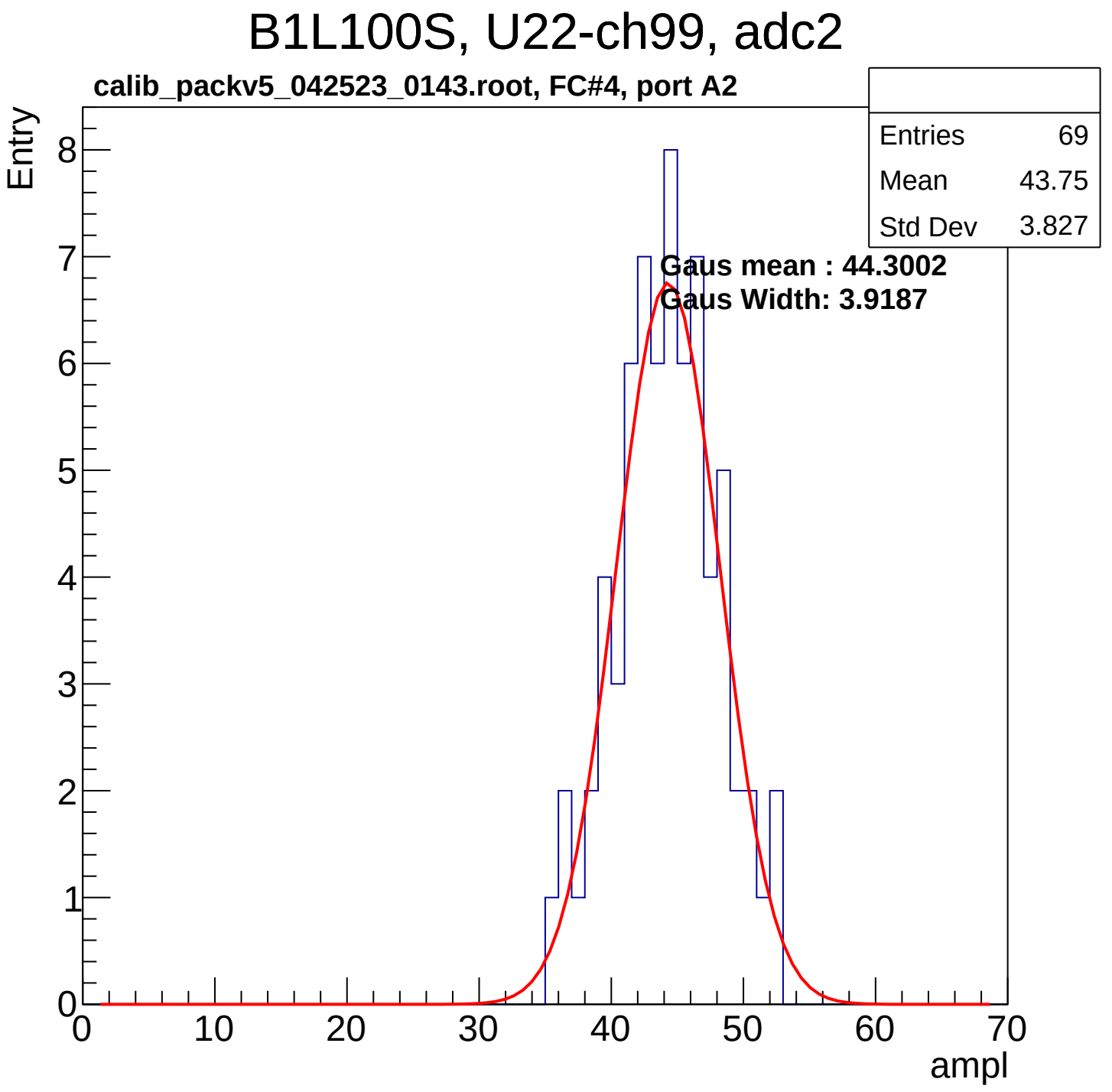
20
30
40
50
60
70

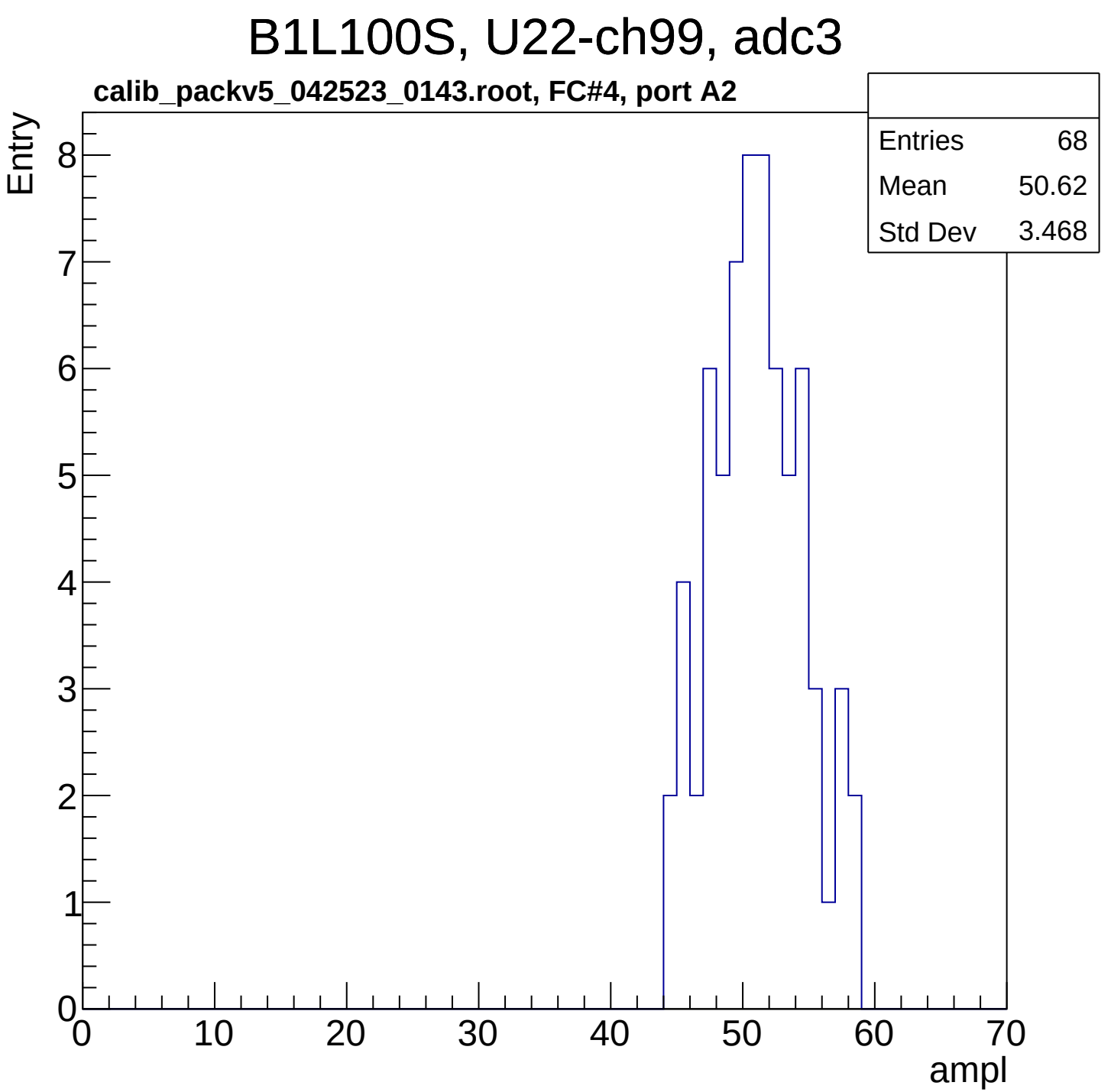

B1L100S, U22-ch98, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 54.5
Std Dev 18.29
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch98, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch99, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 29.15
Std Dev 3.39
Gaus mean : 29.7942
Gaus Width: 3.4497
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch99, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 36.97
Std Dev 3.604
Gaus mean : 37.7071
Gaus Width: 4.2273
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch99, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 43.75
Std Dev 3.827
Gaus mean : 44.3002
Gaus Width: 3.9187
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch99, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 50.62
Std Dev 3.468
Entry
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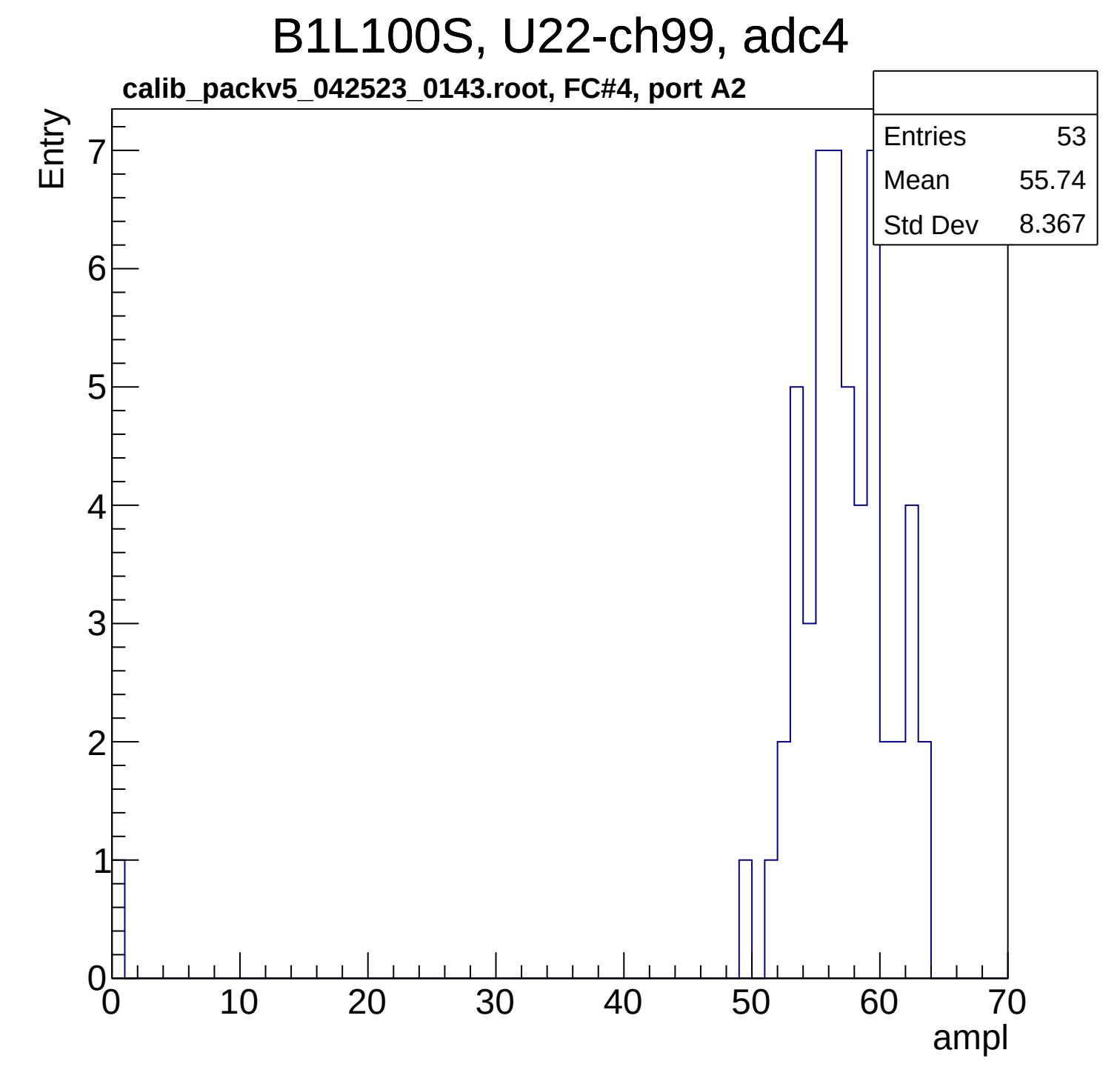
3
4
5
6
7
8
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch99, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 55.74
Std Dev 8.367
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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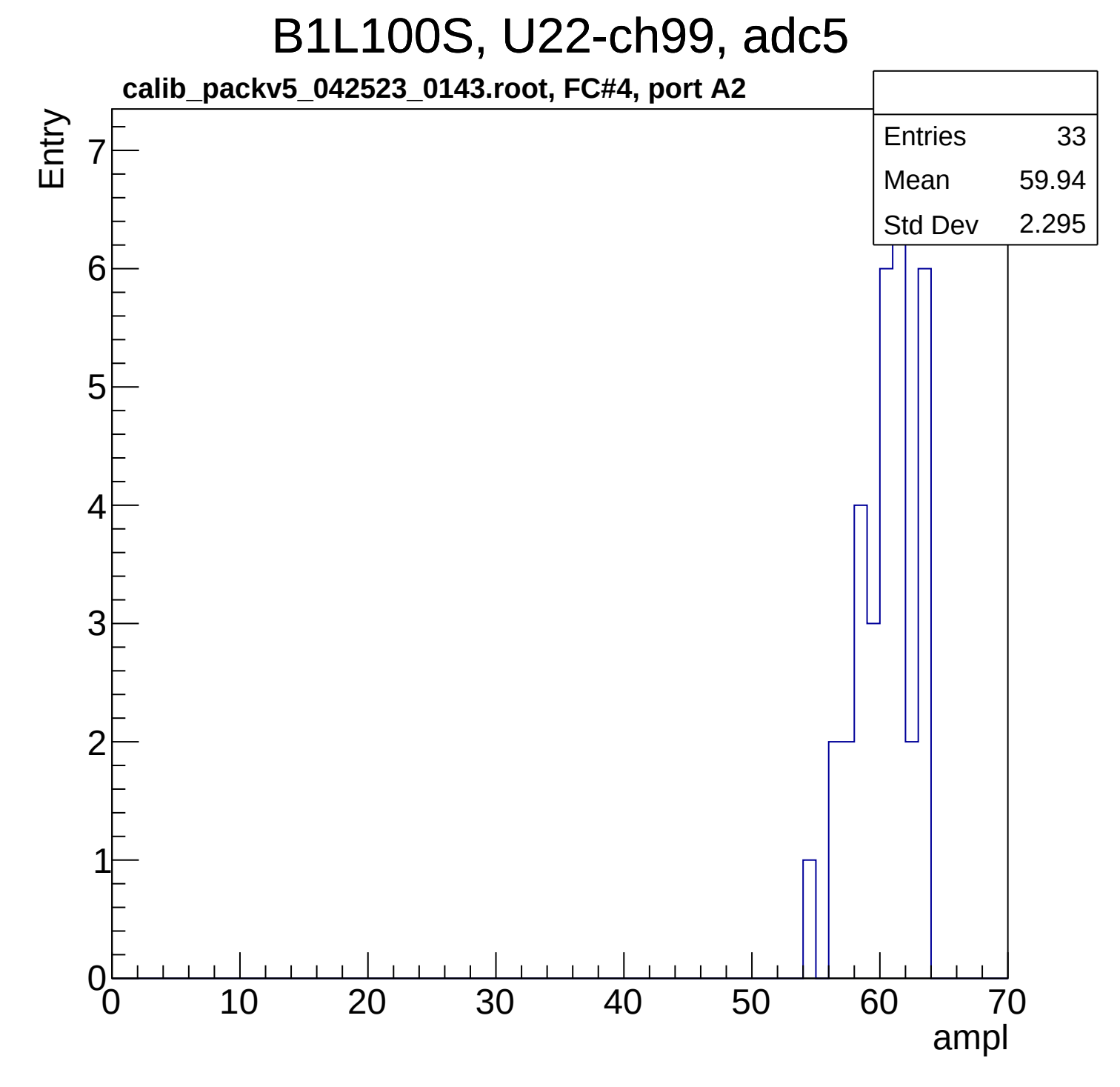
40
50
60
70


B1L100S, U22-ch99, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 59.94
Std Dev 2.295
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
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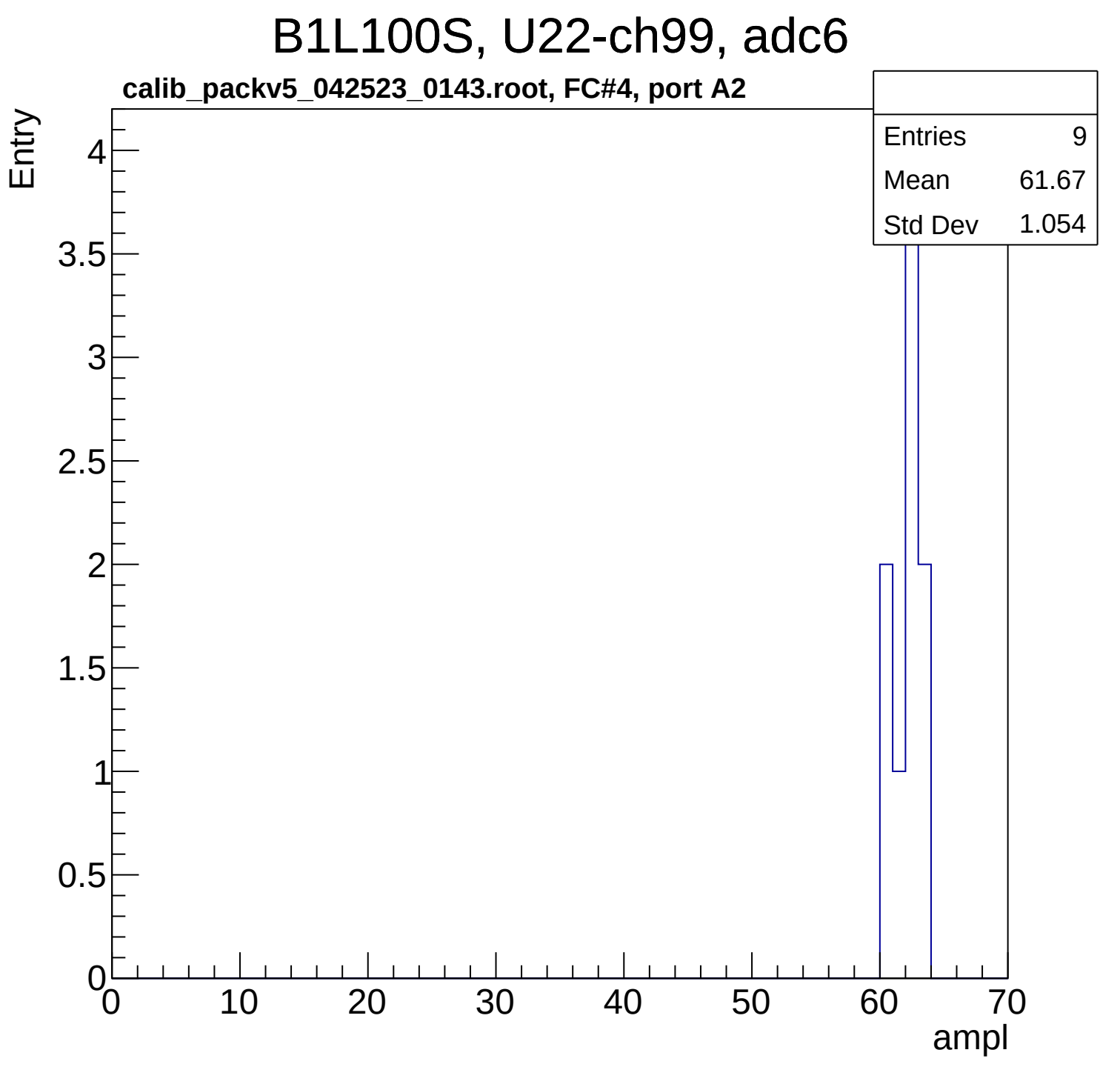
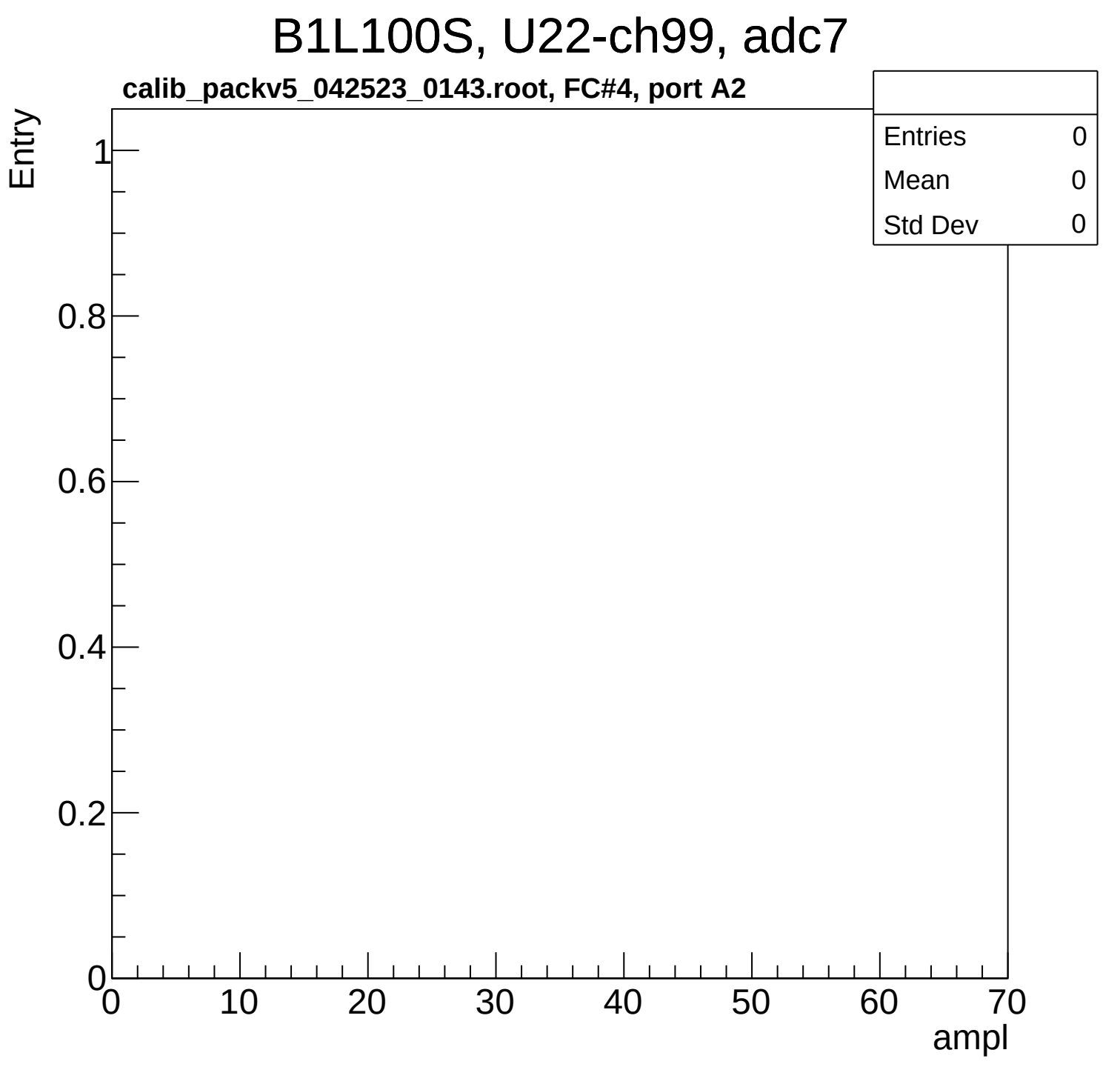
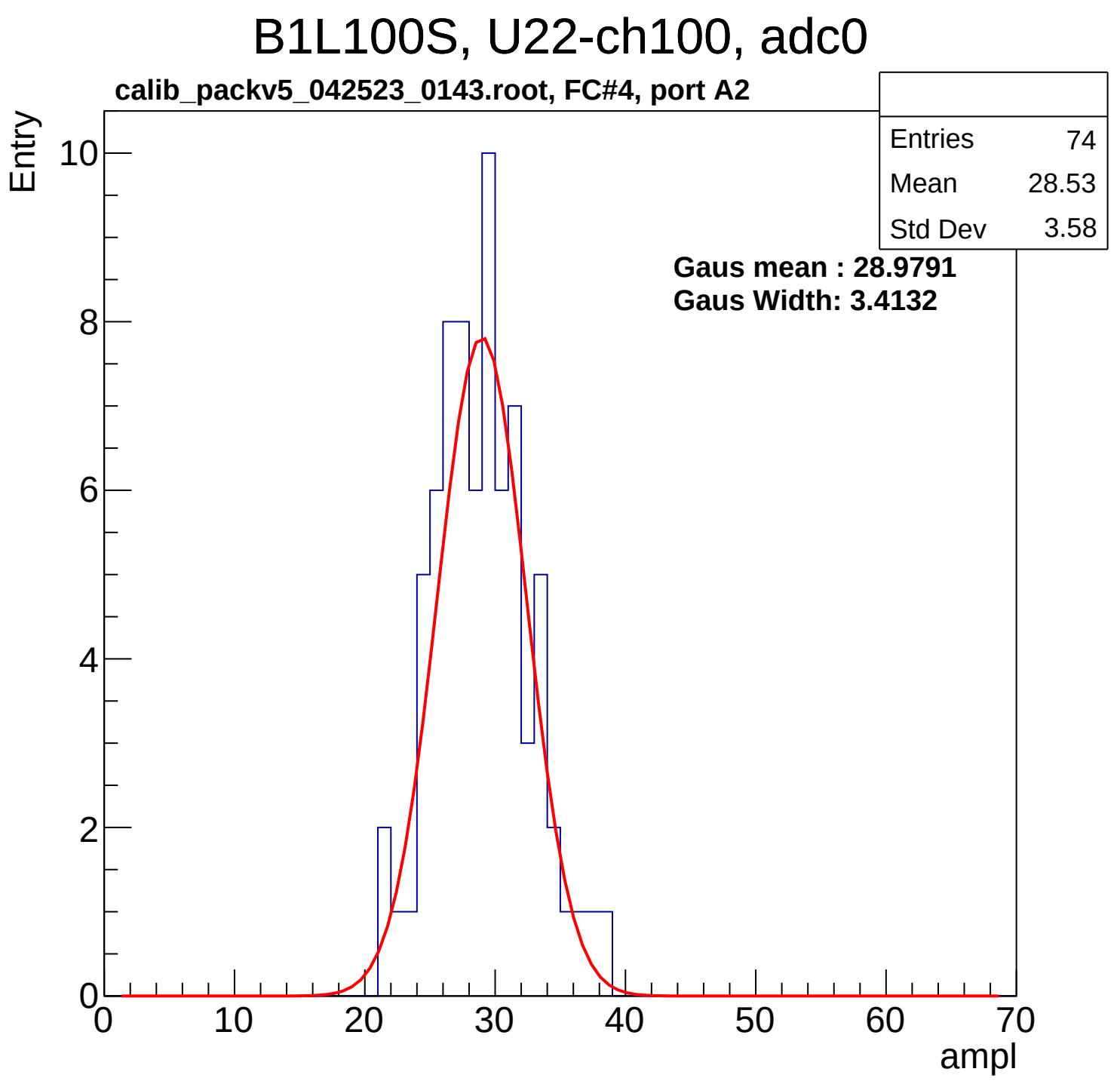
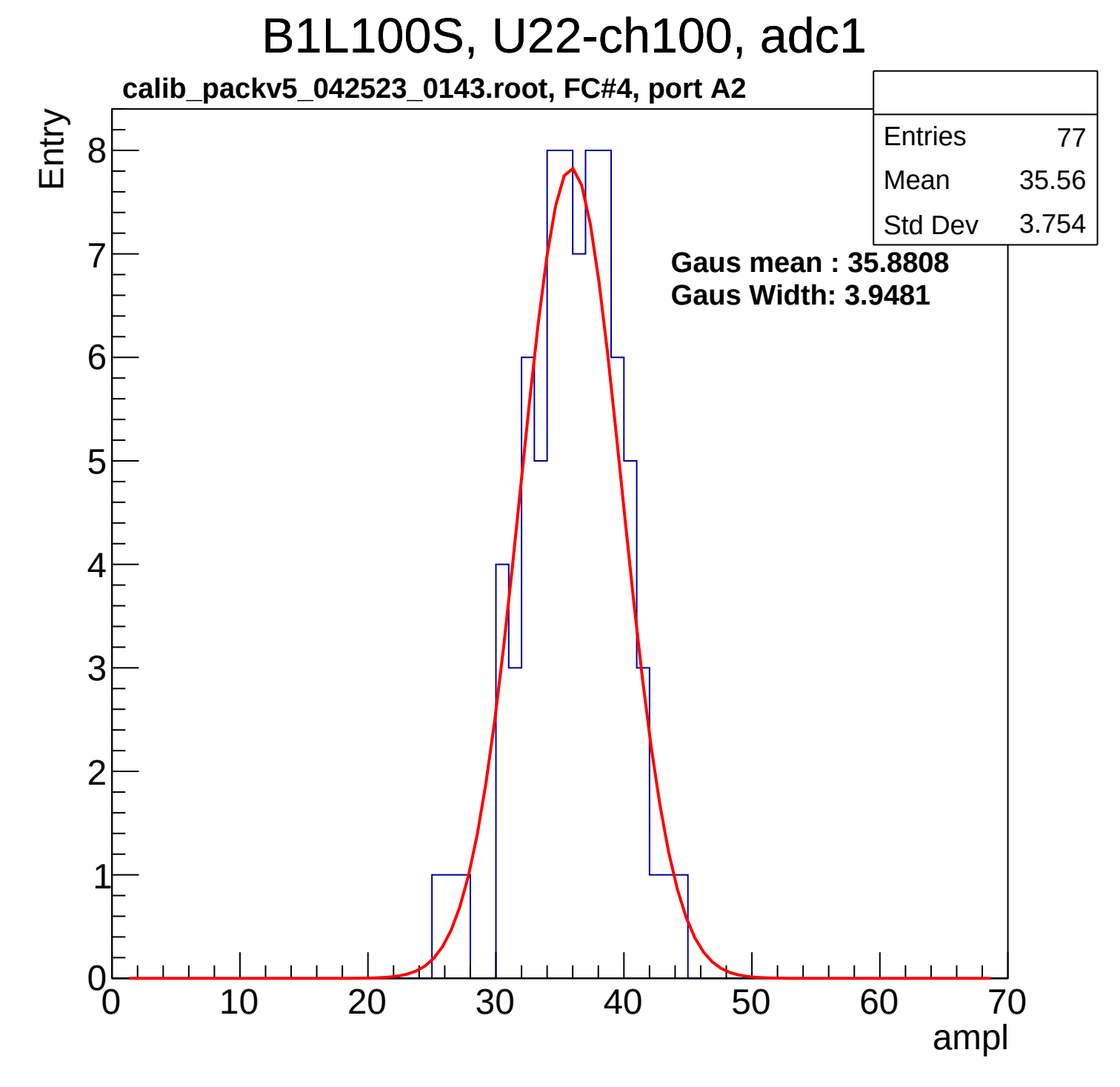
40
50
60
70


B1L100S, U22-ch99, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.67
Std Dev 1.054
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch99, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch100, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 28.53
Std Dev 3.58
Gaus mean : 28.9791
Gaus Width: 3.4132
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch100, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 35.56
Std Dev 3.754
Gaus mean : 35.8808
Gaus Width: 3.9481
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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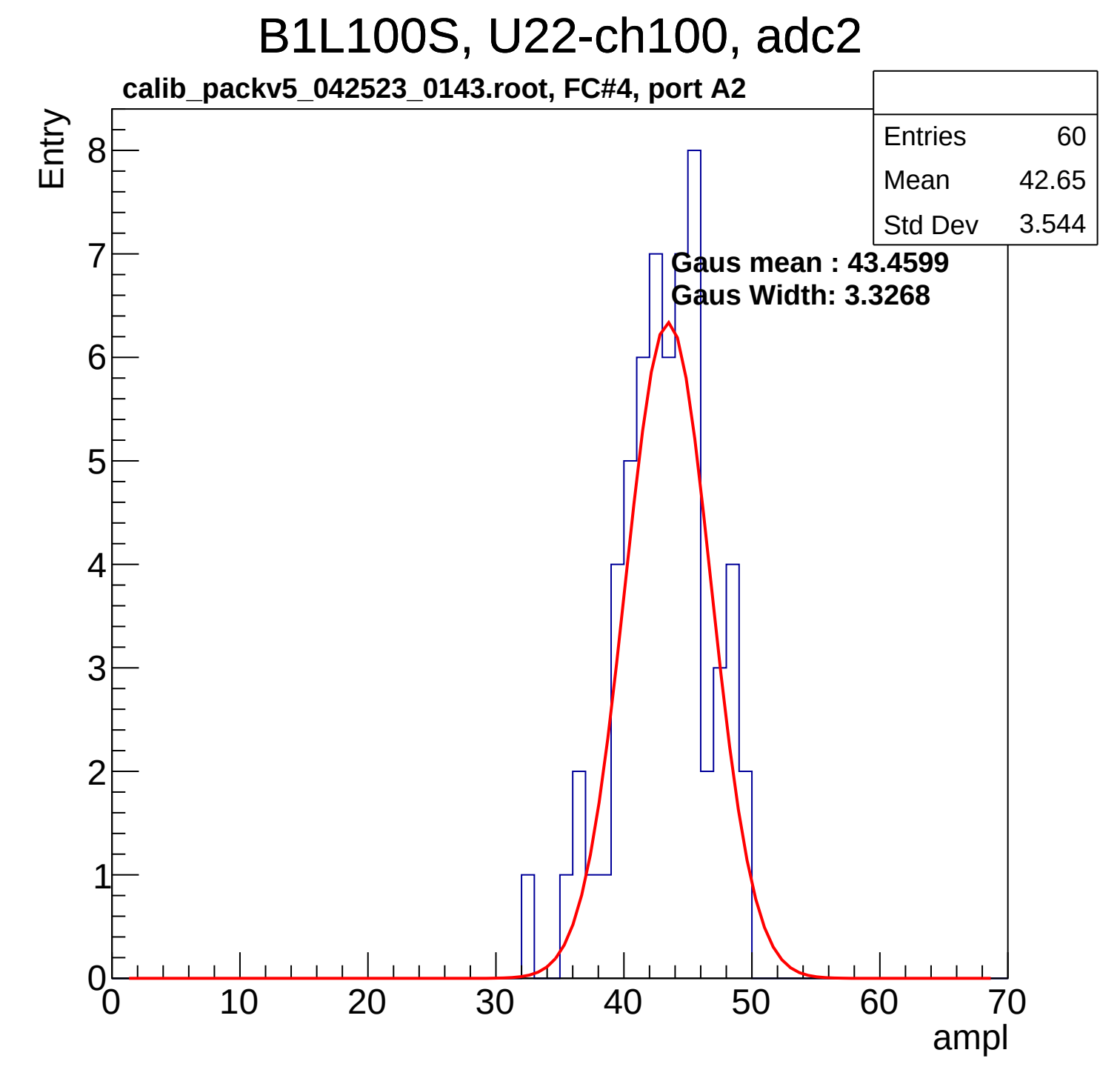
B1L100S, U22-ch100, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 42.65
Std Dev 3.544
Gaus mean : 43.4599
Gaus Width: 3.3268
Entry
ampl
0
1
2
3
4
5
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0
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60
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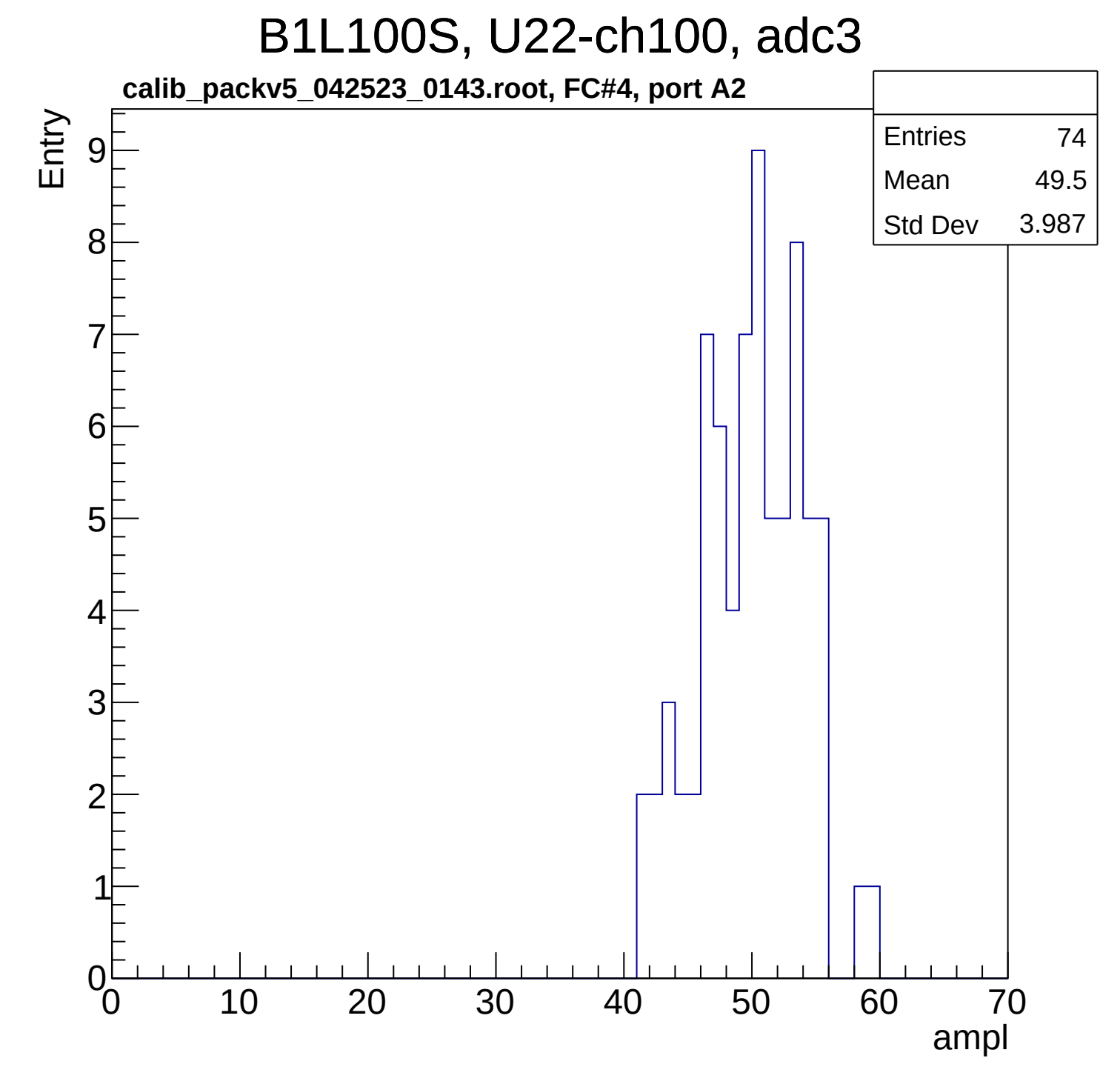
B1L100S, U22-ch100, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 49.5
Std Dev 3.987
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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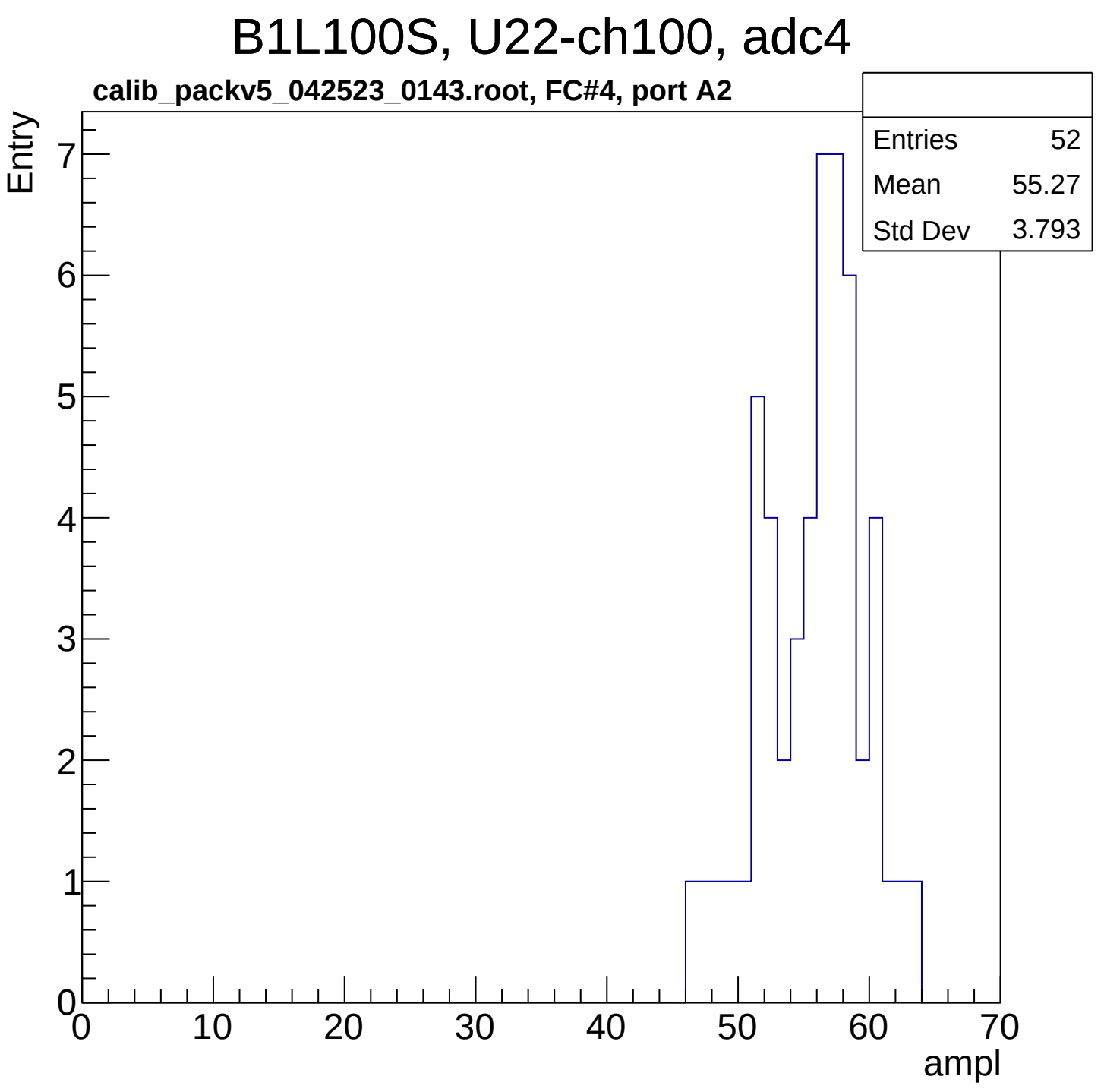
B1L100S, U22-ch100, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.27
Std Dev 3.793
Entry
ampl
0
1
2
3
4
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6
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0
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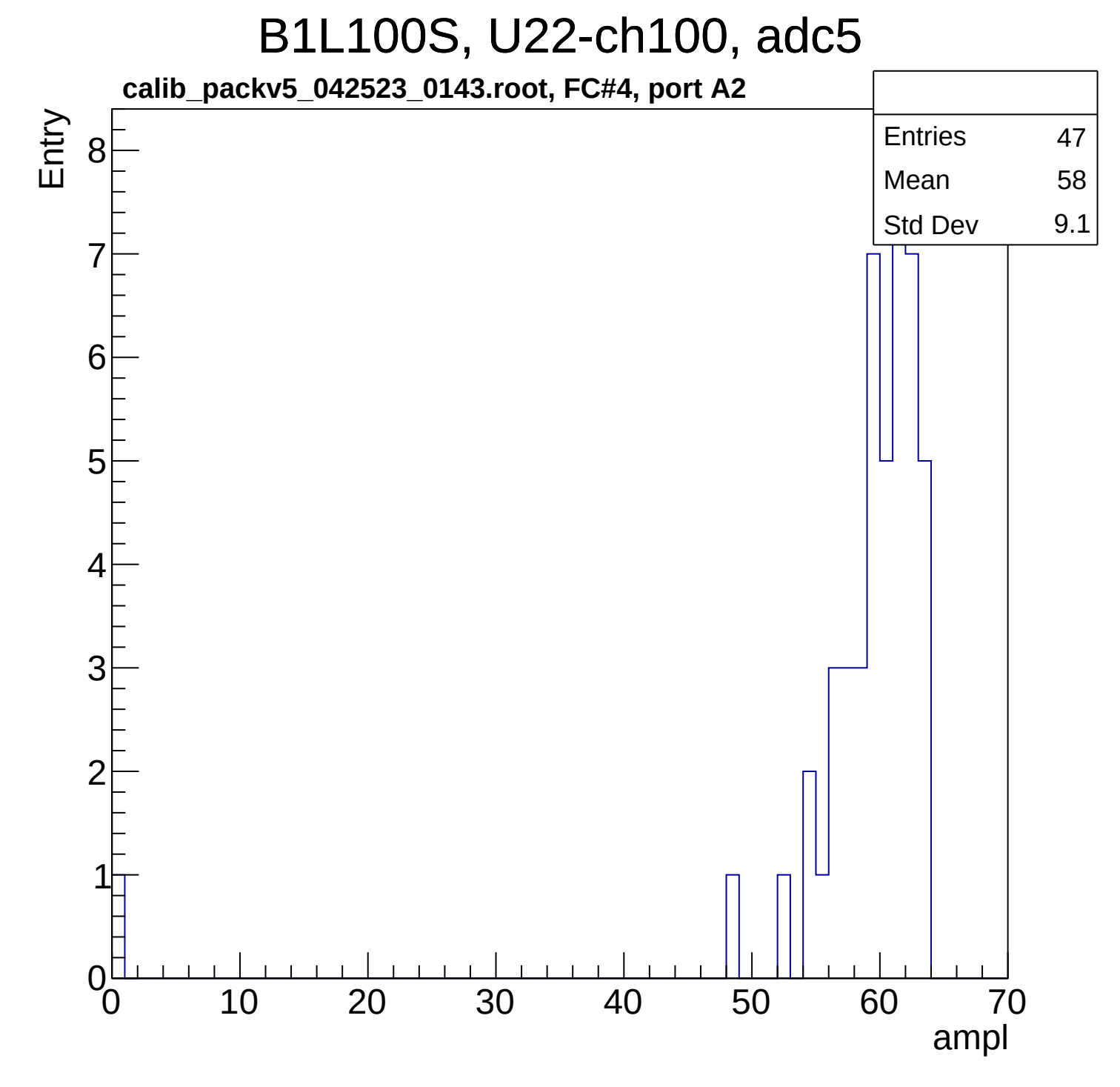
B1L100S, U22-ch100, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 58
Std Dev 9.1
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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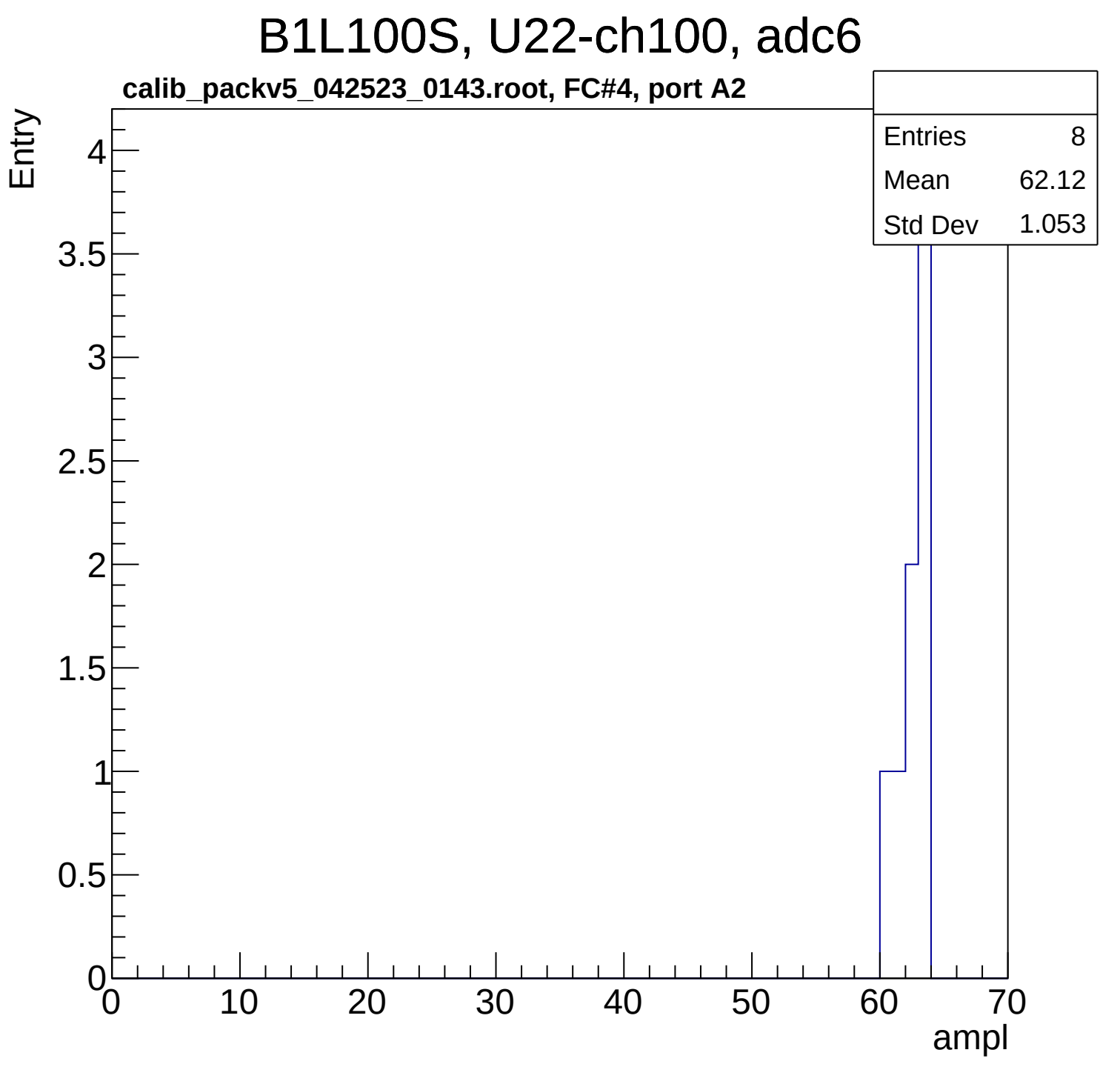
B1L100S, U22-ch100, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 62.12
Std Dev 1.053
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
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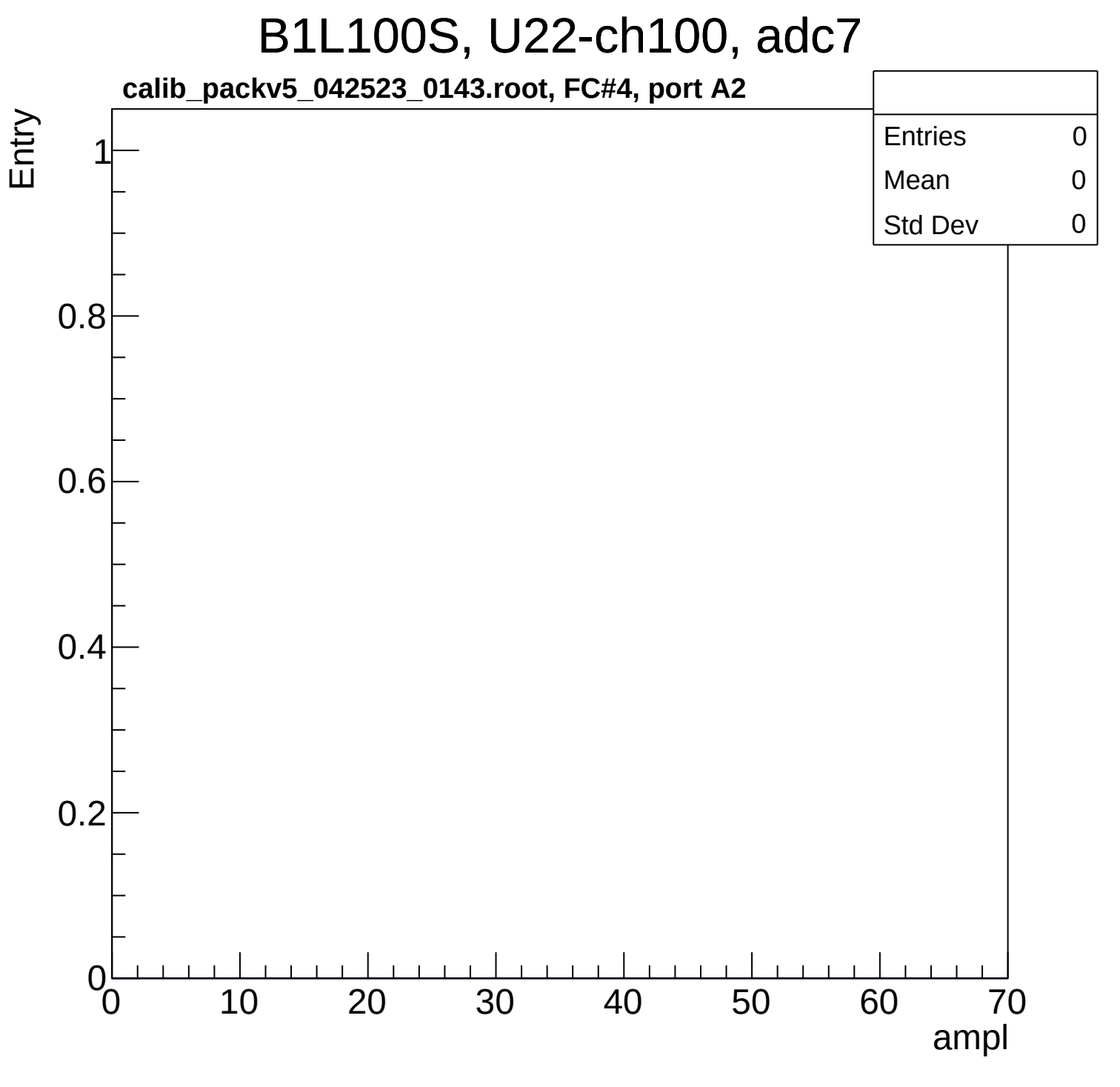
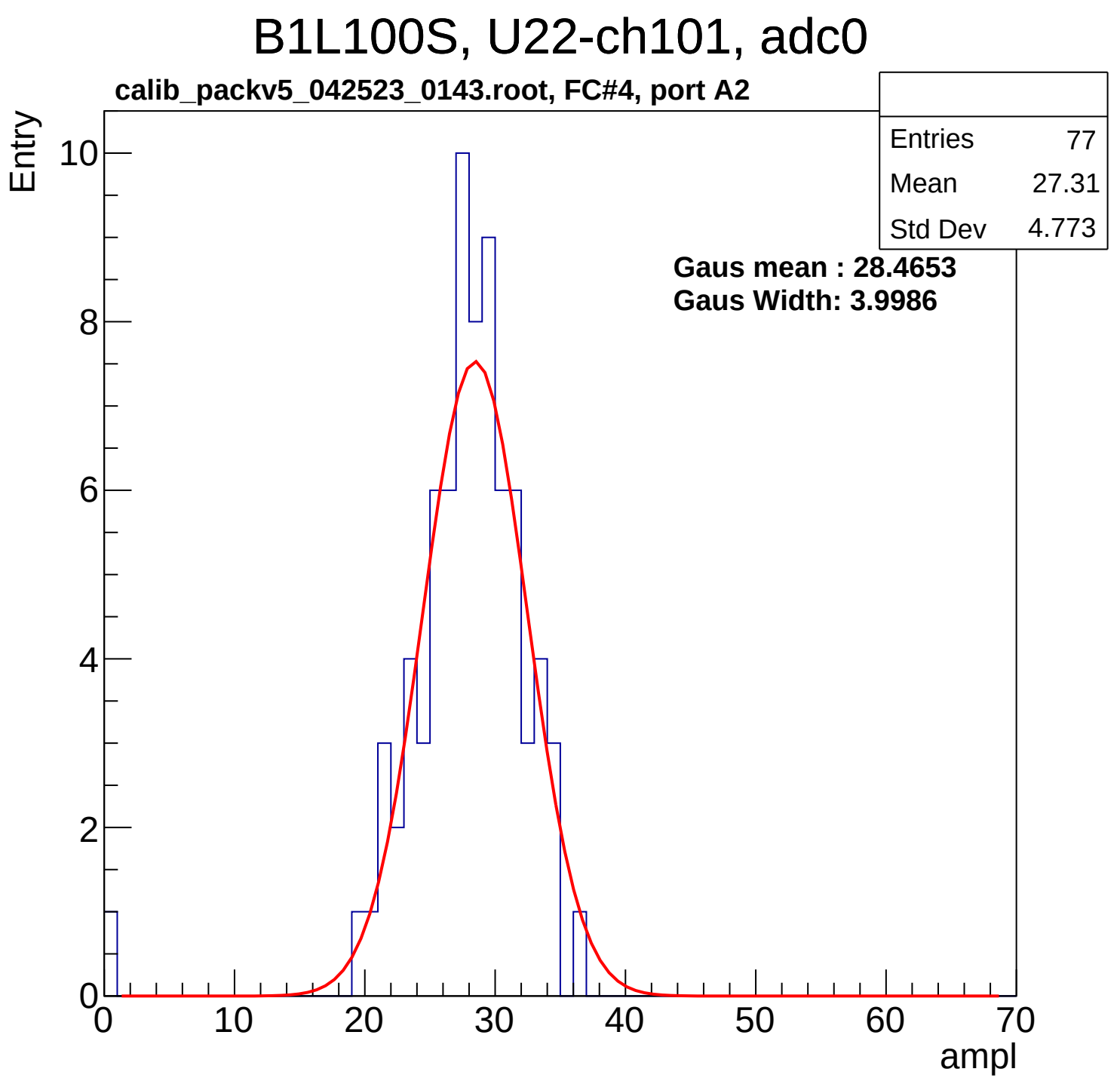

B1L100S, U22-ch100, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch101, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 27.31
Std Dev 4.773
Gaus mean : 28.4653
Gaus Width: 3.9986
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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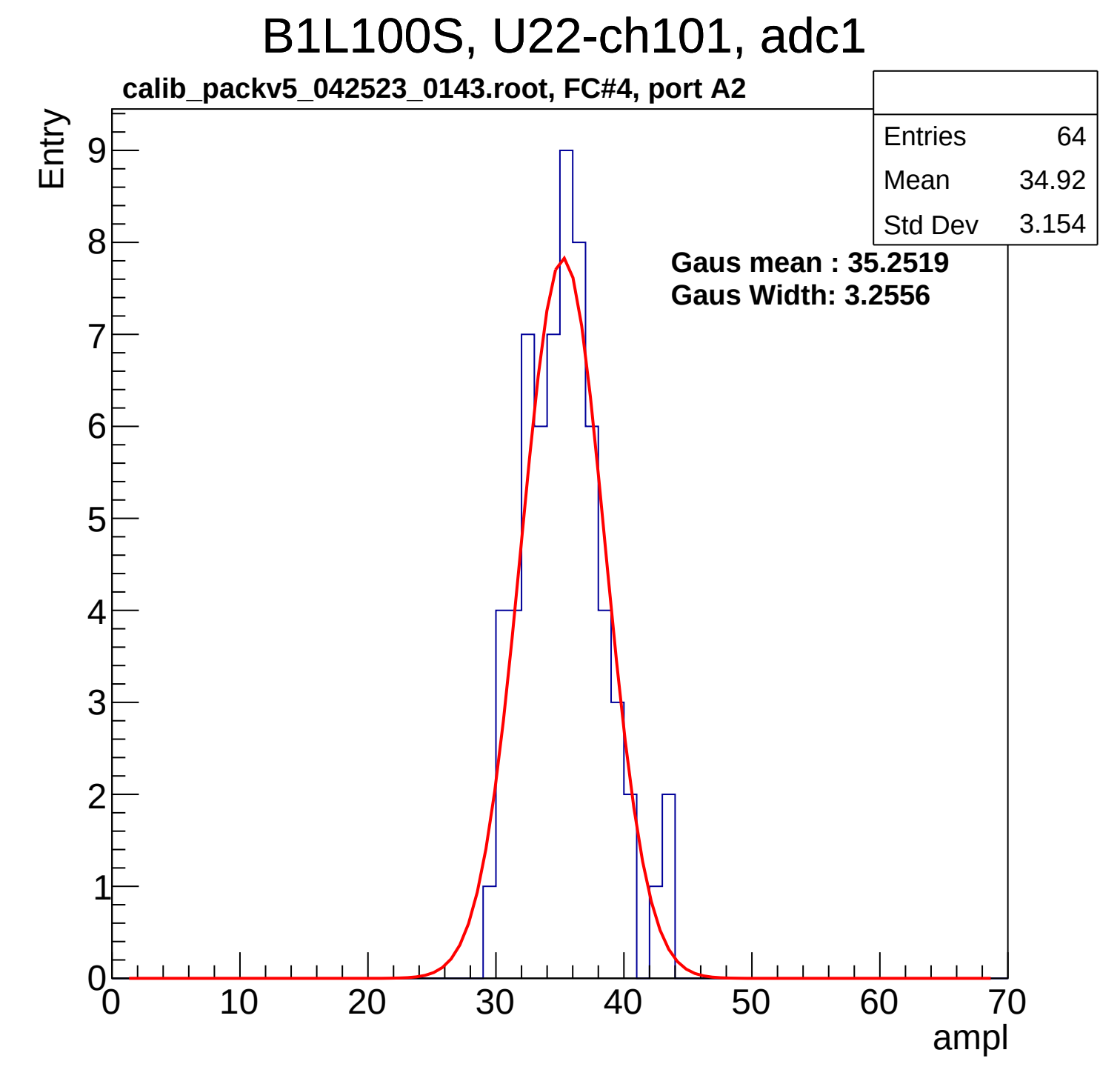
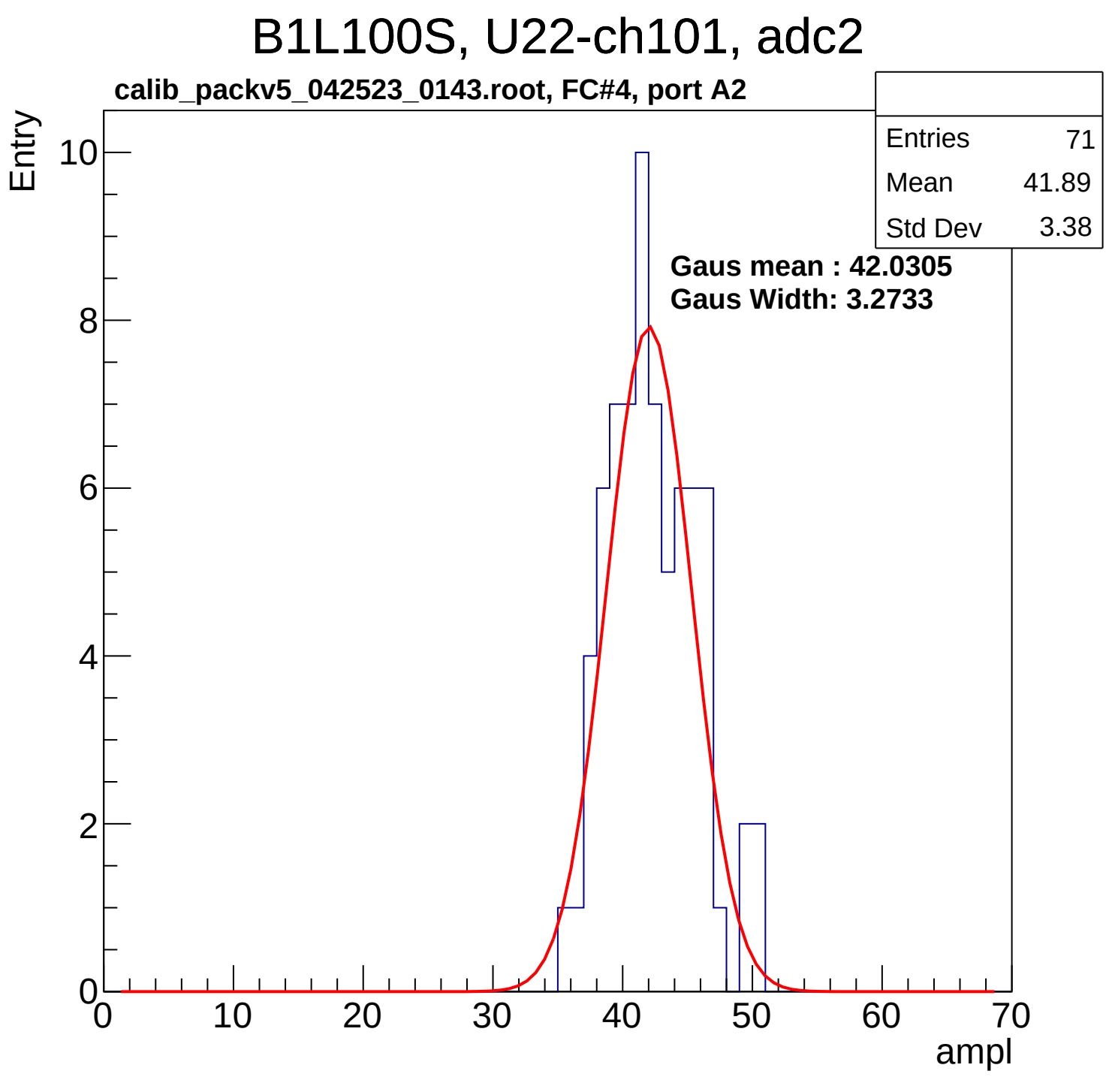

B1L100S, U22-ch101, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 34.92
Std Dev 3.154
Gaus mean : 35.2519
Gaus Width: 3.2556
Entry
ampl
0
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch101, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 41.89
Std Dev 3.38
Gaus mean : 42.0305
Gaus Width: 3.2733
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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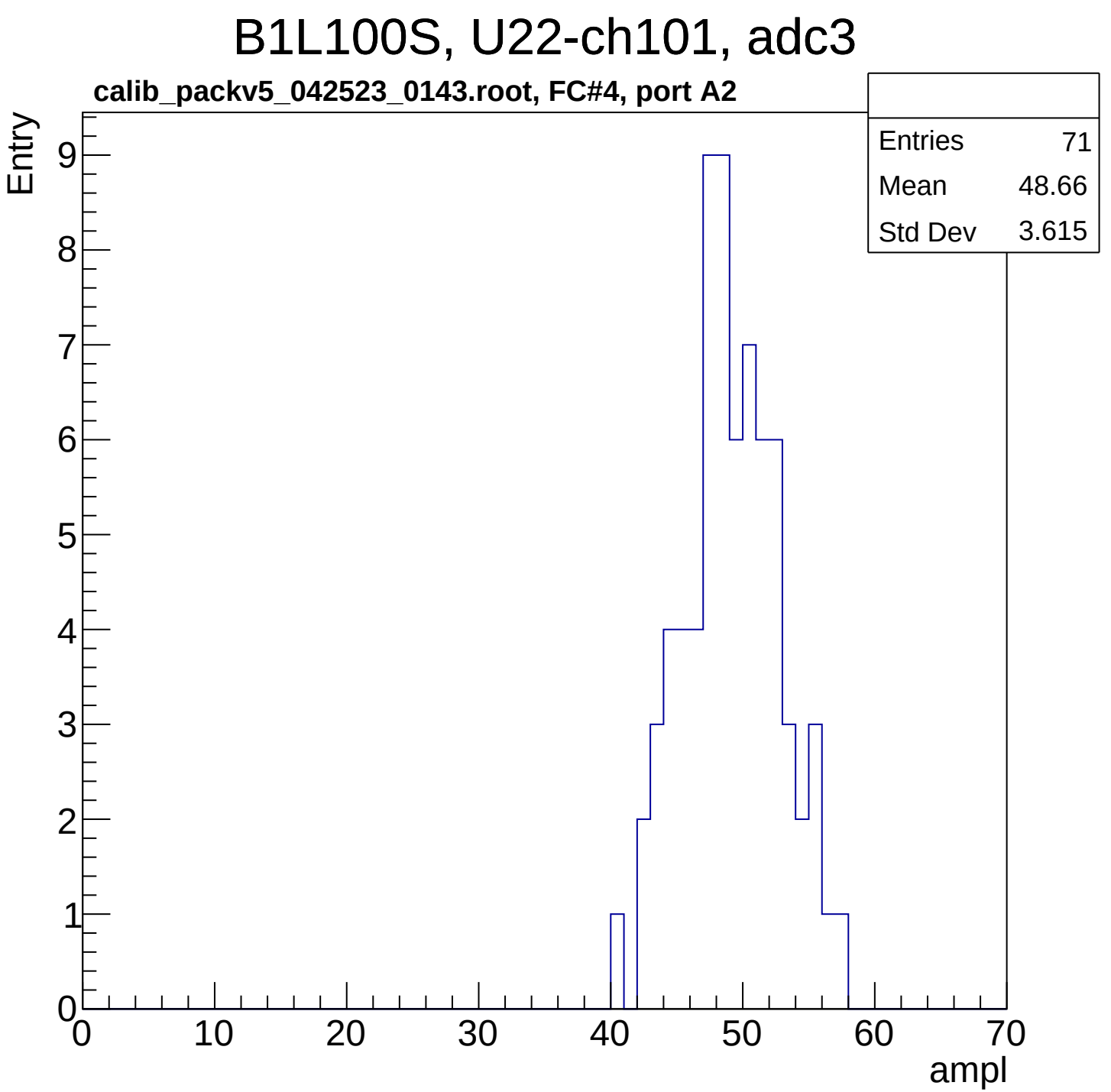
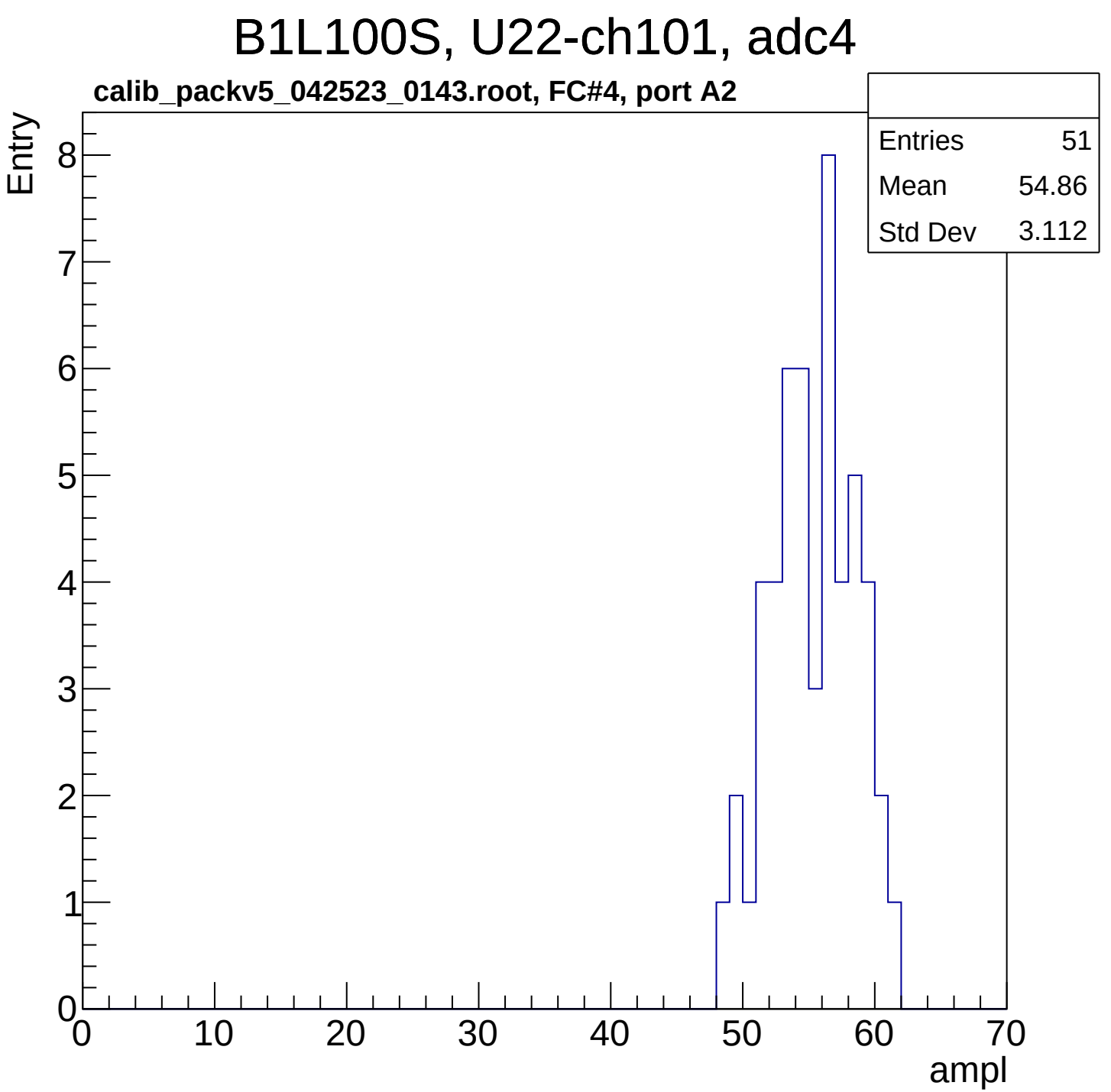
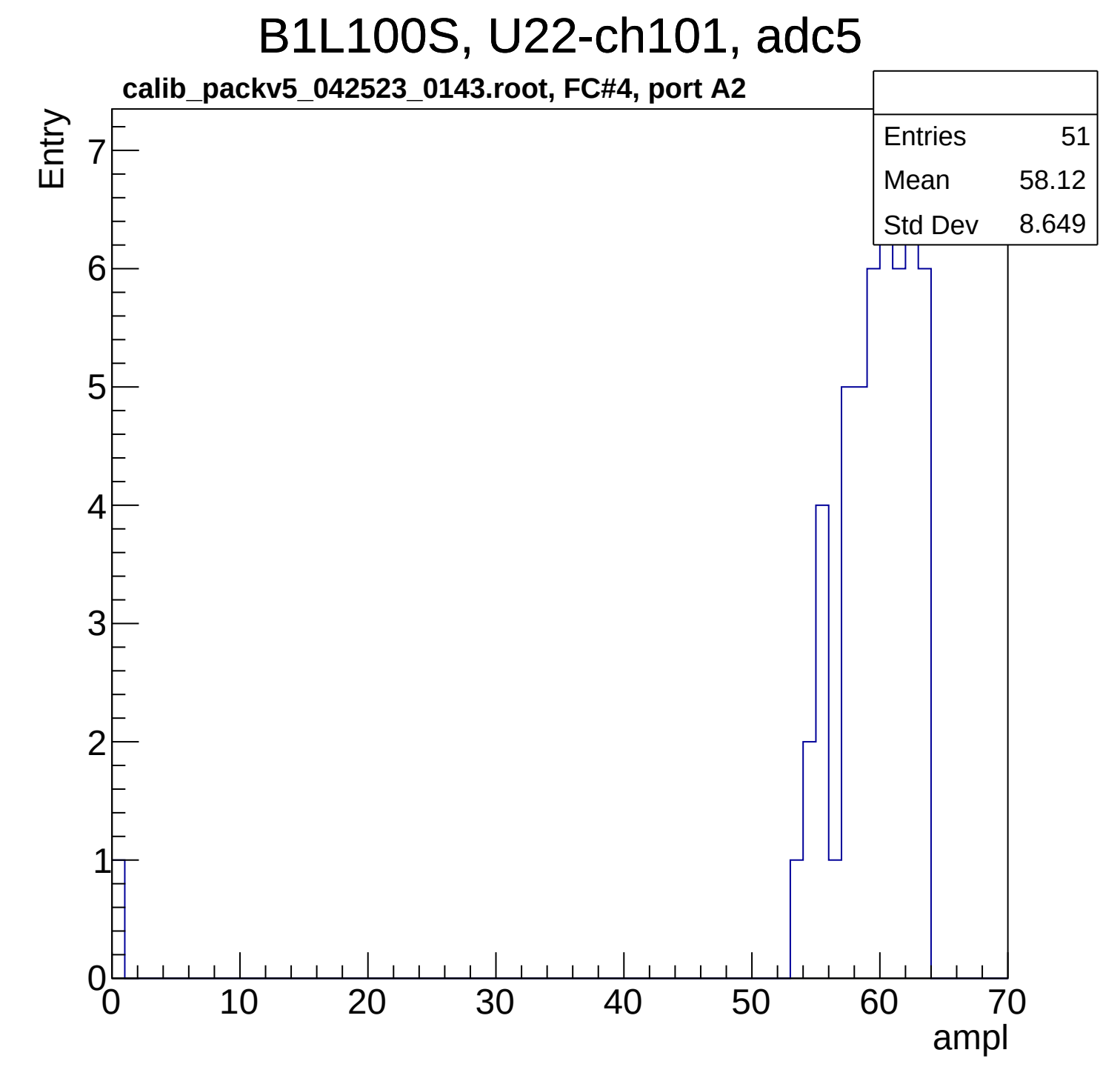

B1L100S, U22-ch101, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 48.66
Std Dev 3.615
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch101, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 54.86
Std Dev 3.112
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch101, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 58.12
Std Dev 8.649
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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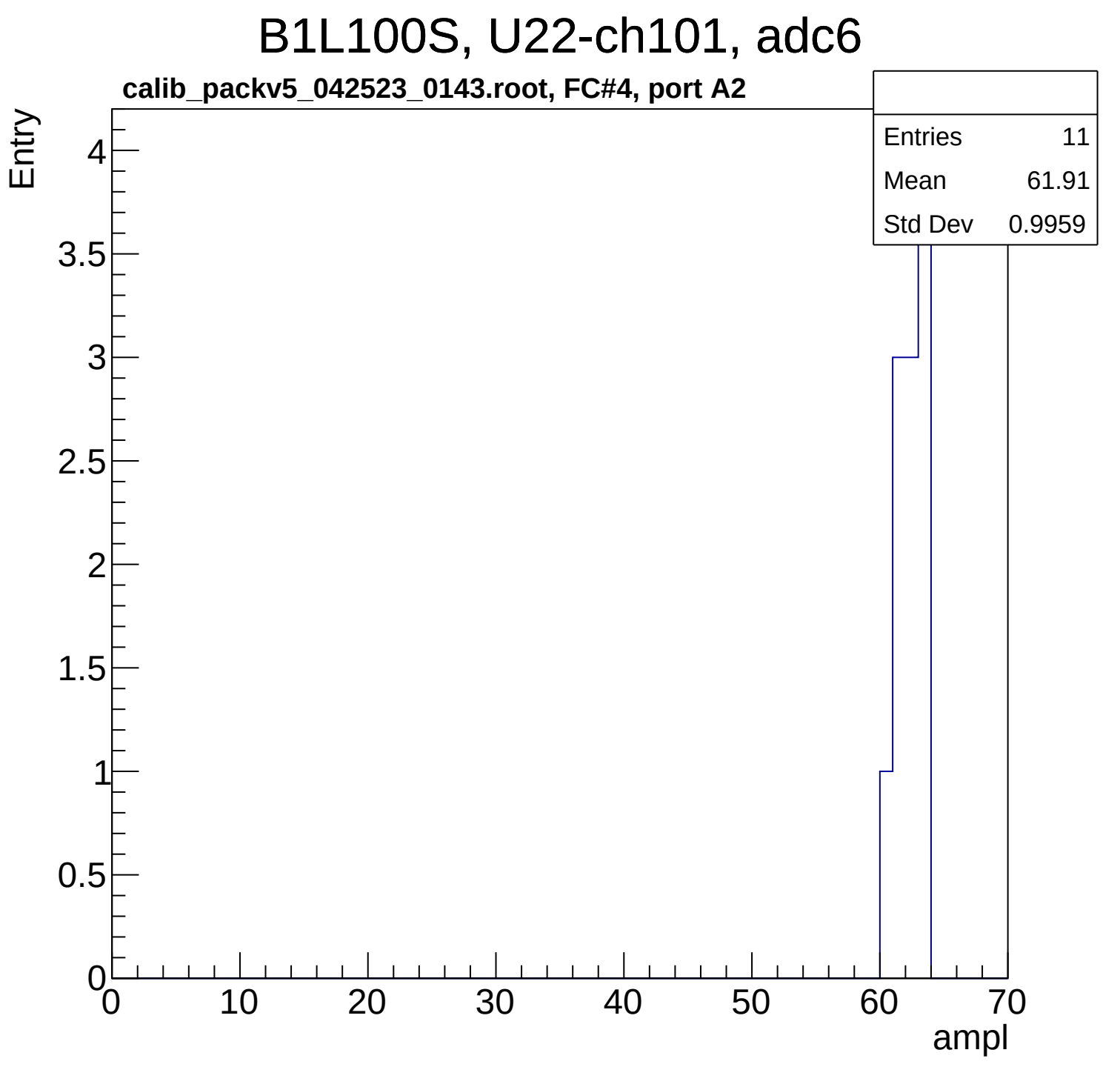
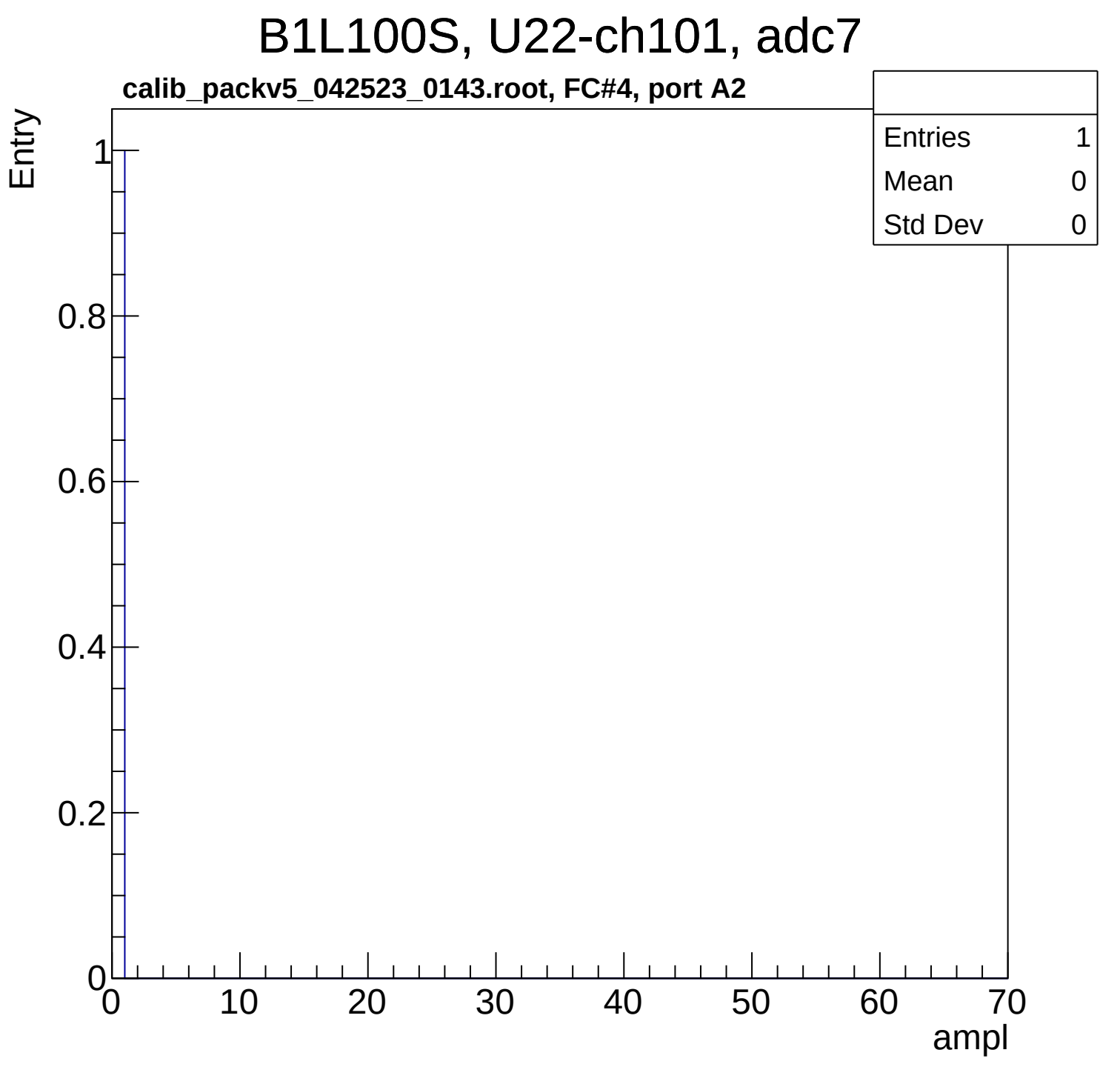
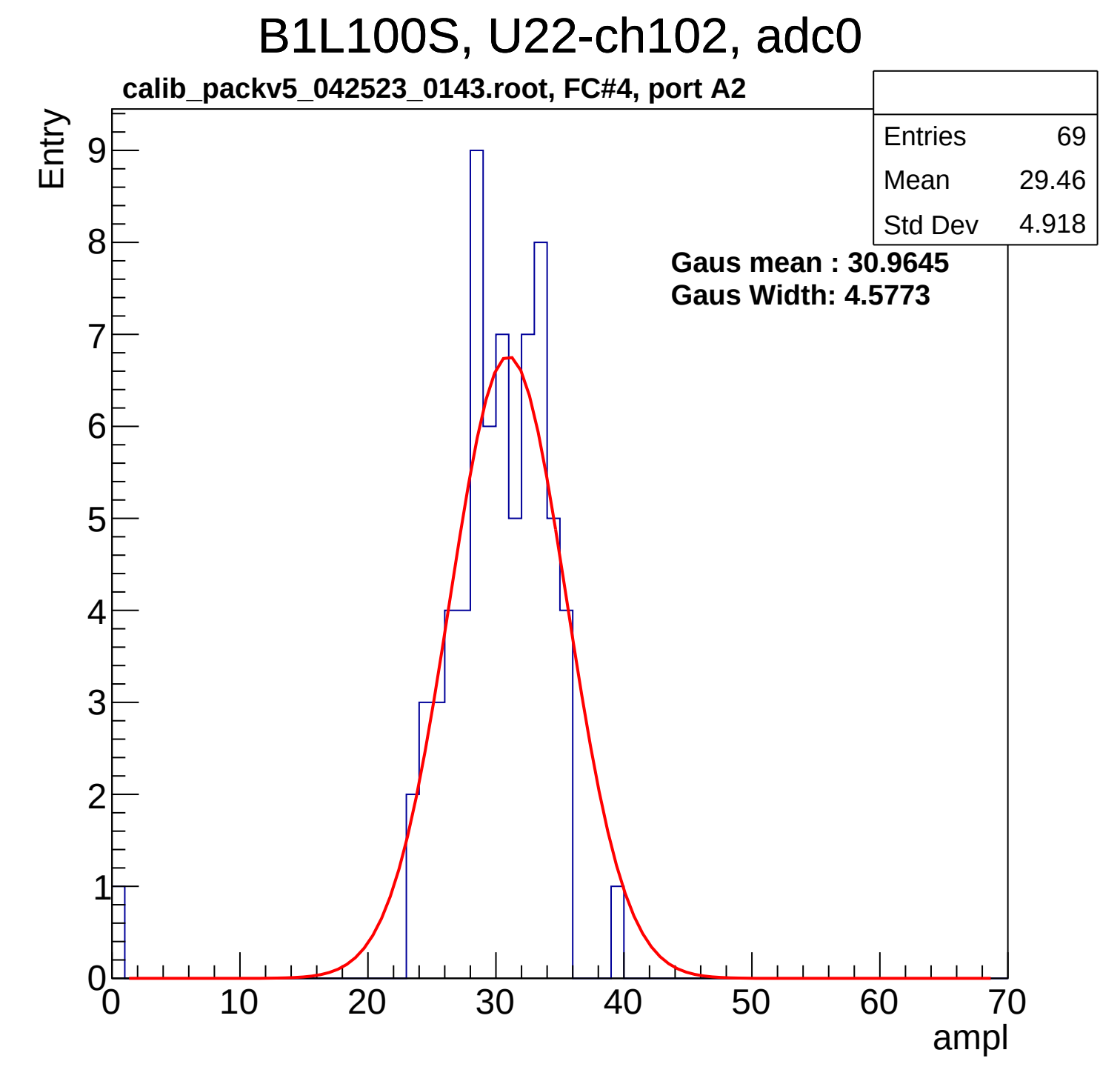
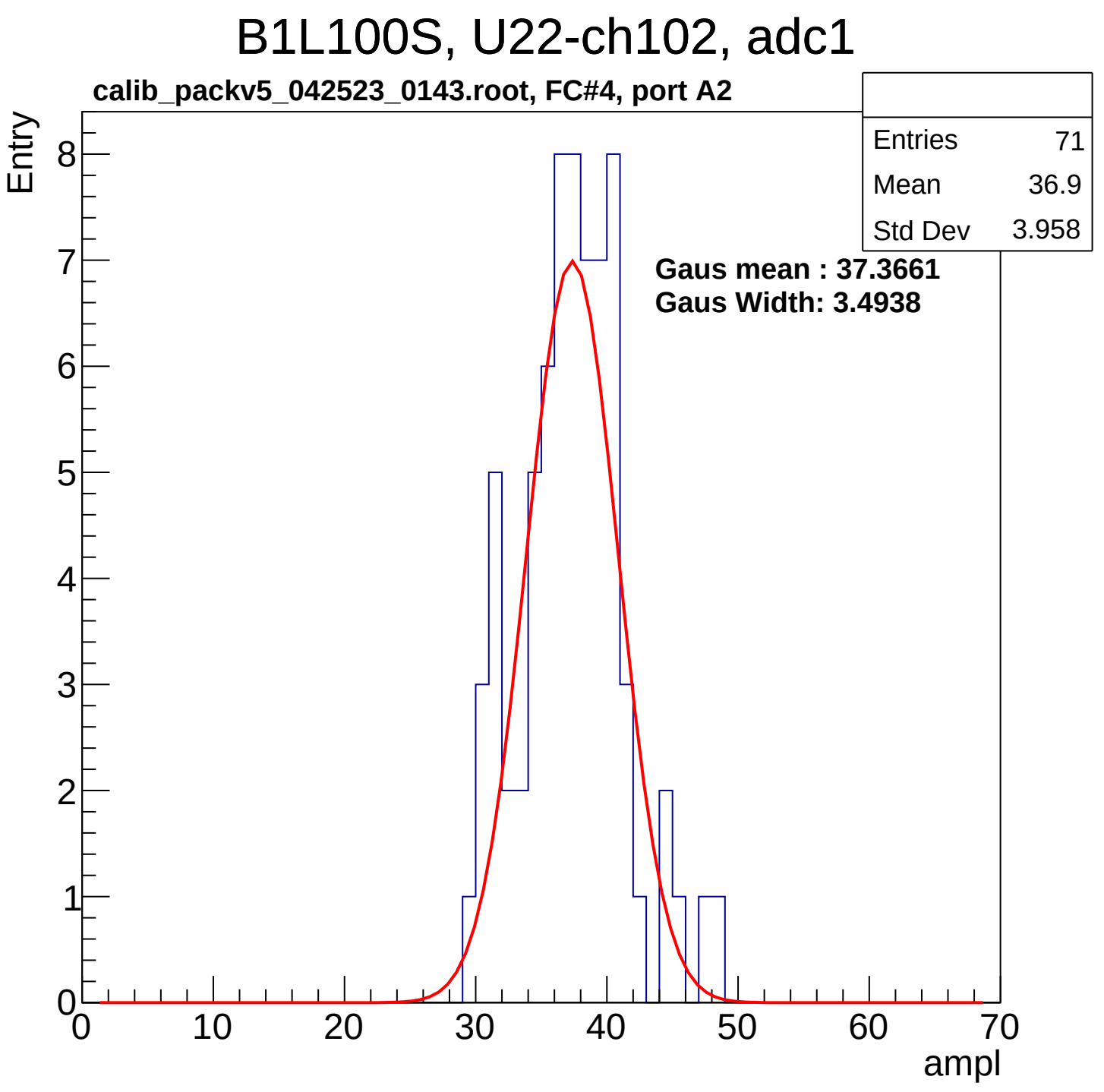
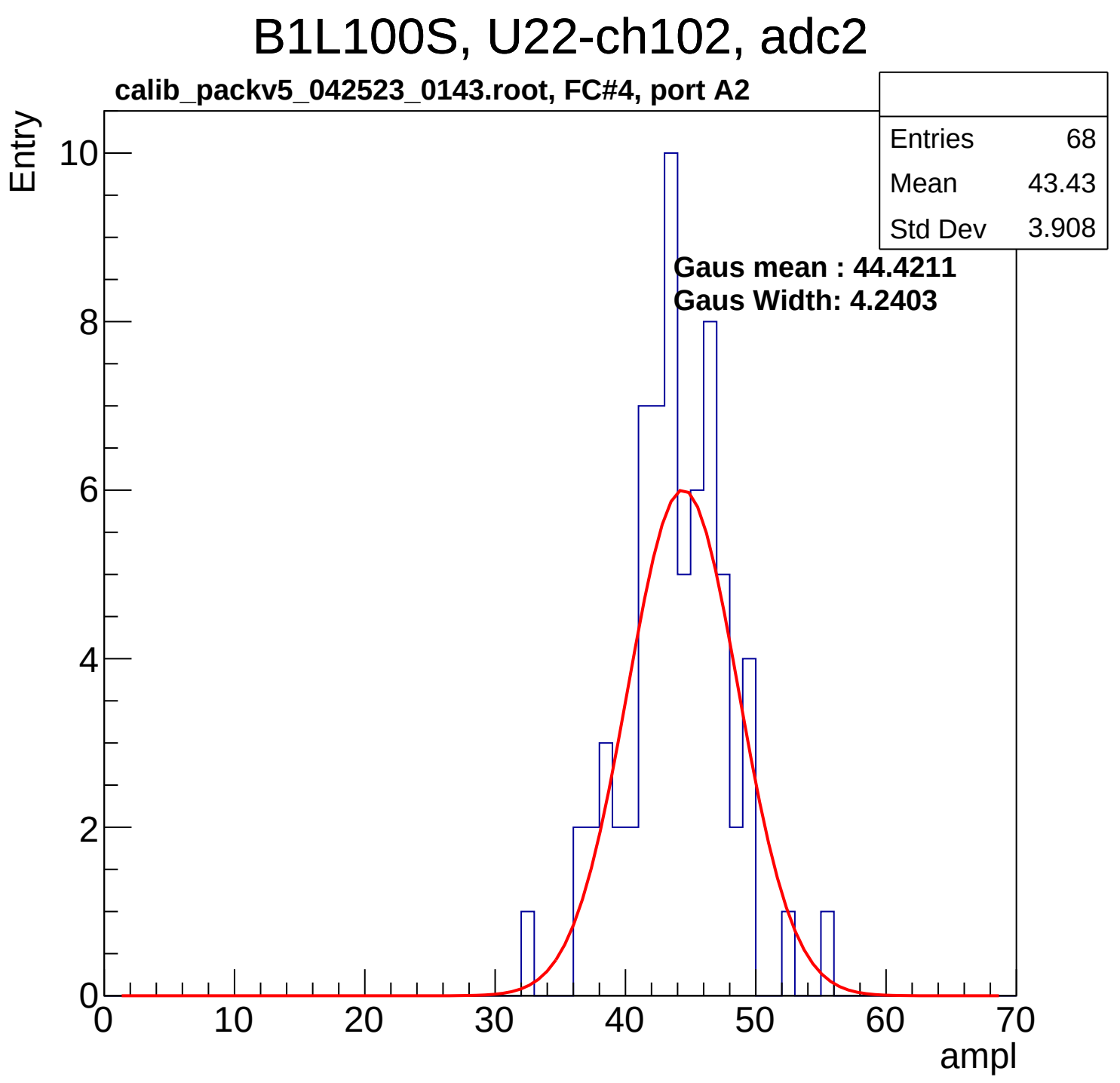
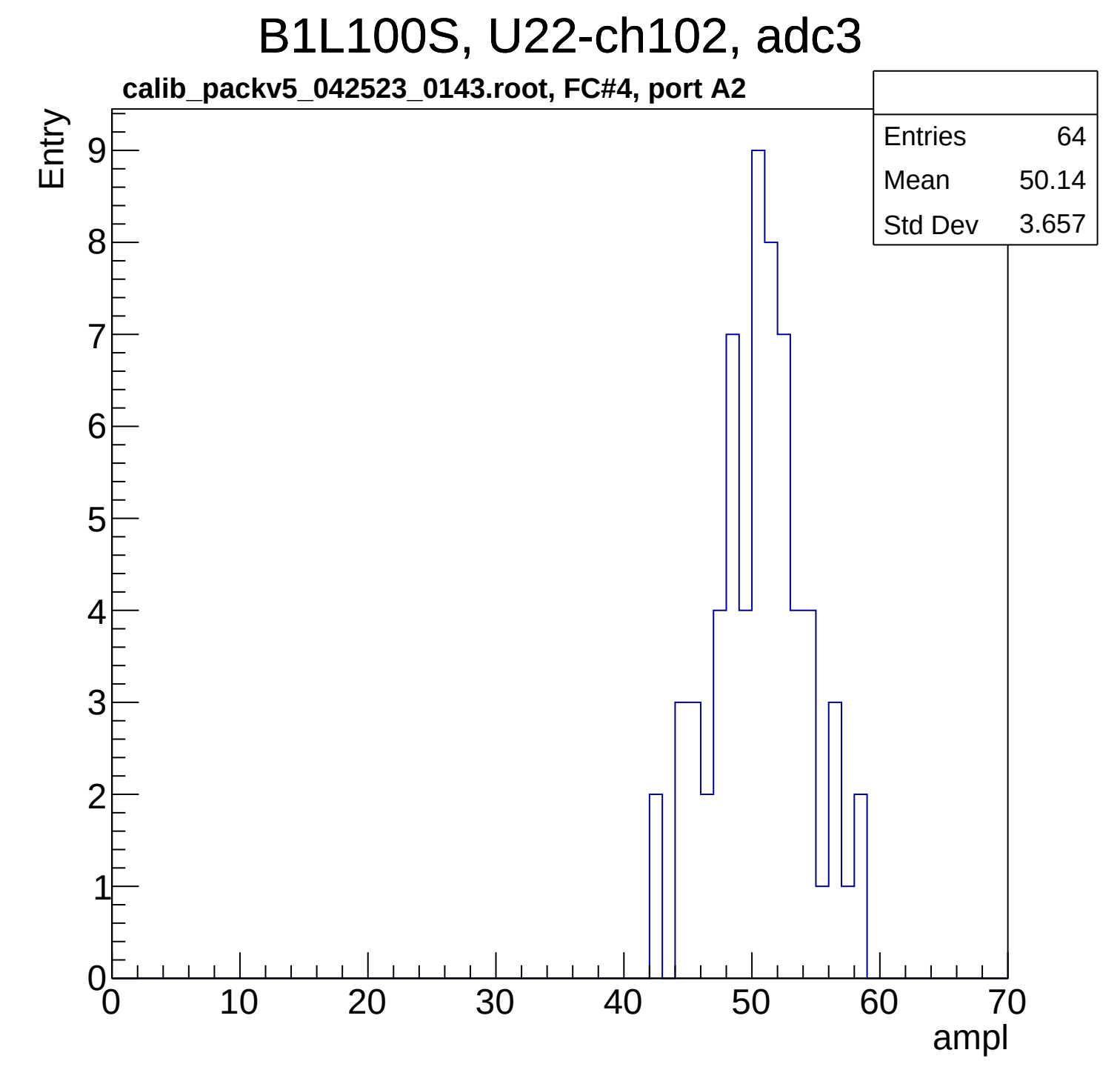

B1L100S, U22-ch101, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 61.91
Std Dev 0.9959
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70

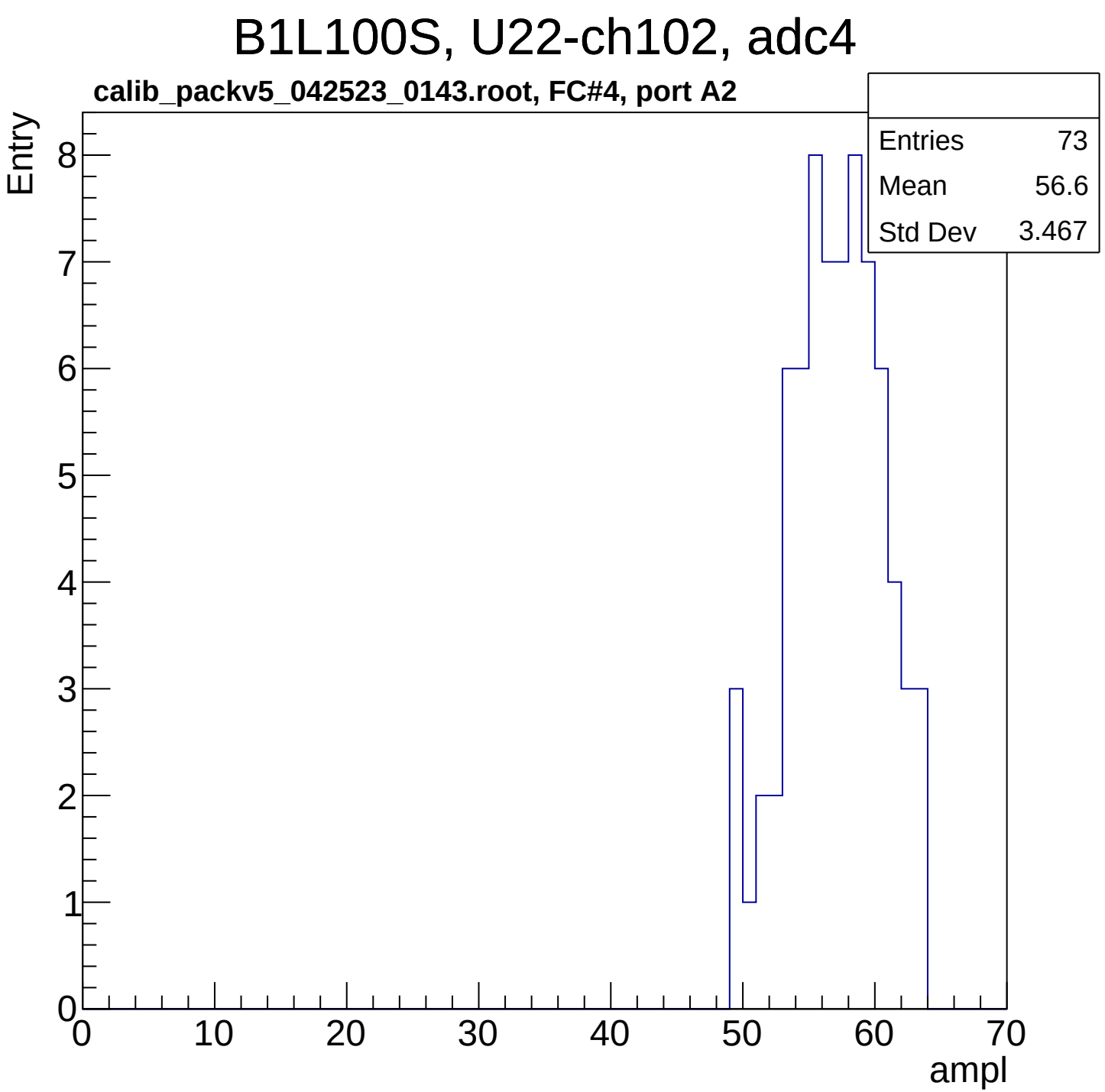

B1L100S, U22-ch101, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch102, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 29.46
Std Dev 4.918
Gaus mean : 30.9645
Gaus Width: 4.5773
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch102, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.9
Std Dev 3.958
Gaus mean : 37.3661
Gaus Width: 3.4938
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch102, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 43.43
Std Dev 3.908
Gaus mean : 44.4211
Gaus Width: 4.2403
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch102, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 50.14
Std Dev 3.657
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch102, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 56.6
Std Dev 3.467
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
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60
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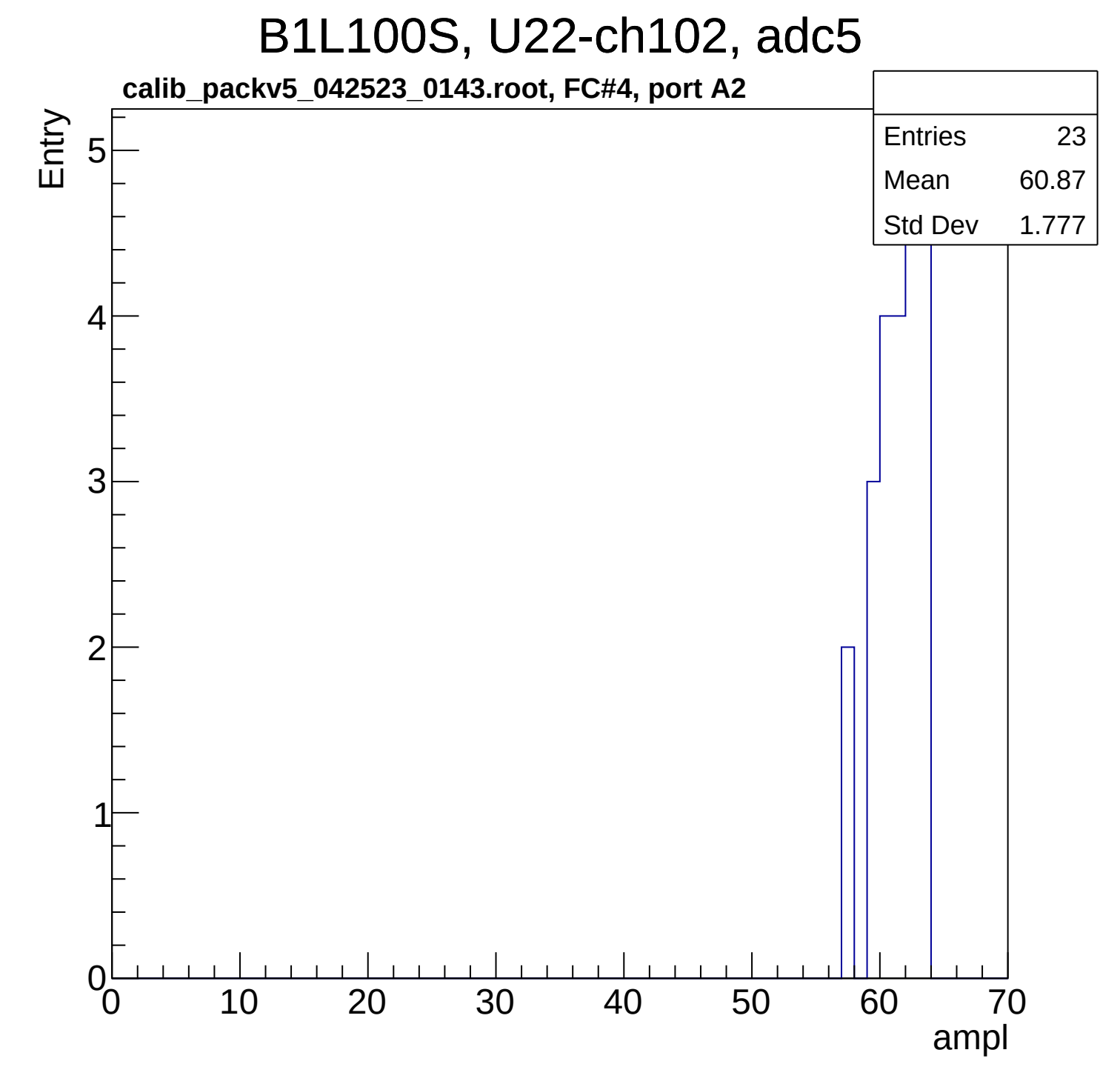
B1L100S, U22-ch102, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 60.87
Std Dev 1.777
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
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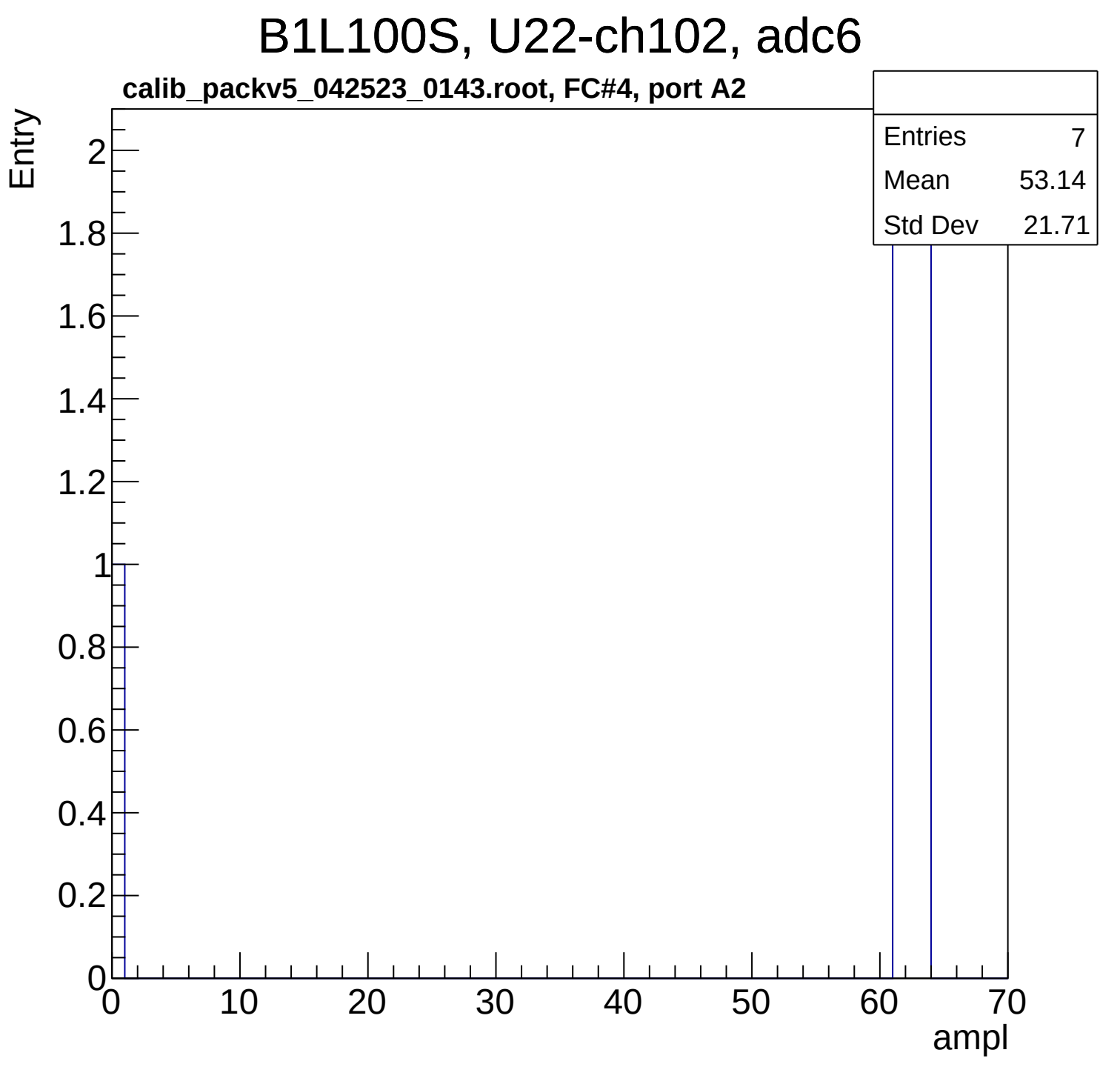
B1L100S, U22-ch102, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 53.14
Std Dev 21.71
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
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60
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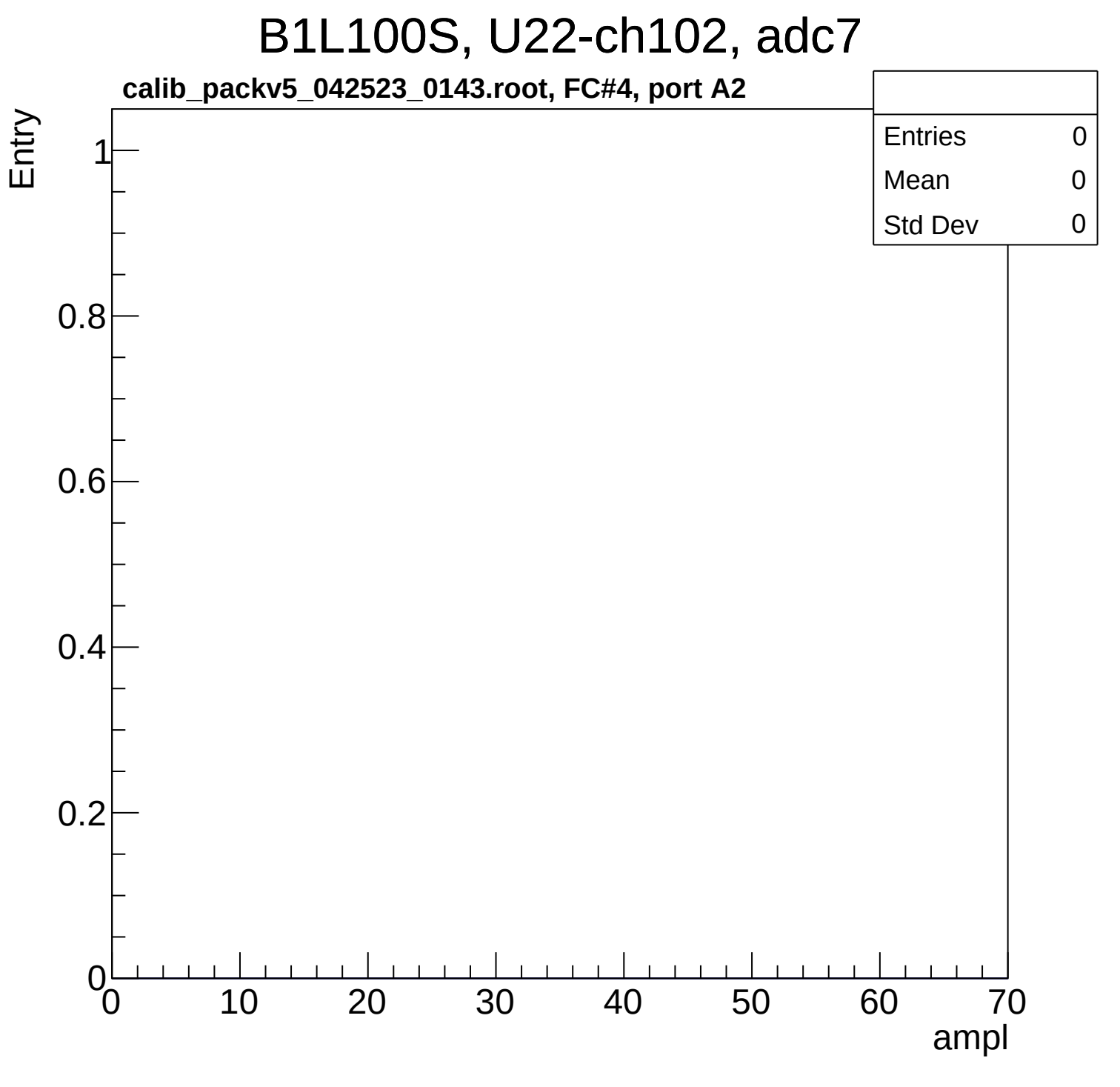
B1L100S, U22-ch102, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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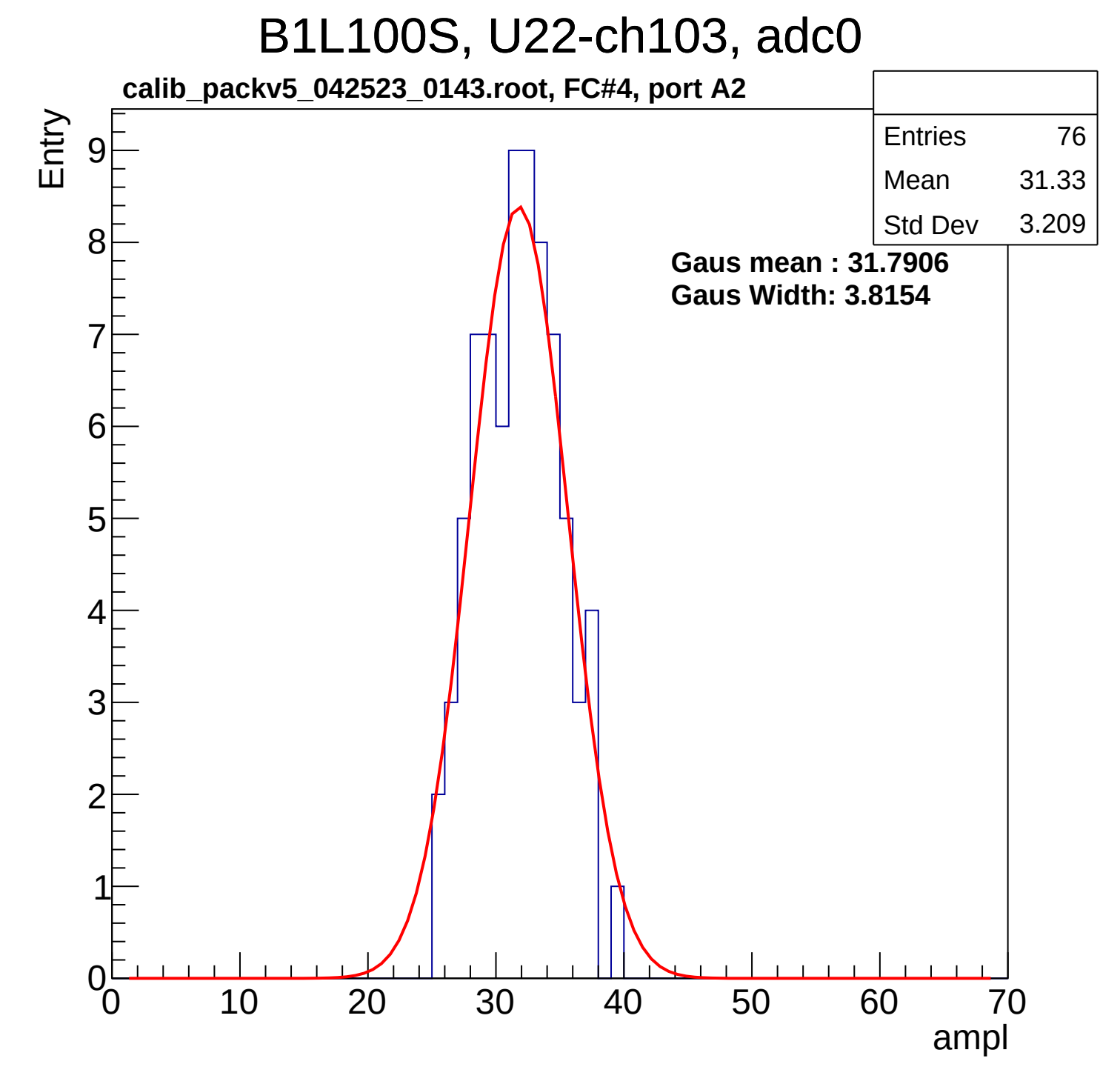
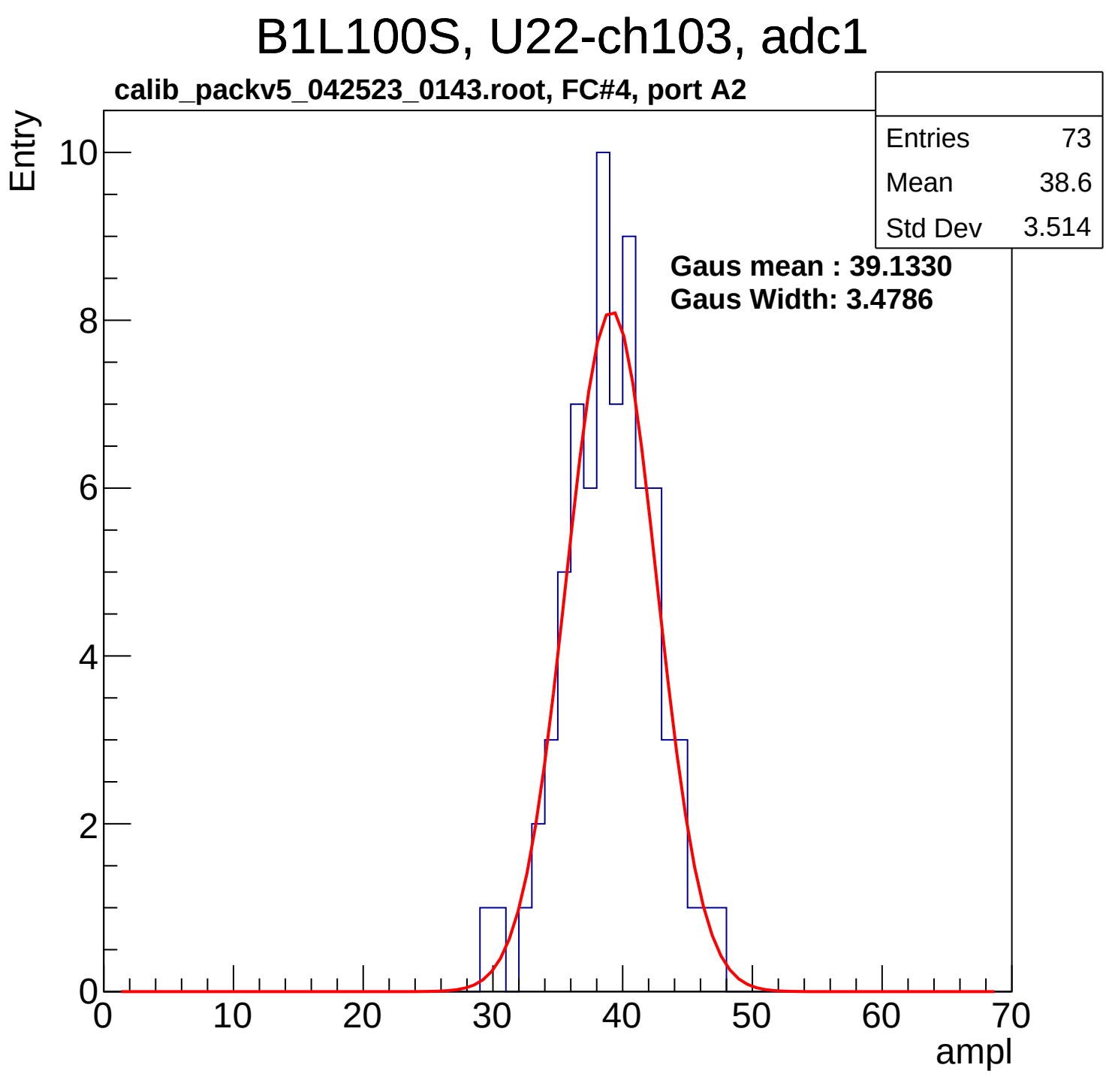
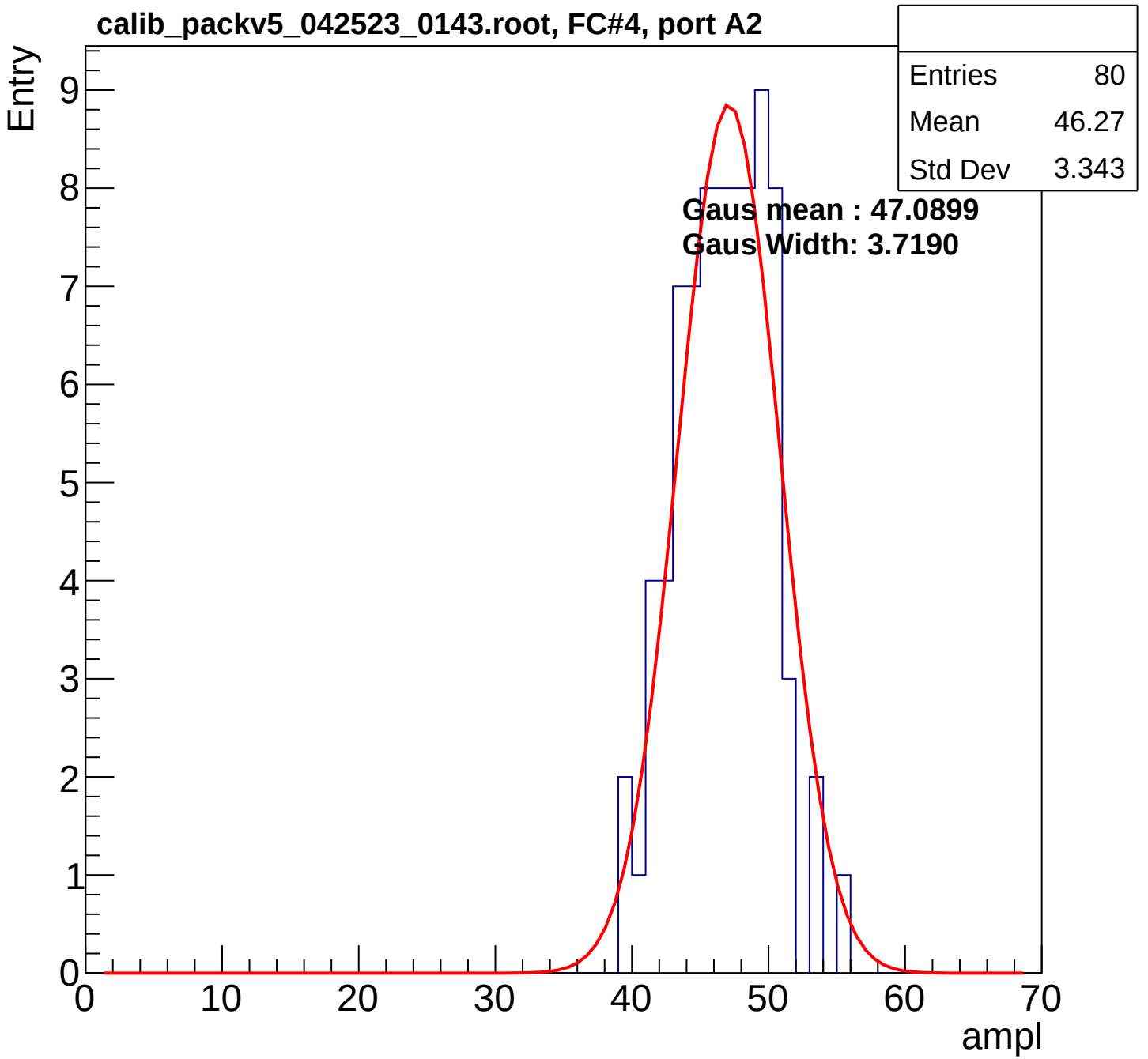
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch103, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 31.33
Std Dev 3.209
Gaus mean : 31.7906
Gaus Width: 3.8154
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch103, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 38.6
Std Dev 3.514
Gaus mean : 39.1330
Gaus Width: 3.4786
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch103, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 46.27
Std Dev 3.343
Gaus mean : 47.0899
Gaus Width: 3.7190
Entry
ampl
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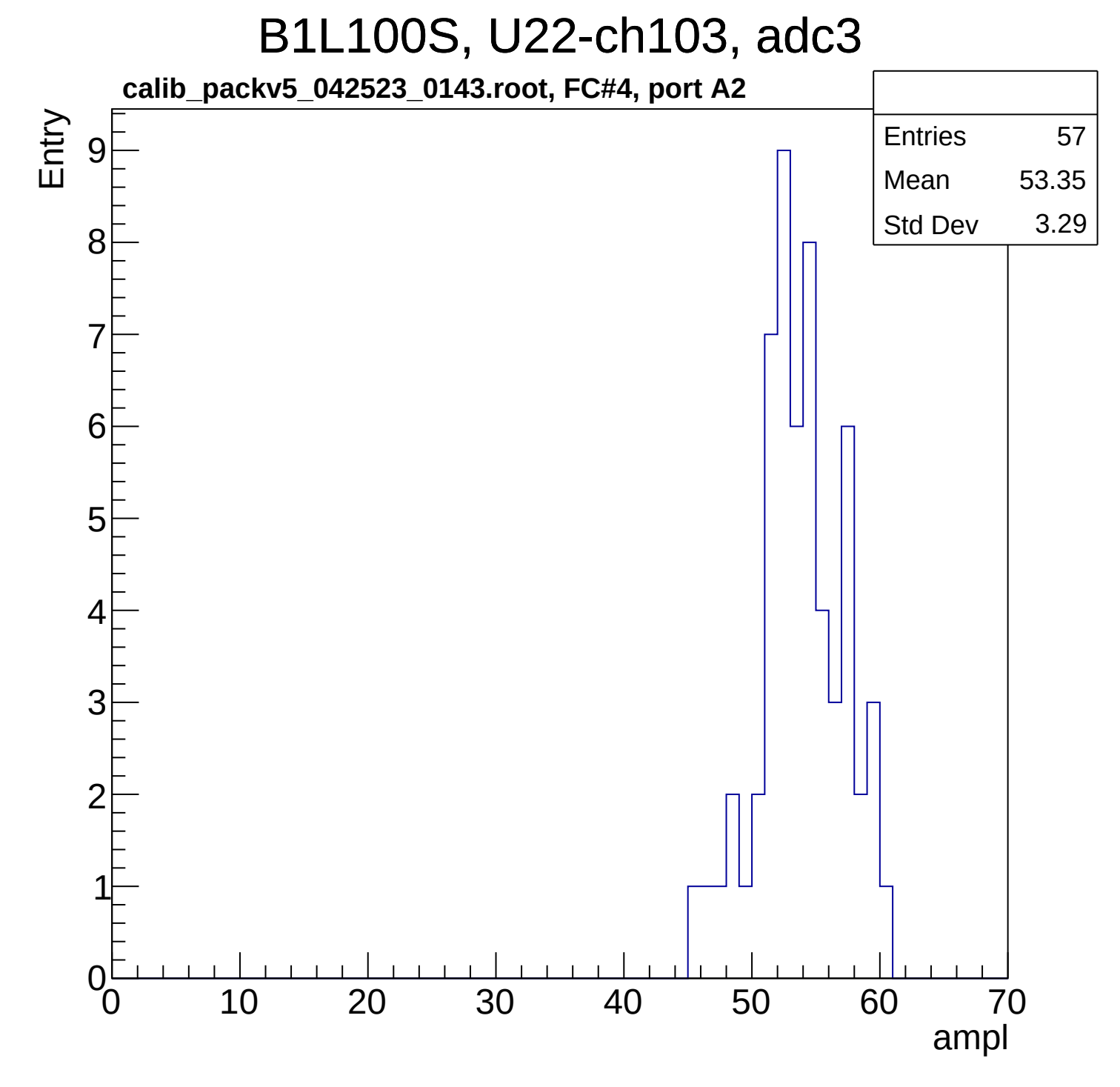
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch103, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 53.35
Std Dev 3.29
Entry
ampl
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2
3
4
5
6
7
8
9
0
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20
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40
50
60
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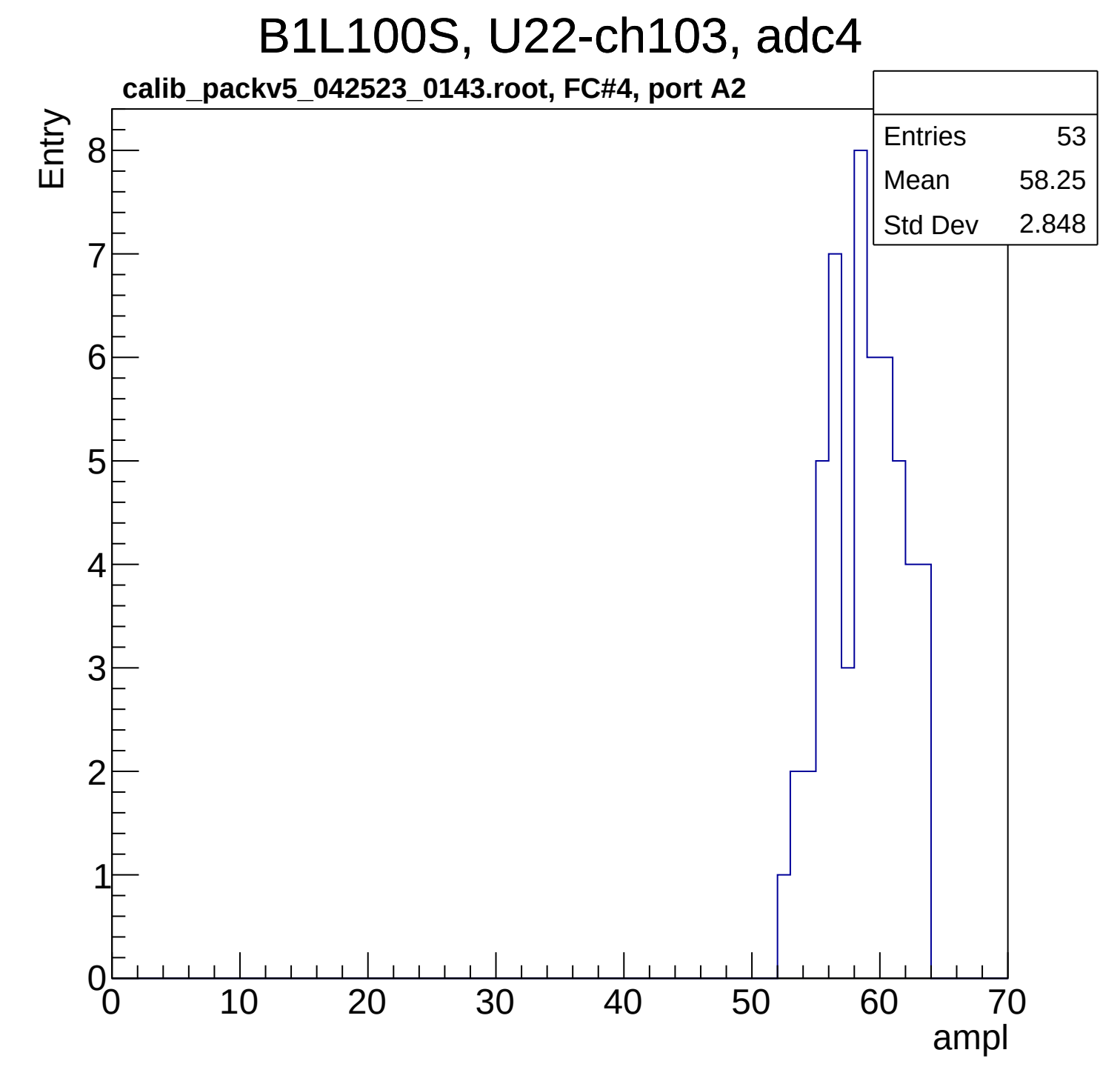
B1L100S, U22-ch103, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 58.25
Std Dev 2.848
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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60
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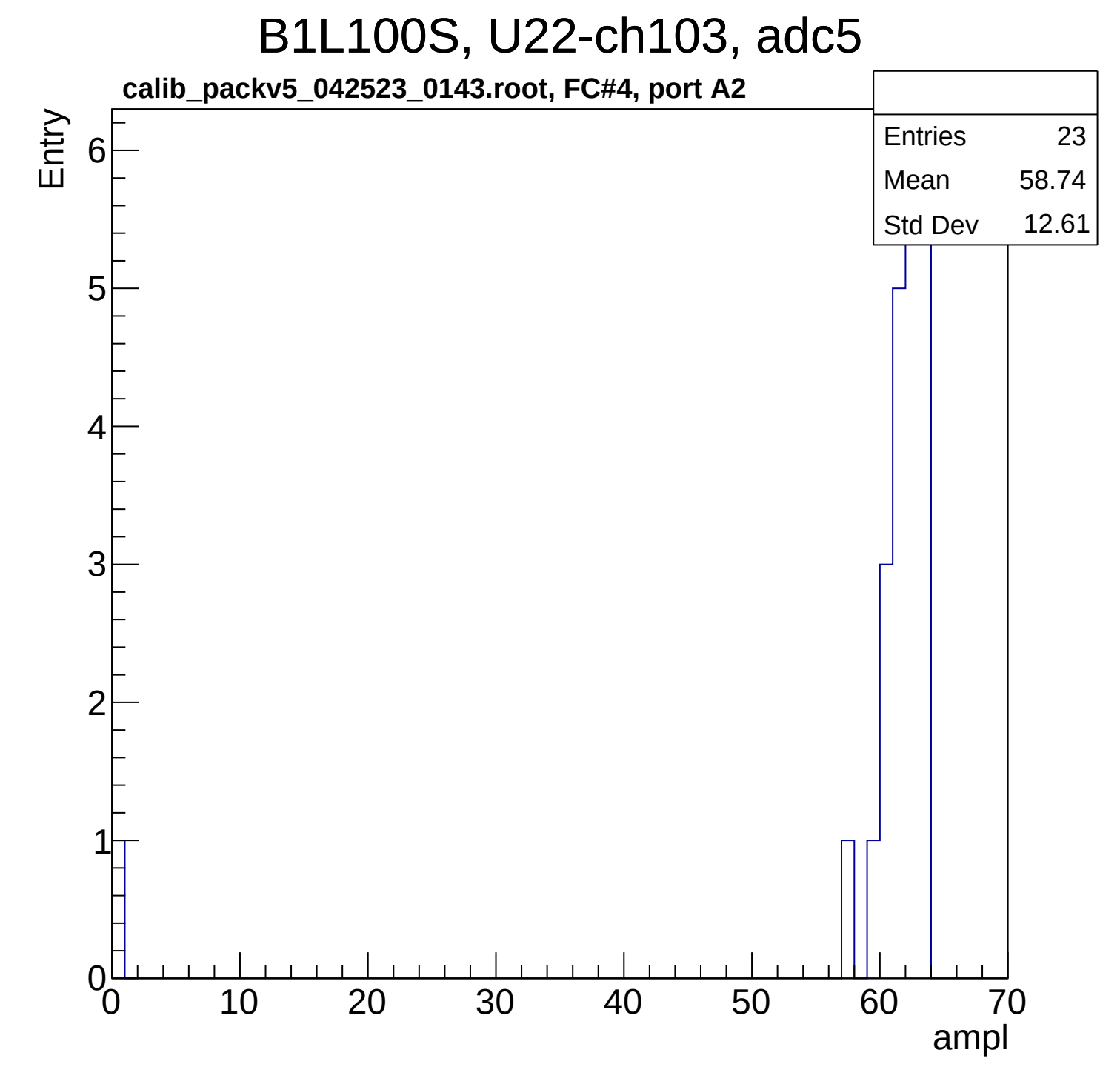
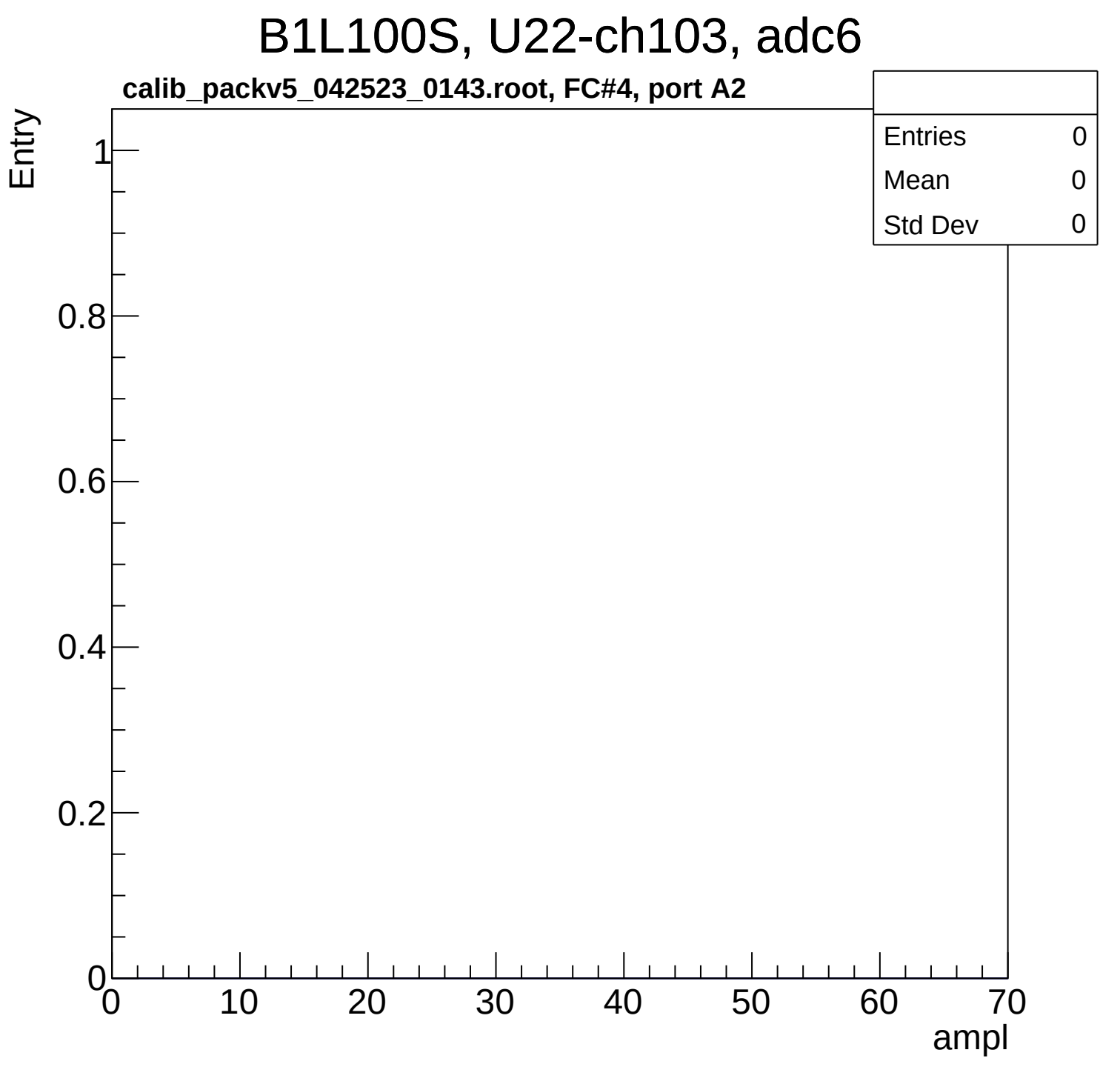

B1L100S, U22-ch103, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 23
Mean 58.74
Std Dev 12.61
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch103, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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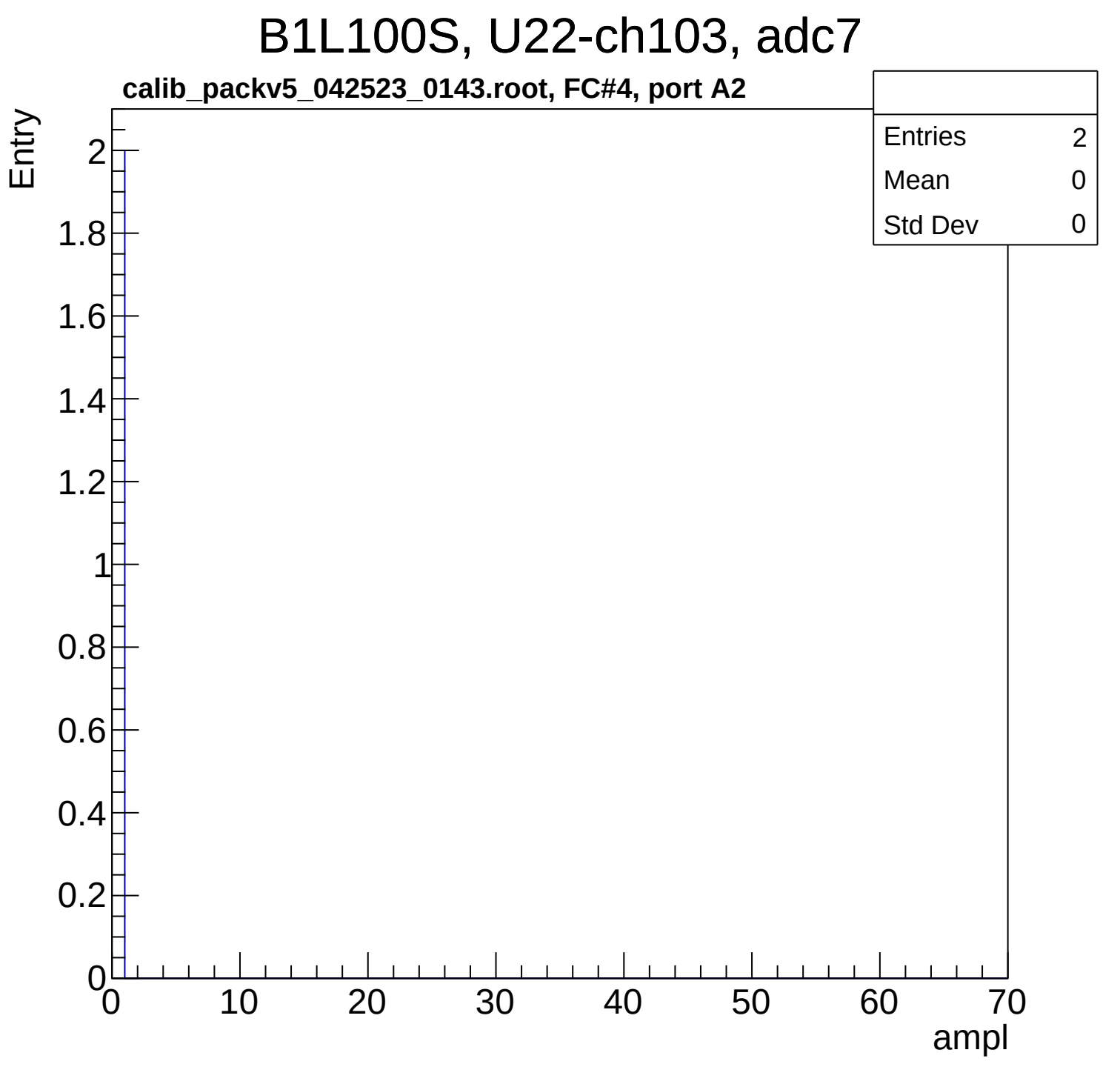
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch103, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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0.6
0.4
0.2
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10
20
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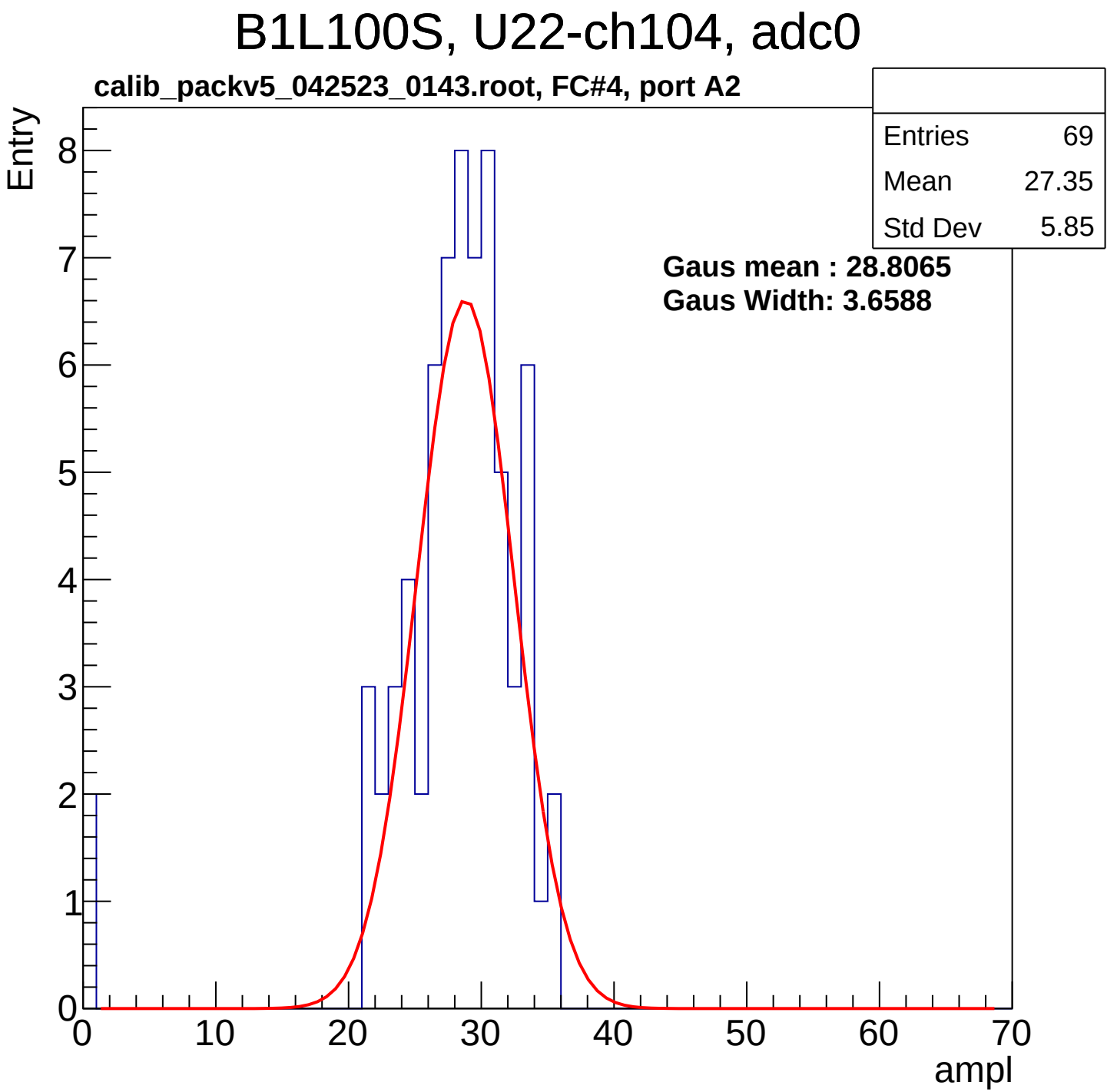
50
60
70
ampl


B1L100S, U22-ch104, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 27.35
Std Dev 5.85
Gaus mean : 28.8065
Gaus Width: 3.6588
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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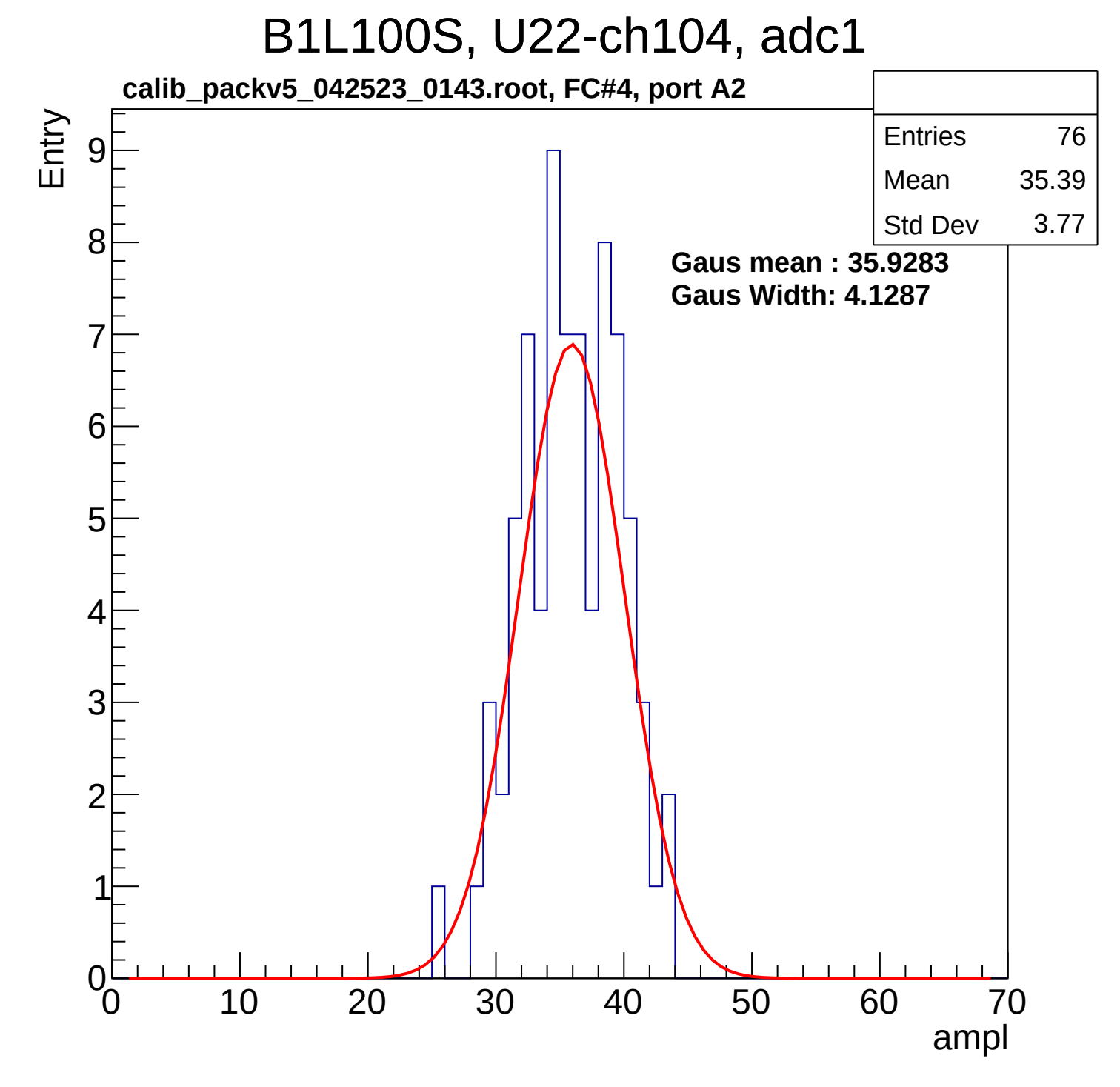
B1L100S, U22-ch104, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 35.39
Std Dev 3.77
Gaus mean : 35.9283
Gaus Width: 4.1287
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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60
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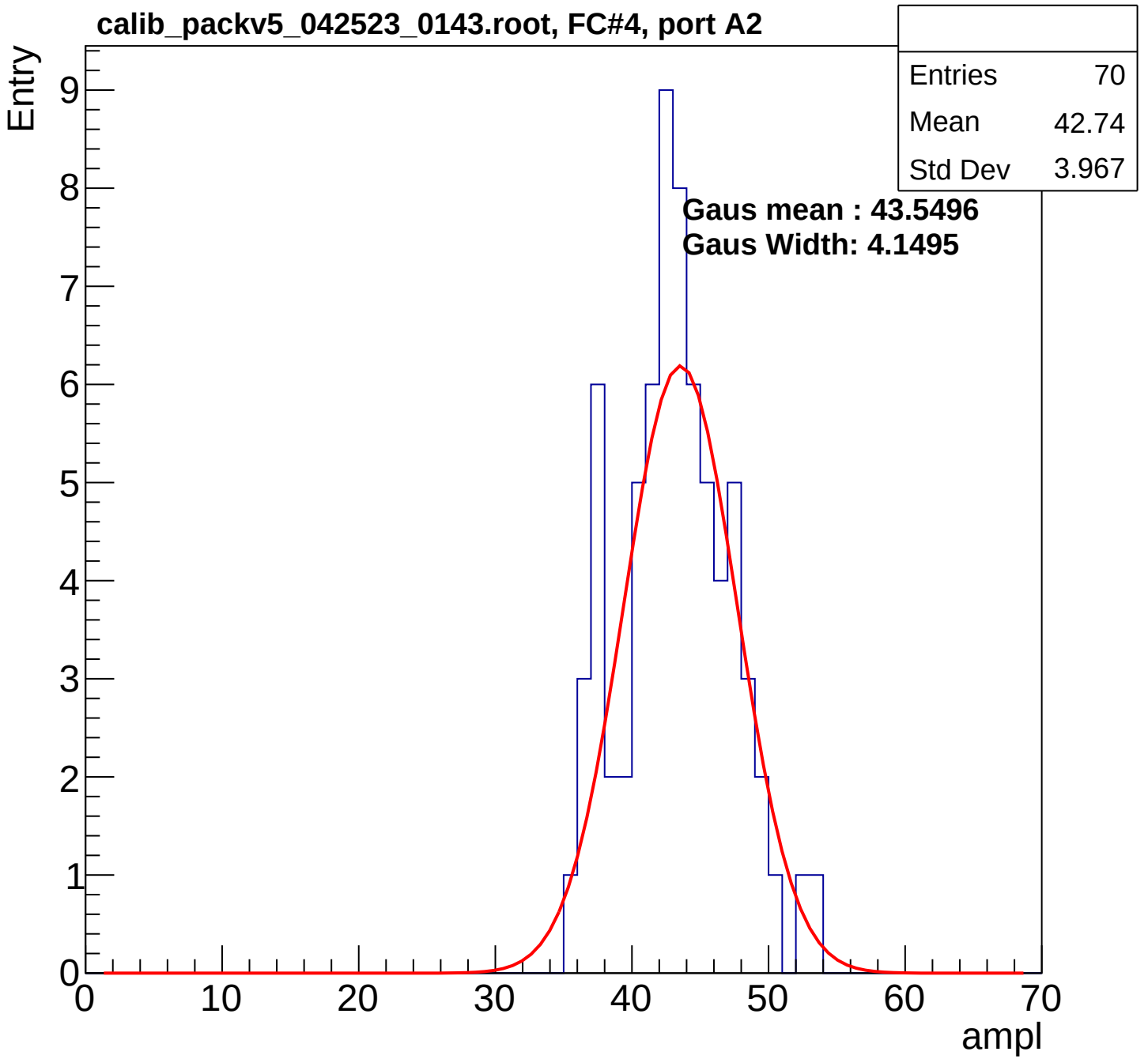
B1L100S, U22-ch104, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 42.74
Std Dev 3.967
Gaus mean : 43.5496
Gaus Width: 4.1495
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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60
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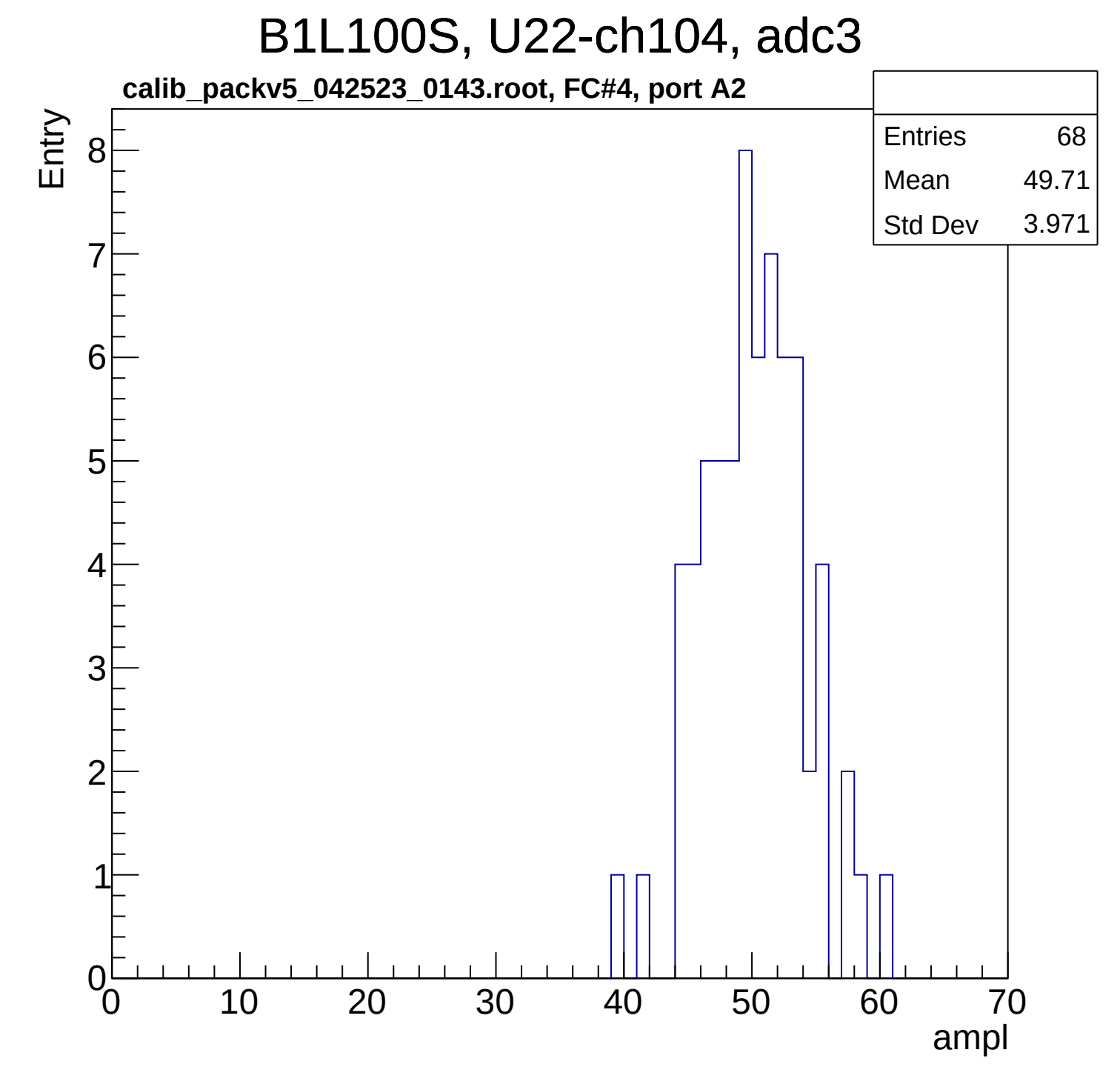
B1L100S, U22-ch104, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 68
Mean 49.71
Std Dev 3.971
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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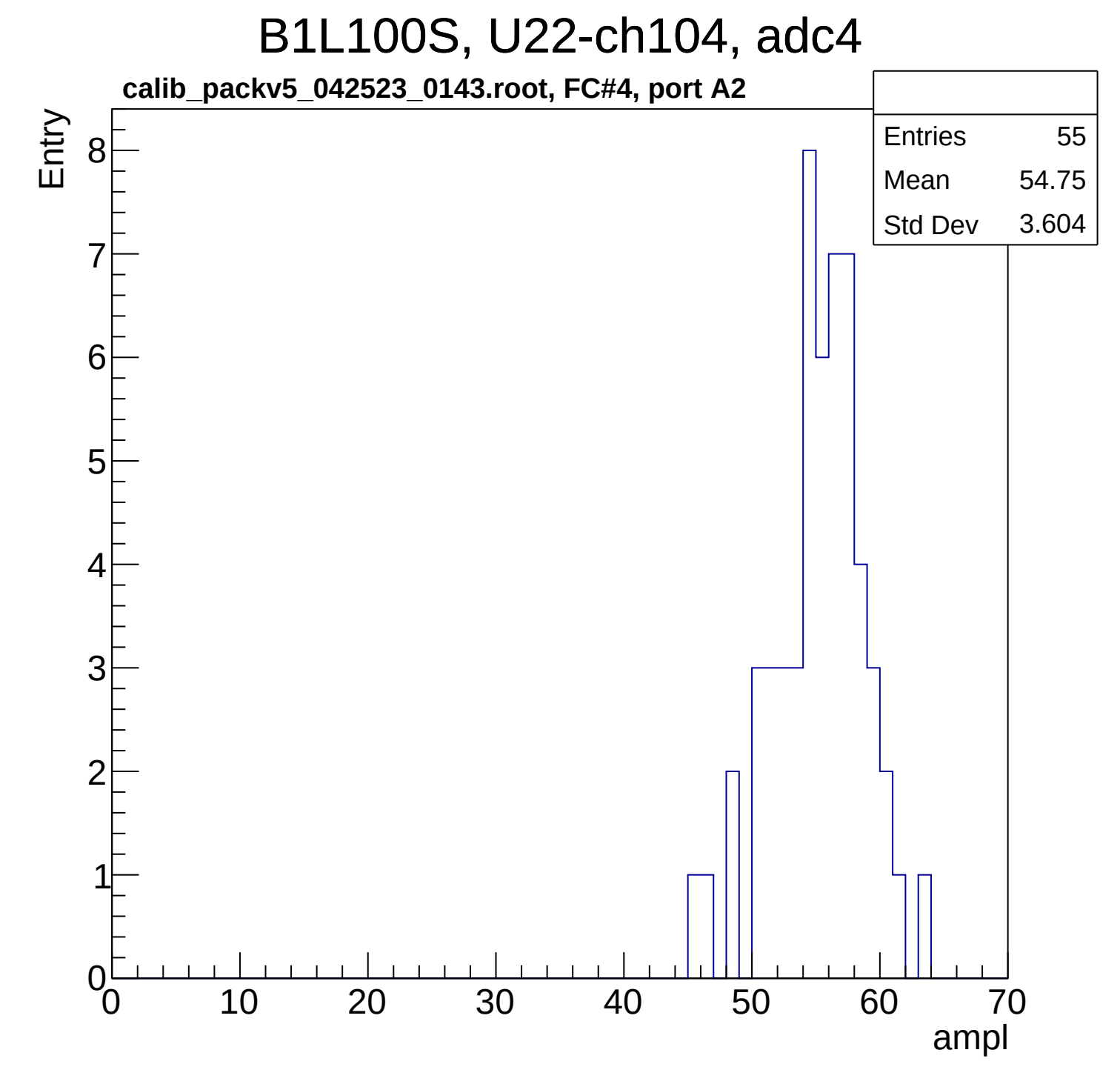
B1L100S, U22-ch104, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 54.75
Std Dev 3.604
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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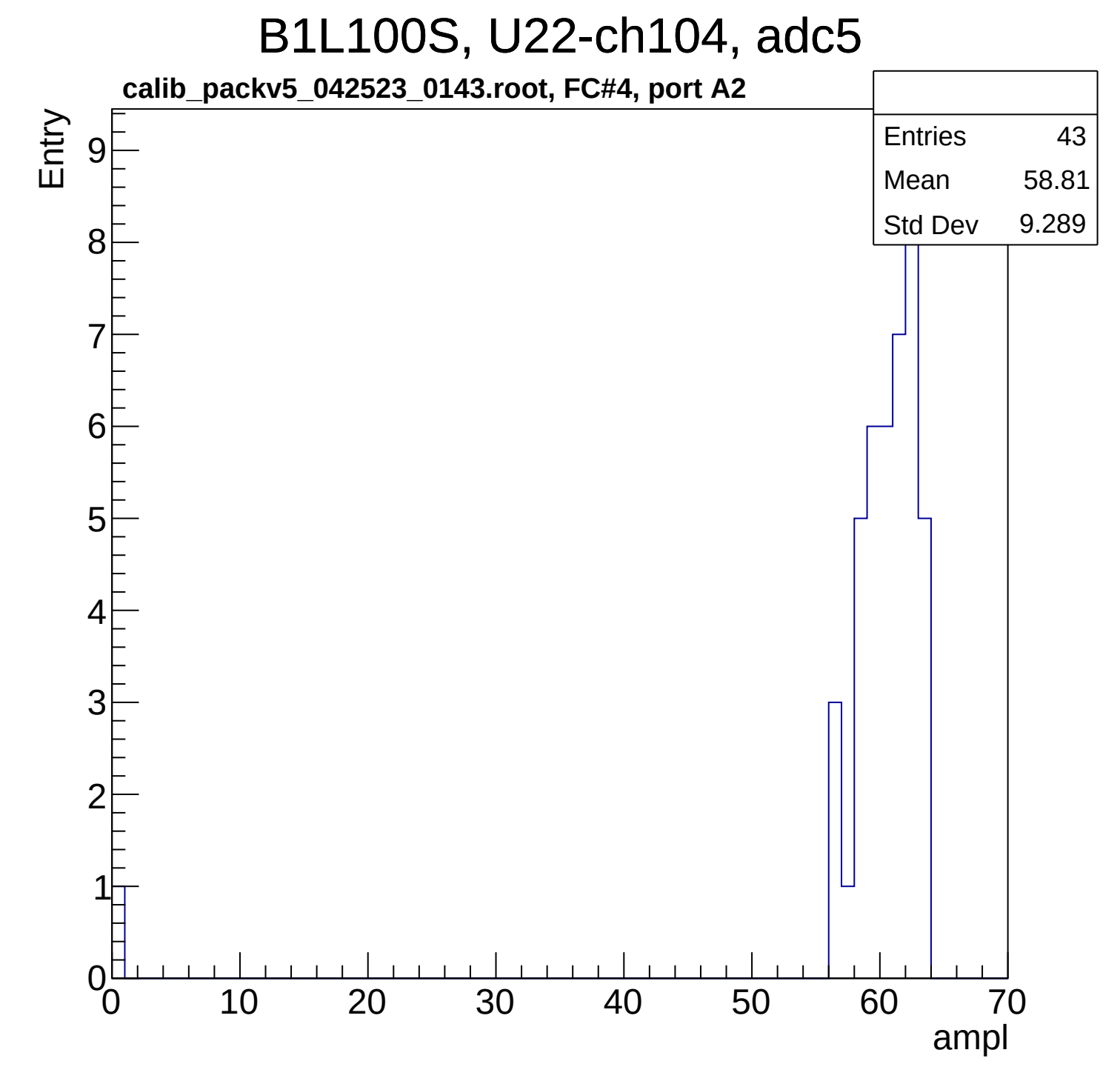
B1L100S, U22-ch104, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 58.81
Std Dev 9.289
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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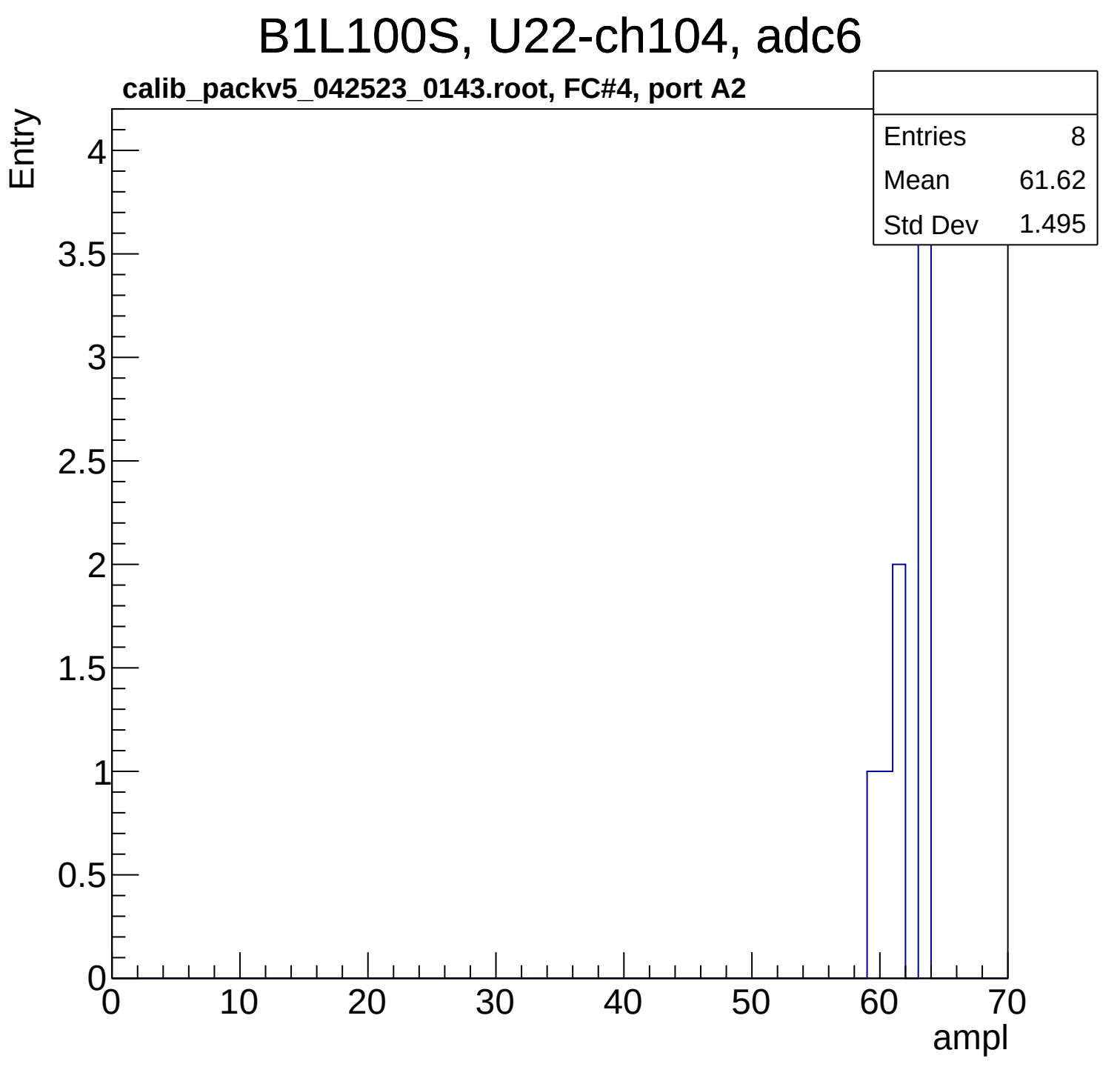
B1L100S, U22-ch104, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.62
Std Dev 1.495
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
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60
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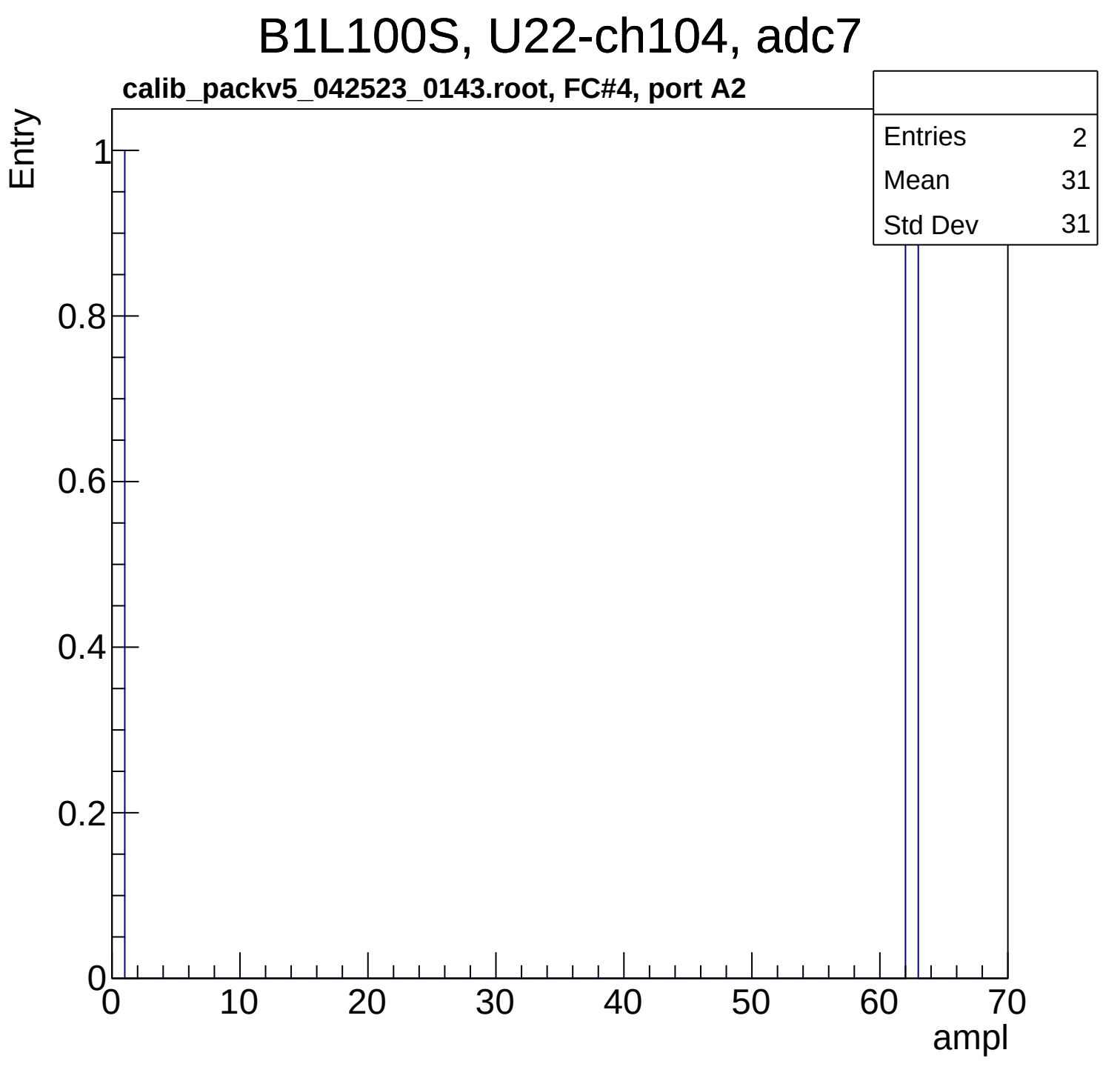
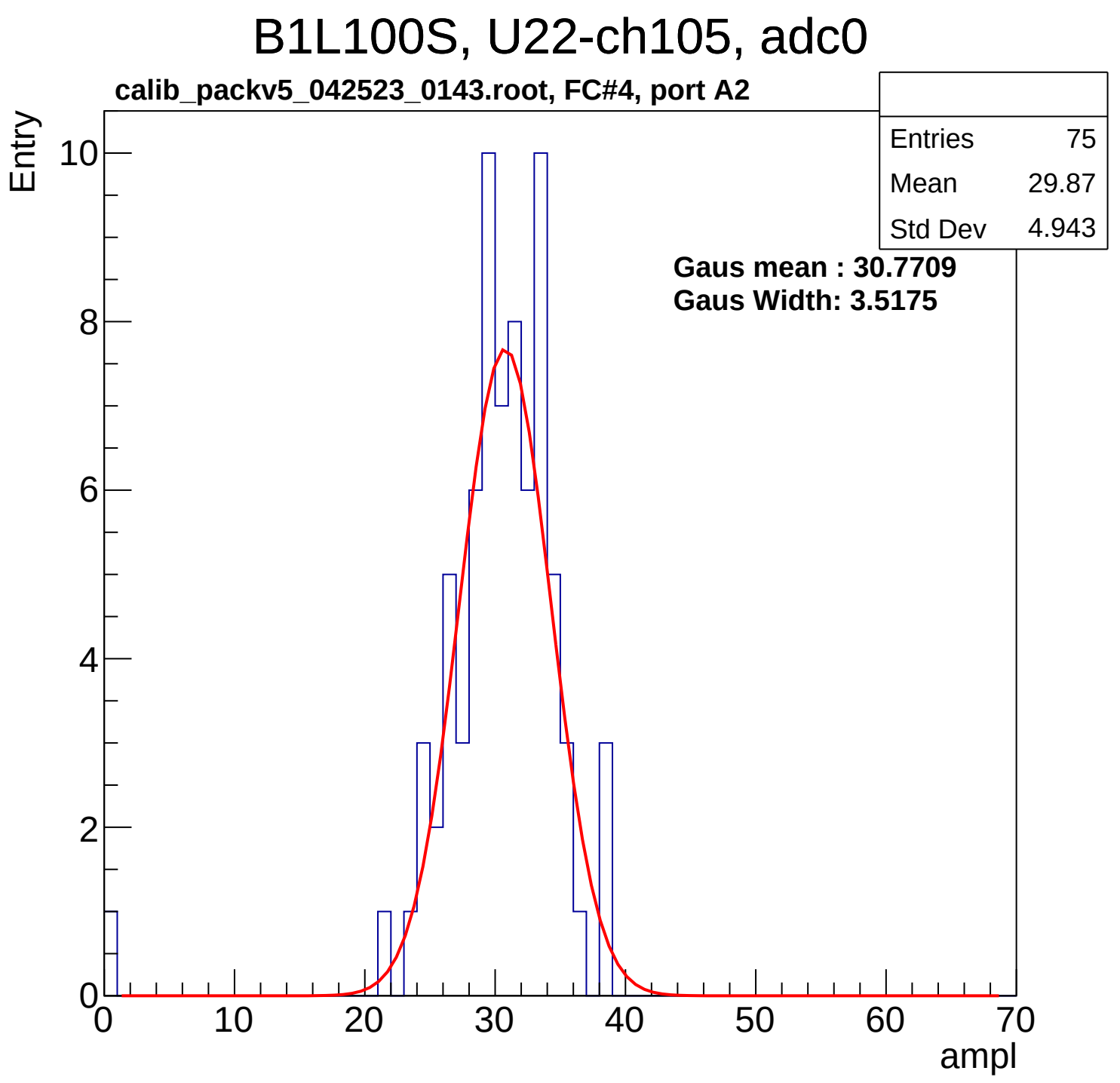
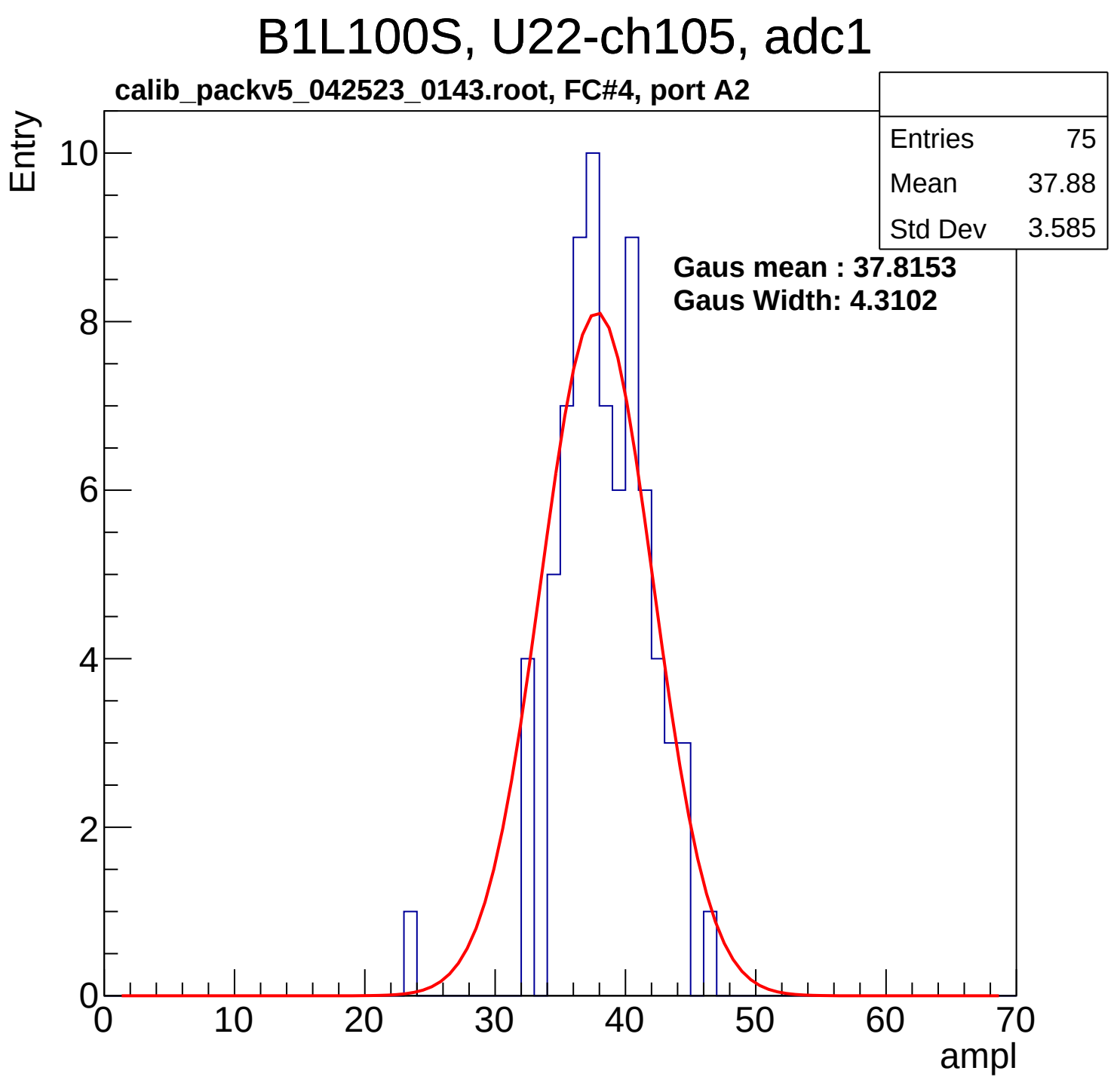
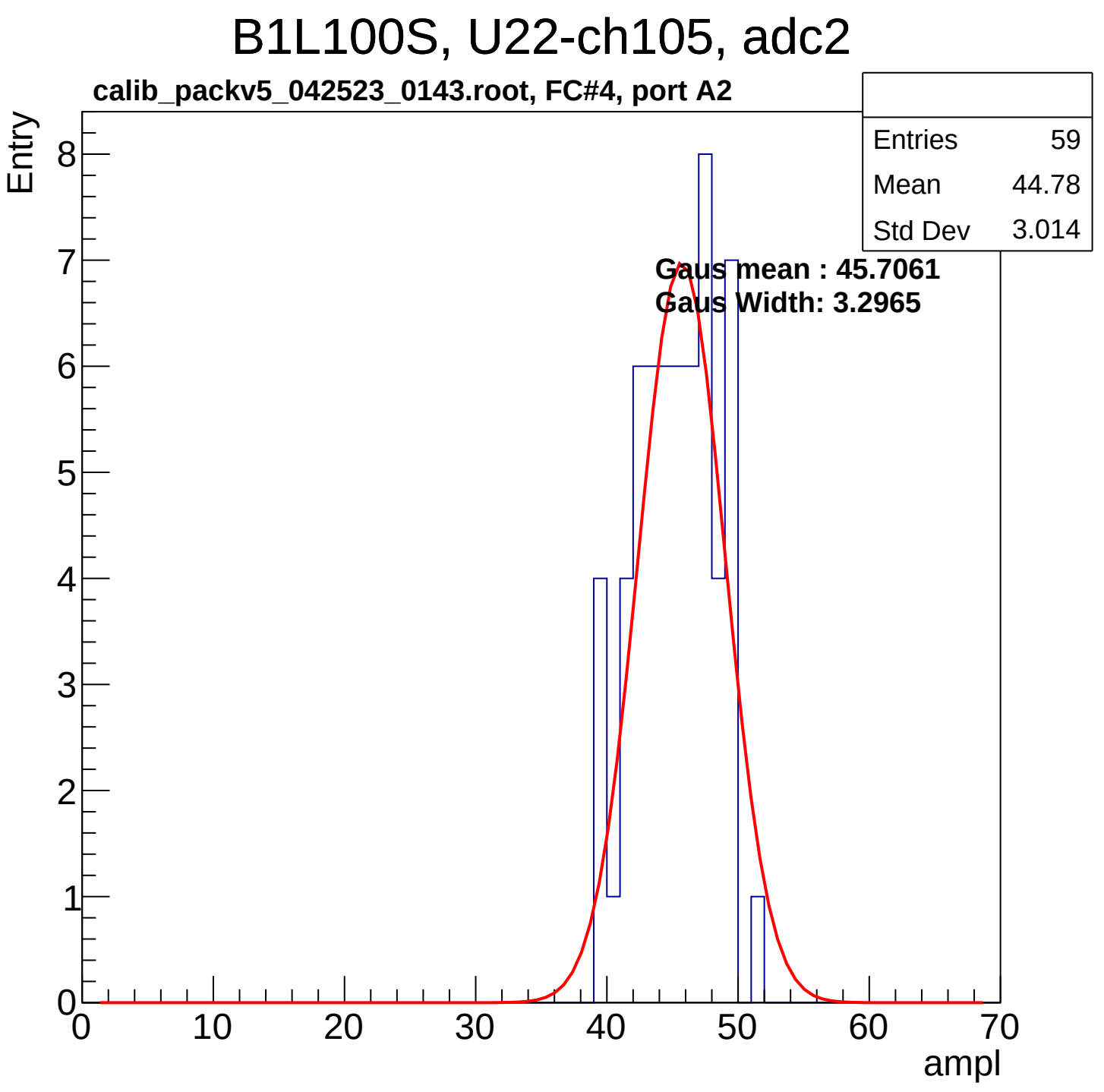
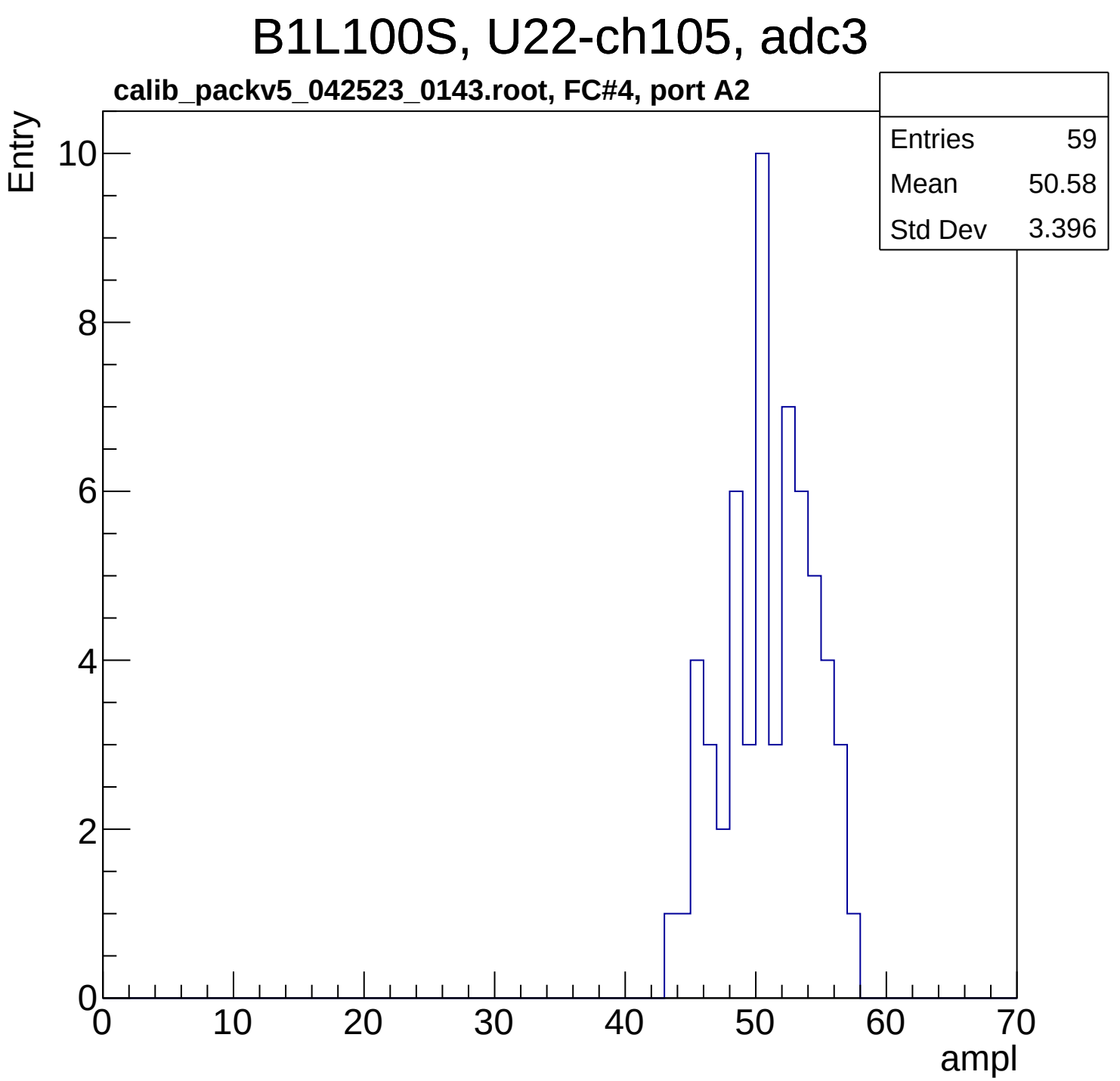
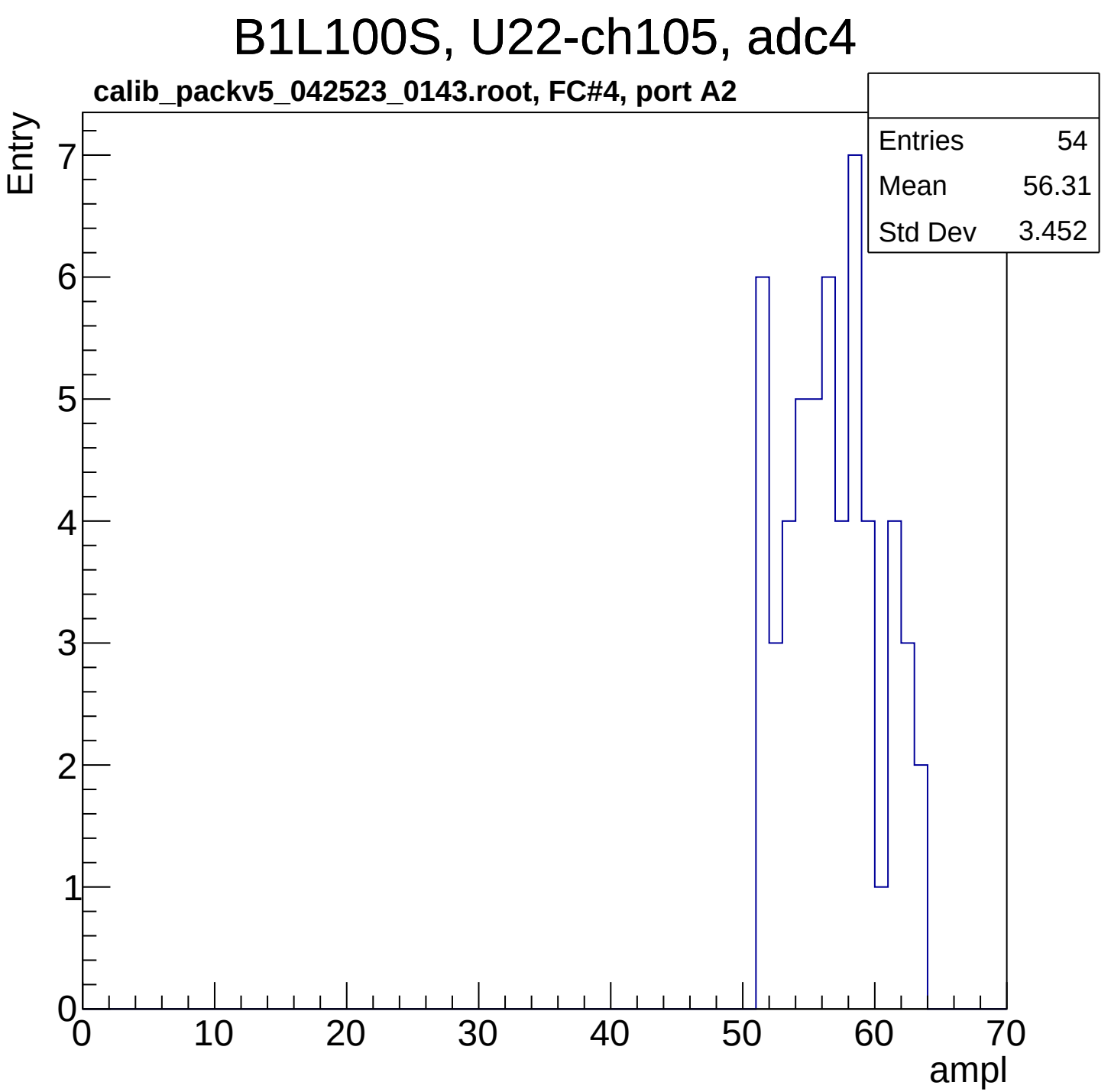

B1L100S, U22-ch104, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch105, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.87
Std Dev 4.943
Gaus mean : 30.7709
Gaus Width: 3.5175
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch105, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 37.88
Std Dev 3.585
Gaus mean : 37.8153
Gaus Width: 4.3102
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch105, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 44.78
Std Dev 3.014
Gaus mean : 45.7061
Gaus Width: 3.2965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch105, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 50.58
Std Dev 3.396
Entry
0
2
4
6
8
10
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch105, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 56.31
Std Dev 3.452
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
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40
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60
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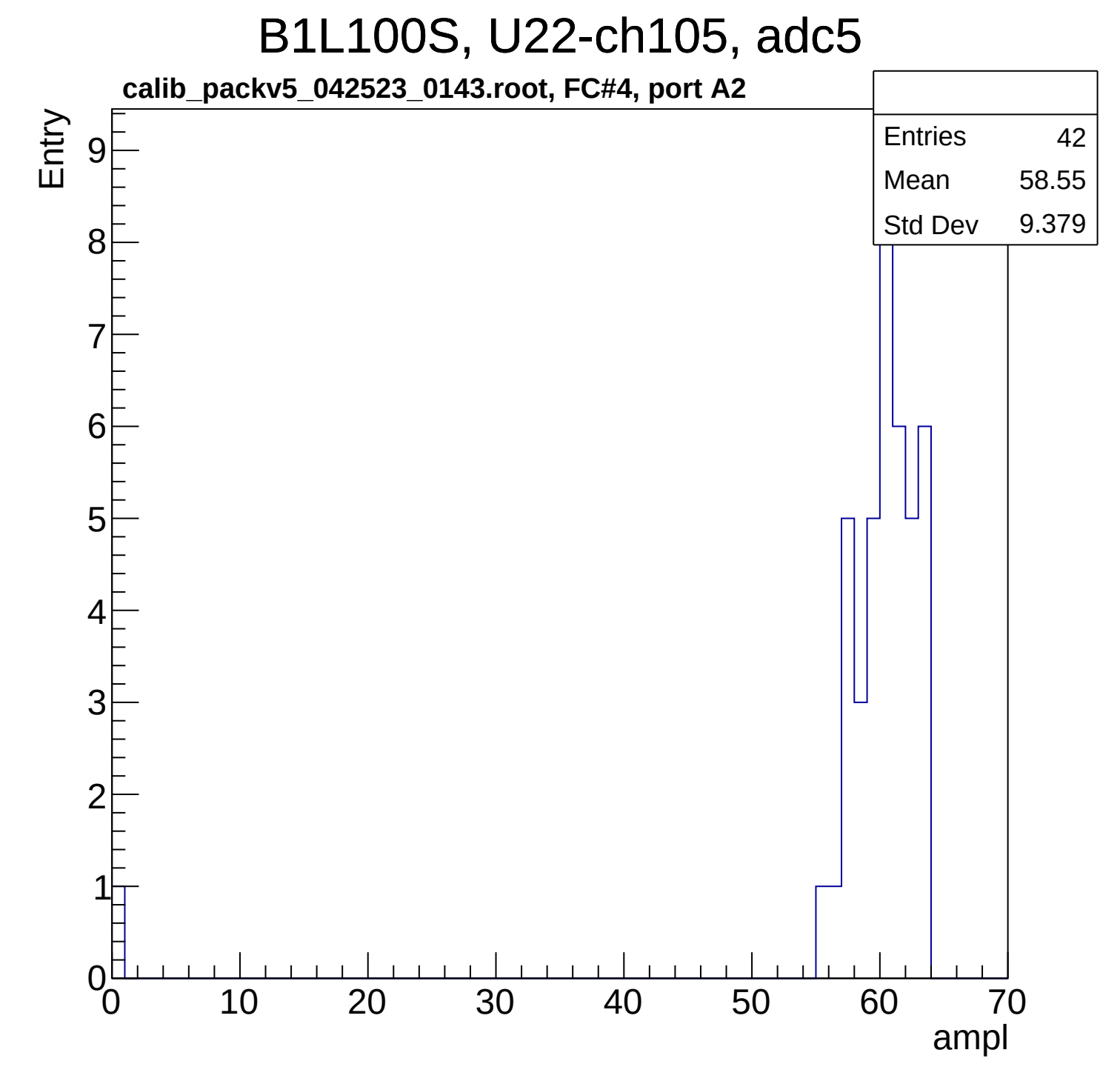
B1L100S, U22-ch105, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.55
Std Dev 9.379
Entry
ampl
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2
3
4
5
6
7
8
9
0
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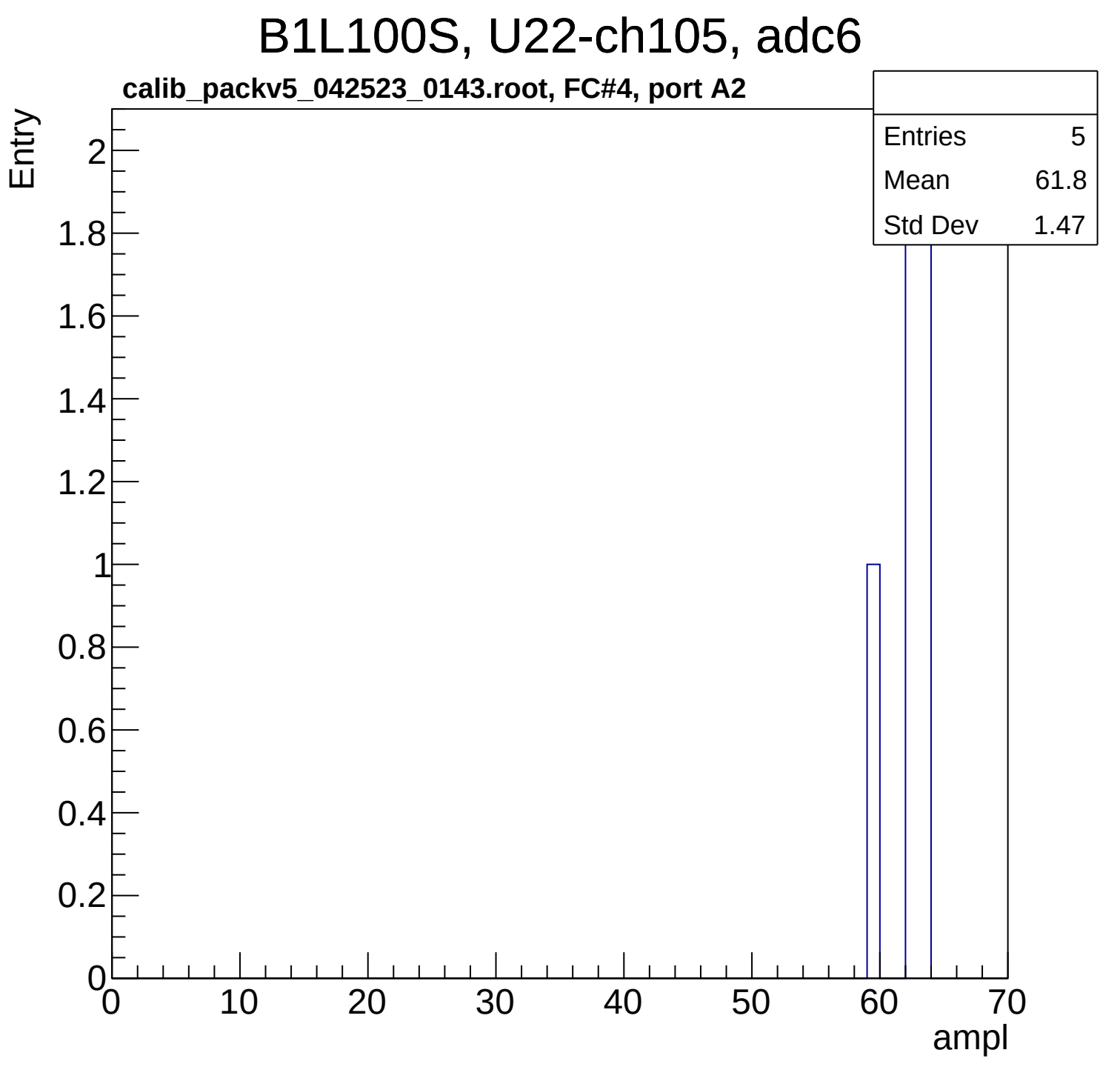
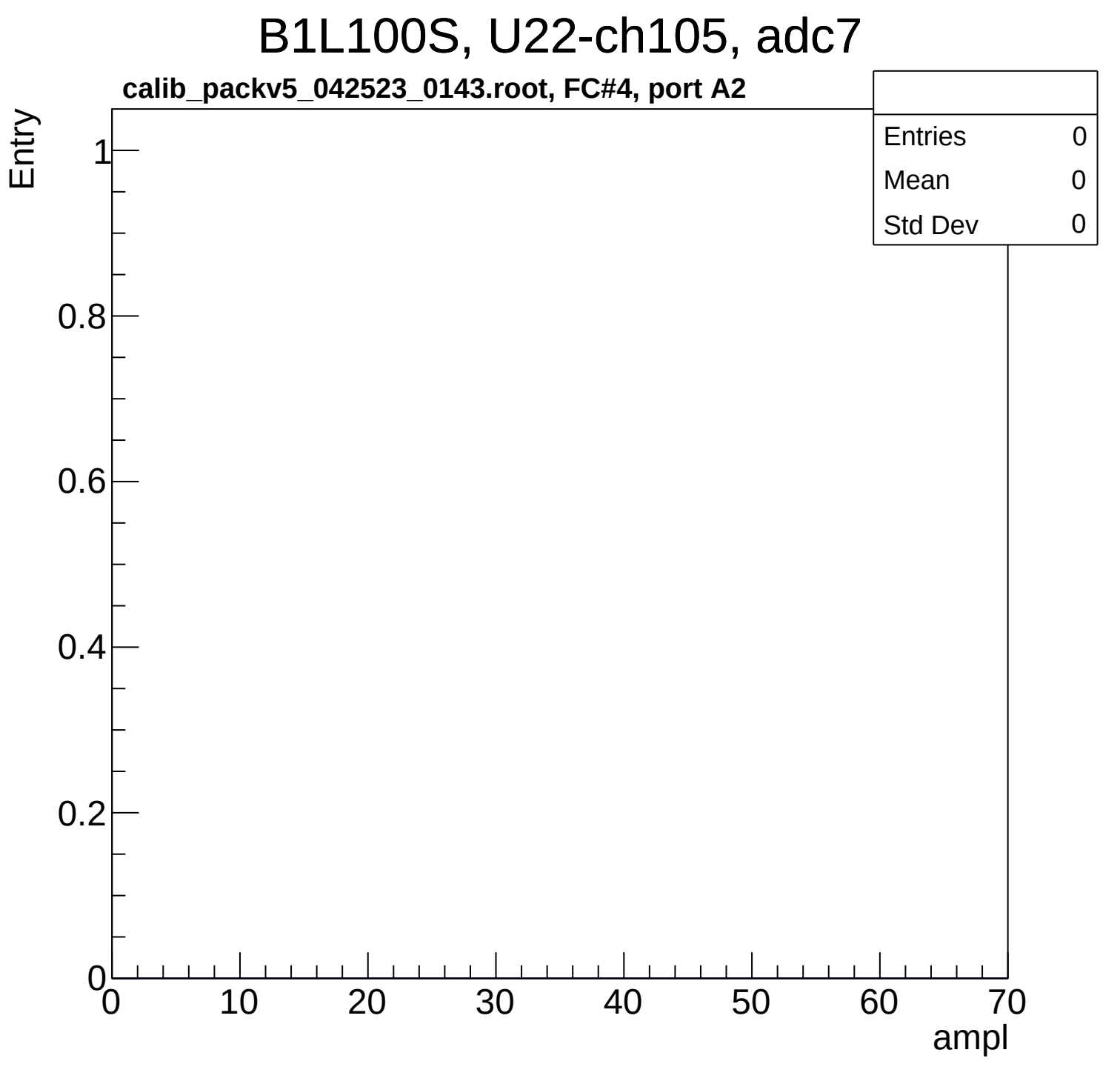
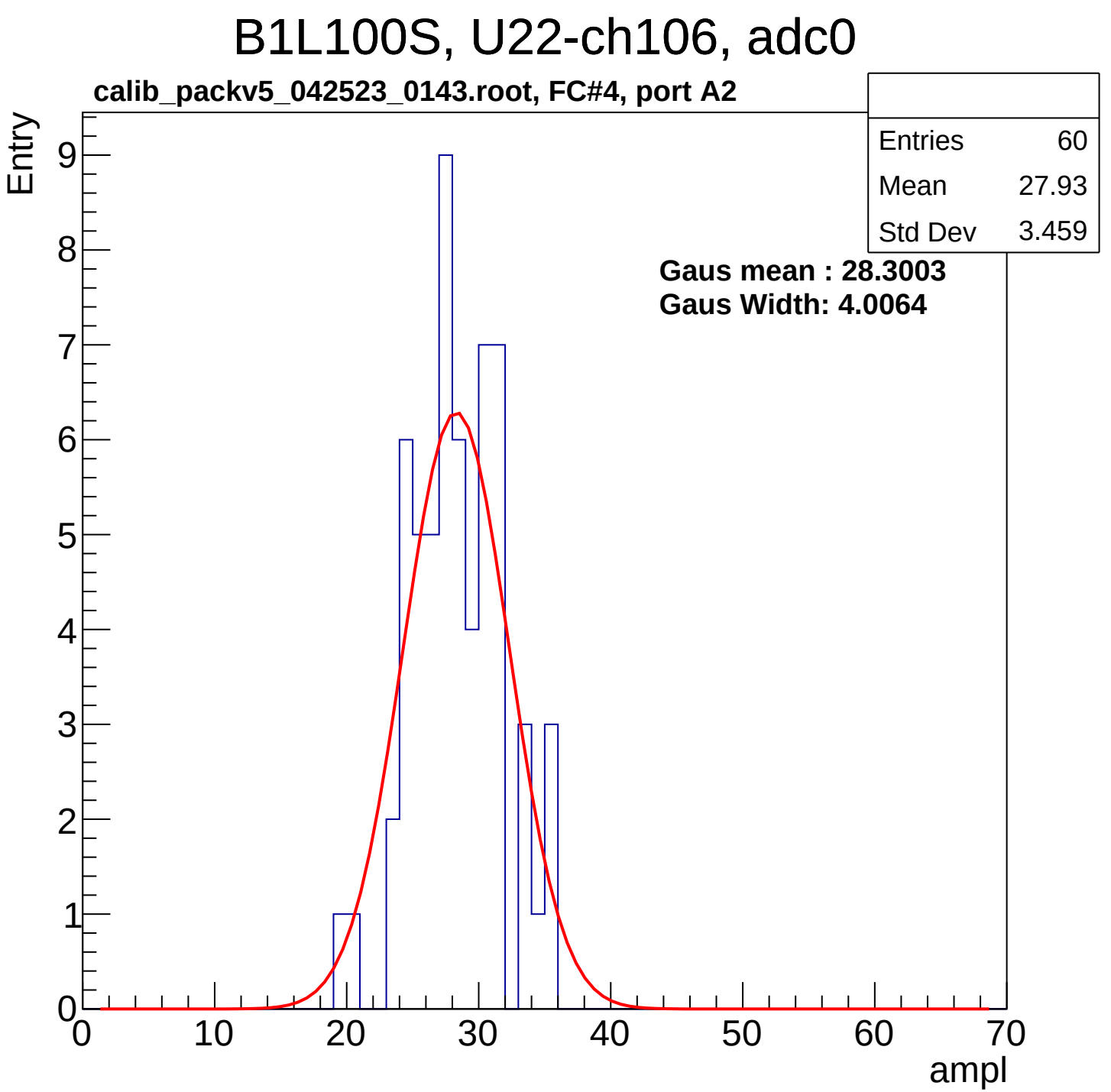
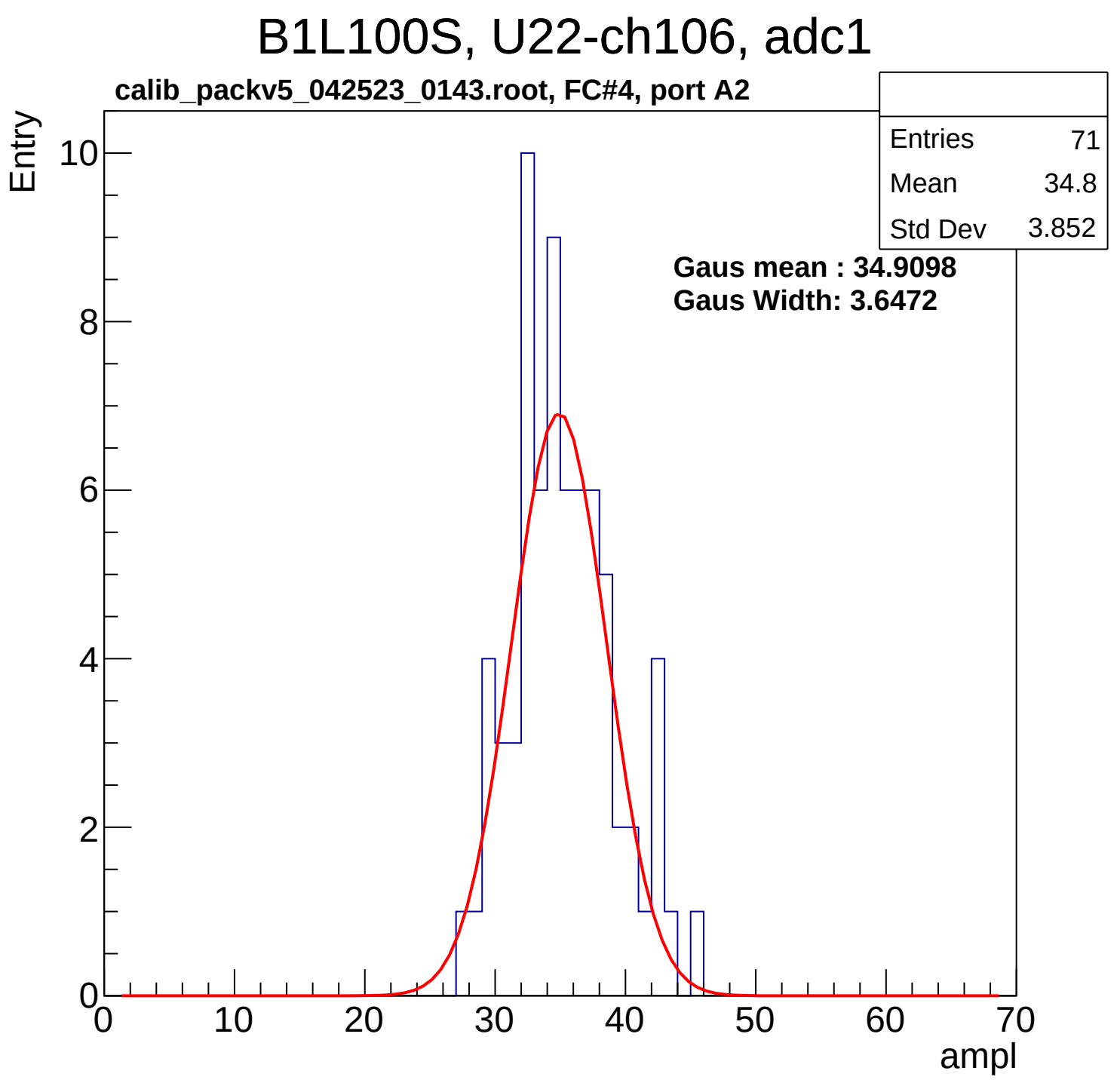
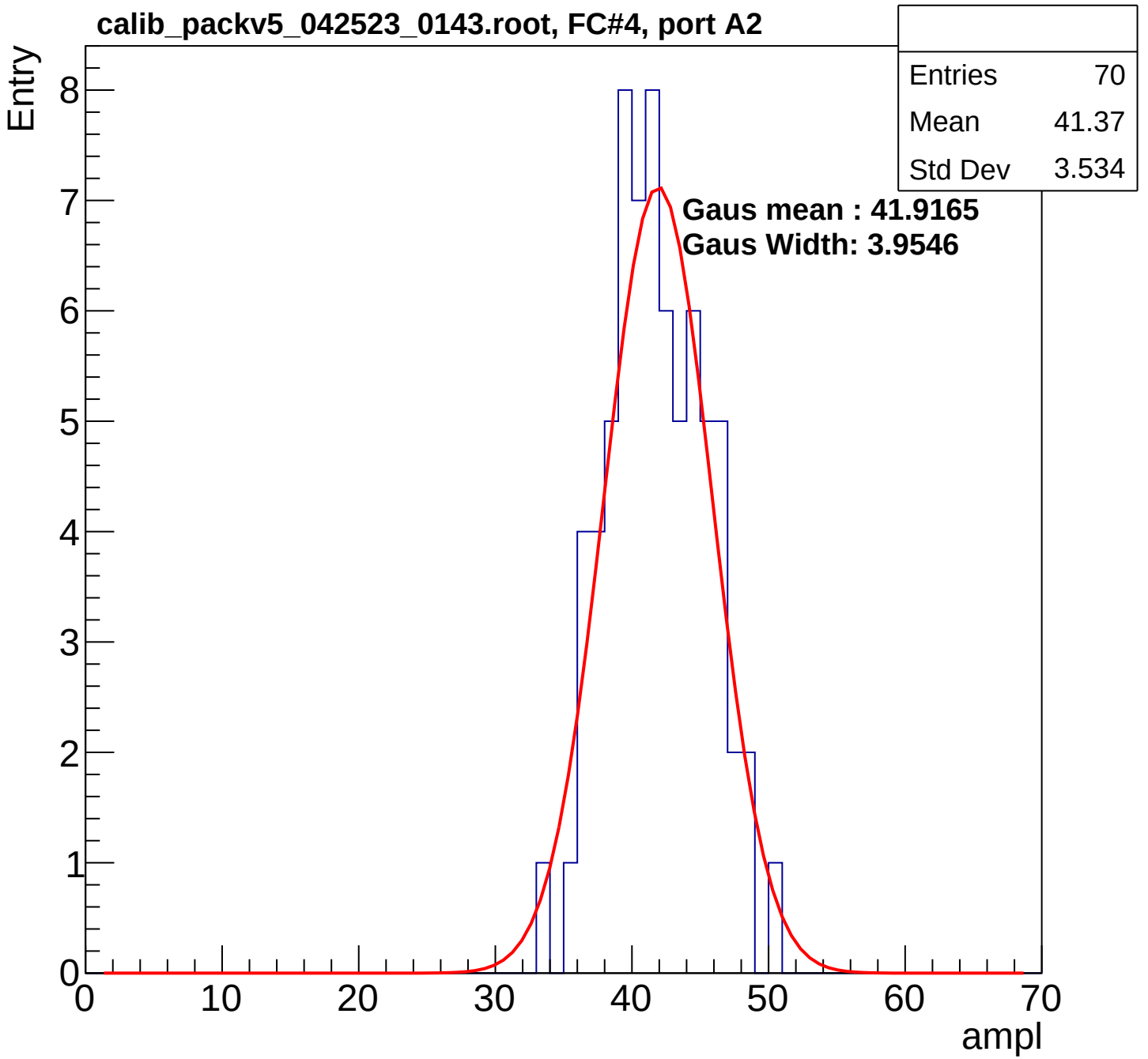
50
60
70


B1L100S, U22-ch105, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 1.47
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch105, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch106, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 27.93
Std Dev 3.459
Gaus mean : 28.3003
Gaus Width: 4.0064
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch106, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 34.8
Std Dev 3.852
Gaus mean : 34.9098
Gaus Width: 3.6472
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch106, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 41.37
Std Dev 3.534
Gaus mean : 41.9165
Gaus Width: 3.9546
Entry
ampl
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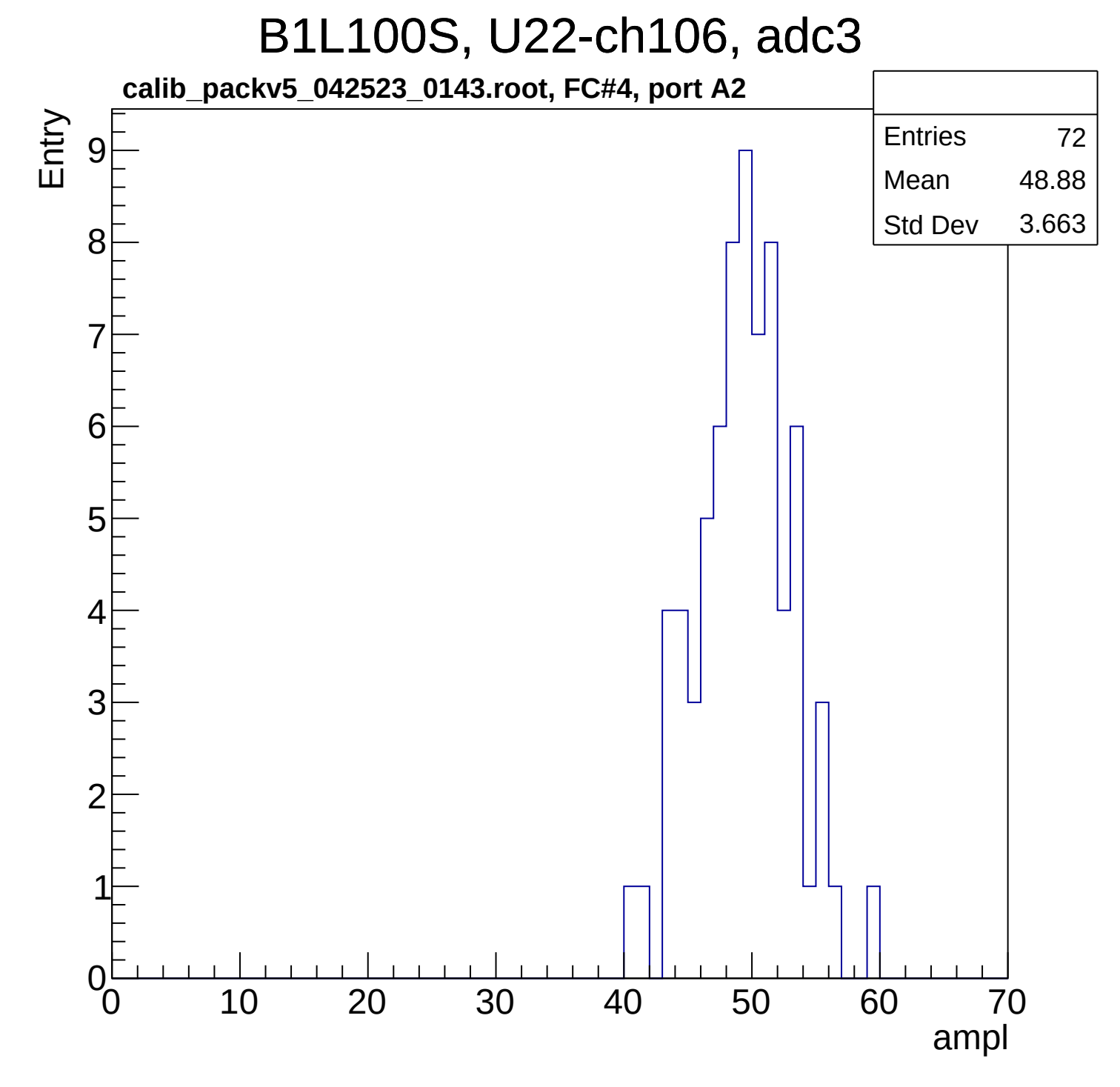
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch106, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 48.88
Std Dev 3.663
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
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20
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40
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60
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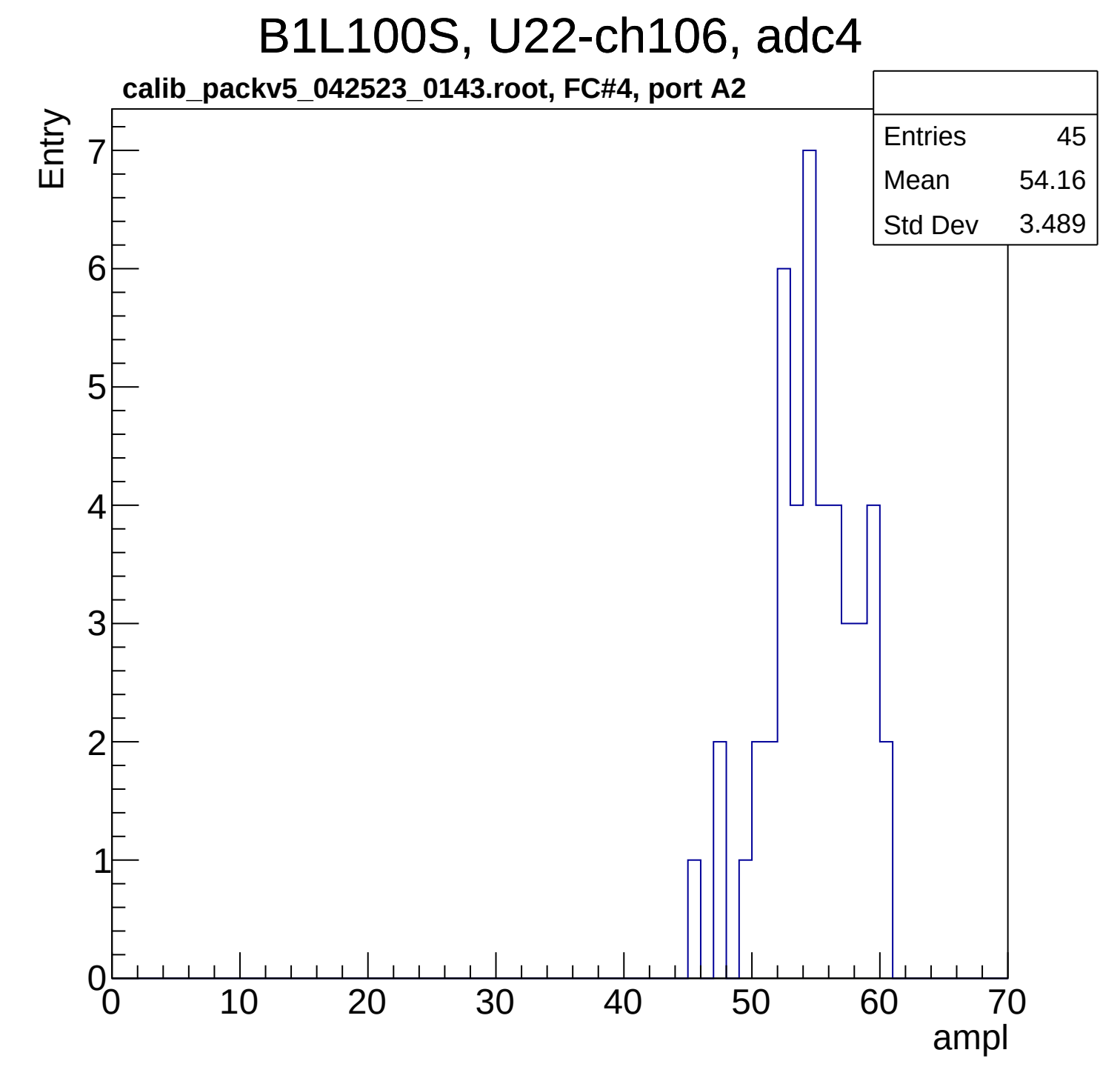
B1L100S, U22-ch106, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 54.16
Std Dev 3.489
Entry
ampl
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1
2
3
4
5
6
7
0
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20
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40
50
60
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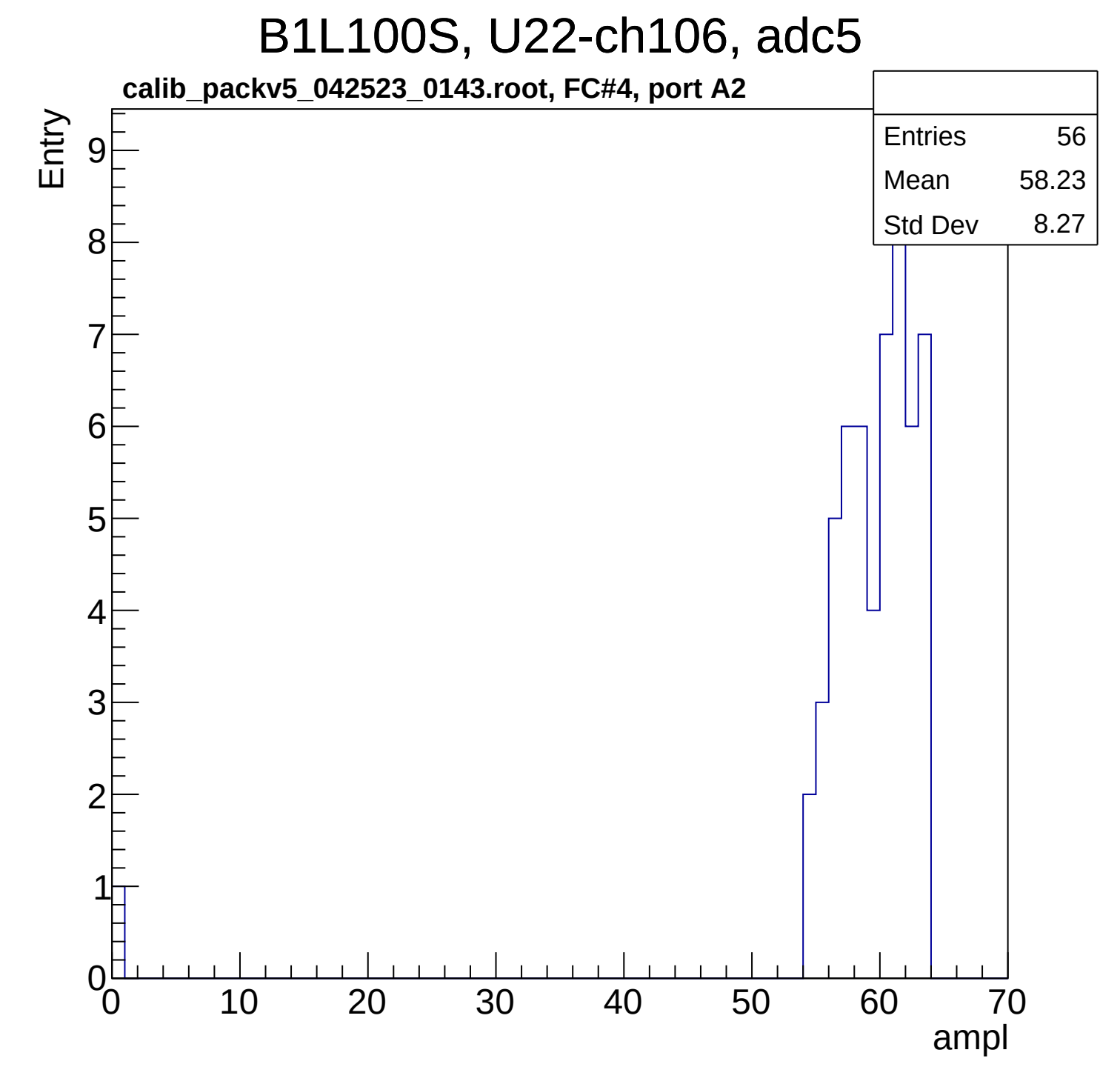
B1L100S, U22-ch106, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 58.23
Std Dev 8.27
Entry
ampl
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1
2
3
4
5
6
7
8
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0
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60
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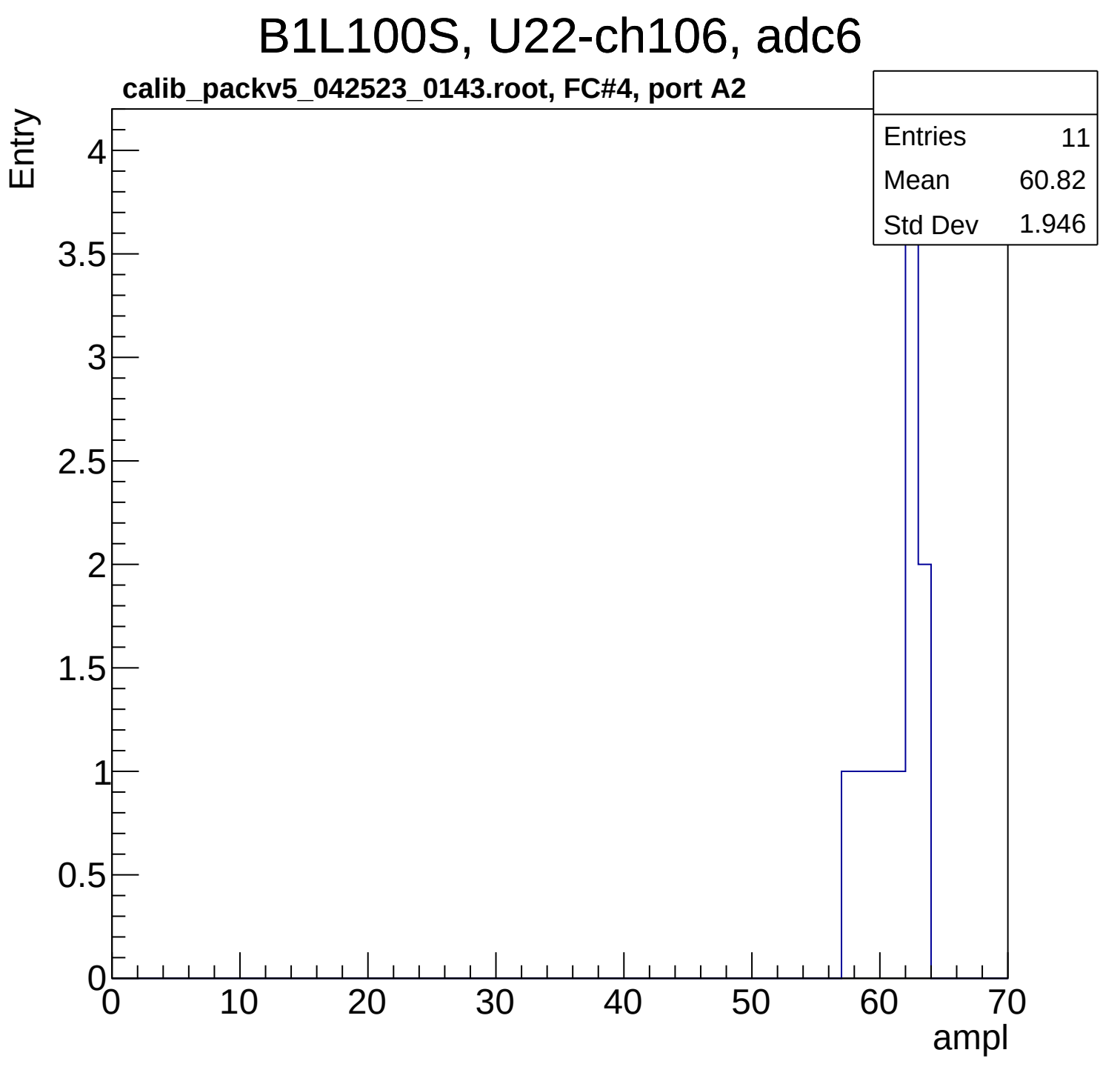
B1L100S, U22-ch106, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 60.82
Std Dev 1.946
Entry
ampl
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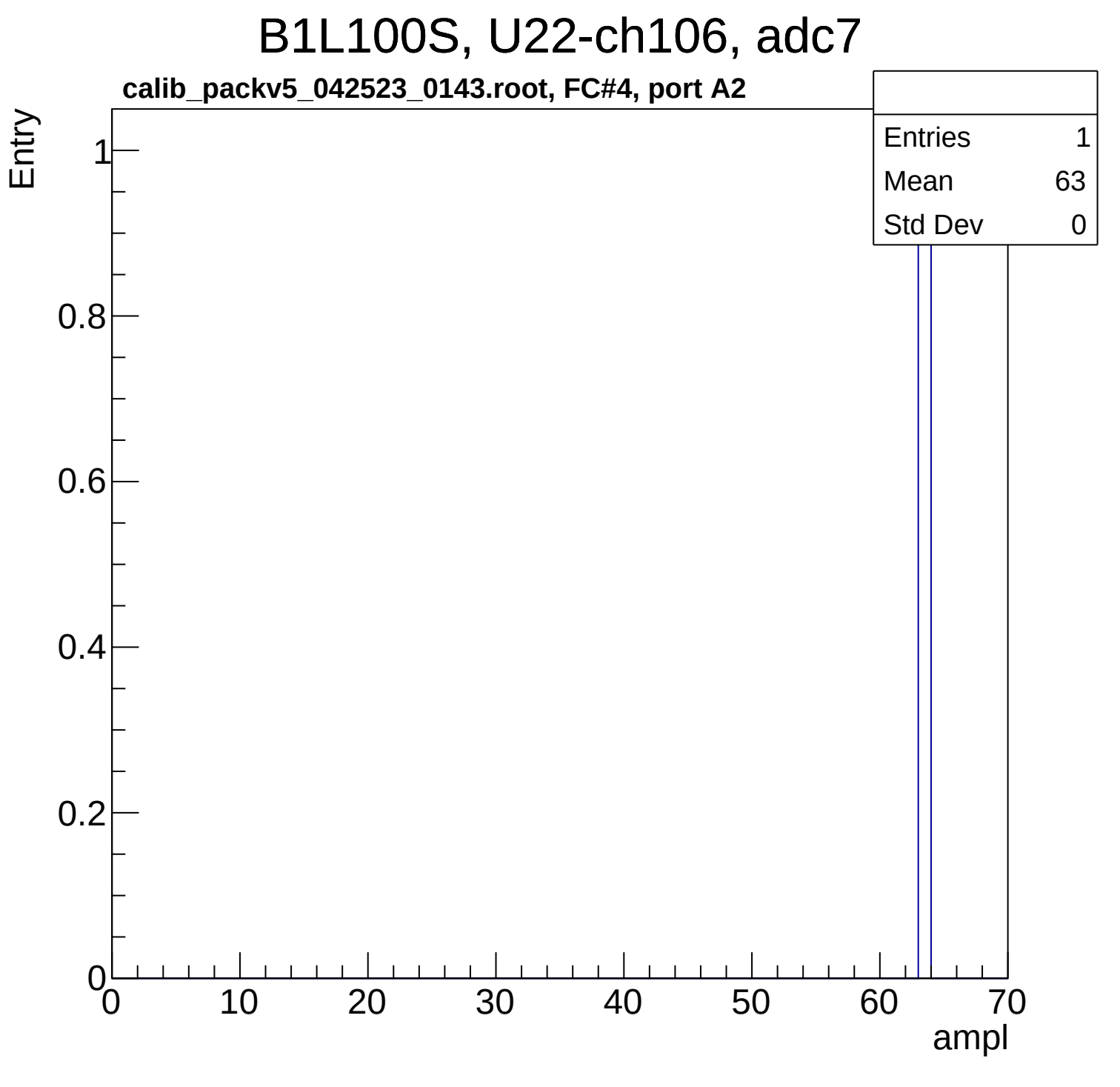
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch106, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 63
Std Dev 0
Entry
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0.6
0.4
0.2
0
0
10
20
30
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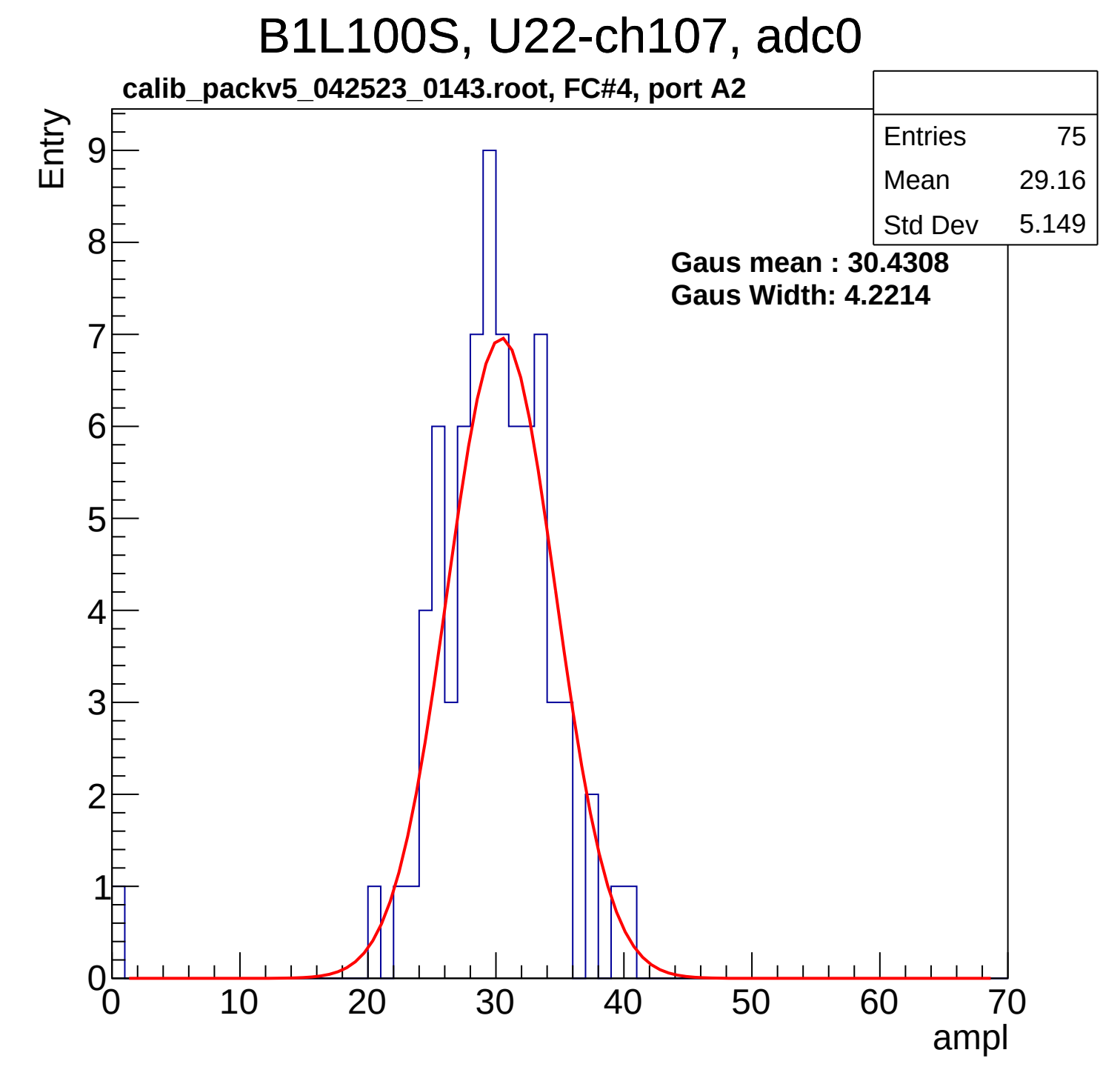
50
60
70
ampl


B1L100S, U22-ch107, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 29.16
Std Dev 5.149
Gaus mean : 30.4308
Gaus Width: 4.2214
Entry
ampl
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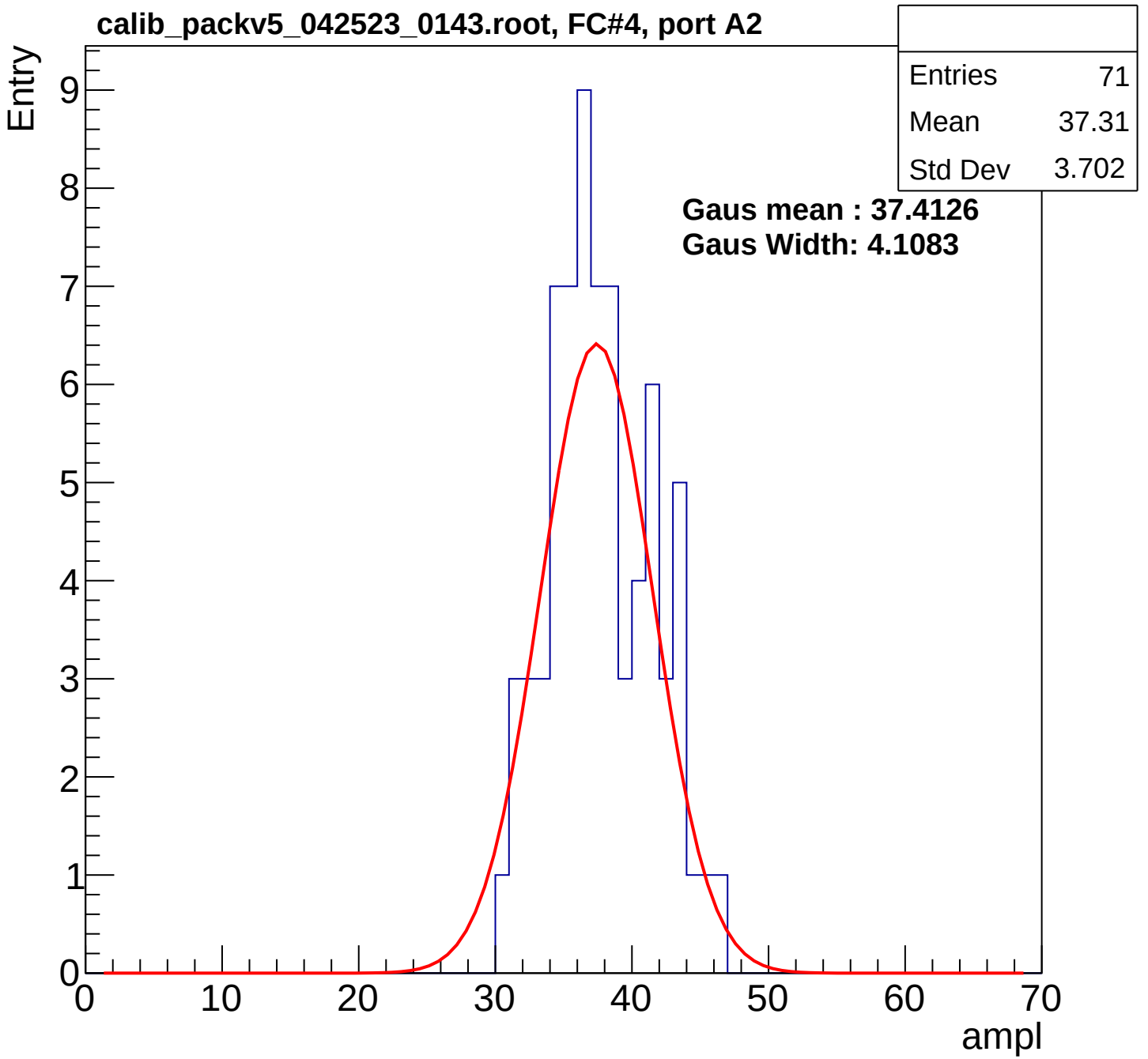
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch107, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 37.31
Std Dev 3.702
Gaus mean : 37.4126
Gaus Width: 4.1083
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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40
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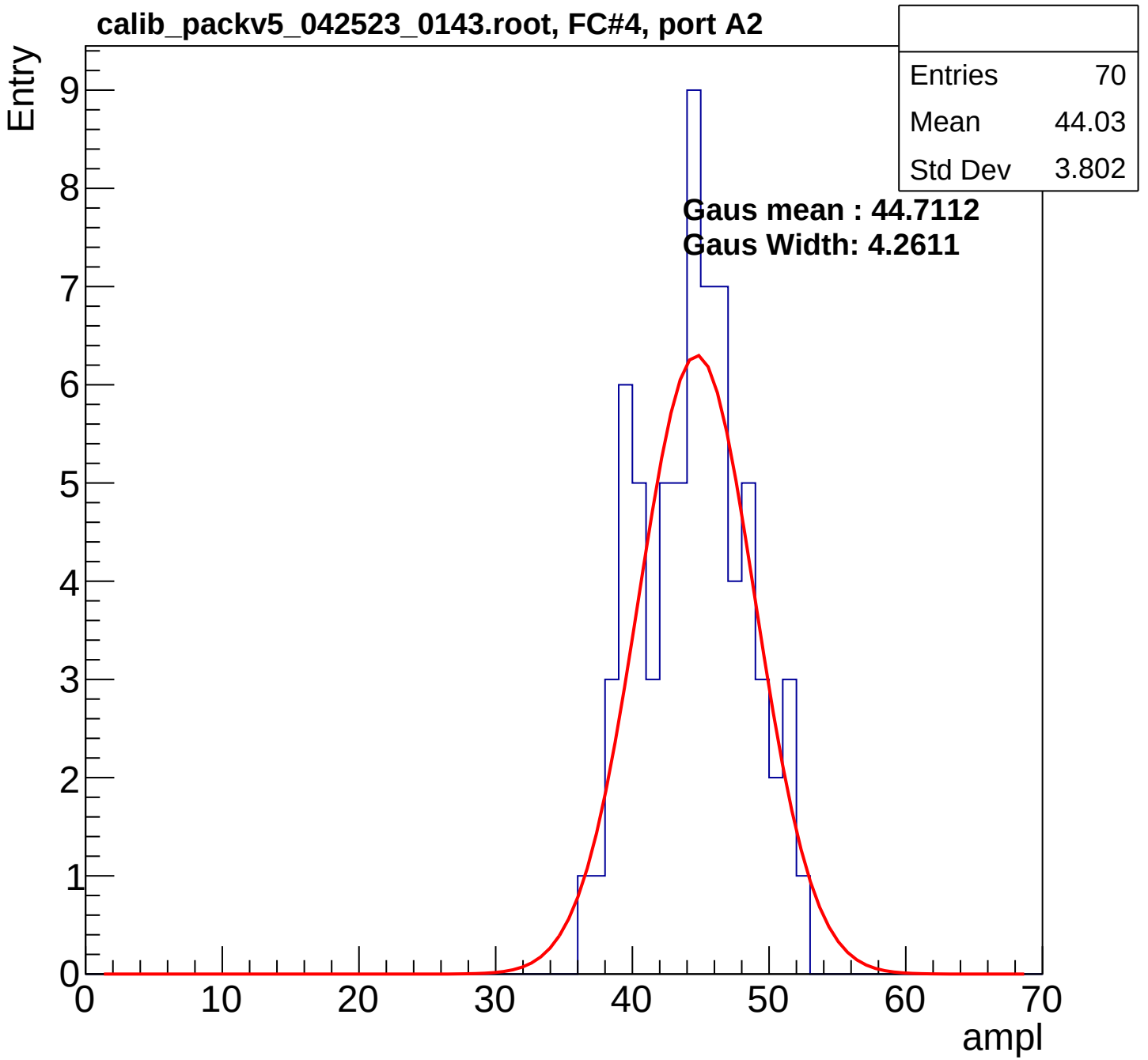
60
70


B1L100S, U22-ch107, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 44.03
Std Dev 3.802
Gaus mean : 44.7112
Gaus Width: 4.2611
Entry
ampl
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3
4
5
6
7
8
9
0
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20
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40
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60
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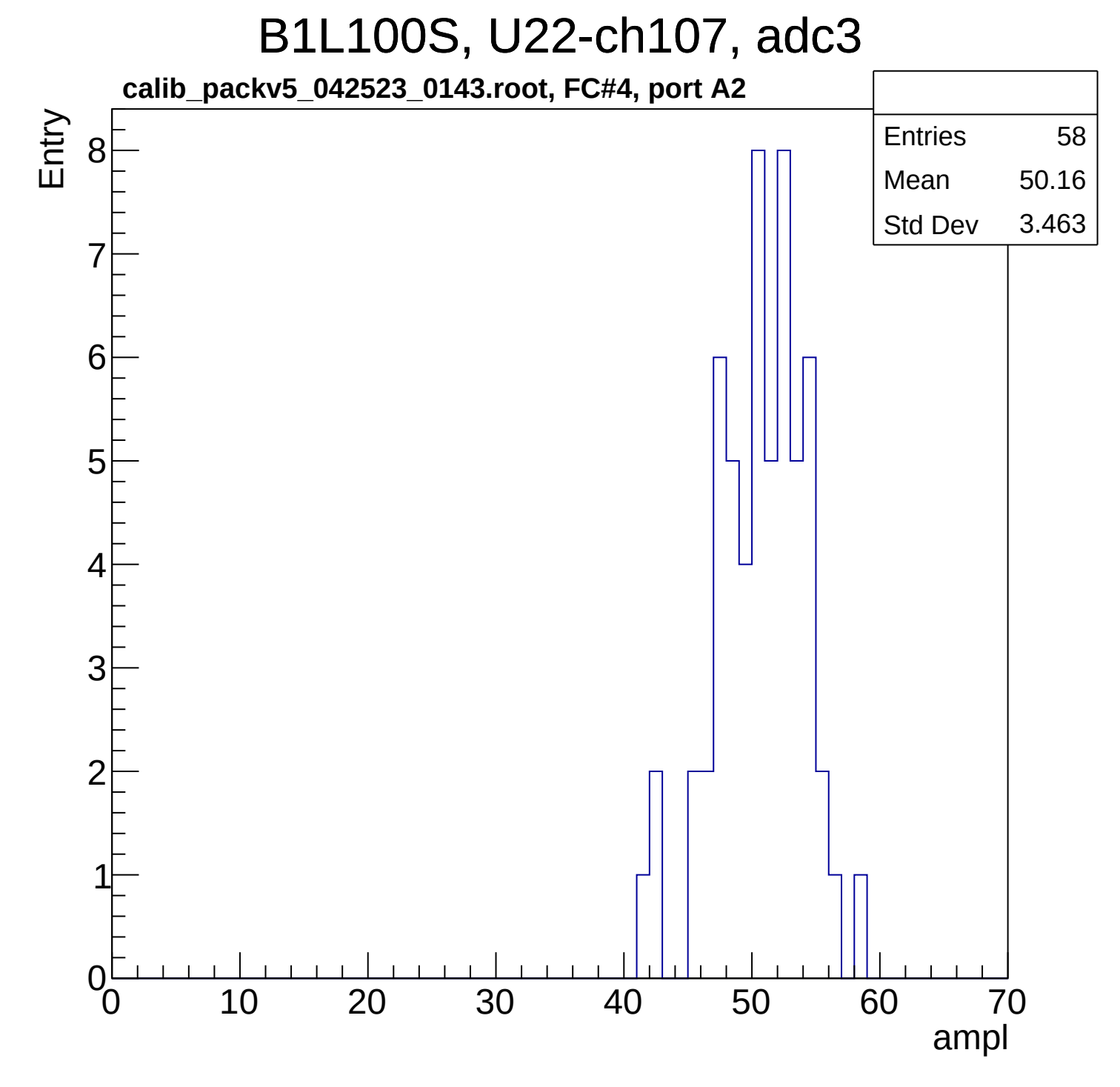
B1L100S, U22-ch107, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 50.16
Std Dev 3.463
Entry
ampl
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2
3
4
5
6
7
8
0
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60
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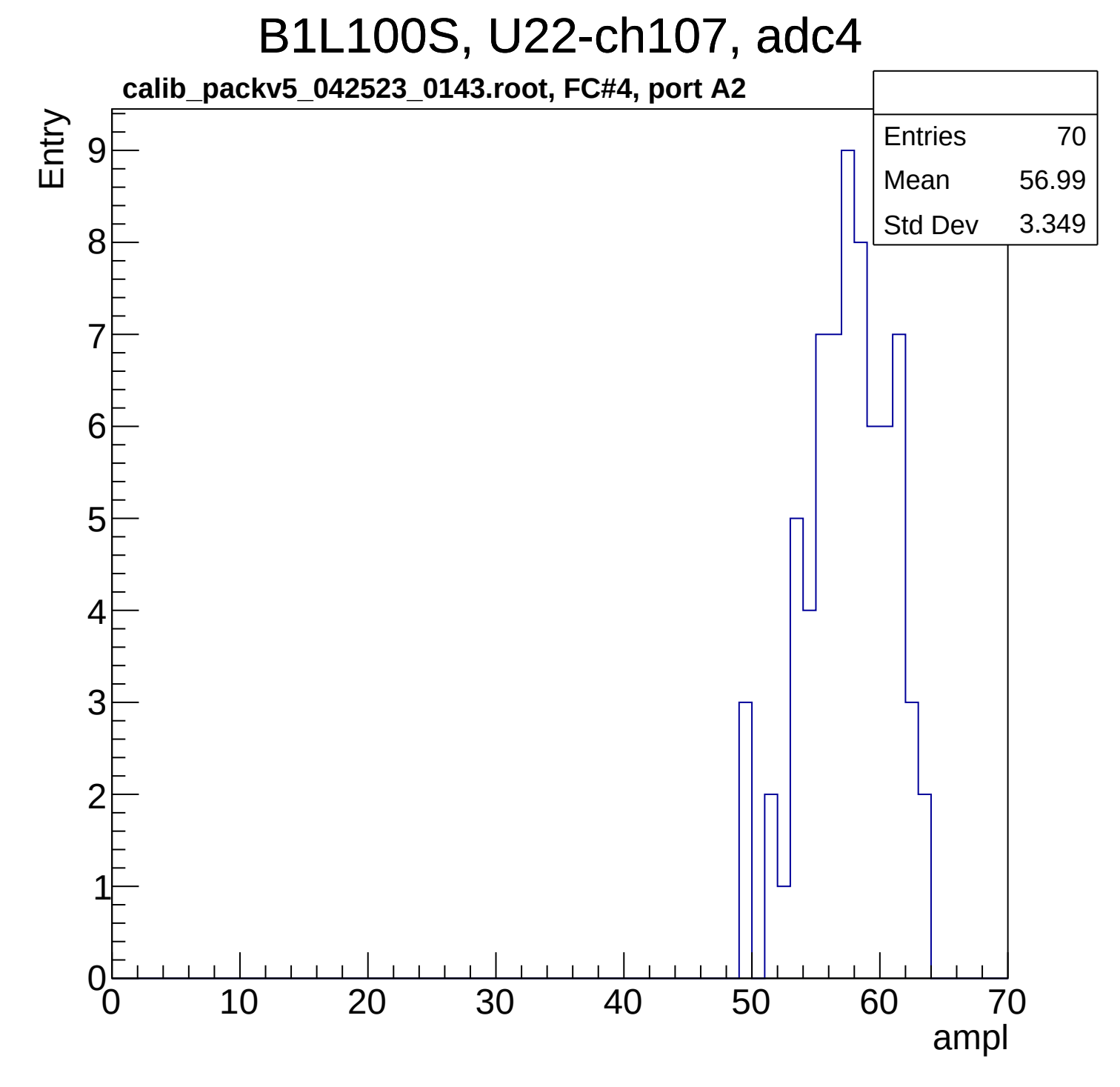
B1L100S, U22-ch107, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 56.99
Std Dev 3.349
Entry
ampl
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2
3
4
5
6
7
8
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0
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20
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60
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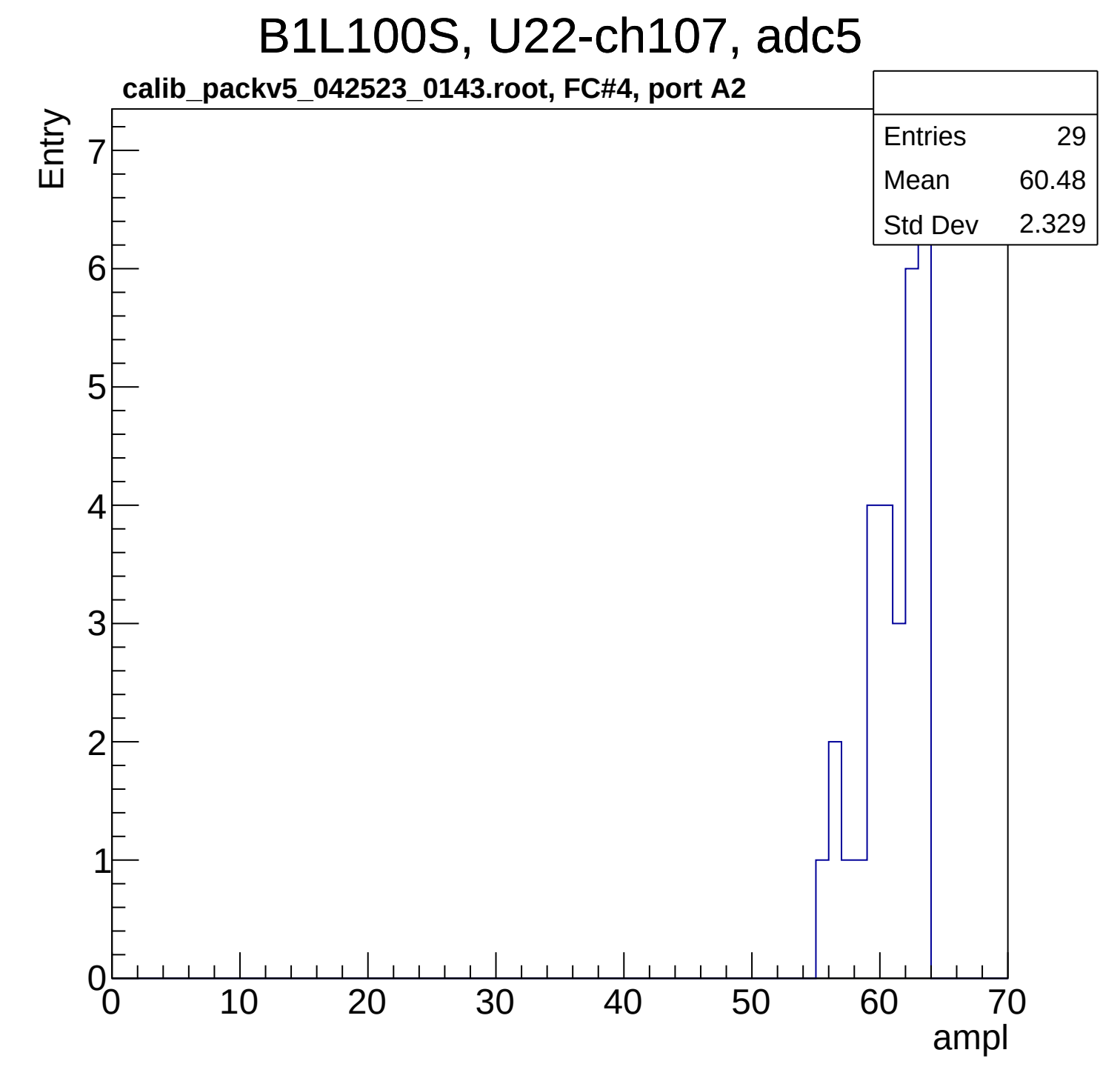
B1L100S, U22-ch107, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 29
Mean 60.48
Std Dev 2.329
Entry
ampl
0
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2
3
4
5
6
7
0
10
20
30
40
50
60
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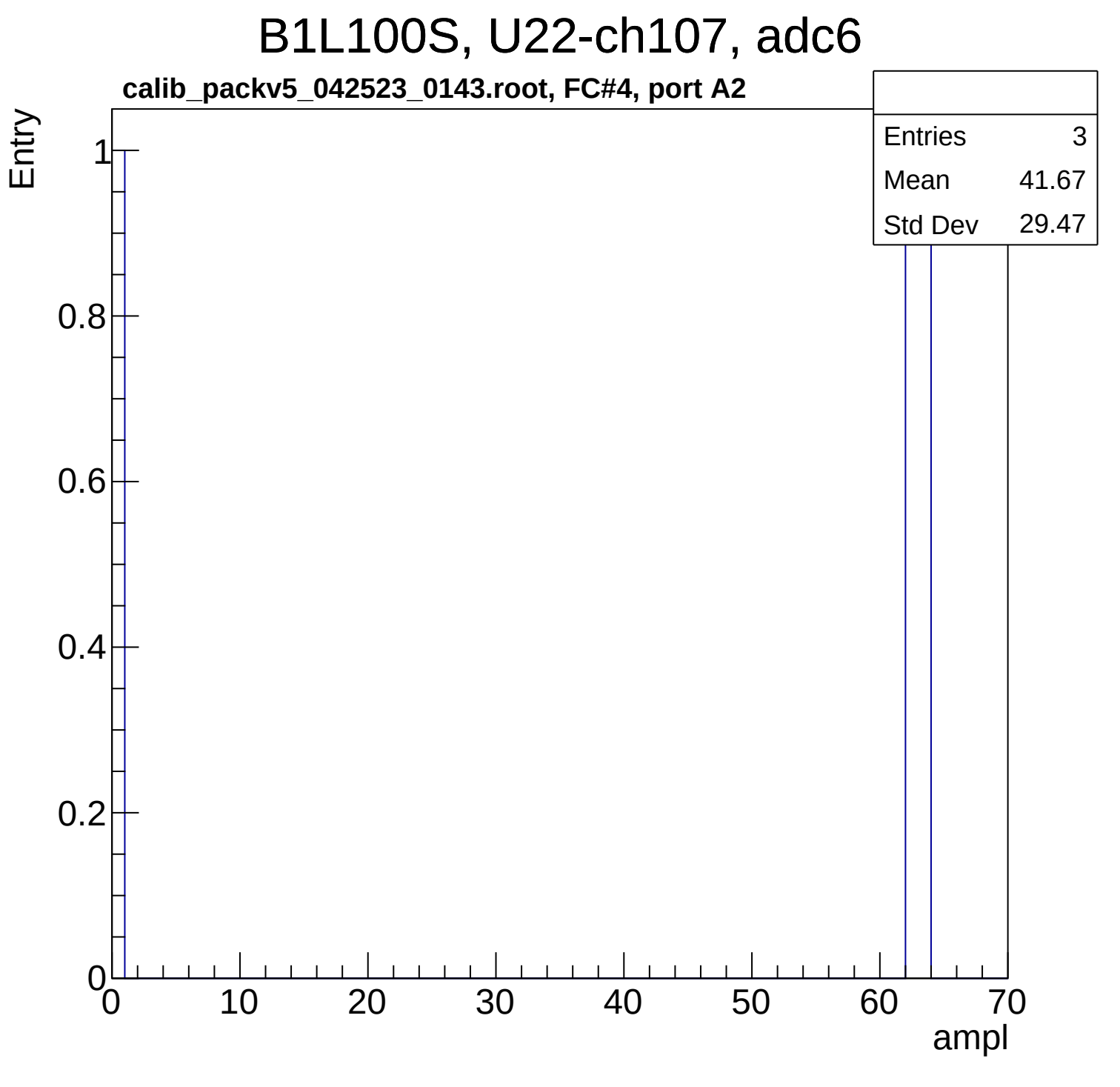
B1L100S, U22-ch107, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 41.67
Std Dev 29.47
Entry
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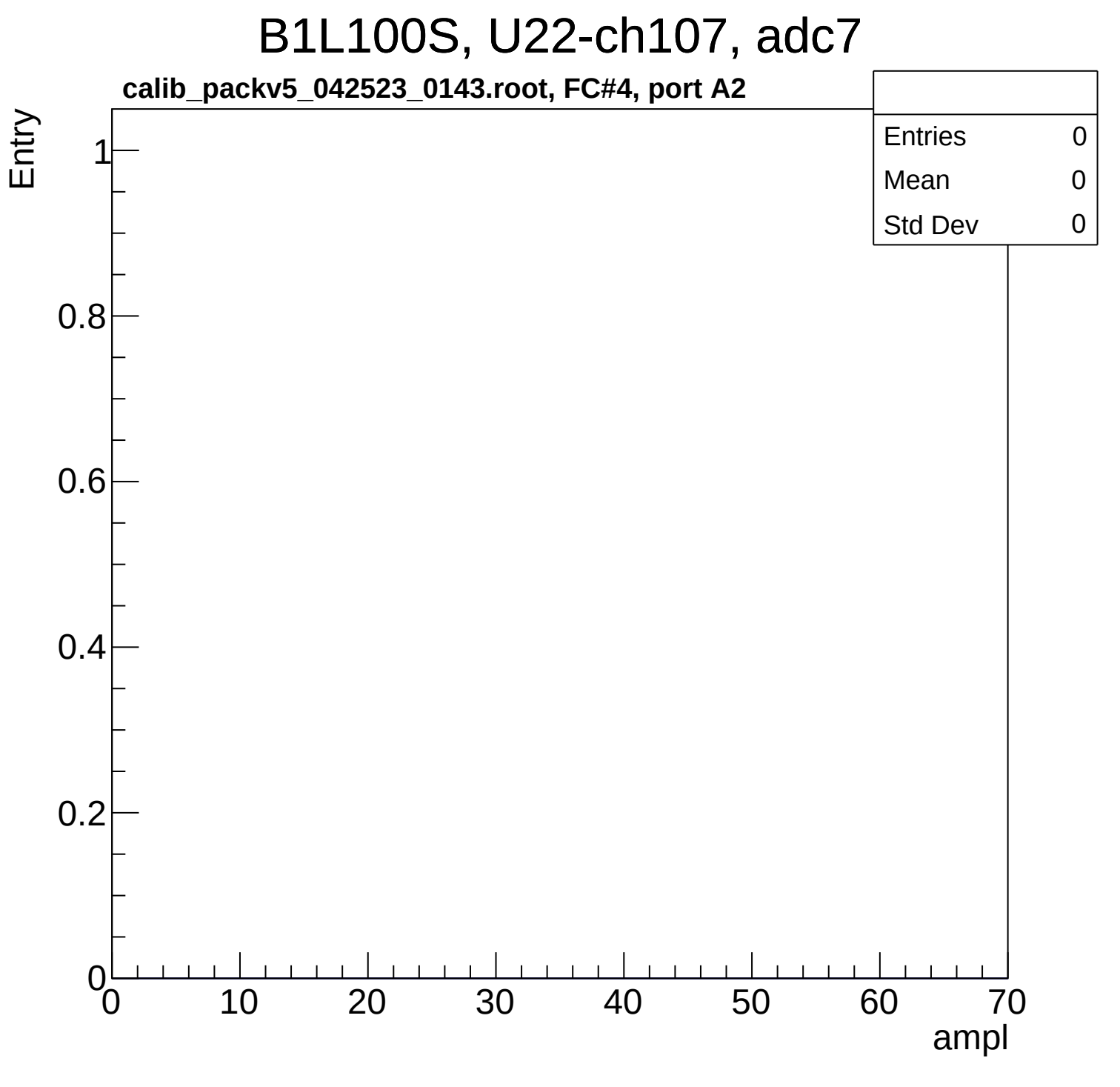
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch107, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
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0.6
0.4
0.2
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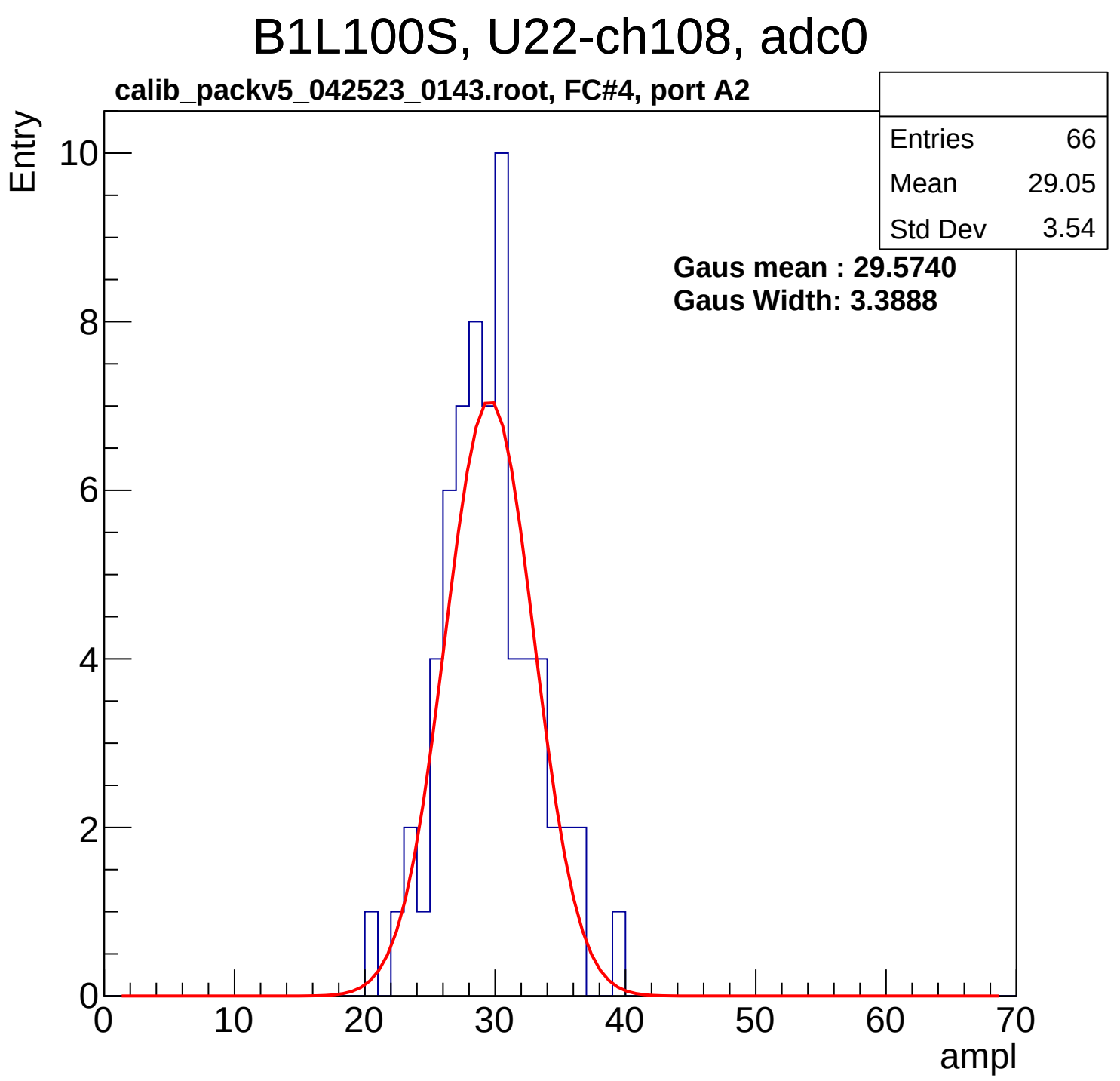
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch108, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 29.05
Std Dev 3.54
Gaus mean : 29.5740
Gaus Width: 3.3888
Entry
ampl
0
2
4
6
8
10
0
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20
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40
50
60
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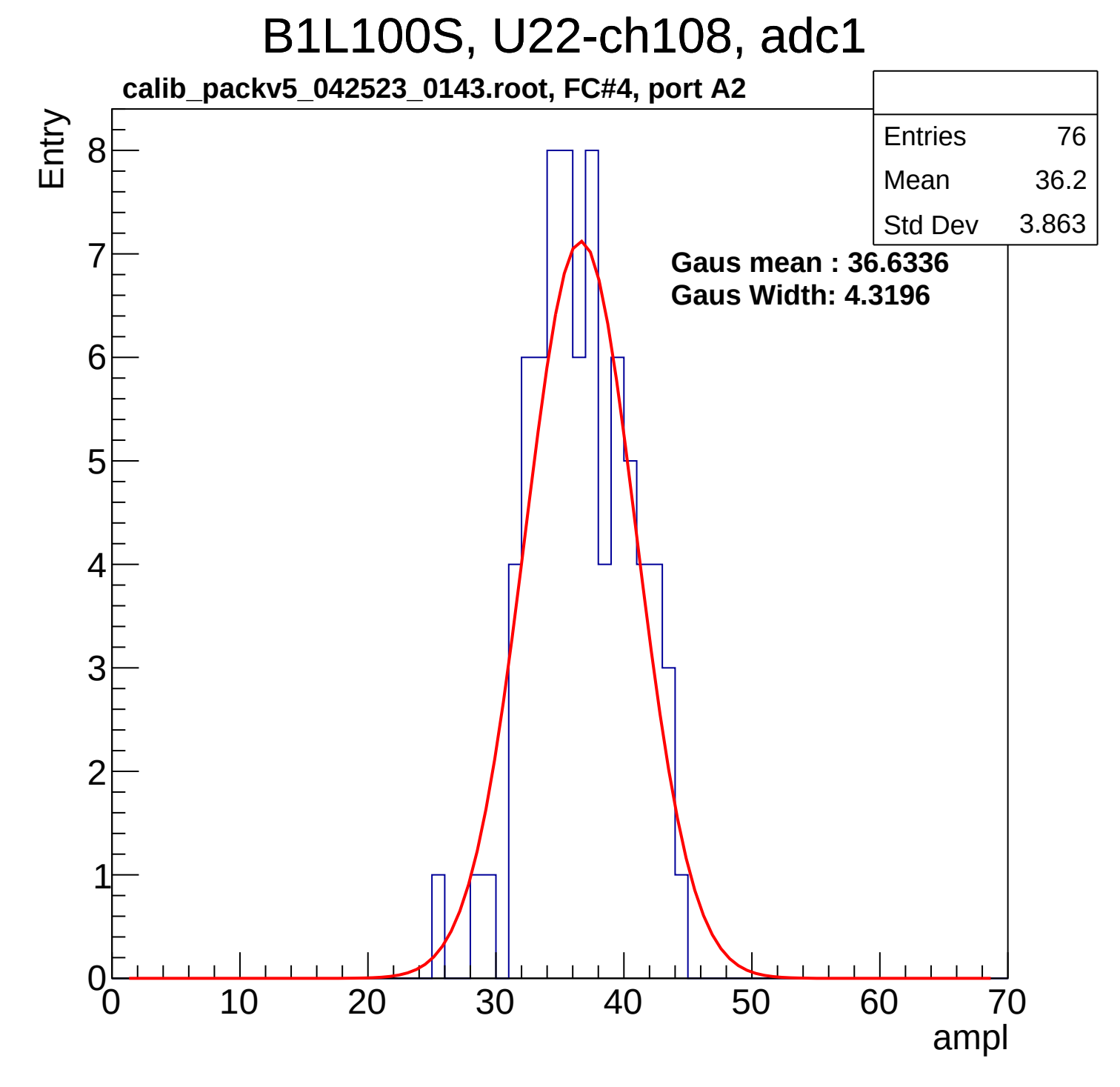
B1L100S, U22-ch108, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 36.2
Std Dev 3.863
Gaus mean : 36.6336
Gaus Width: 4.3196
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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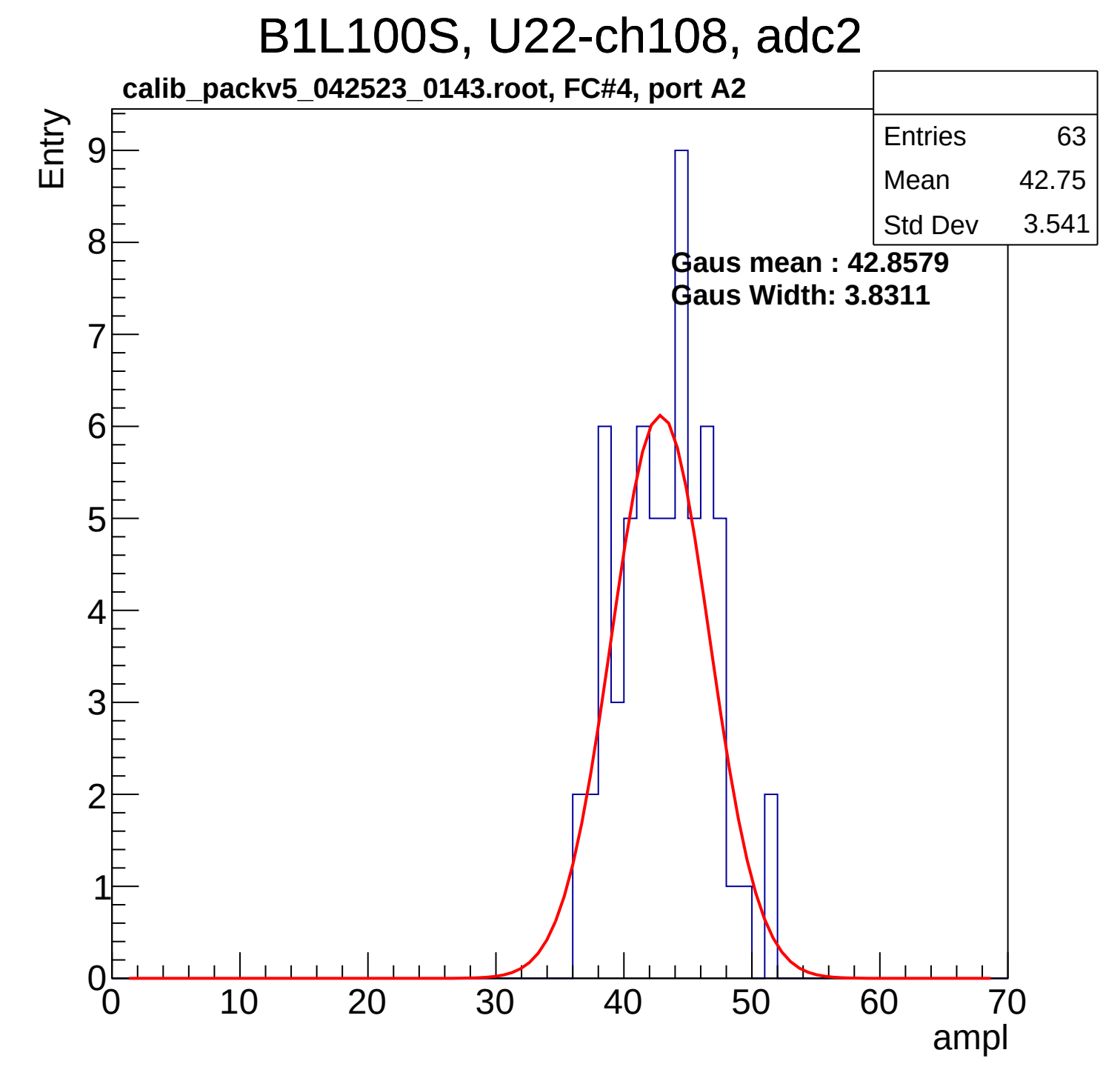
B1L100S, U22-ch108, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 42.75
Std Dev 3.541
Gaus mean : 42.8579
Gaus Width: 3.8311
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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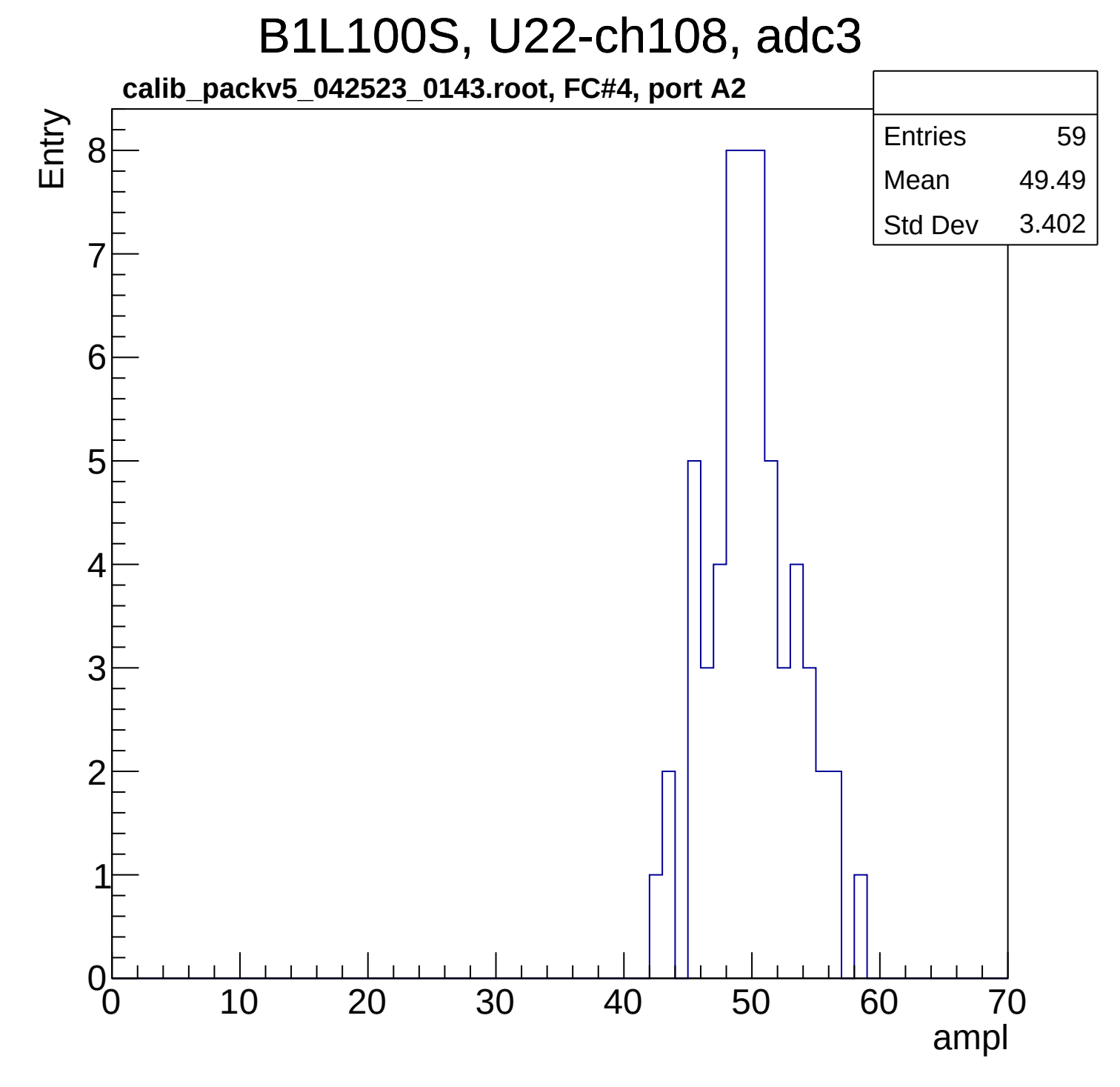
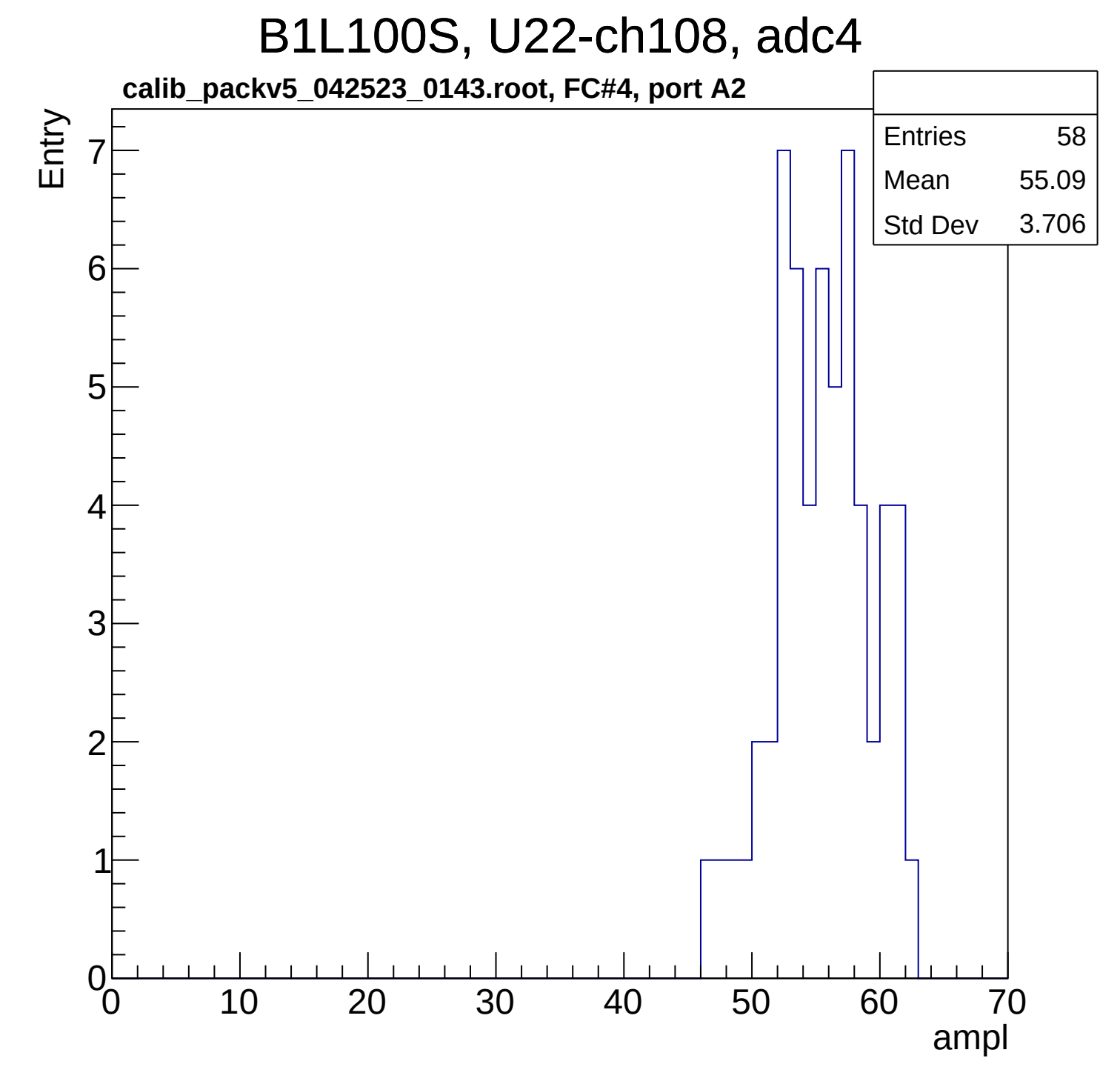
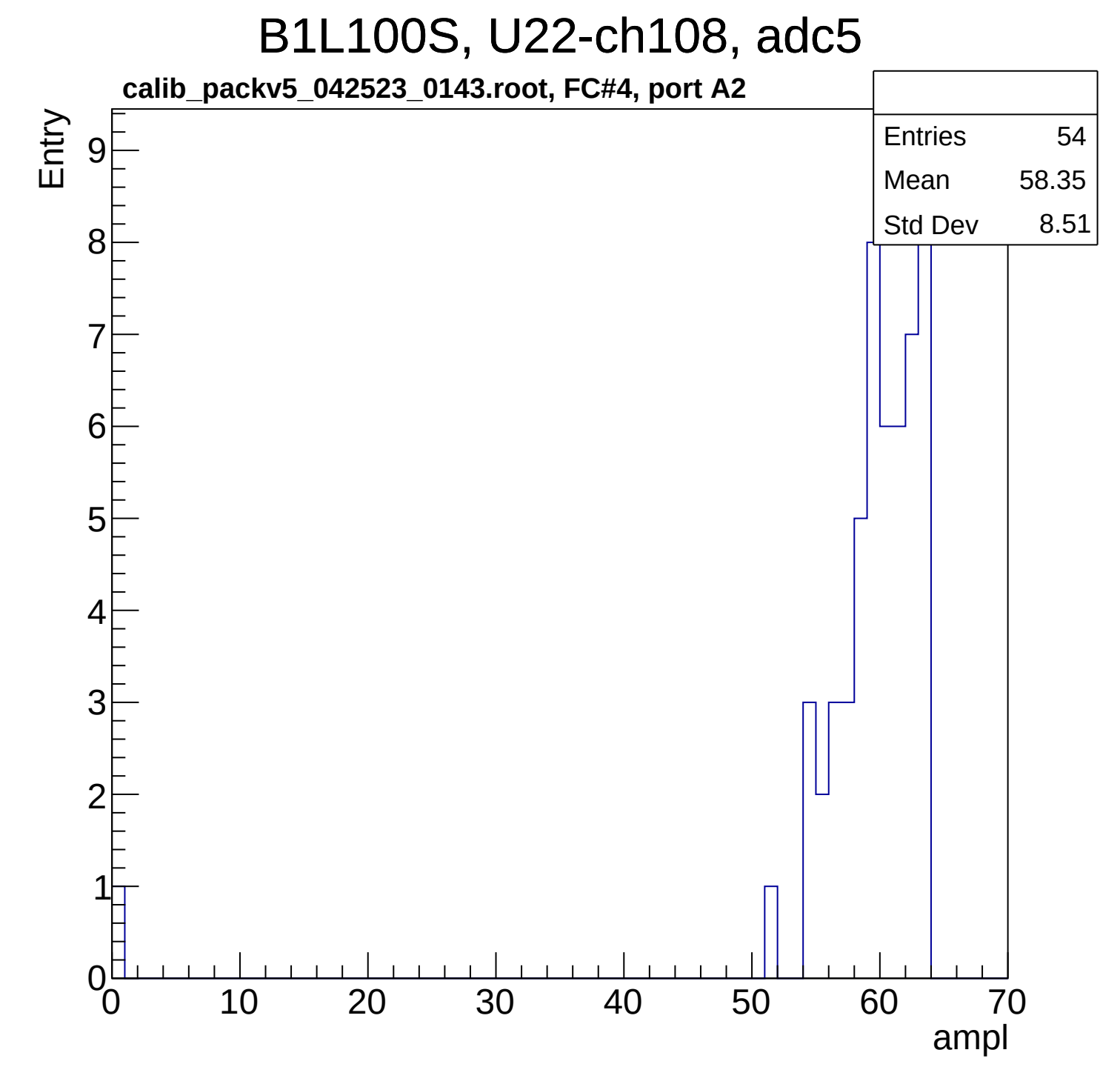

B1L100S, U22-ch108, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 49.49
Std Dev 3.402
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch108, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 55.09
Std Dev 3.706
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch108, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 58.35
Std Dev 8.51
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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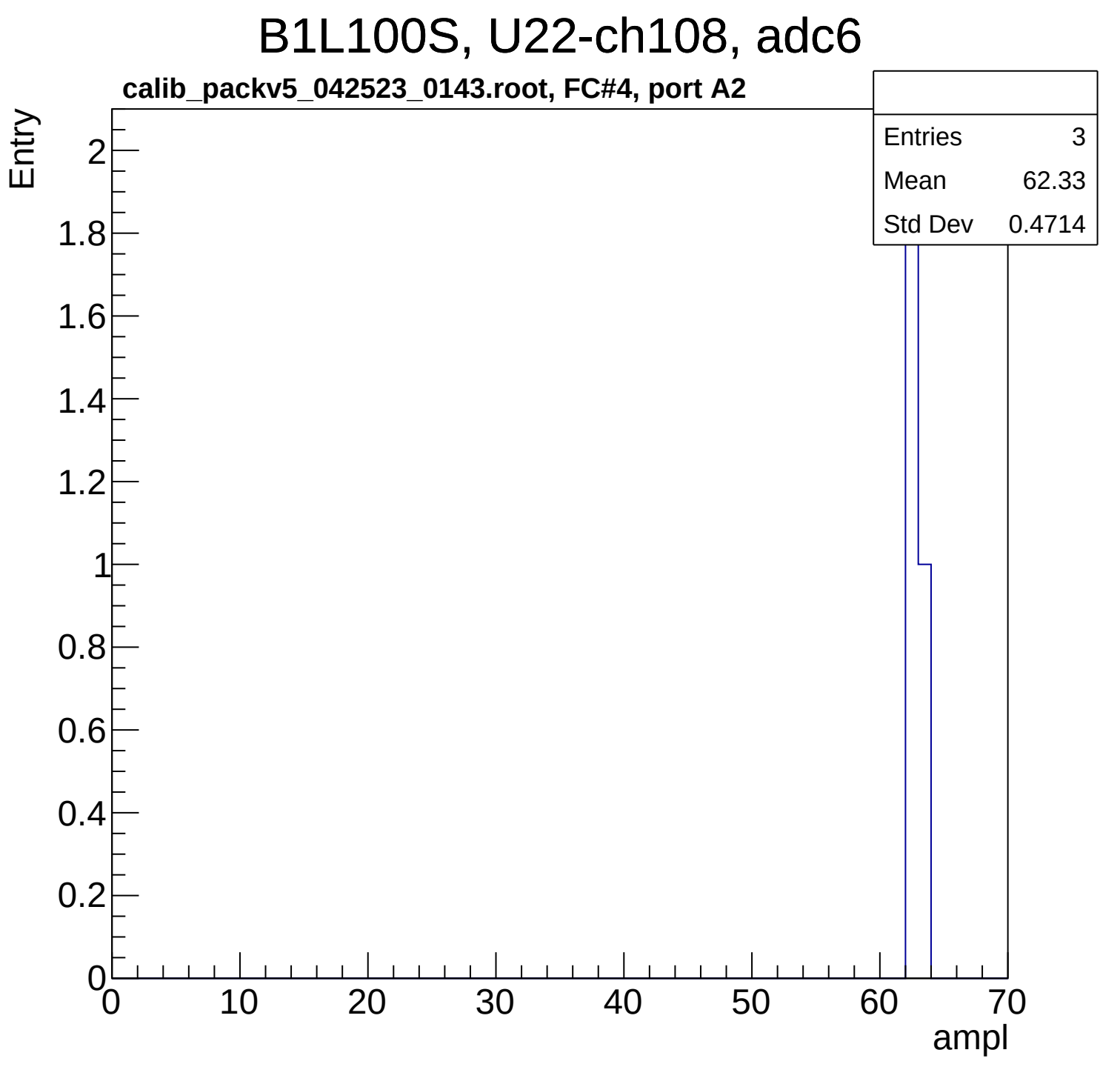
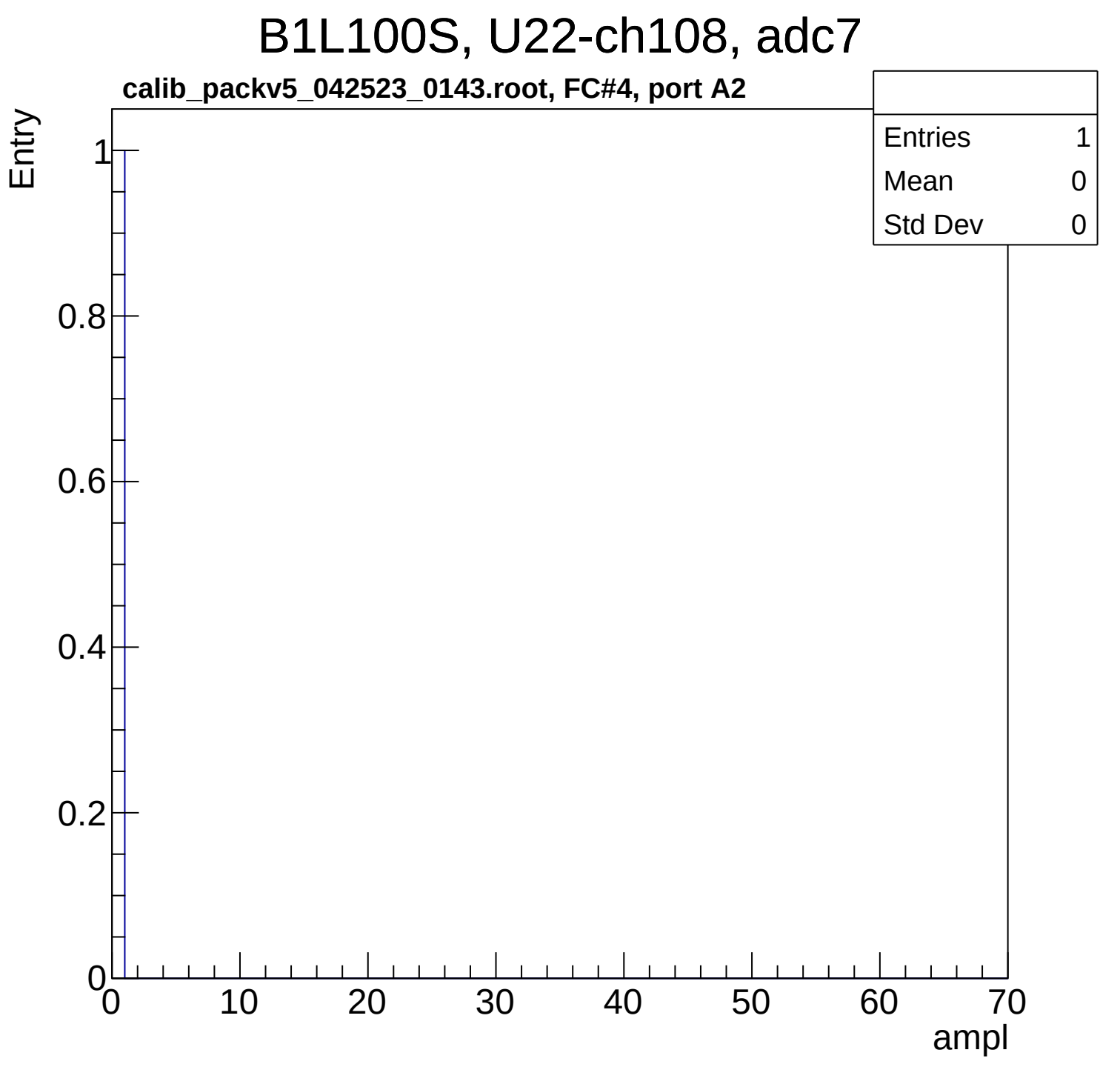
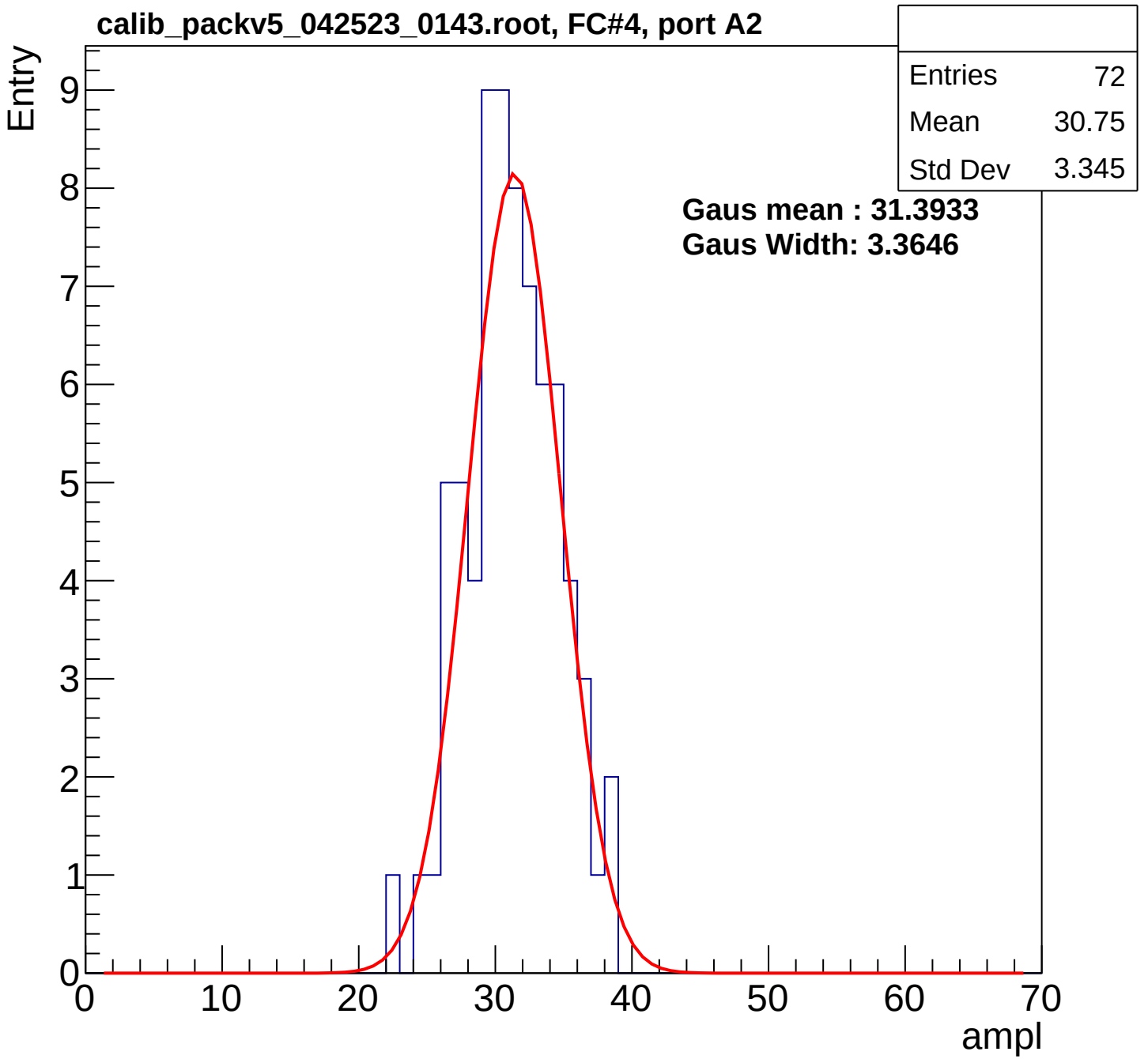
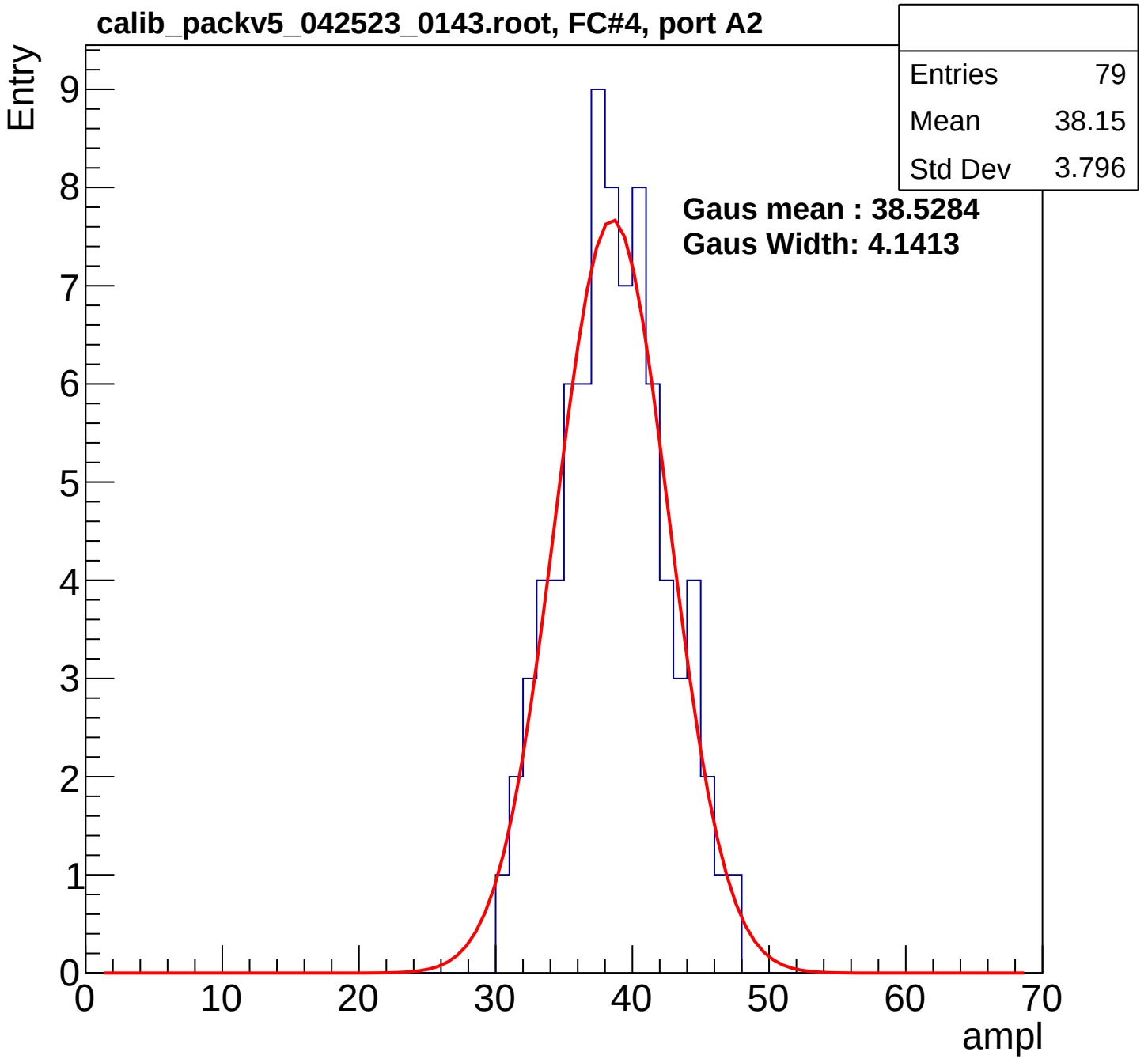

B1L100S, U22-ch108, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.33
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch108, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch109, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.75
Std Dev 3.345
Gaus mean : 31.3933
Gaus Width: 3.3646
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch109, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 38.15
Std Dev 3.796
Gaus mean : 38.5284
Gaus Width: 4.1413
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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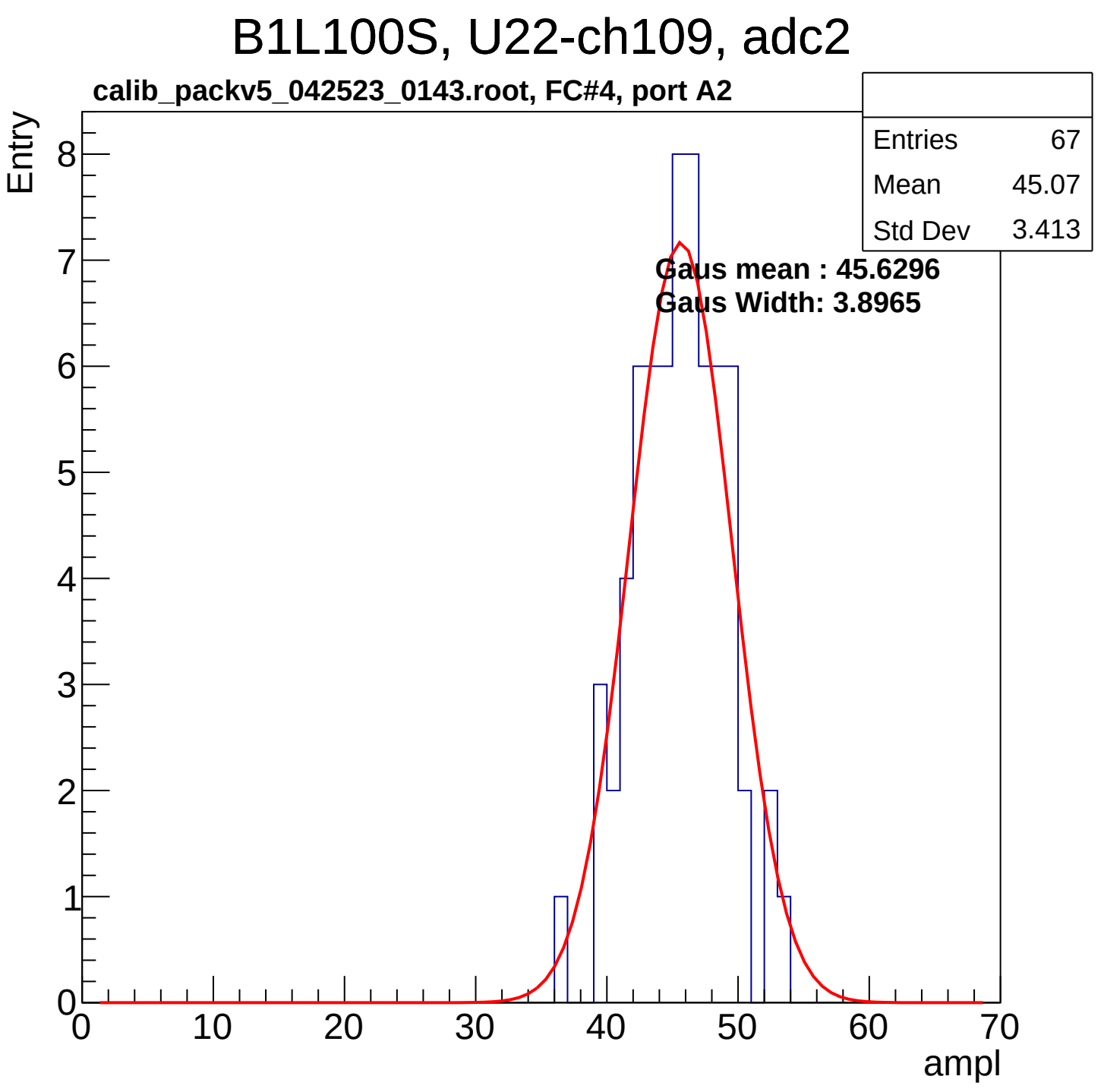
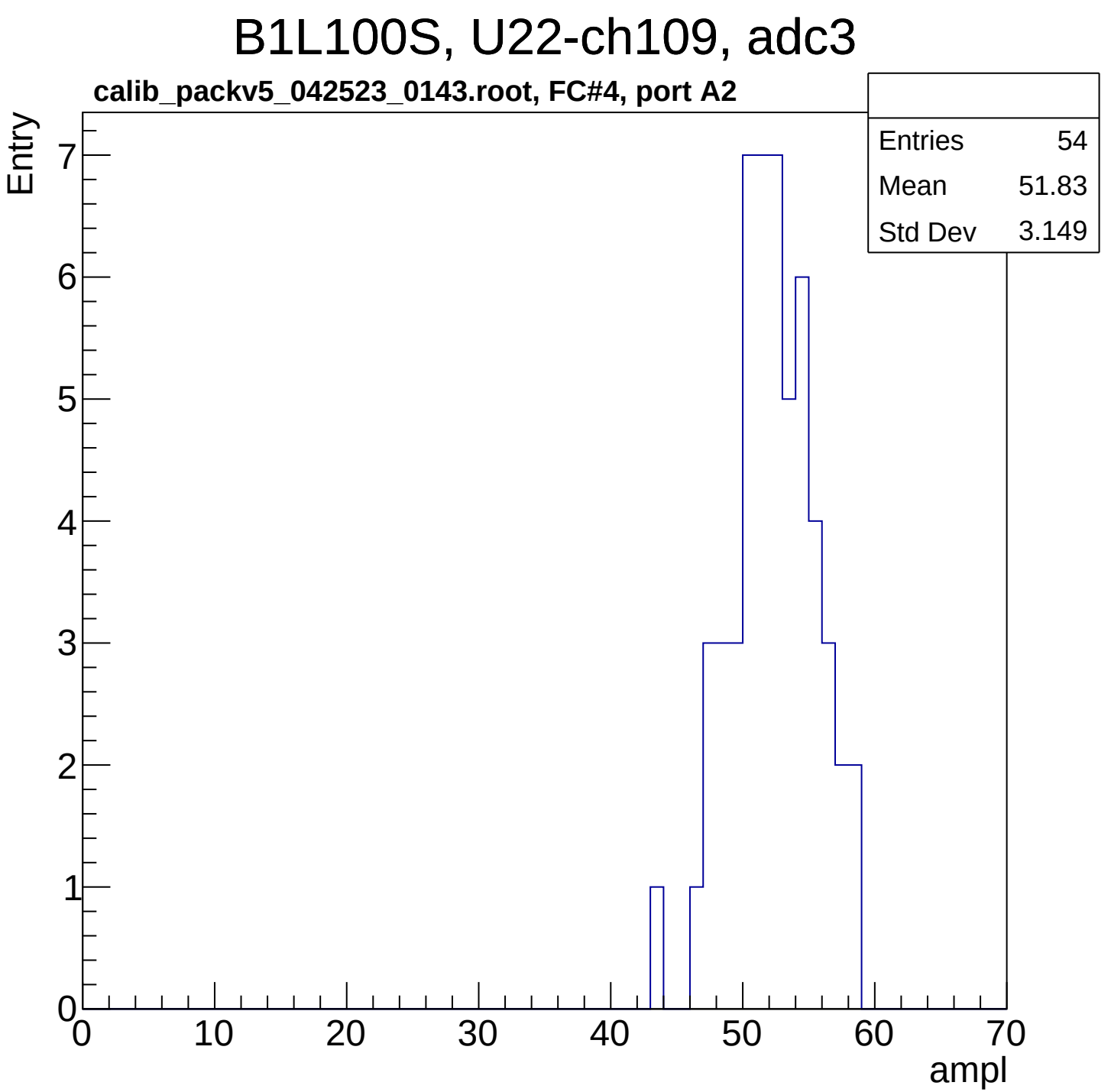
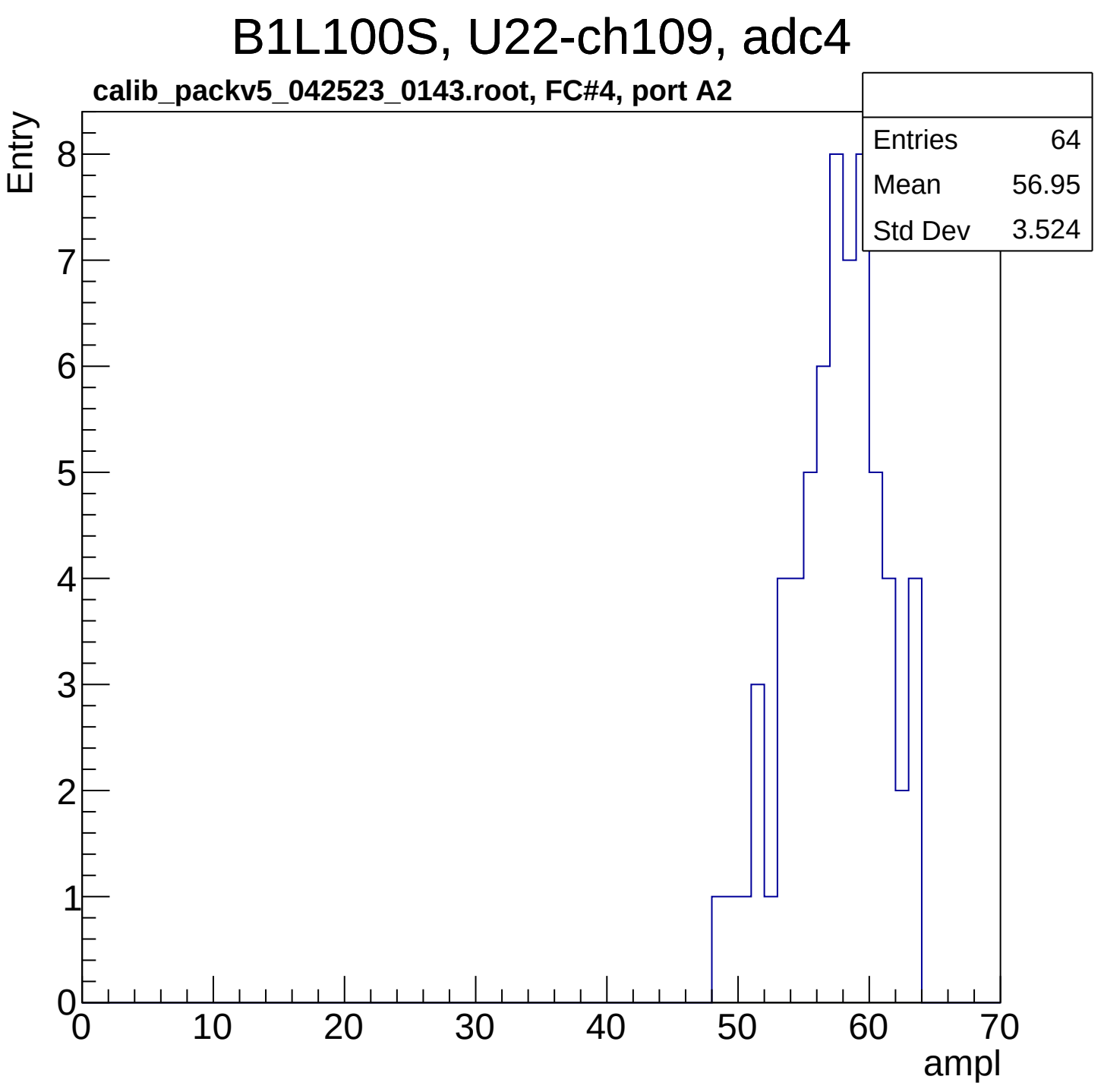

B1L100S, U22-ch109, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 45.07
Std Dev 3.413
Gaus mean : 45.6296
Gaus Width: 3.8965
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch109, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 51.83
Std Dev 3.149
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch109, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 56.95
Std Dev 3.524
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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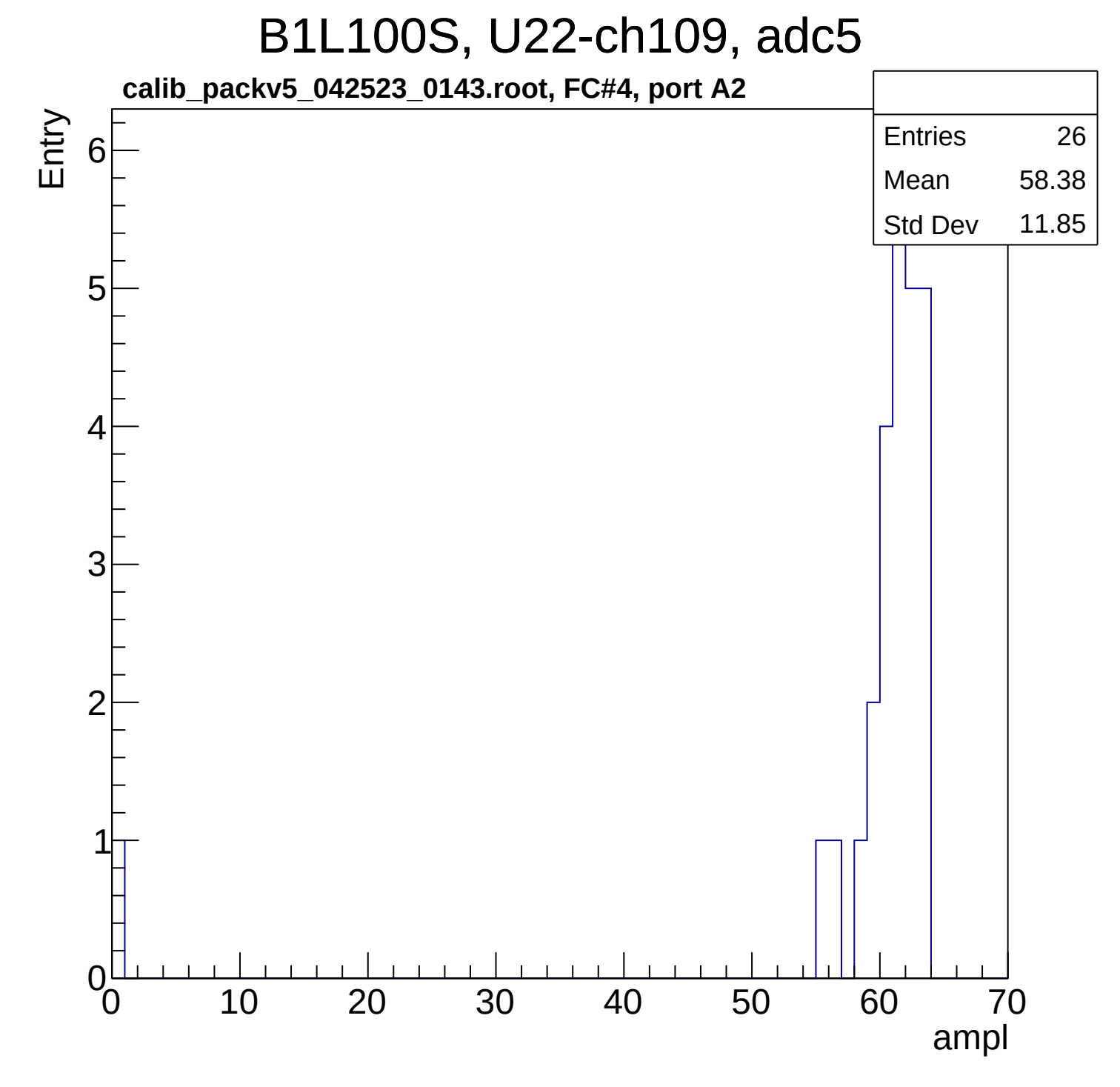
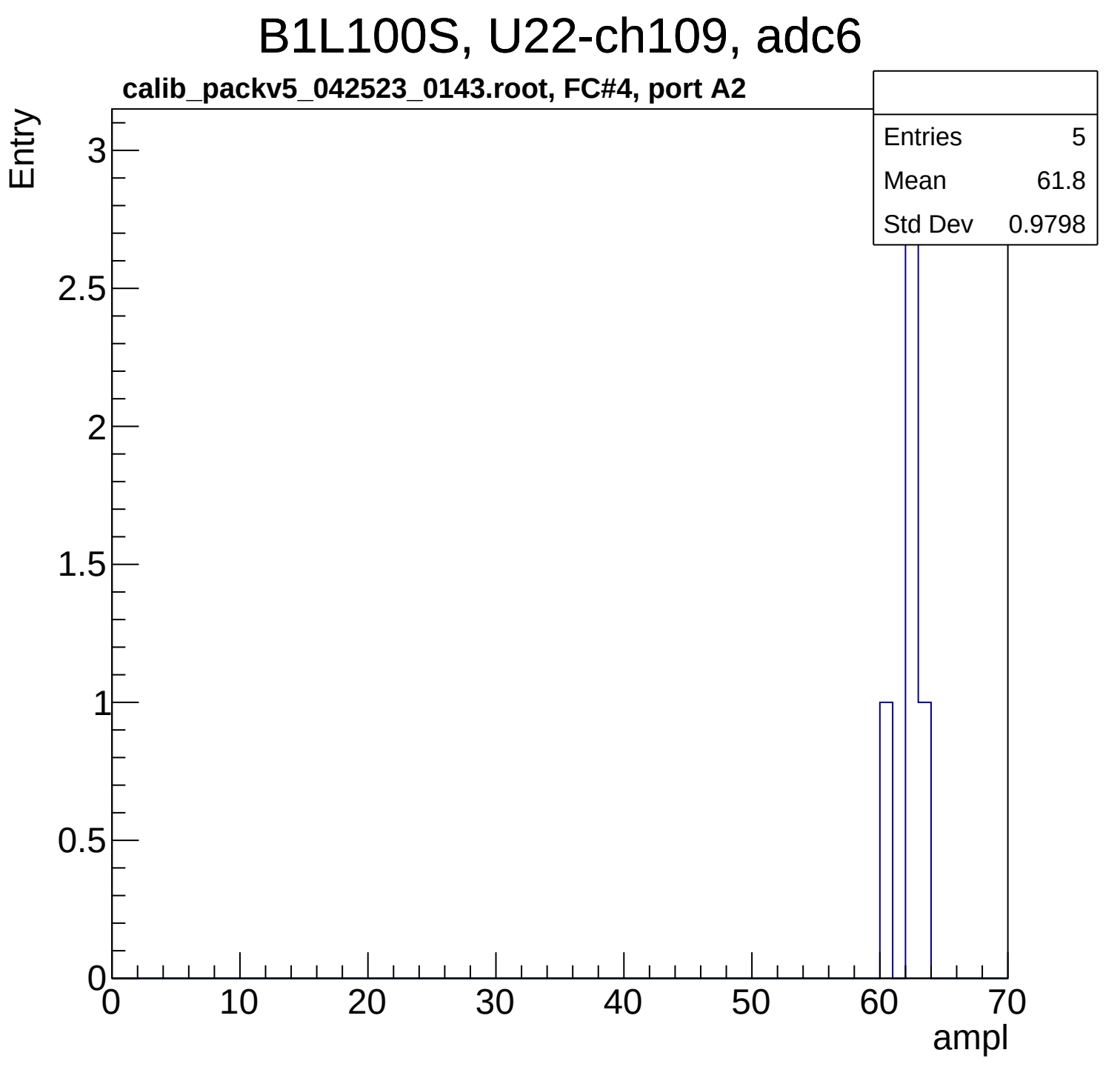
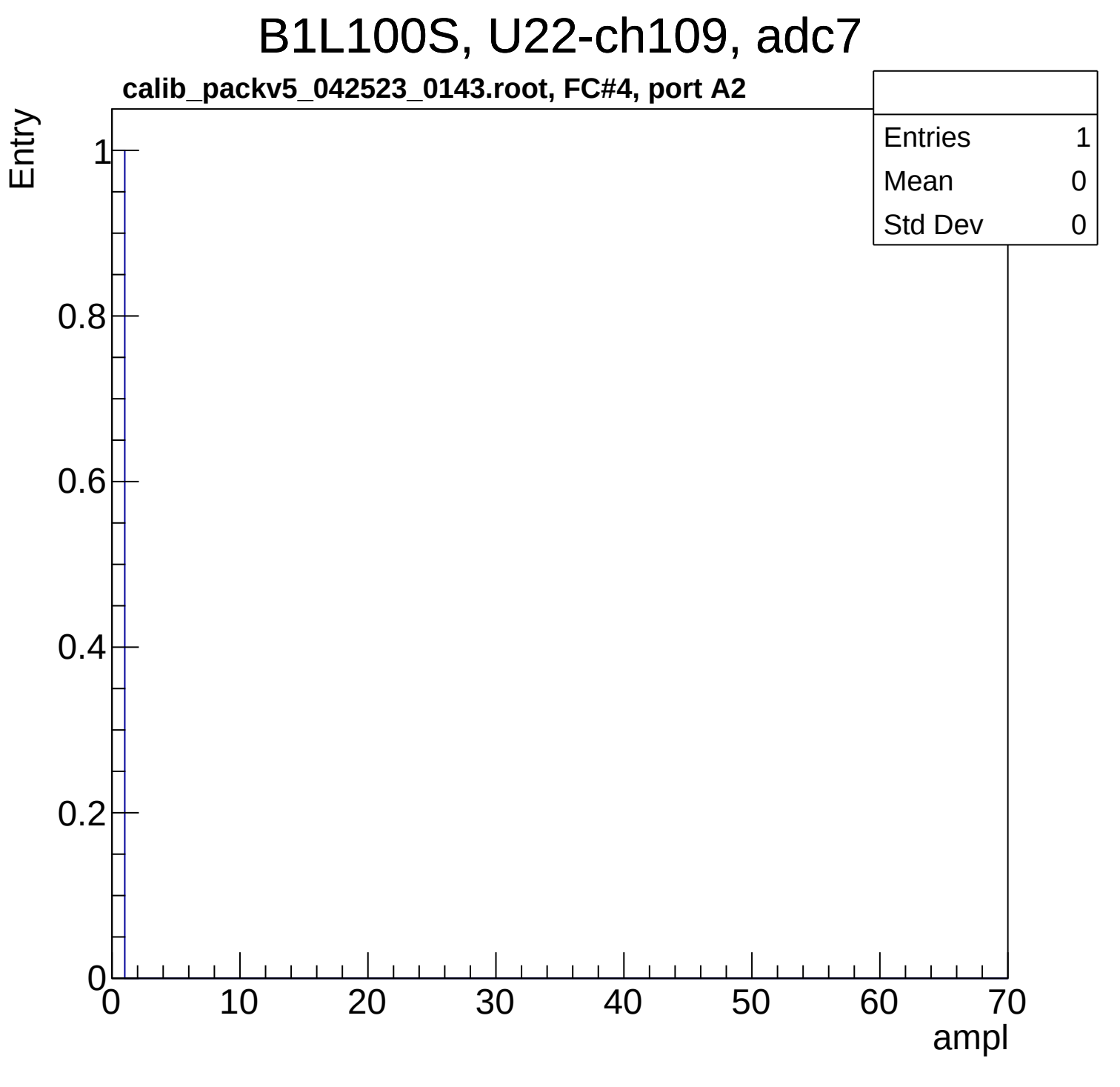
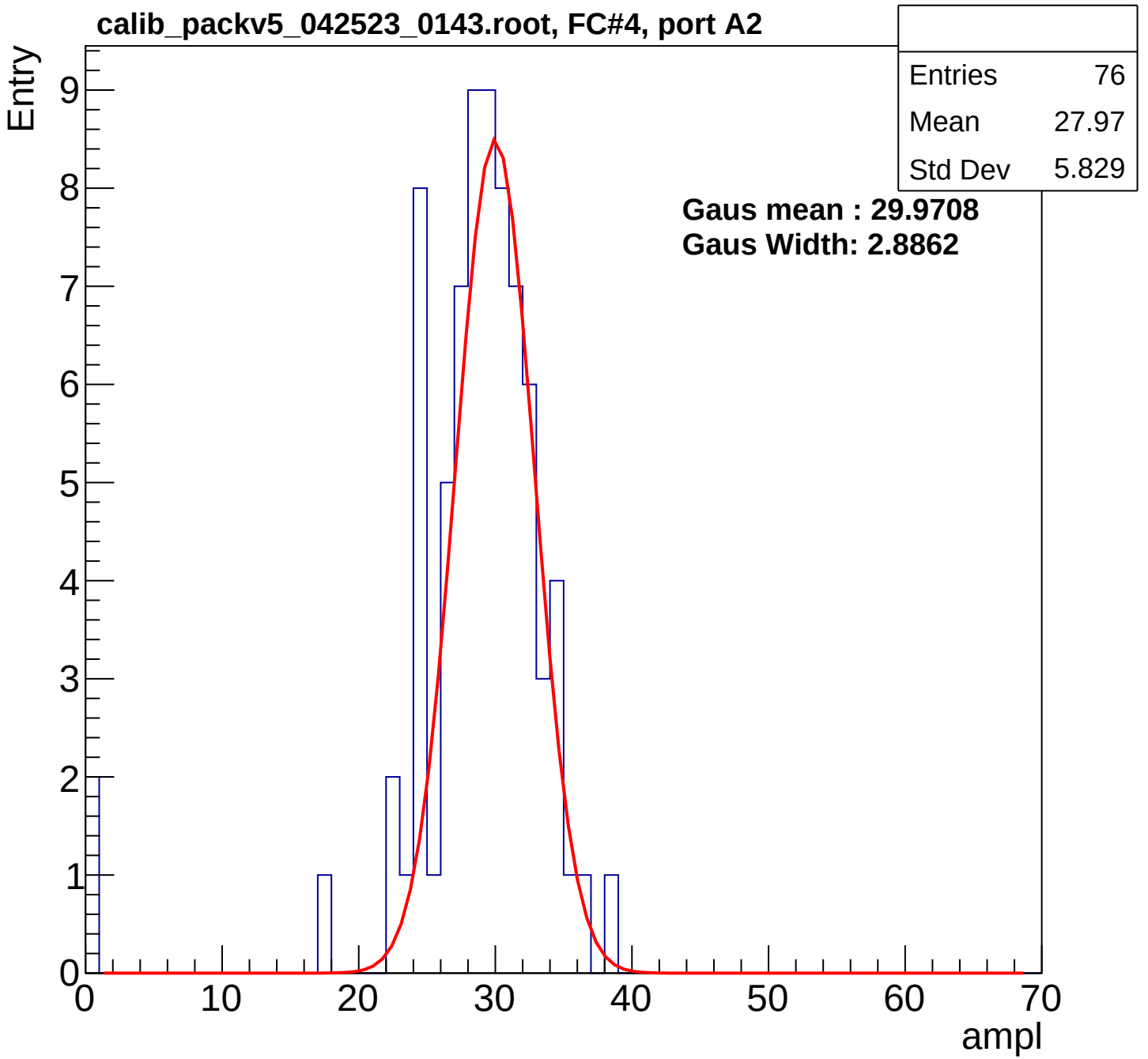
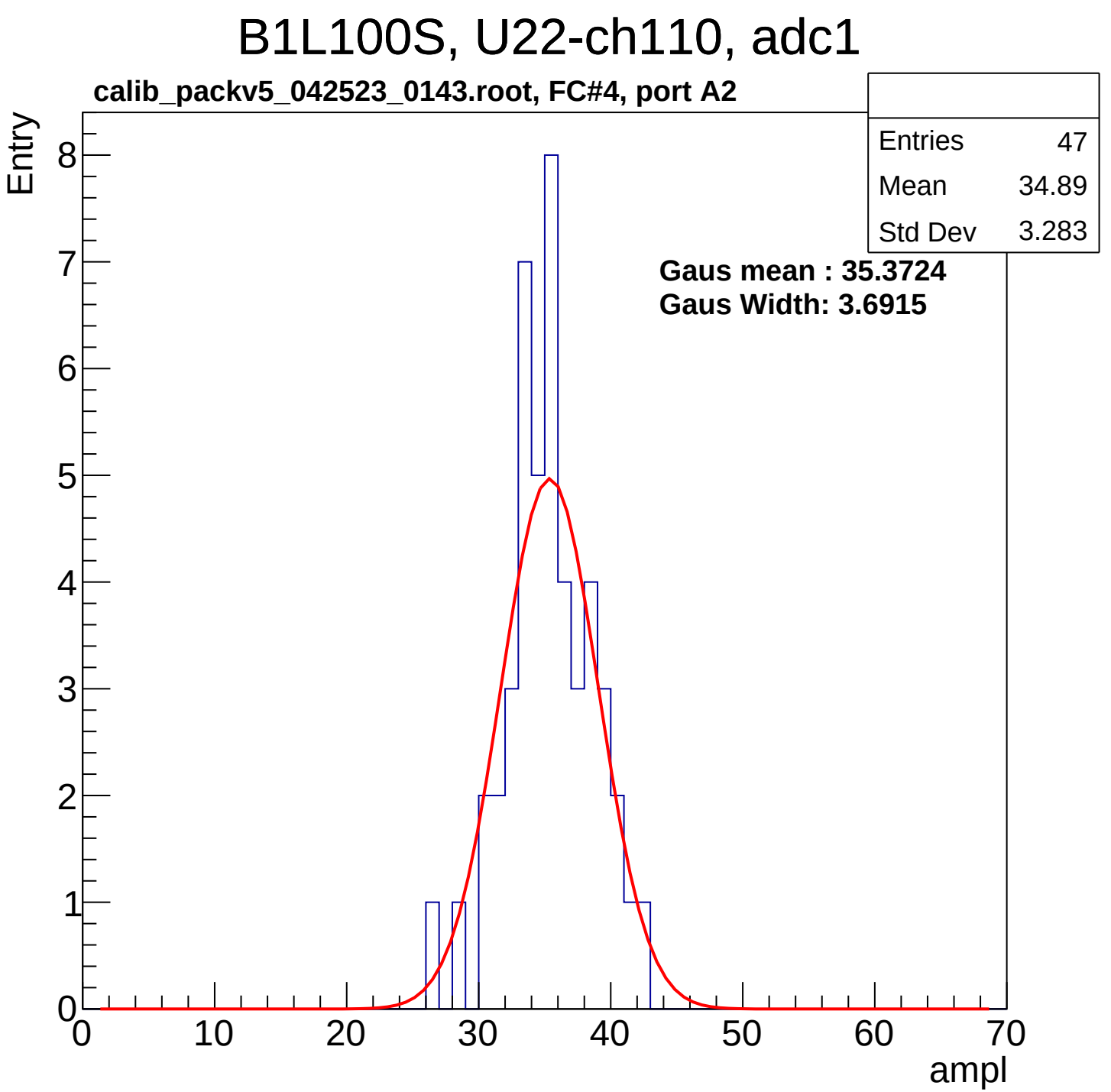
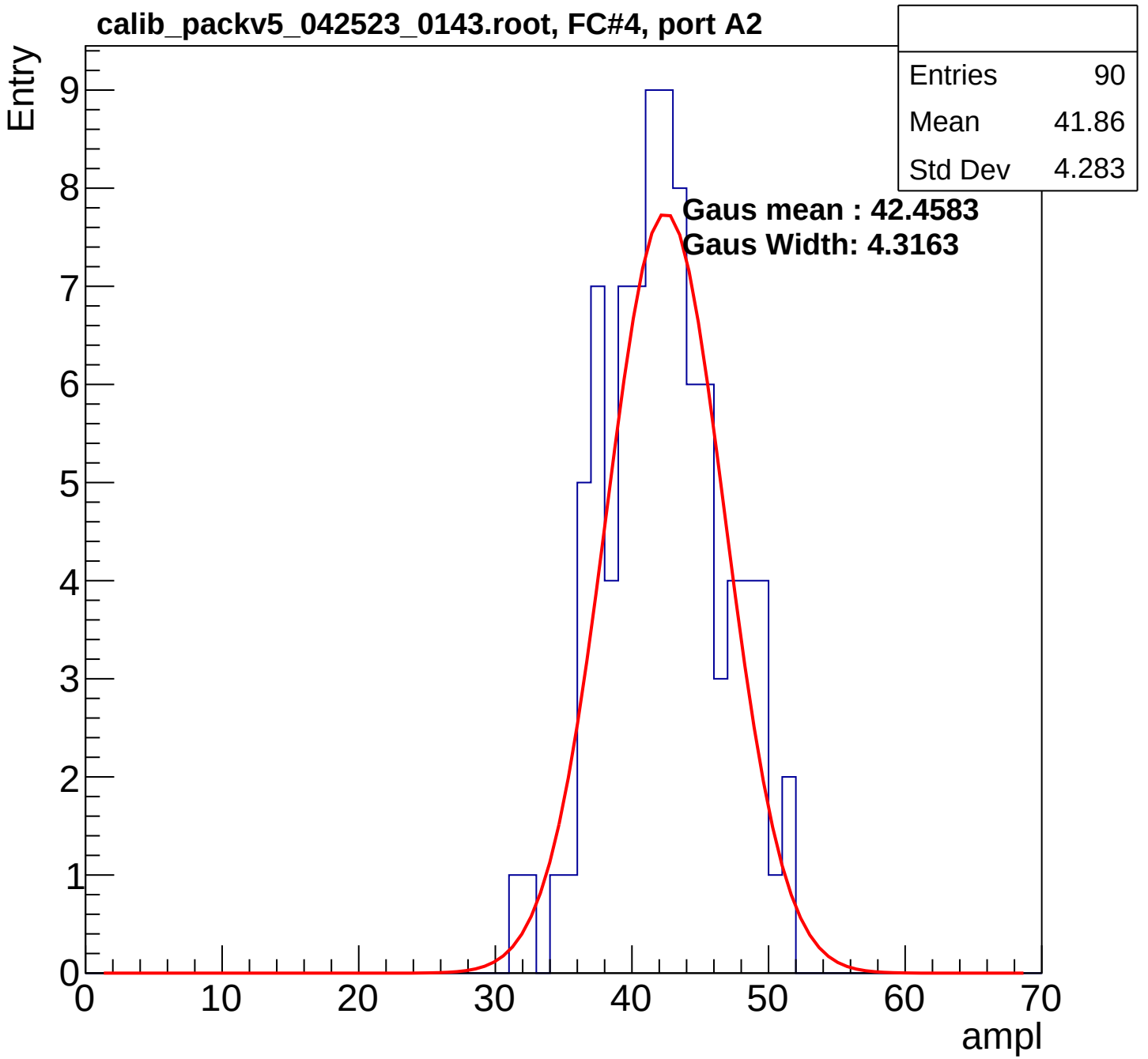

B1L100S, U22-ch109, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 26
Mean 58.38
Std Dev 11.85
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70

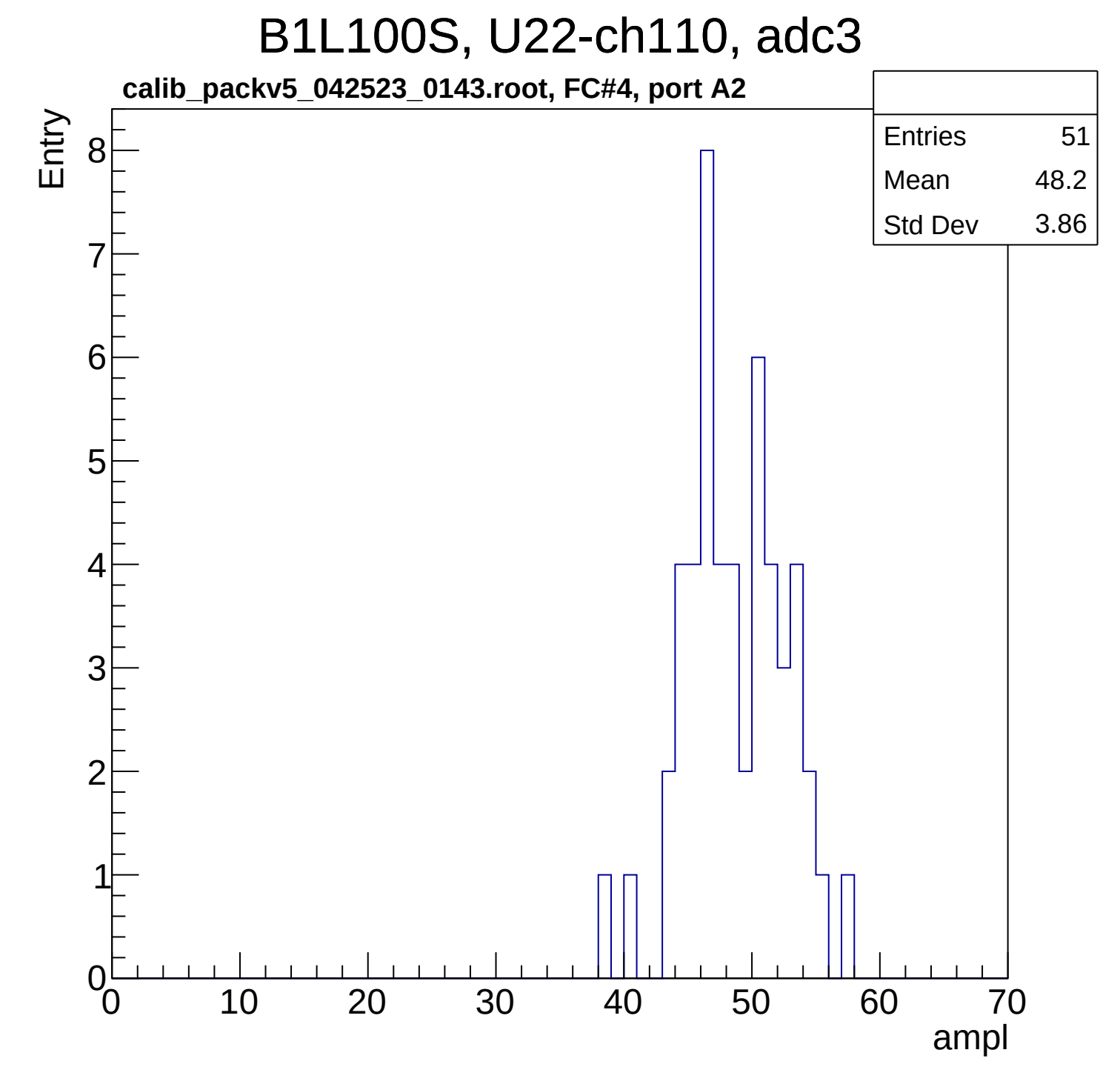

B1L100S, U22-ch109, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 61.8
Std Dev 0.9798
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch109, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch110, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 27.97
Std Dev 5.829
Gaus mean : 29.9708
Gaus Width: 2.8862
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch110, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 47
Mean 34.89
Std Dev 3.283
Gaus mean : 35.3724
Gaus Width: 3.6915
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch110, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 90
Mean 41.86
Std Dev 4.283
Gaus mean : 42.4583
Gaus Width: 4.3163
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch110, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 48.2
Std Dev 3.86
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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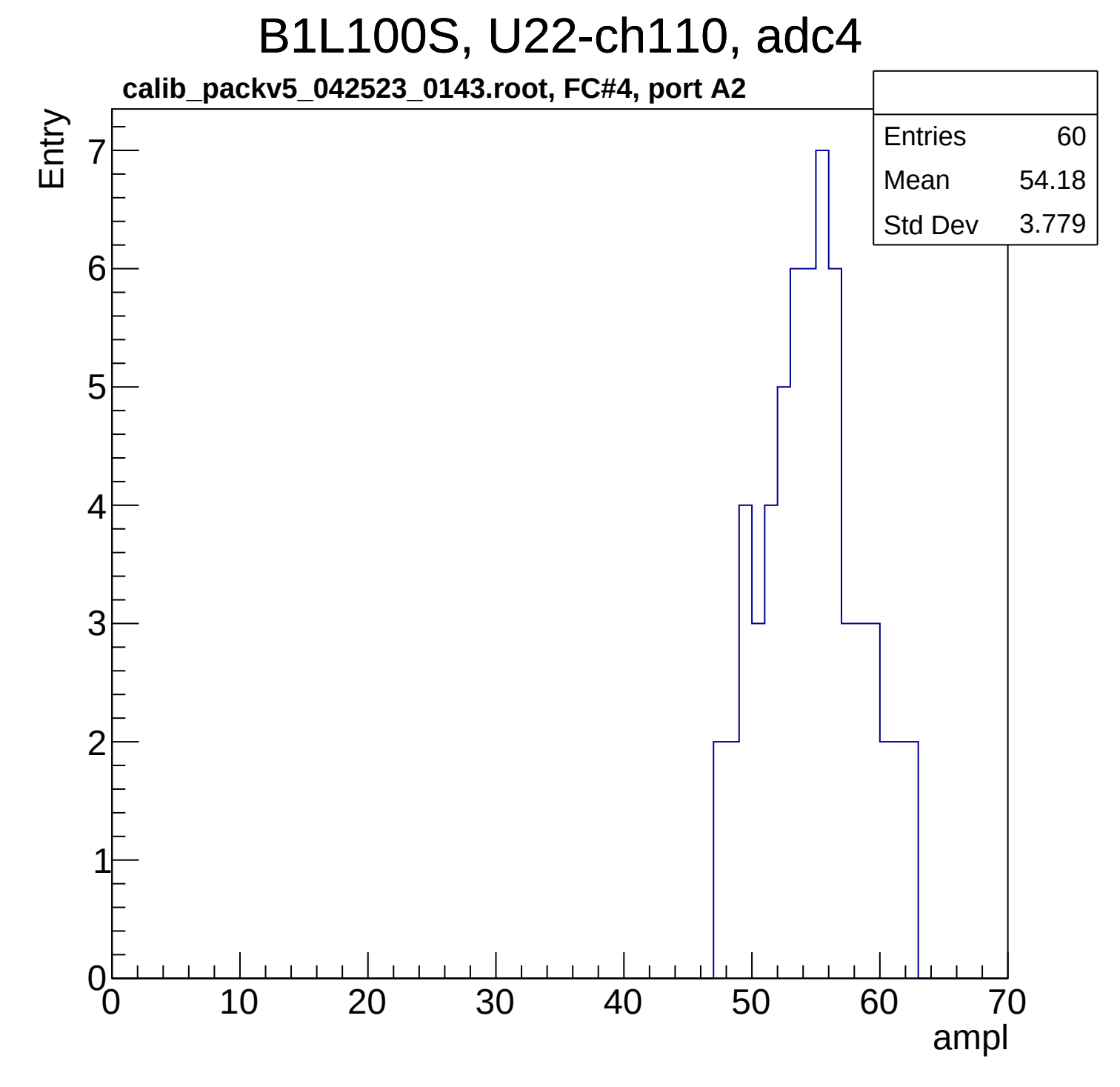
40
50
60
70


B1L100S, U22-ch110, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 54.18
Std Dev 3.779
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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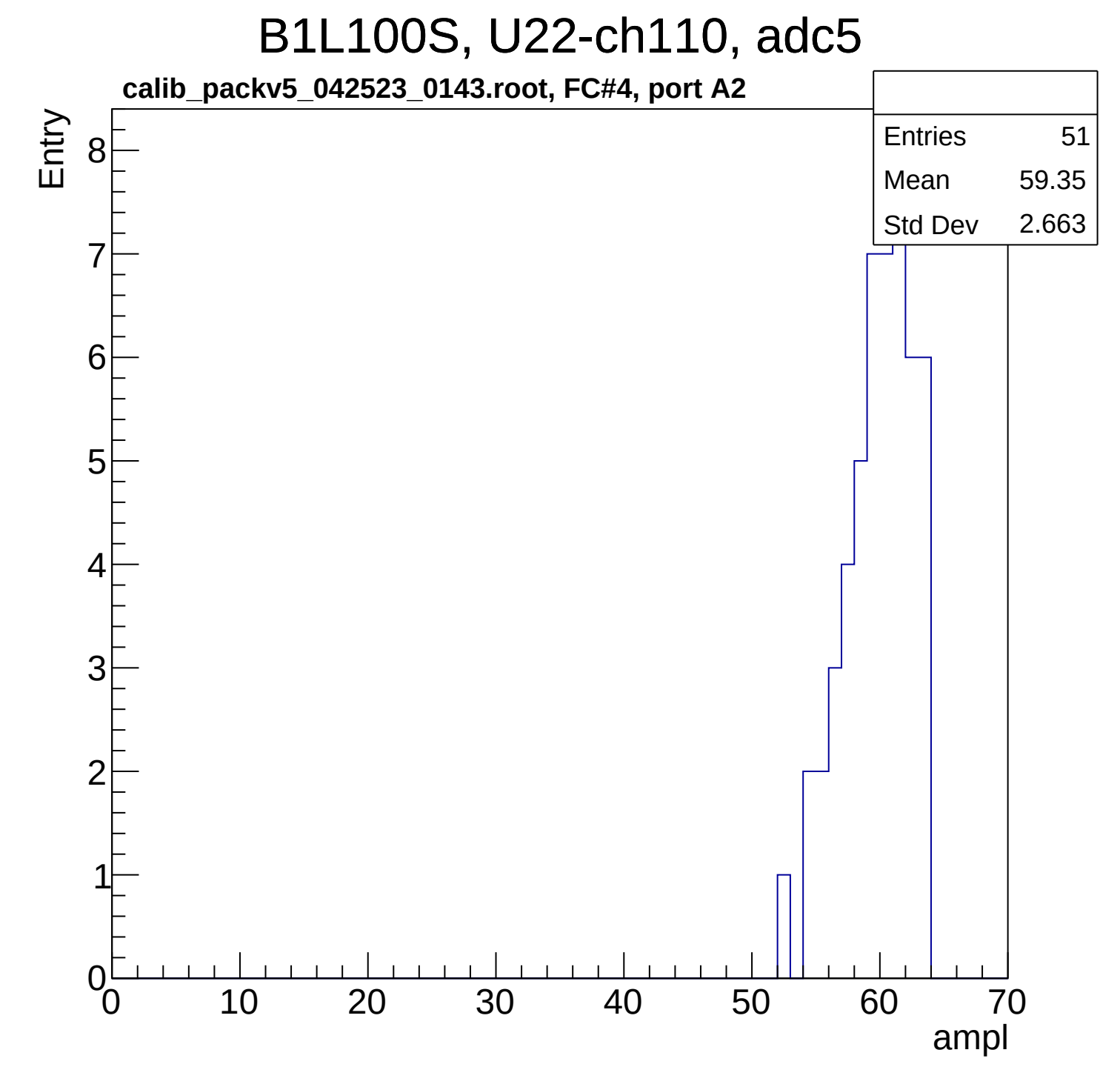
40
50
60
70


B1L100S, U22-ch110, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 59.35
Std Dev 2.663
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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60
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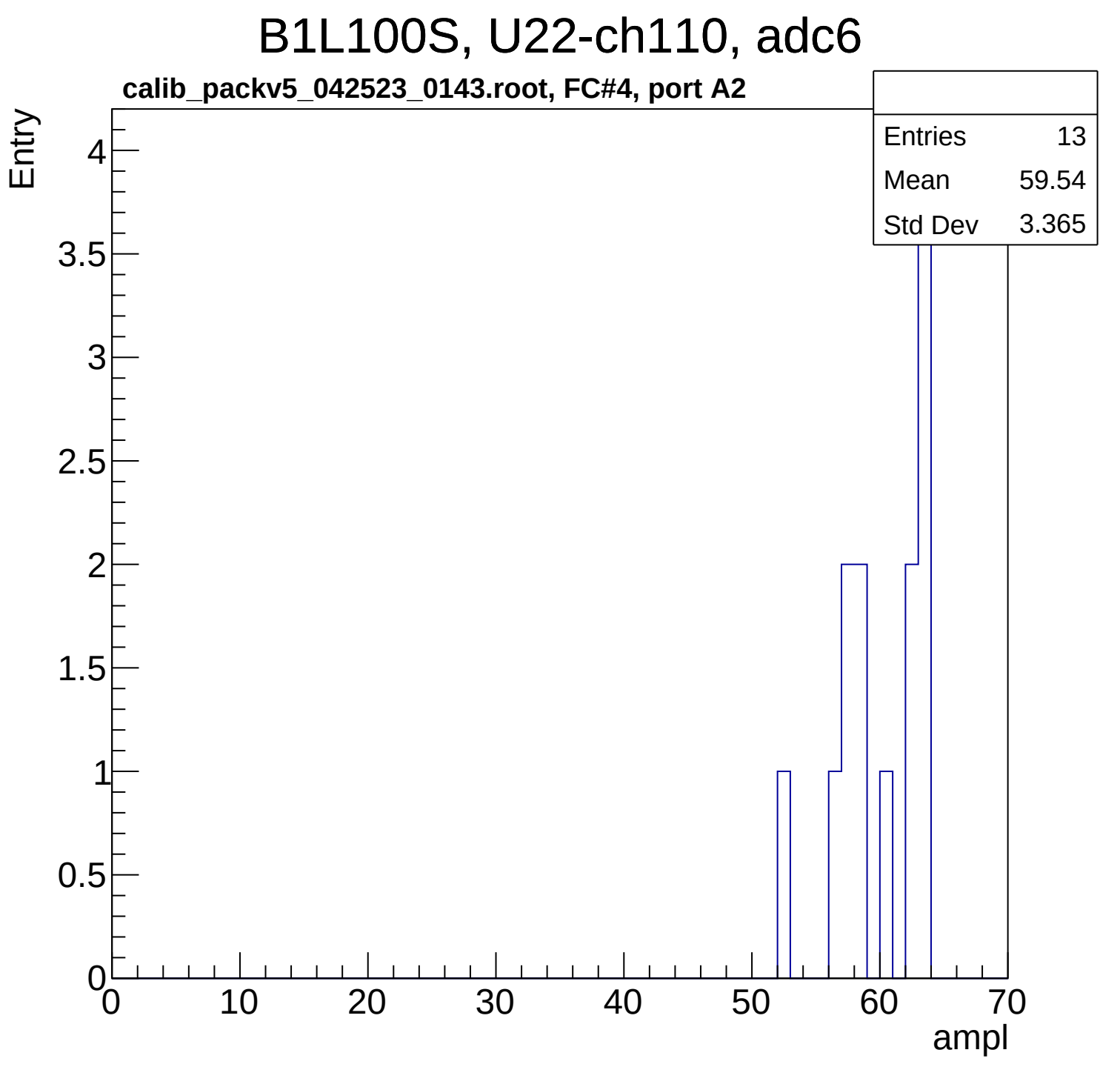
B1L100S, U22-ch110, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 13
Mean 59.54
Std Dev 3.365
Entry
ampl
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0.5
1
1.5
2
2.5
3
3.5
4
0
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20
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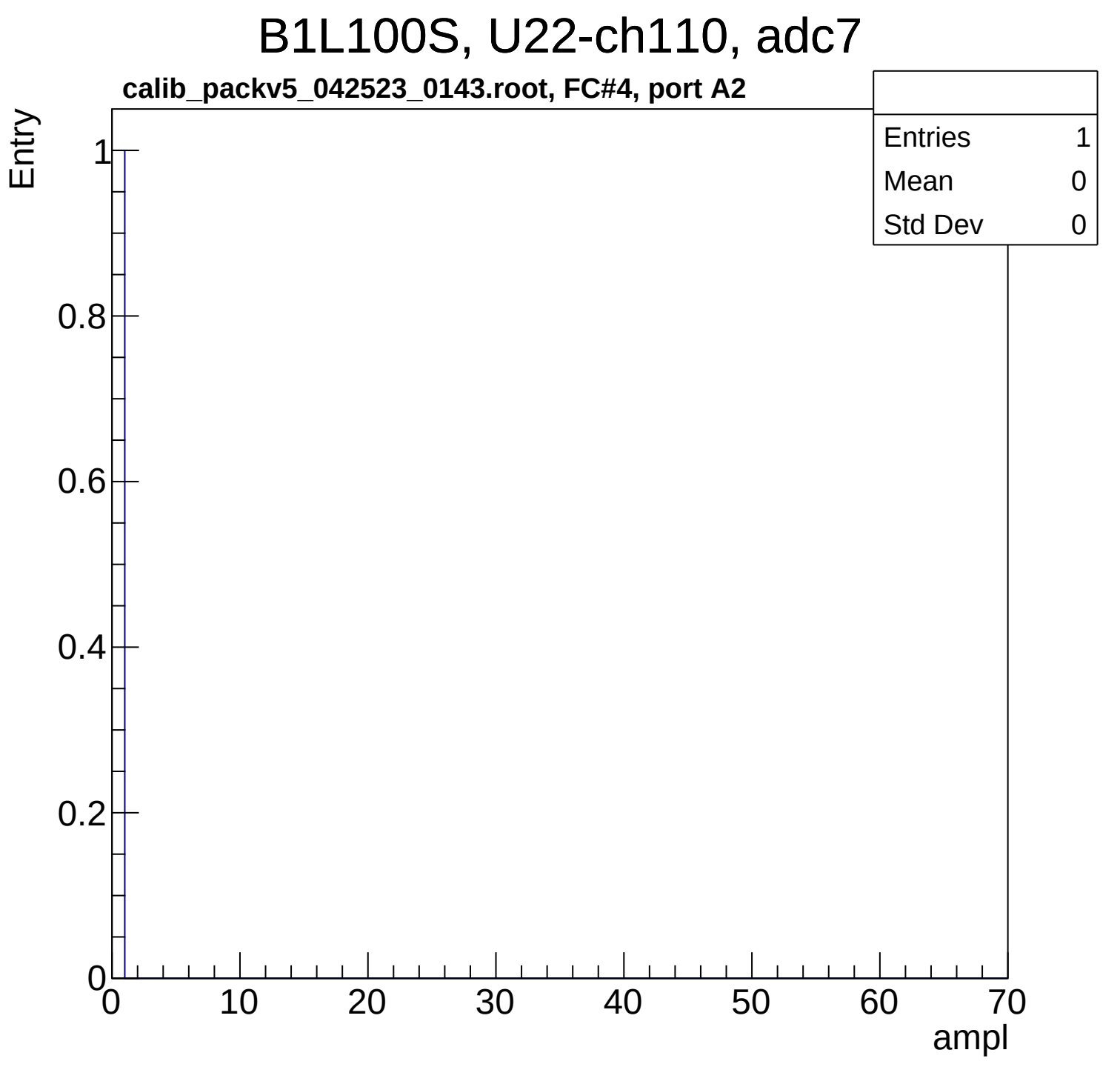
40
50
60
70


B1L100S, U22-ch110, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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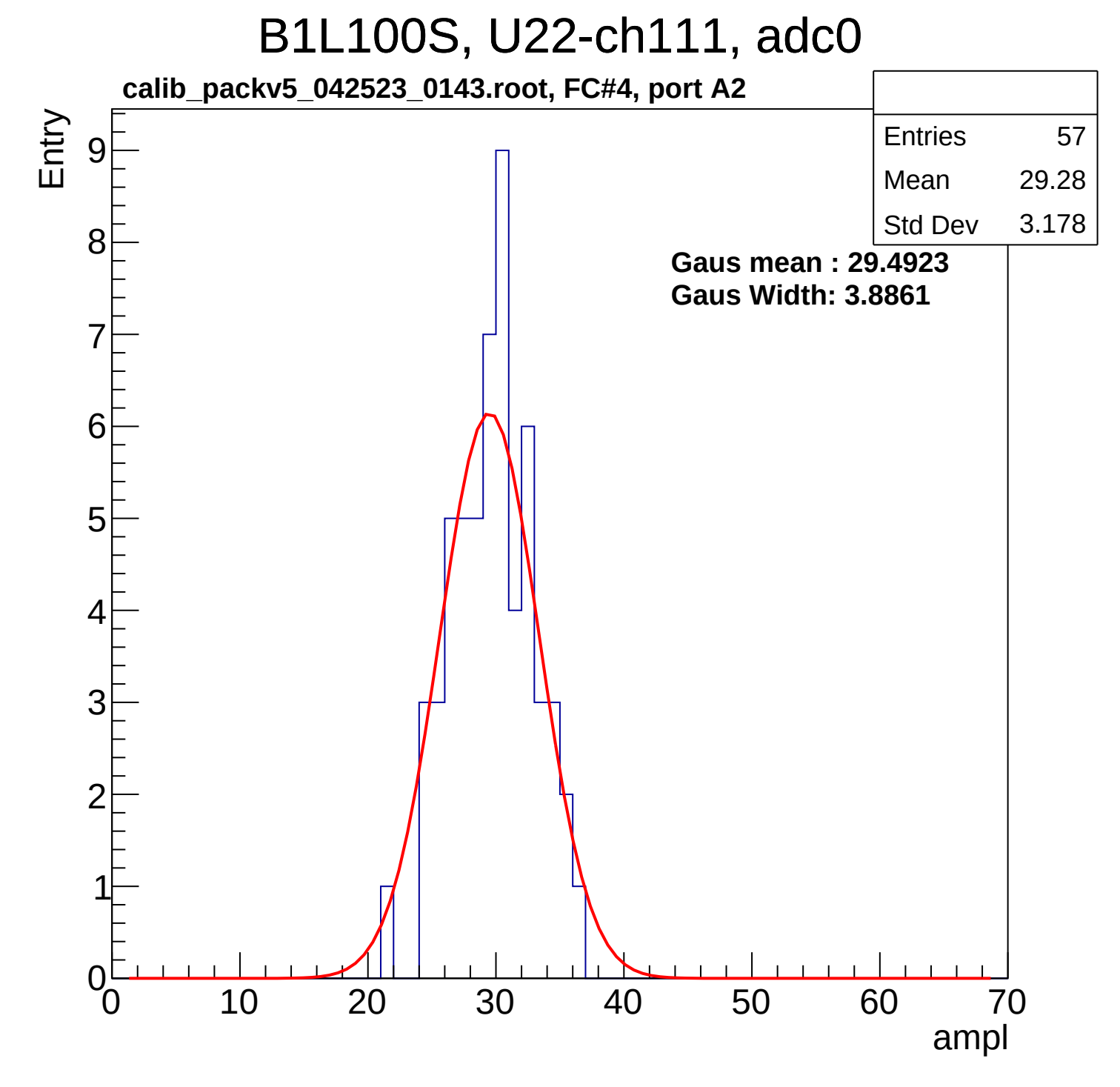
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch111, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 29.28
Std Dev 3.178
Gaus mean : 29.4923
Gaus Width: 3.8861
Entry
ampl
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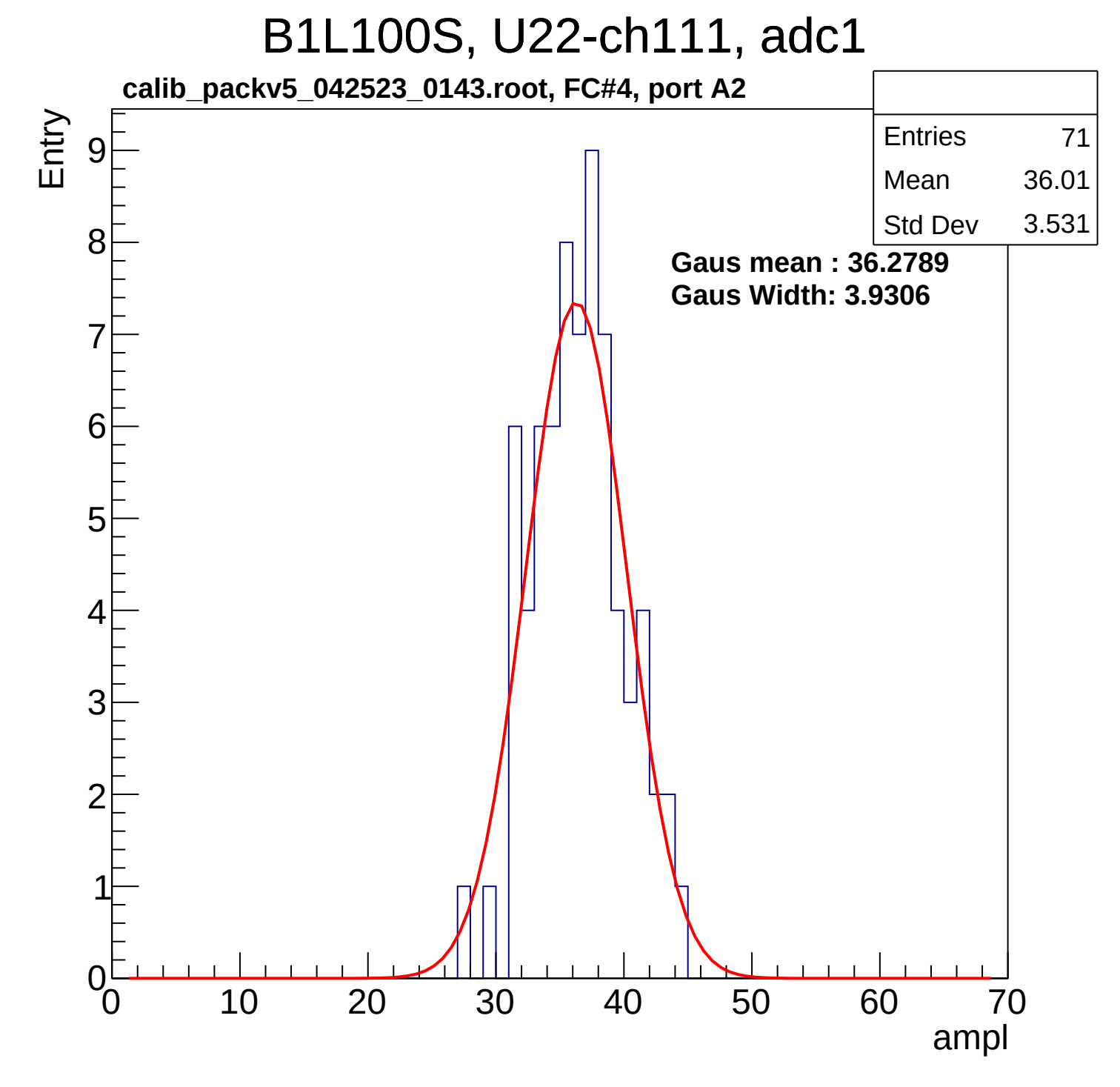
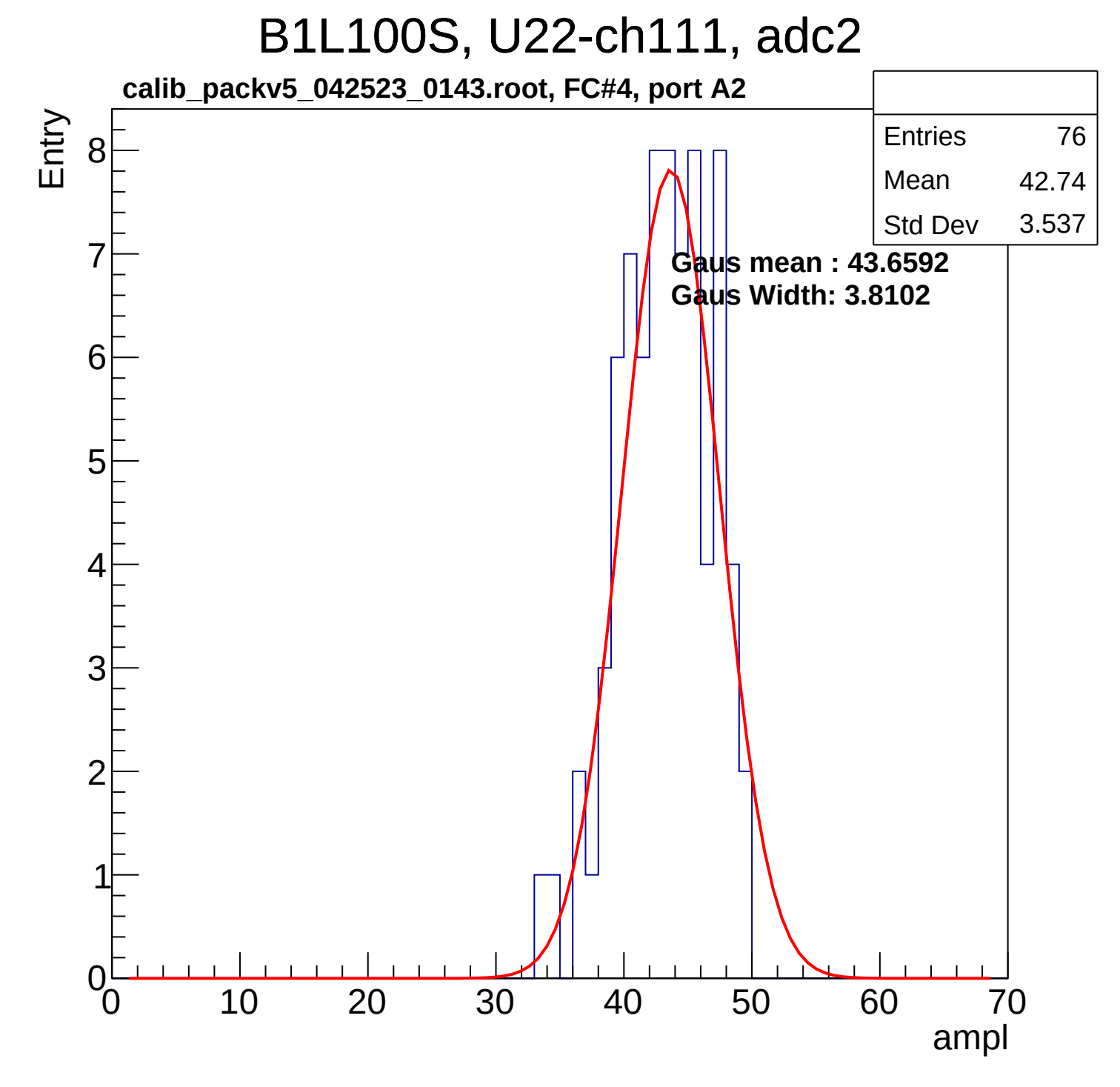
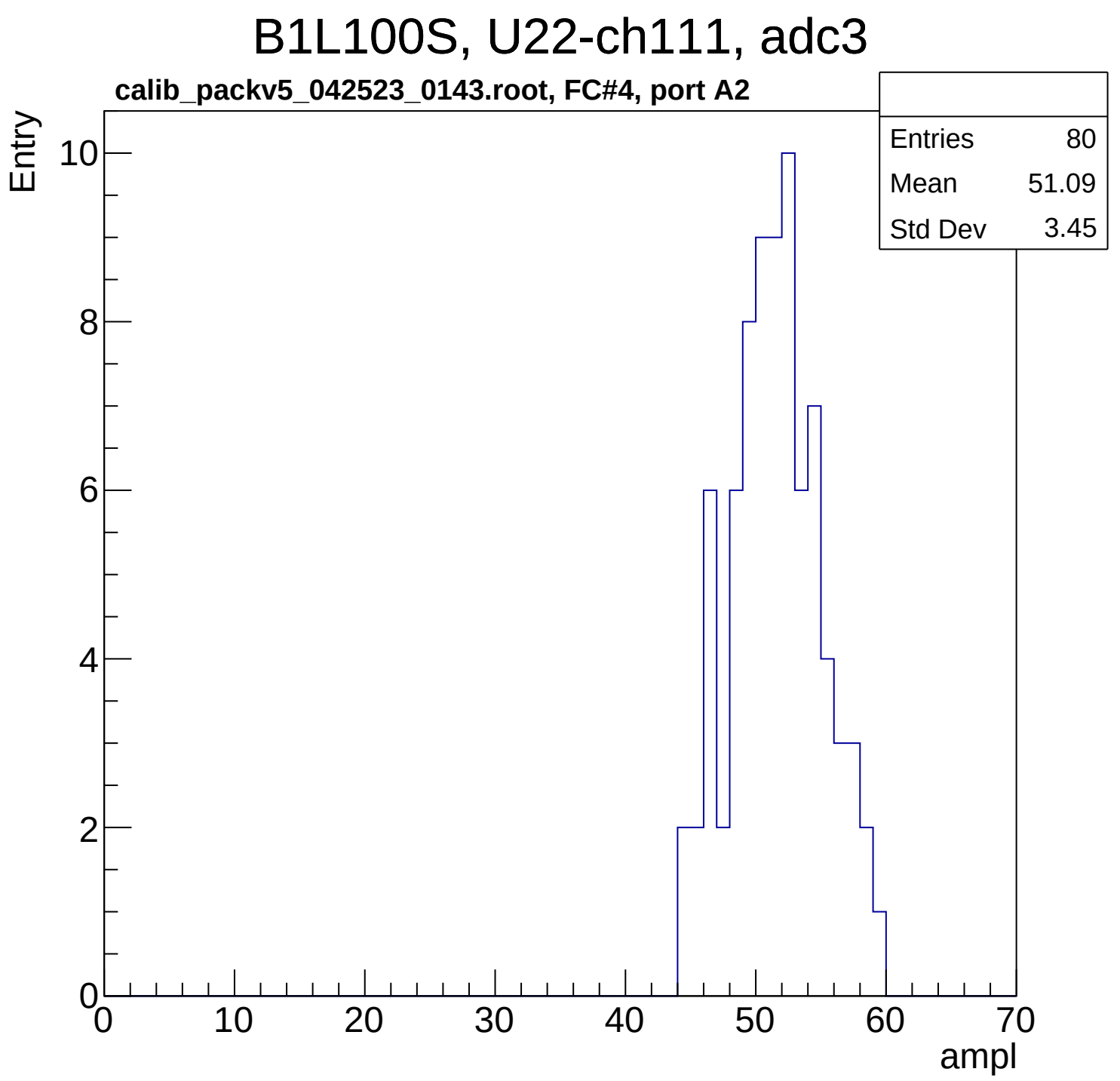
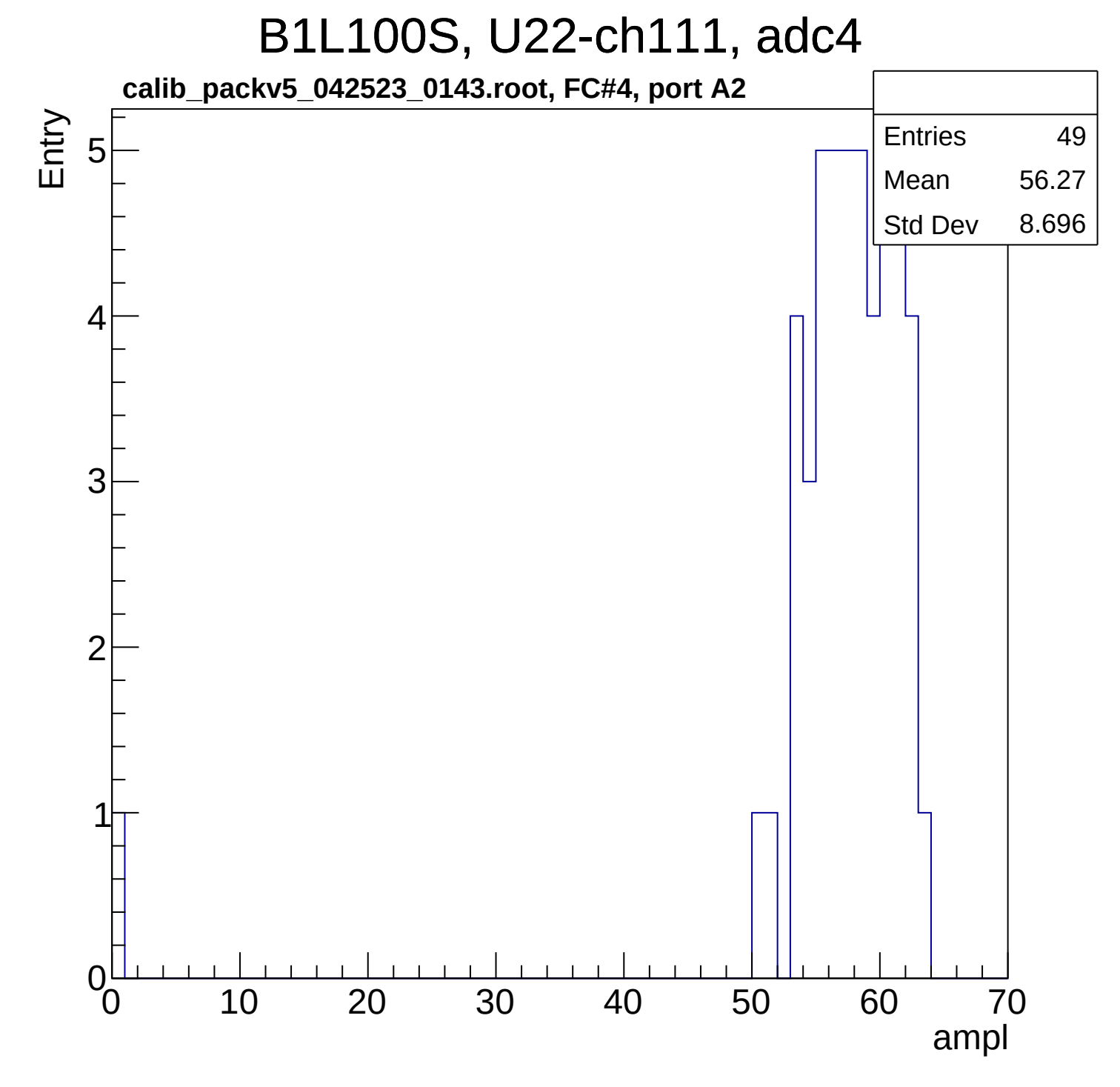
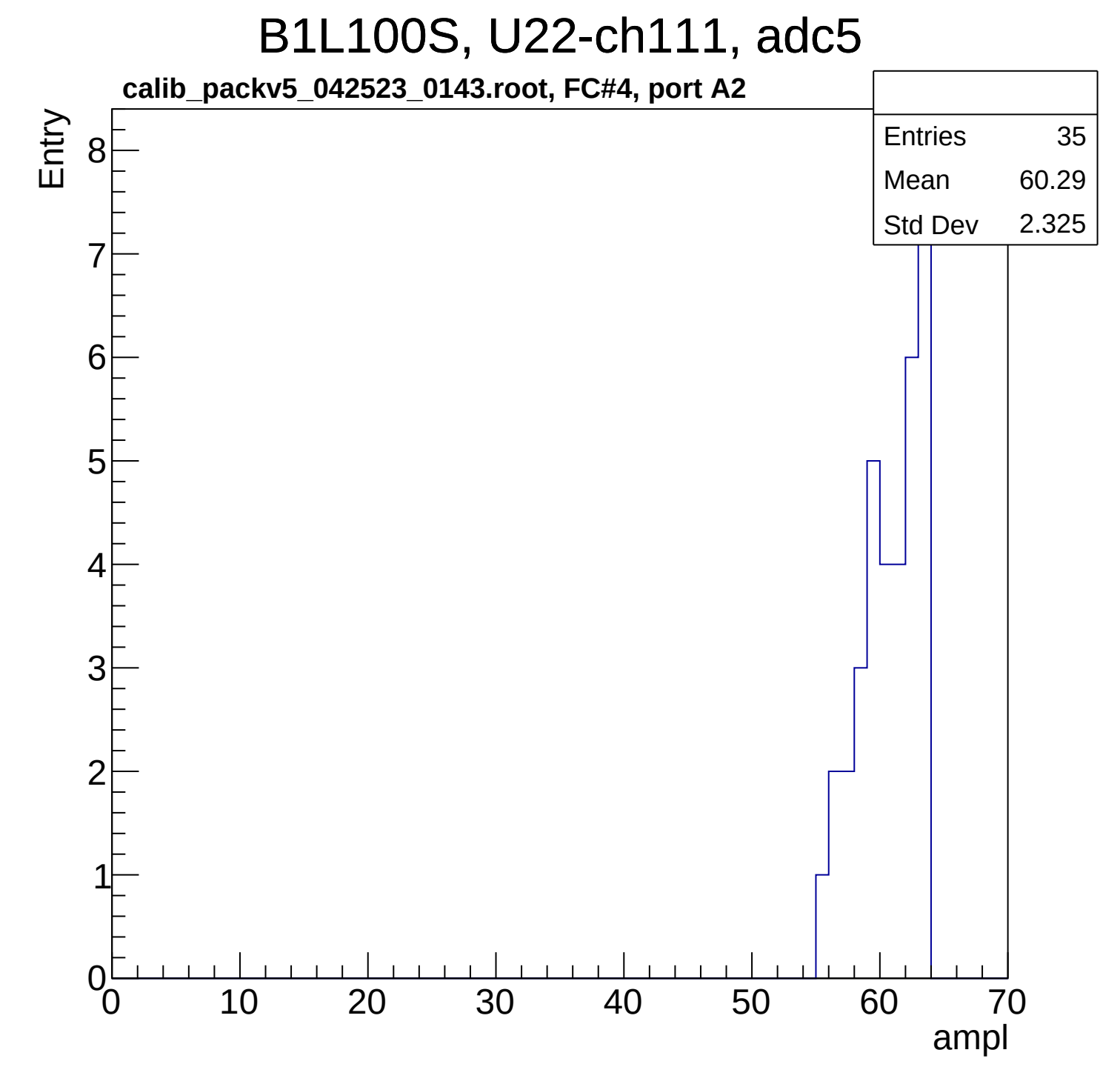
10
20
30
40
50
60
70
1
2
3
4
5
6
7
8
9


B1L100S, U22-ch111, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 36.01
Std Dev 3.531
Gaus mean : 36.2789
Gaus Width: 3.9306
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch111, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 76
Mean 42.74
Std Dev 3.537
Gaus mean : 43.6592
Gaus Width: 3.8102
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch111, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 51.09
Std Dev 3.45
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch111, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 49
Mean 56.27
Std Dev 8.696
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U22-ch111, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 35
Mean 60.29
Std Dev 2.325
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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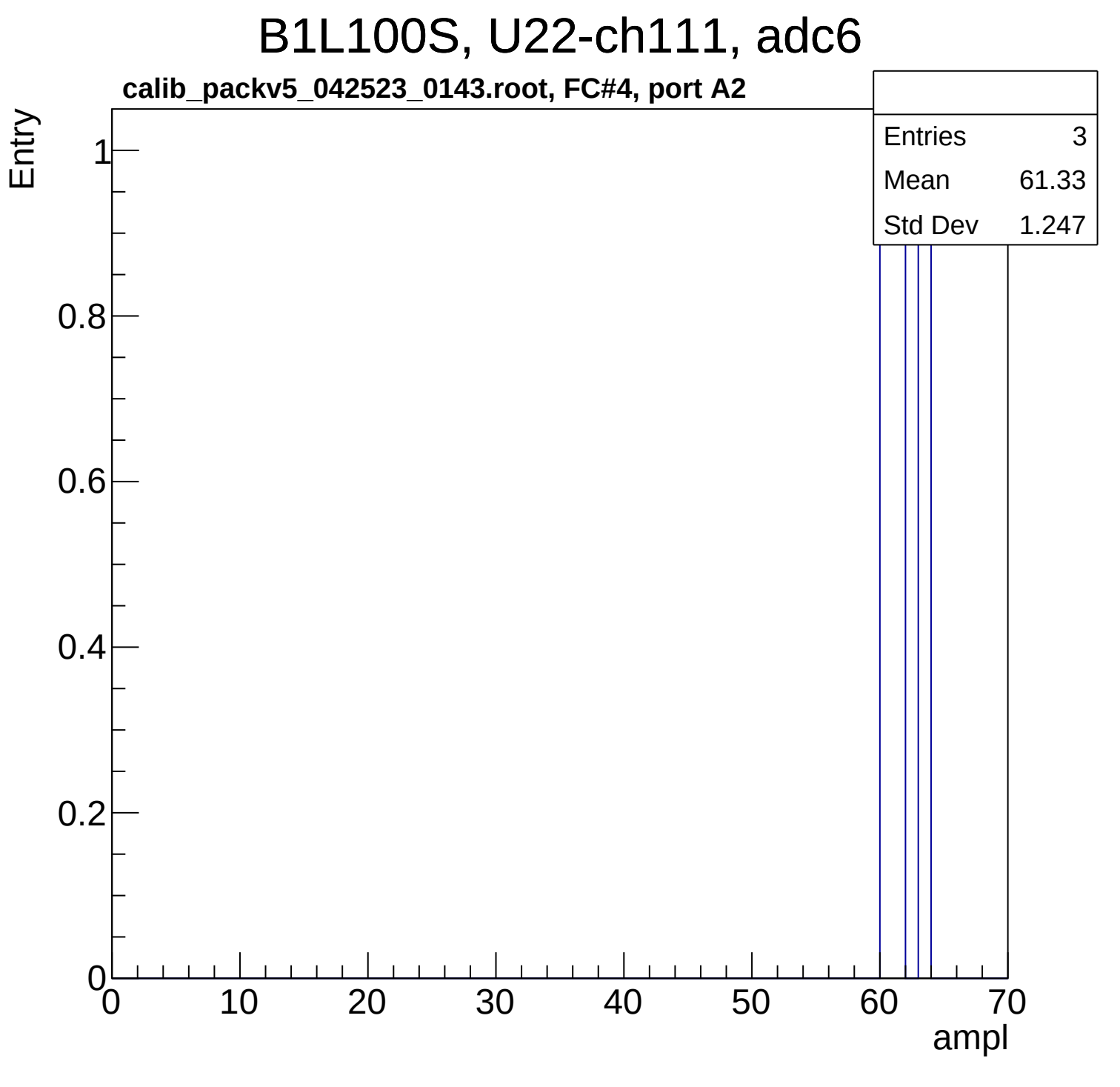
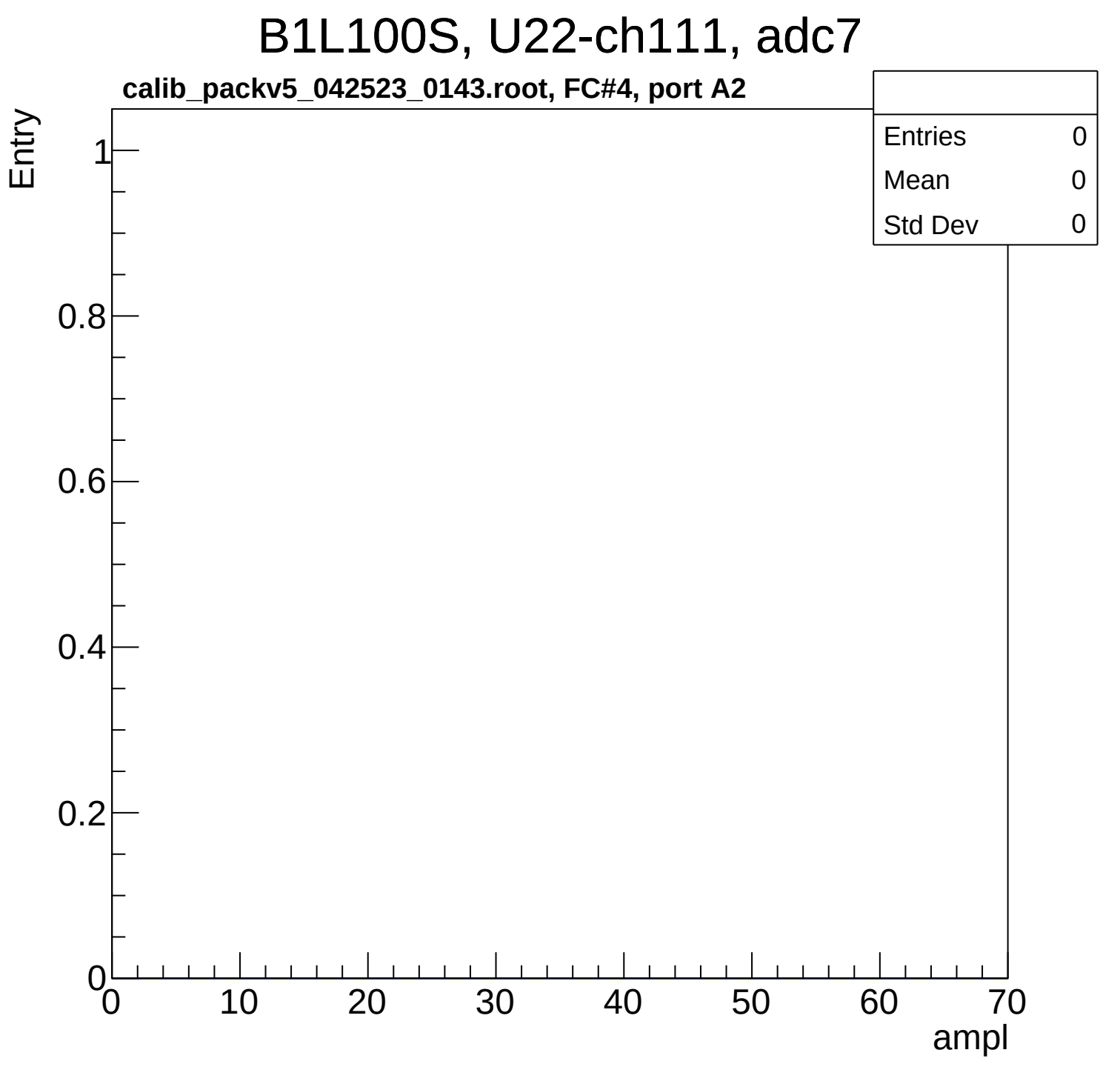

B1L100S, U22-ch111, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 61.33
Std Dev 1.247
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U22-ch111, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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30
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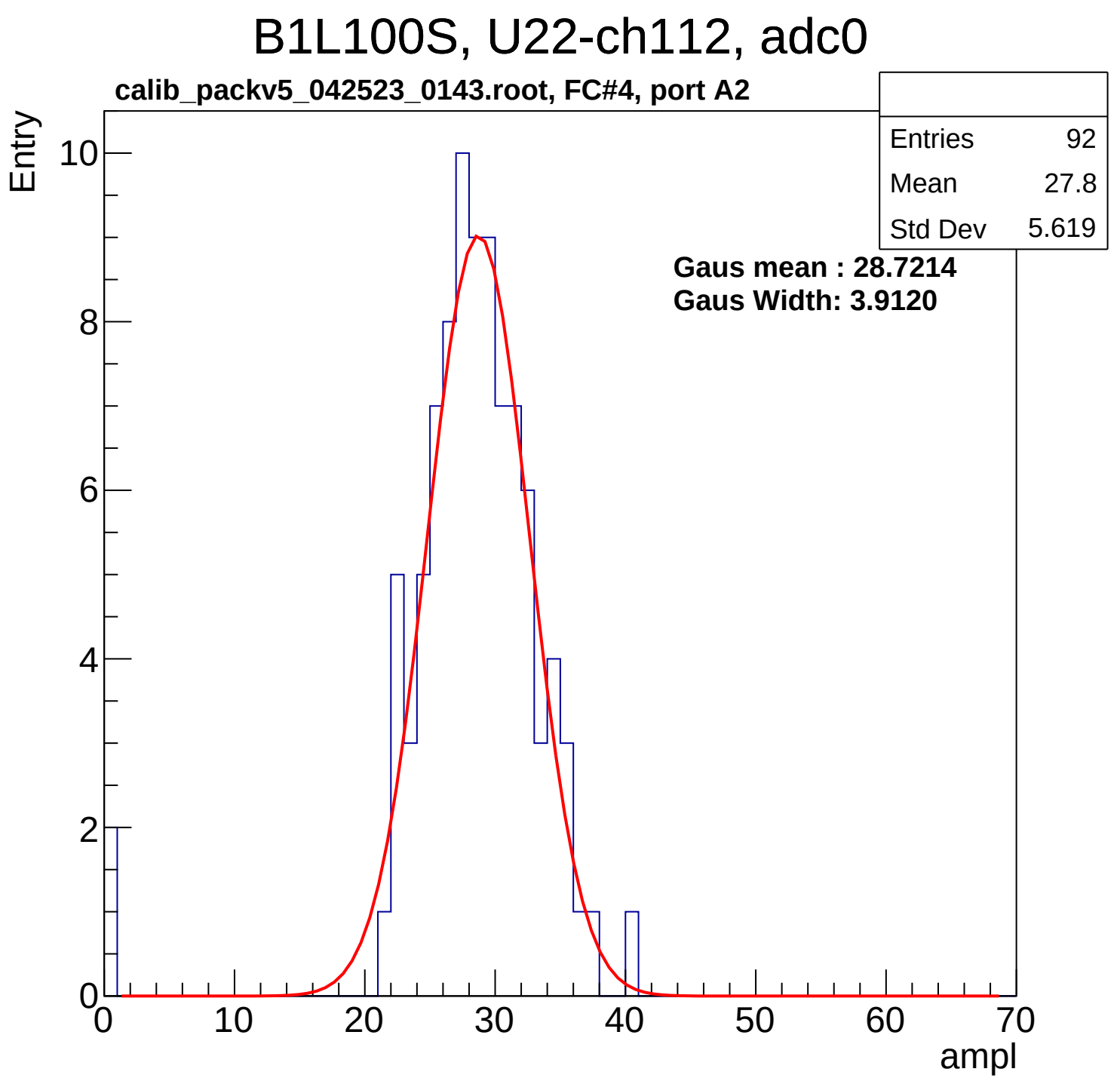
50
60
70
ampl


B1L100S, U22-ch112, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 92
Mean 27.8
Std Dev 5.619
Gaus mean : 28.7214
Gaus Width: 3.9120
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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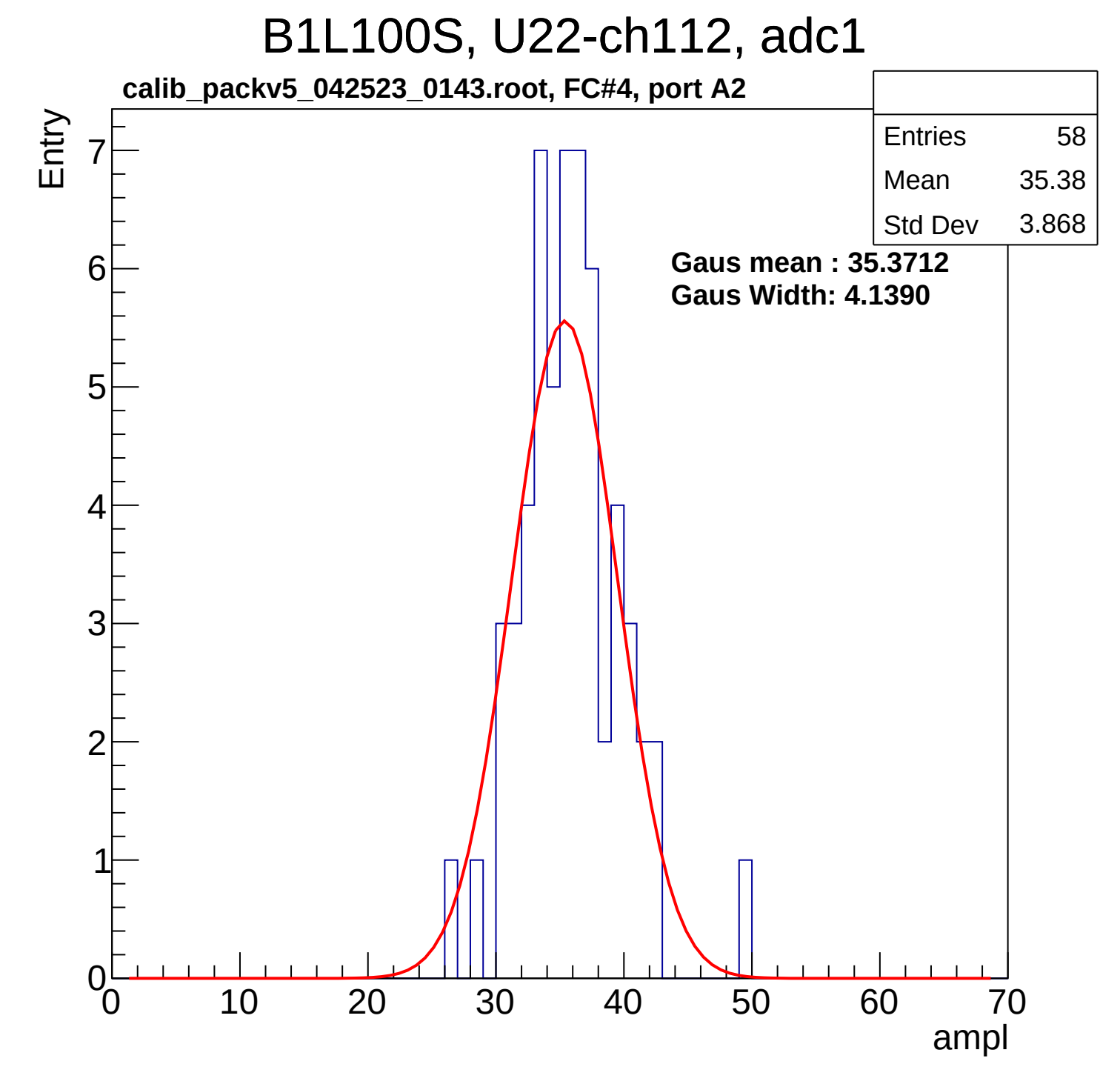
B1L100S, U22-ch112, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 58
Mean 35.38
Std Dev 3.868
Gaus mean : 35.3712
Gaus Width: 4.1390
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
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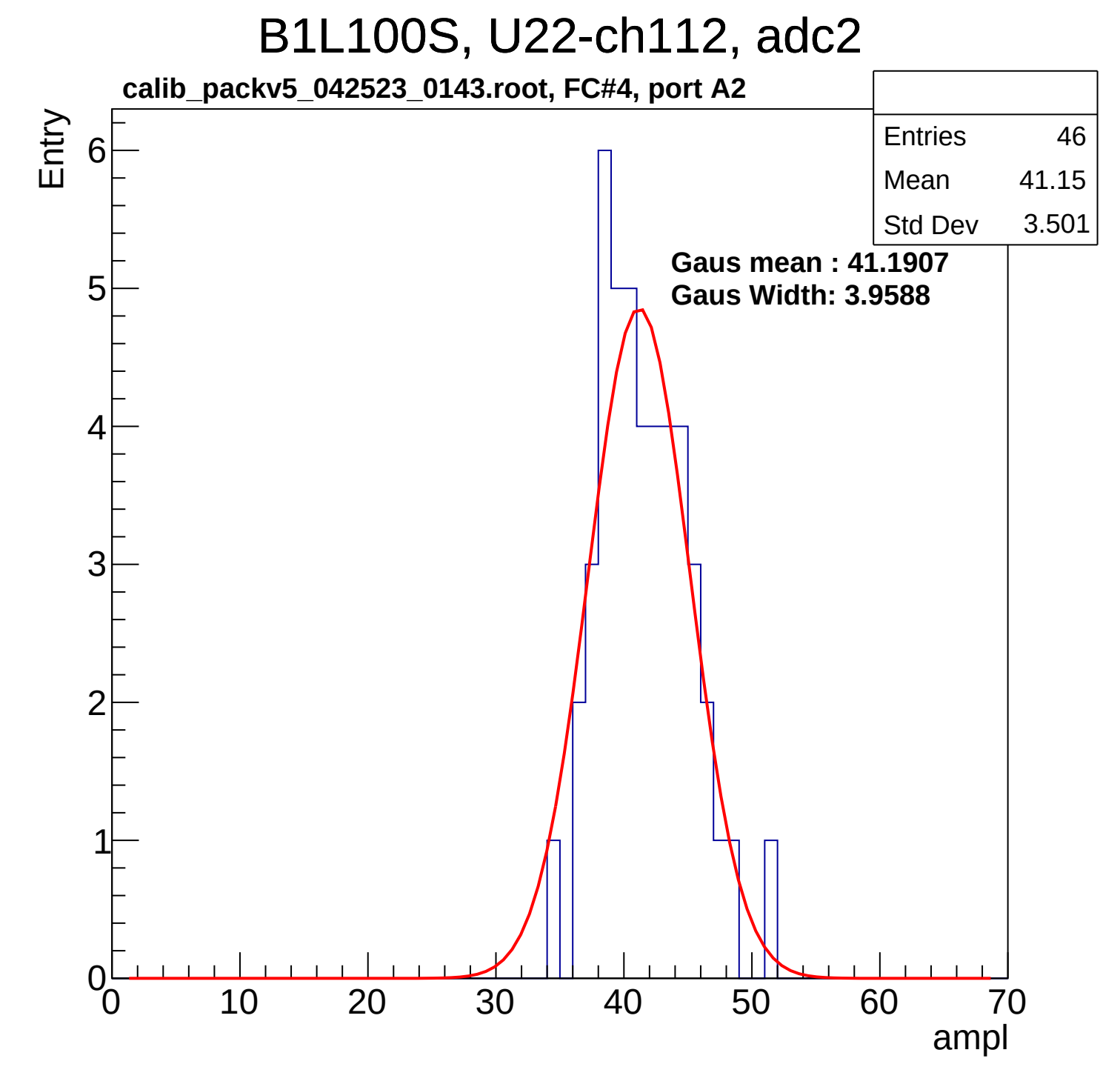
B1L100S, U22-ch112, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 41.15
Std Dev 3.501
Gaus mean : 41.1907
Gaus Width: 3.9588
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
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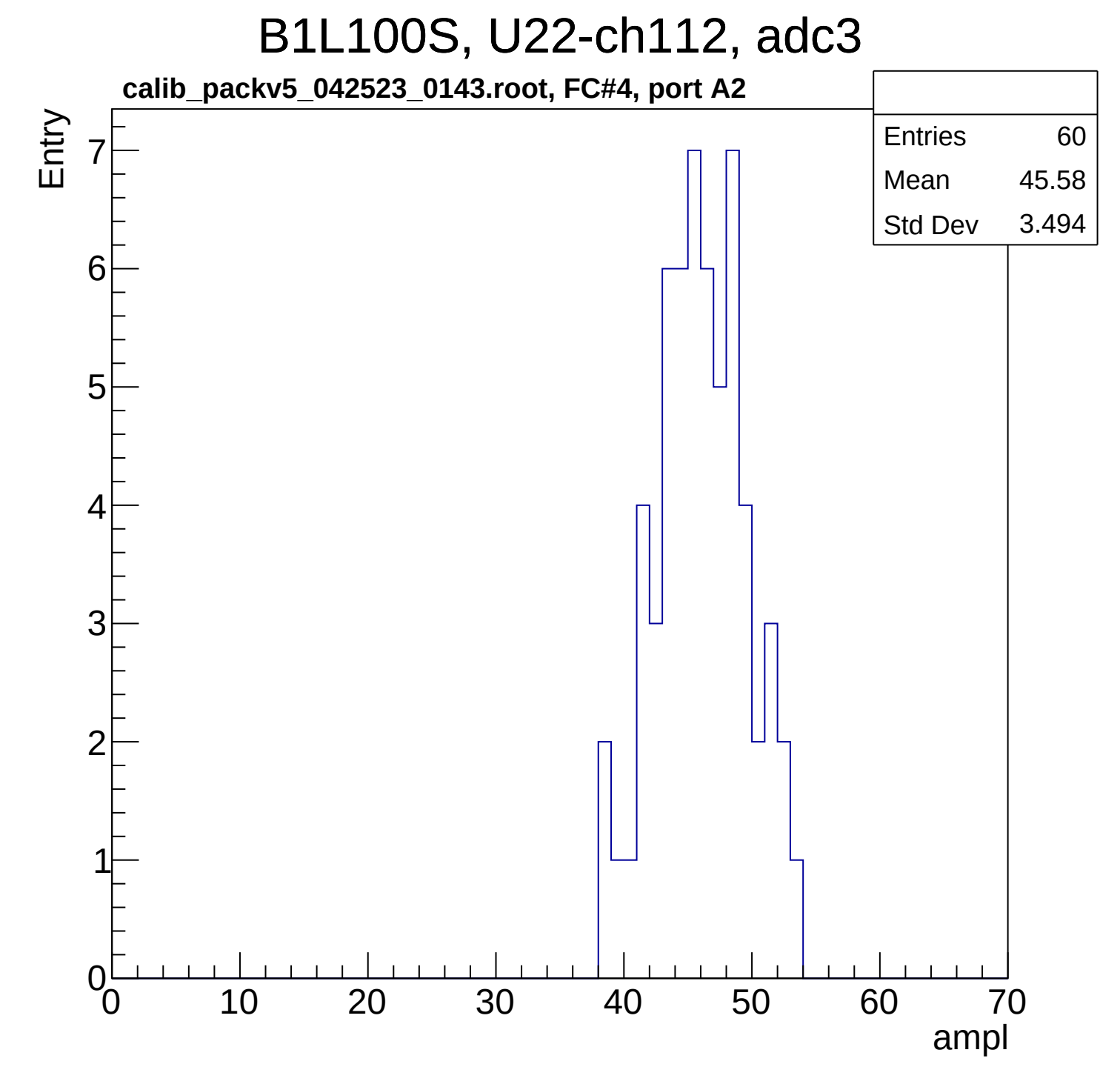
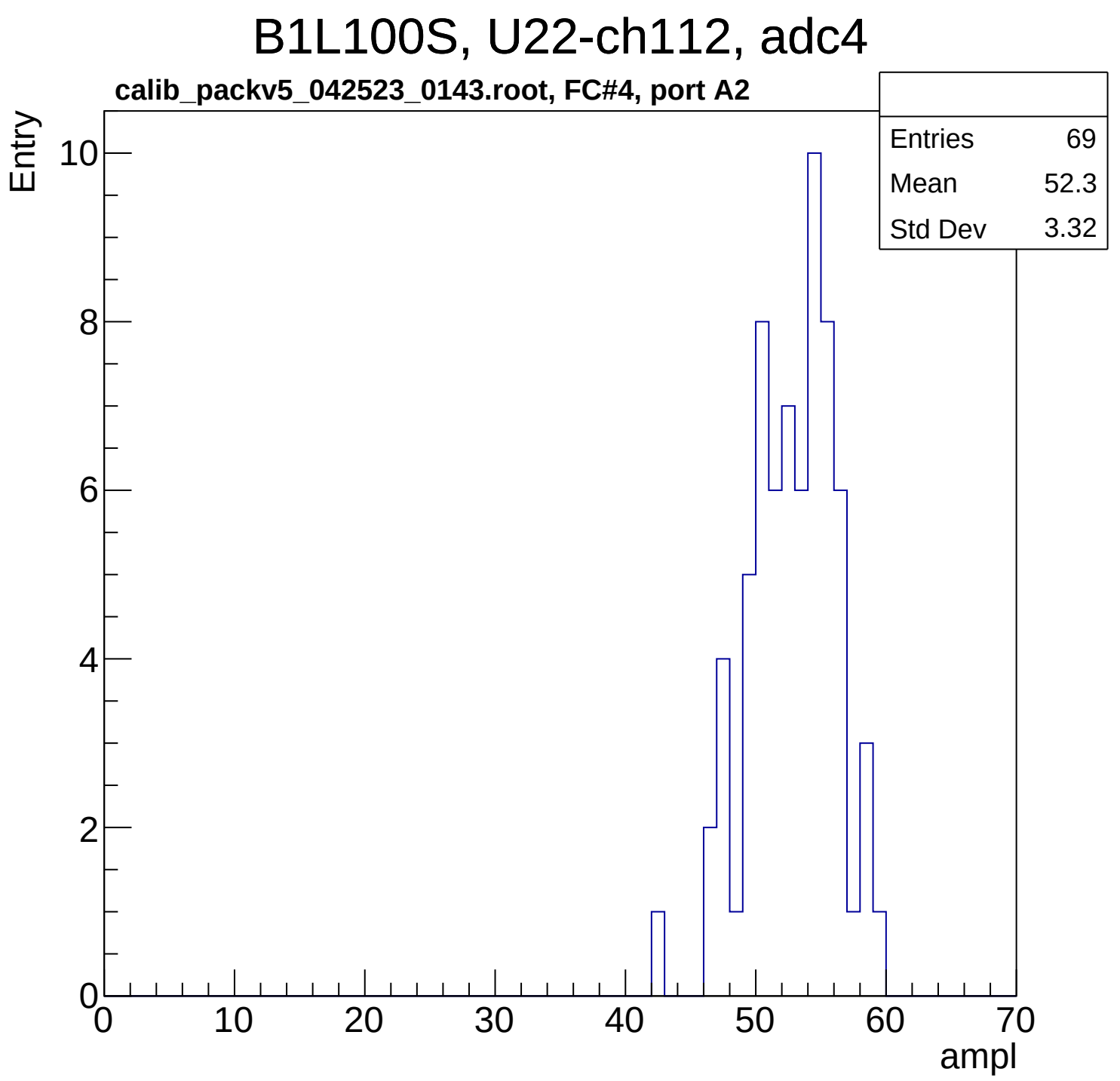
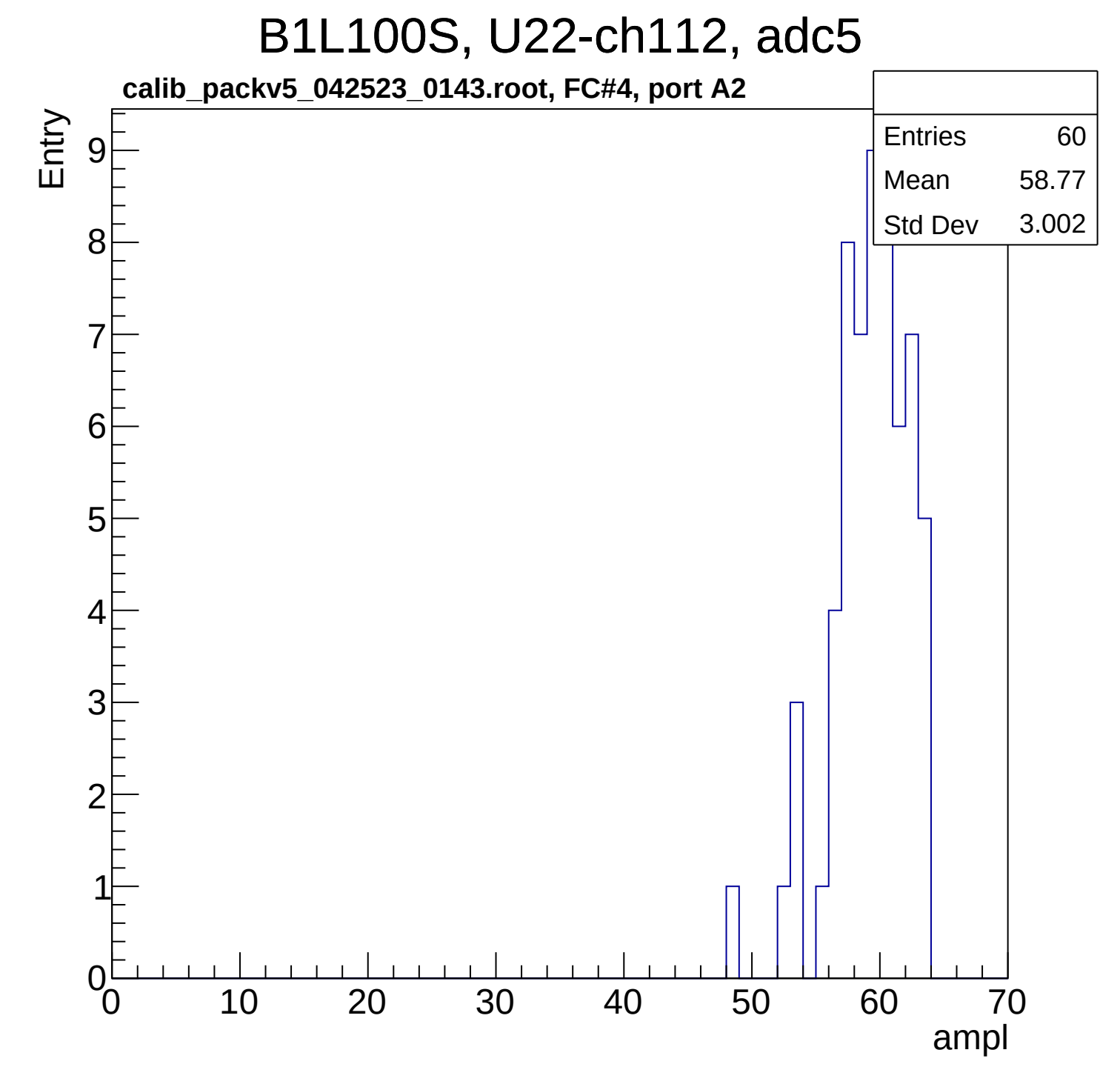

B1L100S, U22-ch112, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 45.58
Std Dev 3.494
Entry
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch112, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 52.3
Std Dev 3.32
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch112, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 58.77
Std Dev 3.002
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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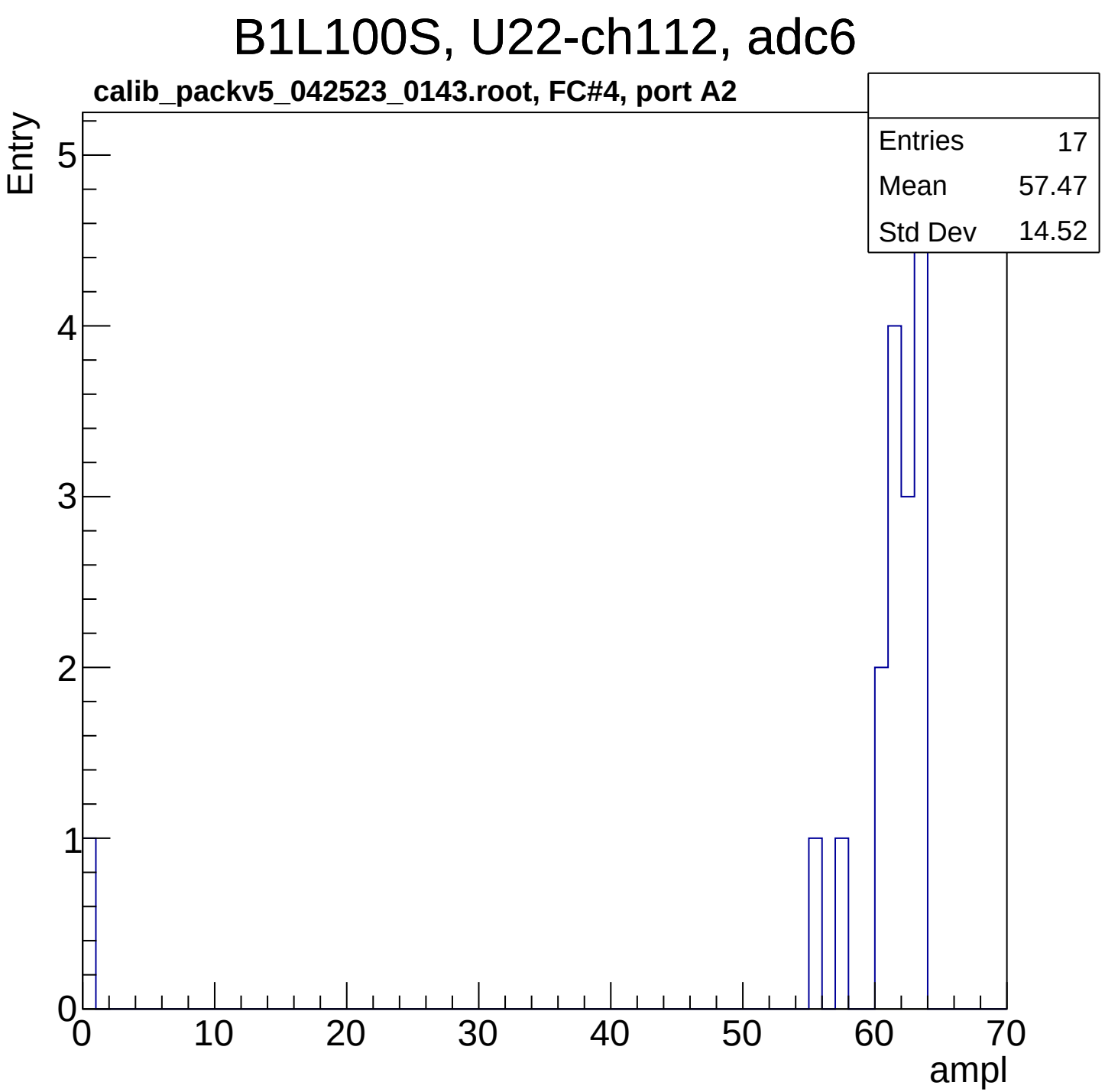
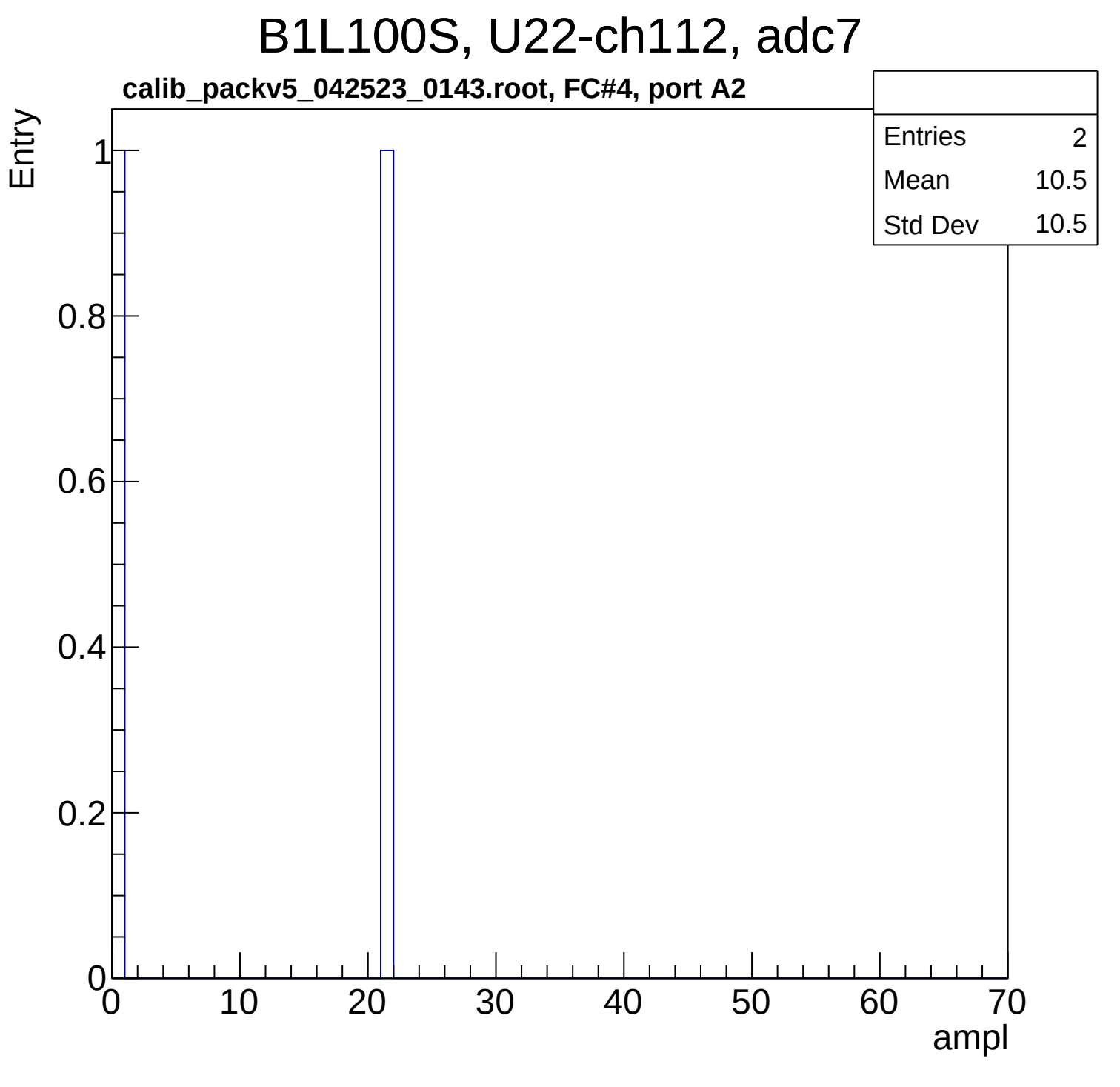
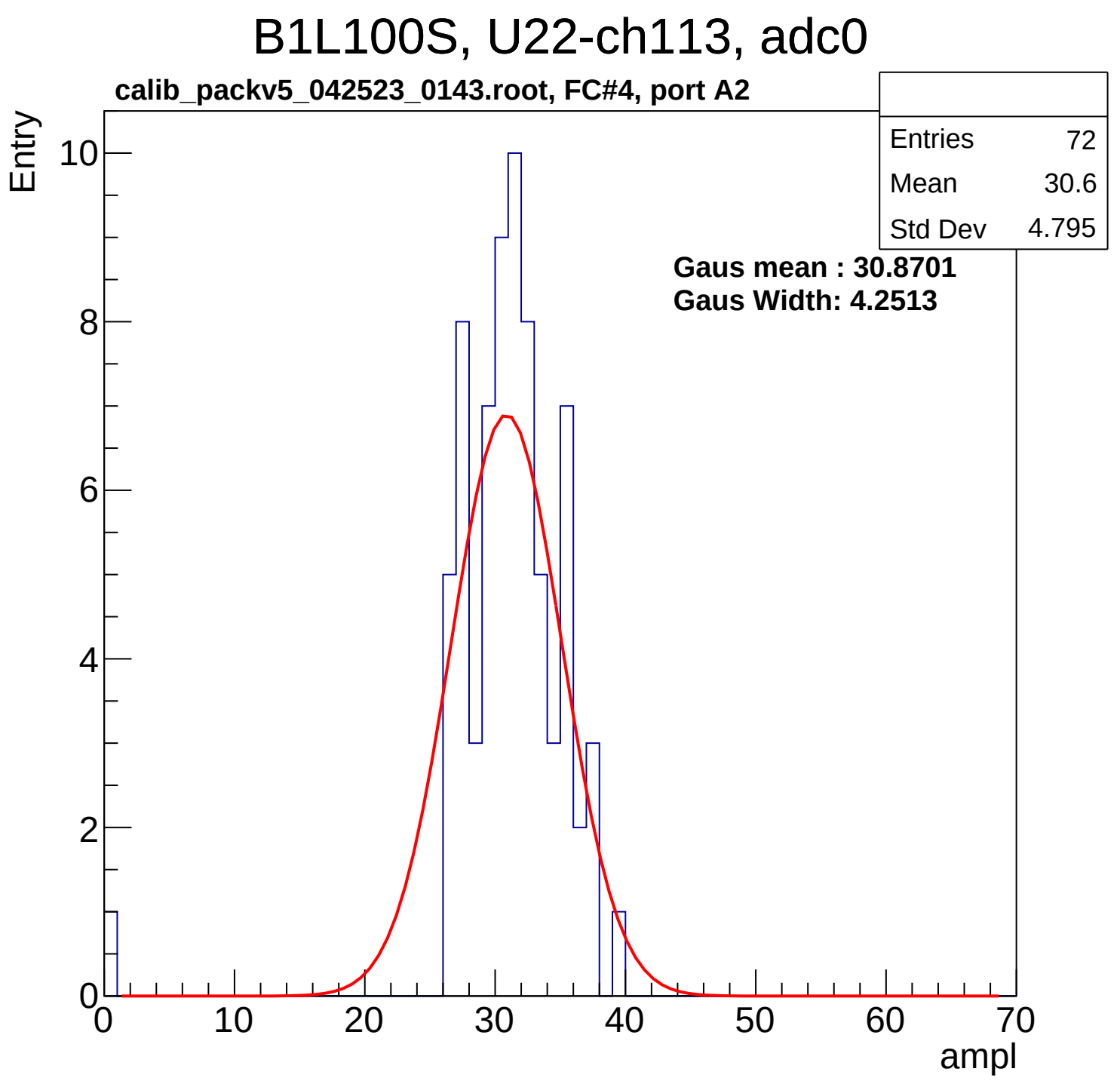
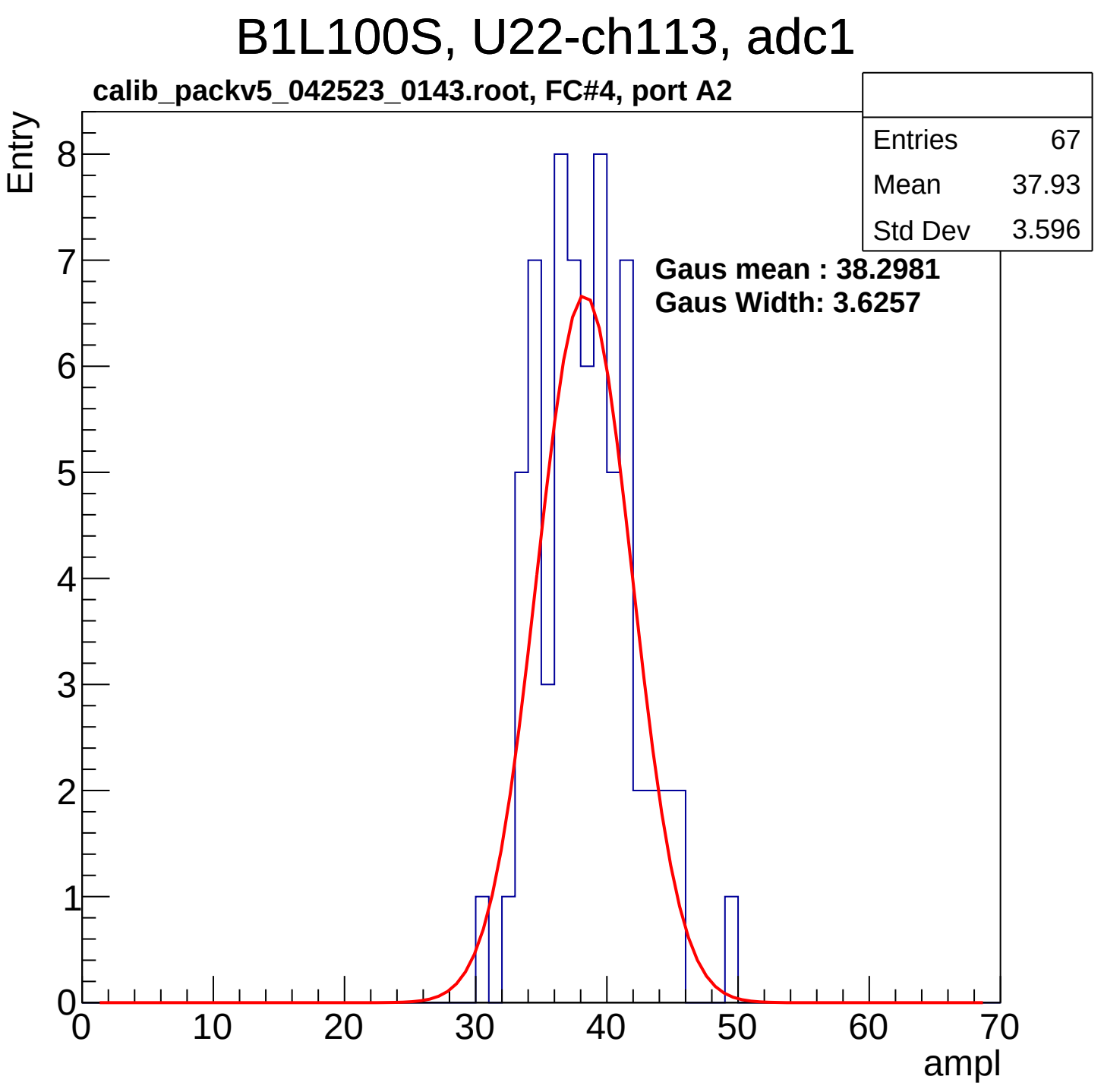

B1L100S, U22-ch112, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 17
Mean 57.47
Std Dev 14.52
Entry
ampl
0
1
2
3
4
5
0
10
20
30
40
50
60
70


B1L100S, U22-ch112, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 10.5
Std Dev 10.5
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
70


B1L100S, U22-ch113, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 72
Mean 30.6
Std Dev 4.795
Gaus mean : 30.8701
Gaus Width: 4.2513
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch113, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 37.93
Std Dev 3.596
Gaus mean : 38.2981
Gaus Width: 3.6257
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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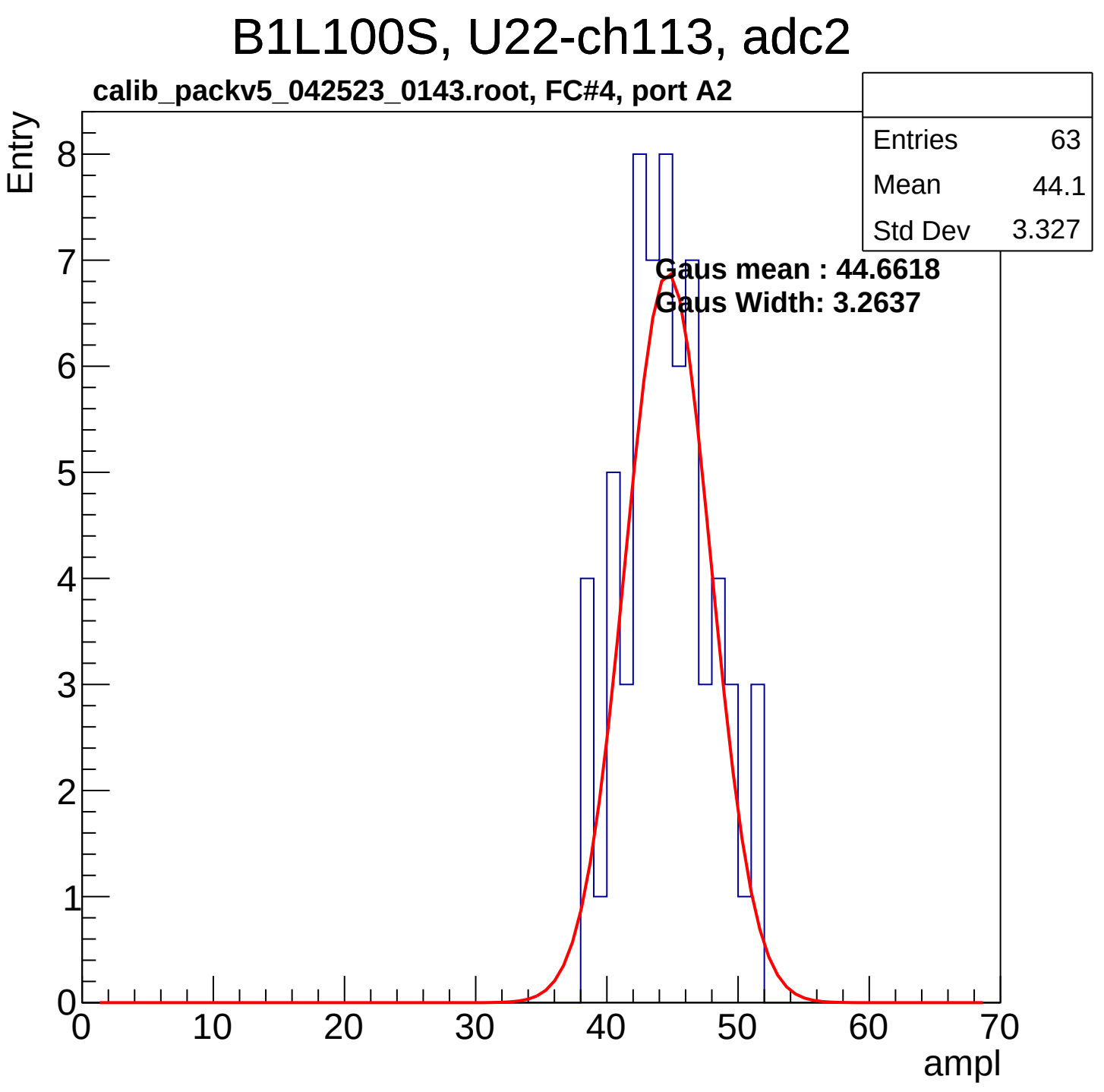
B1L100S, U22-ch113, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 44.1
Std Dev 3.327
Gaus mean : 44.6618
Gaus Width: 3.2637
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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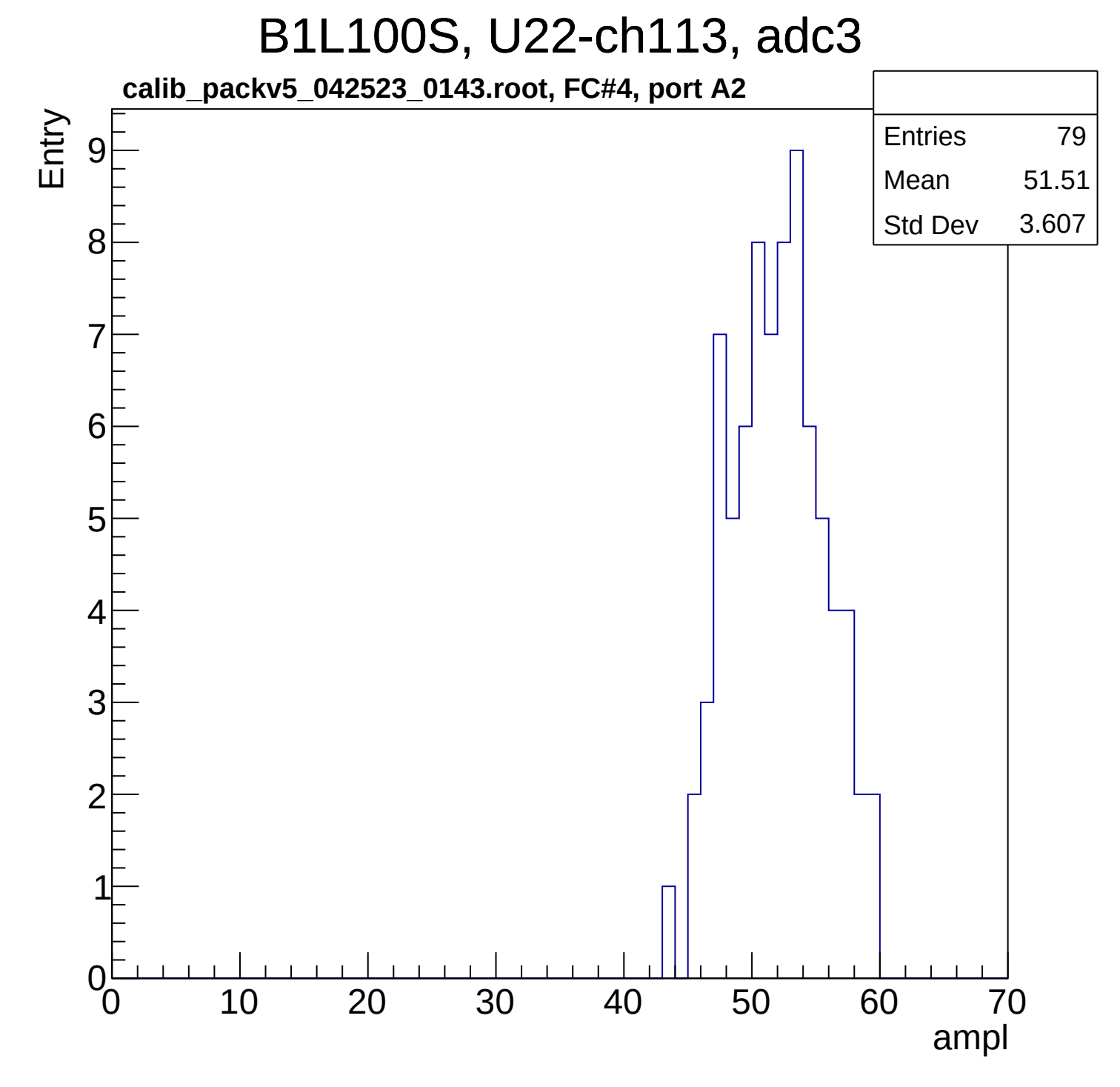
B1L100S, U22-ch113, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 51.51
Std Dev 3.607
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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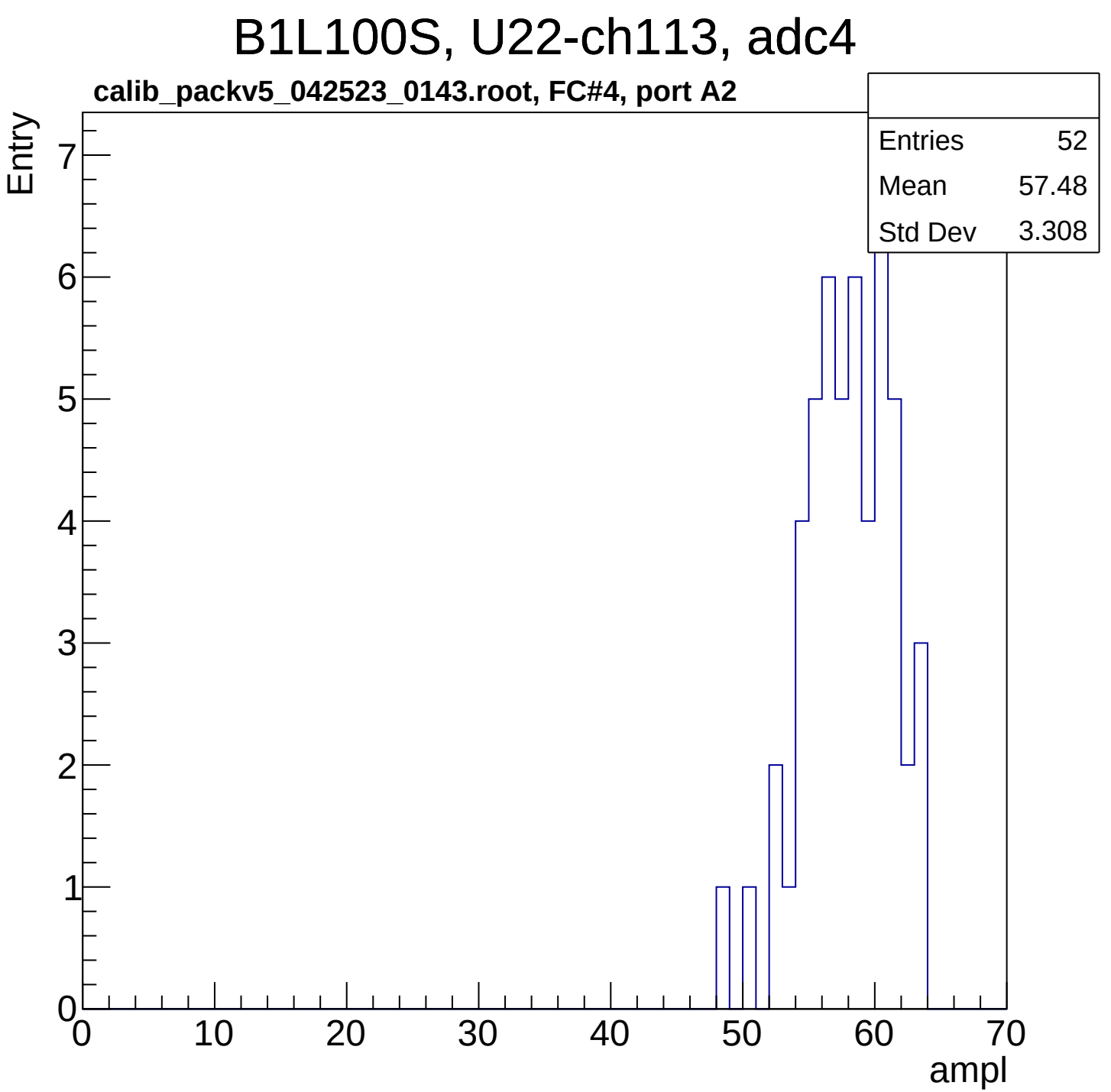
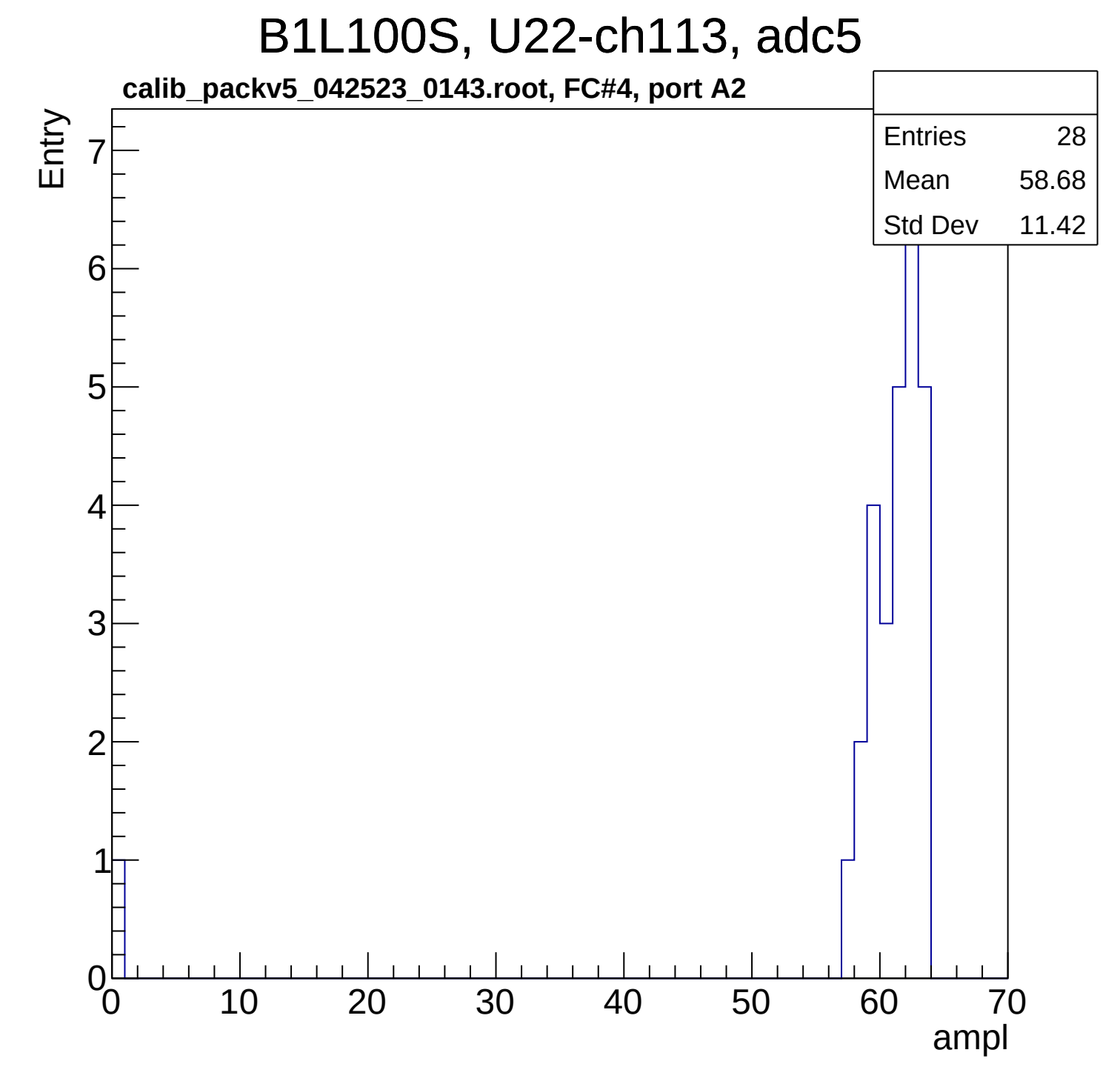

B1L100S, U22-ch113, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 57.48
Std Dev 3.308
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch113, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 28
Mean 58.68
Std Dev 11.42
Entry
ampl
0
1
2
3
4
5
6
7
0
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20
30
40
50
60
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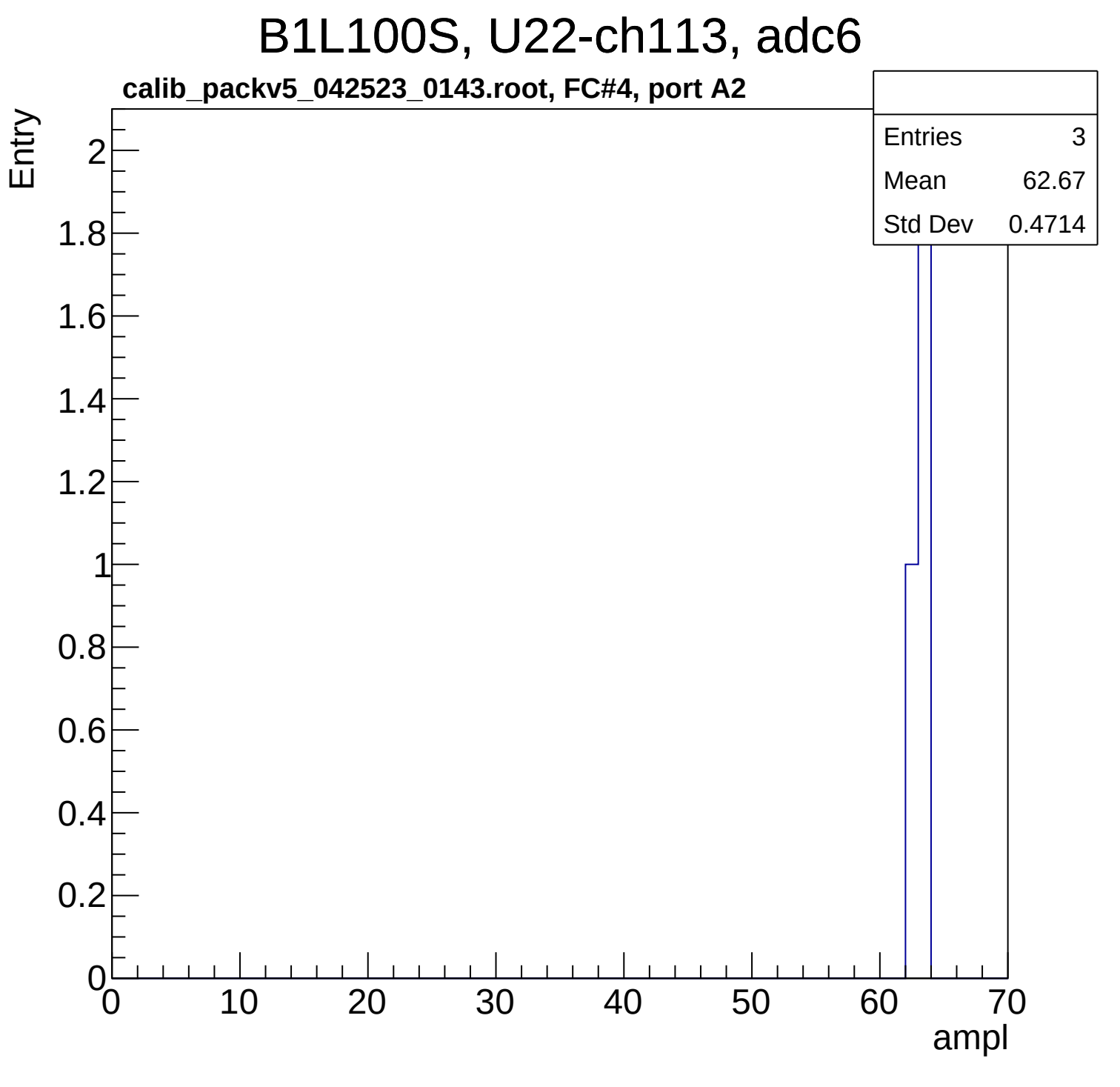
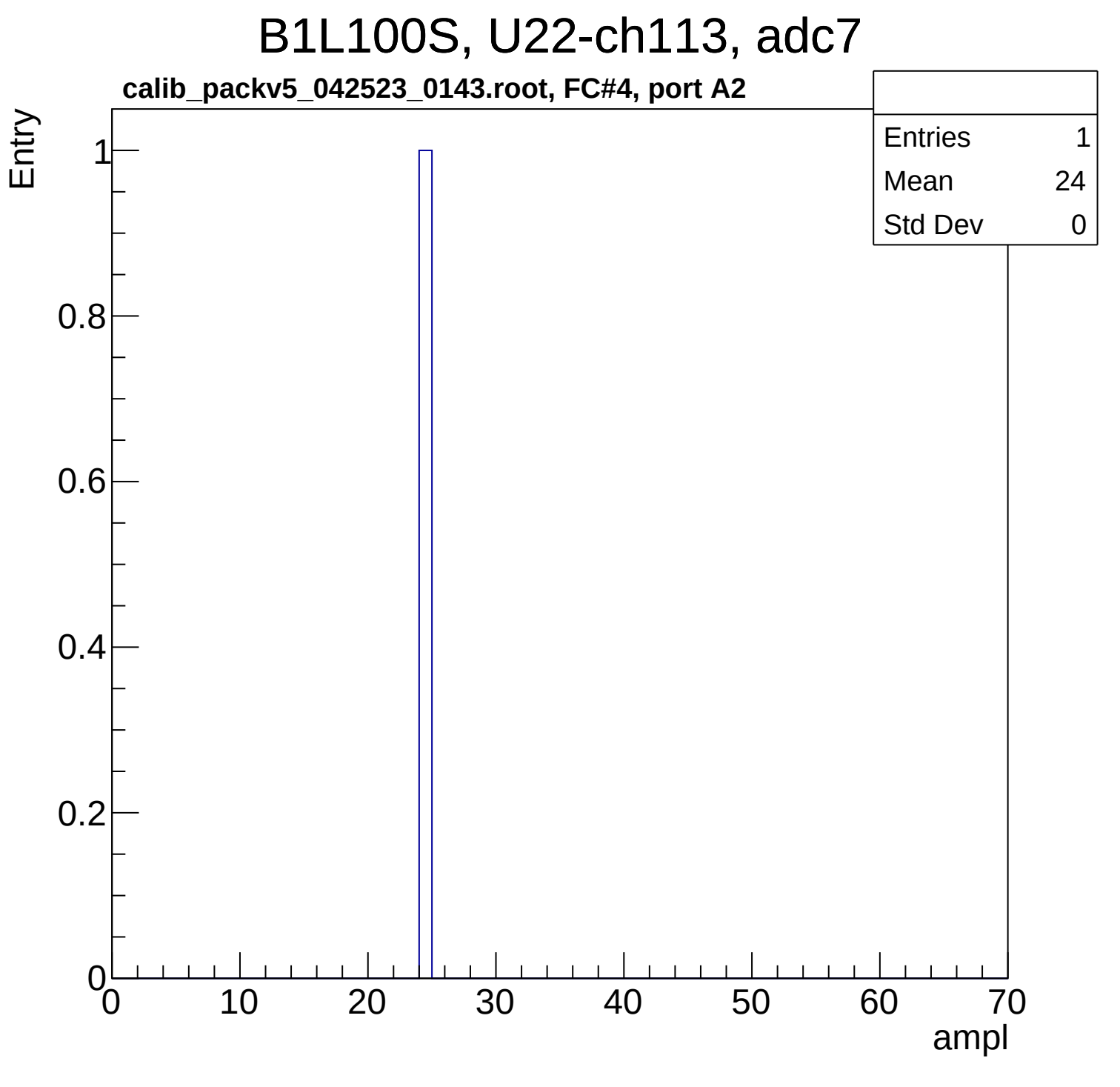

B1L100S, U22-ch113, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62.67
Std Dev 0.4714
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch113, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 24
Std Dev 0
Entry
ampl
0
0.2
0.4
0.6
0.8
1
0
10
20
30
40
50
60
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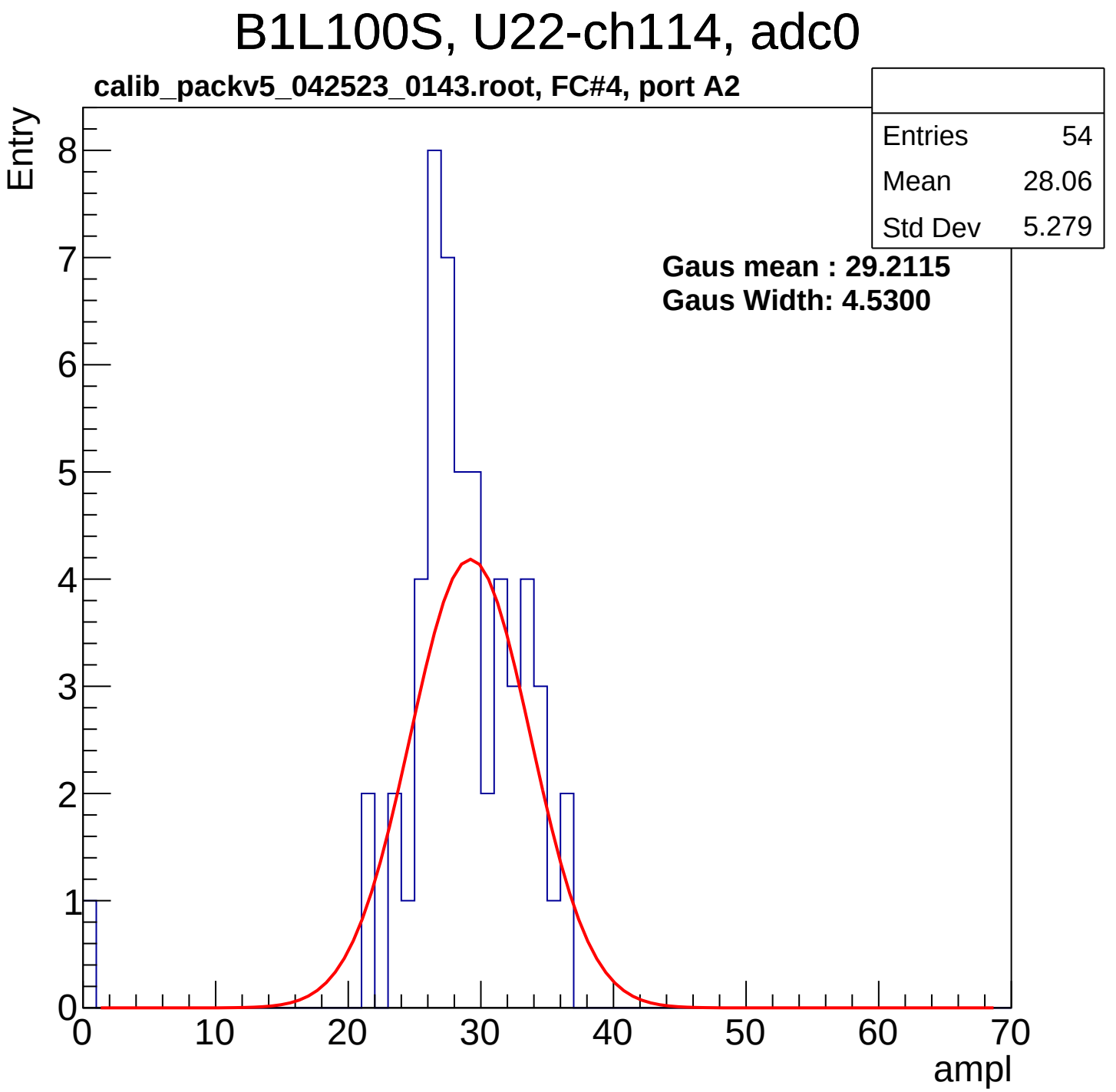
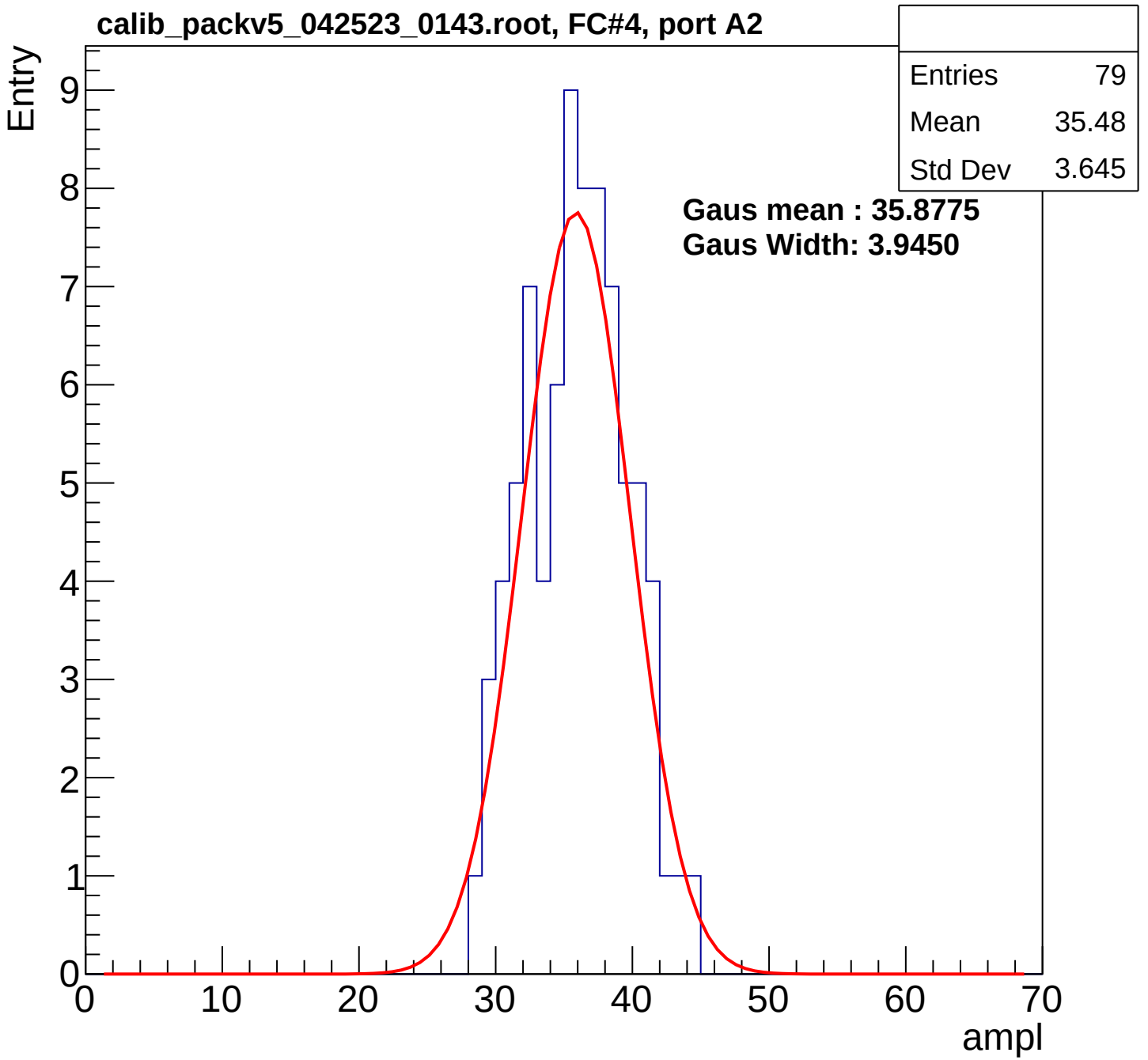

B1L100S, U22-ch114, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 54
Mean 28.06
Std Dev 5.279
Gaus mean : 29.2115
Gaus Width: 4.5300
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch114, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 35.48
Std Dev 3.645
Gaus mean : 35.8775
Gaus Width: 3.9450
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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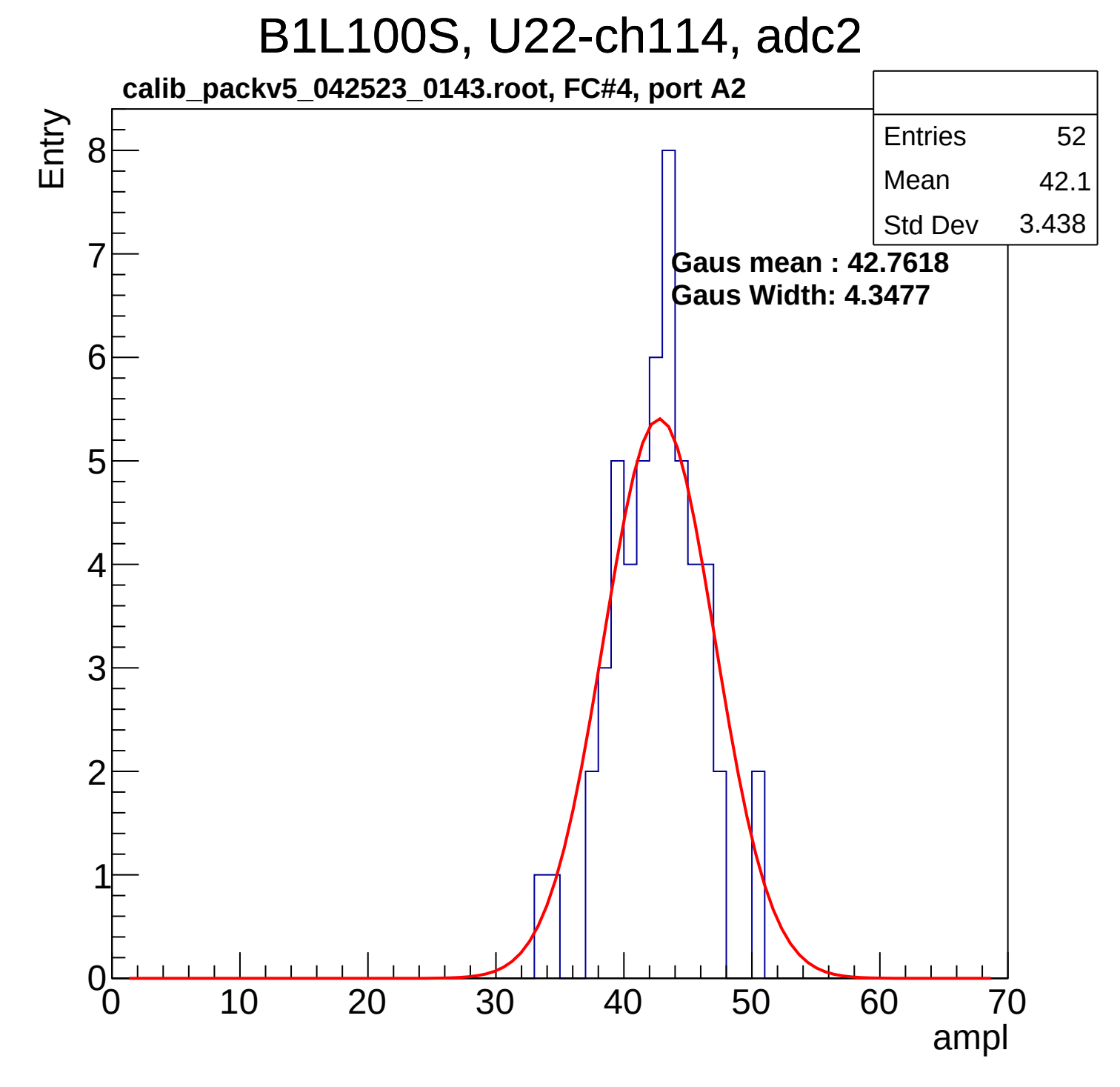
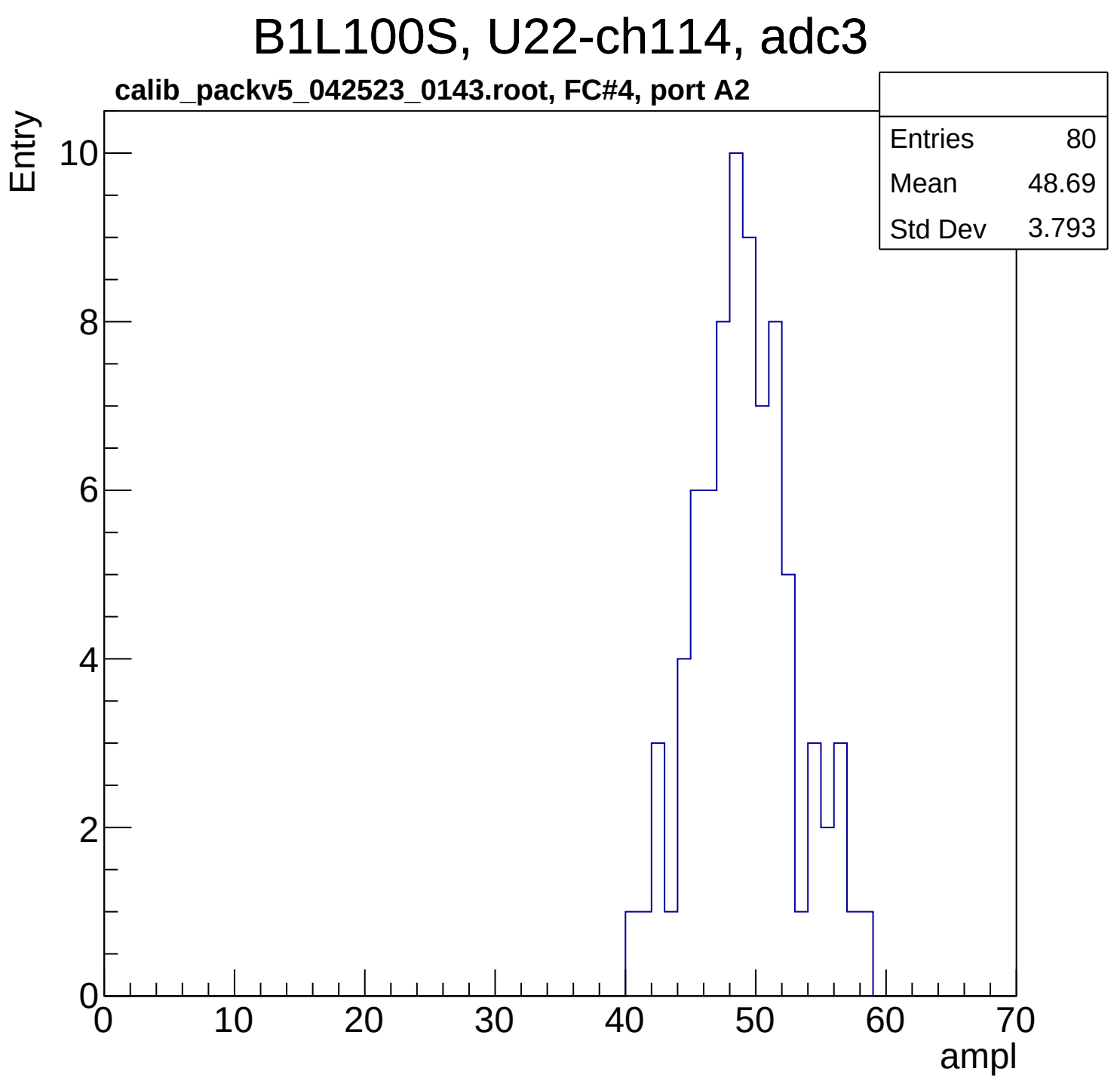

B1L100S, U22-ch114, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 42.1
Std Dev 3.438
Gaus mean : 42.7618
Gaus Width: 4.3477
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch114, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 48.69
Std Dev 3.793
Entry
ampl
0
2
4
6
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10
0
10
20
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40
50
60
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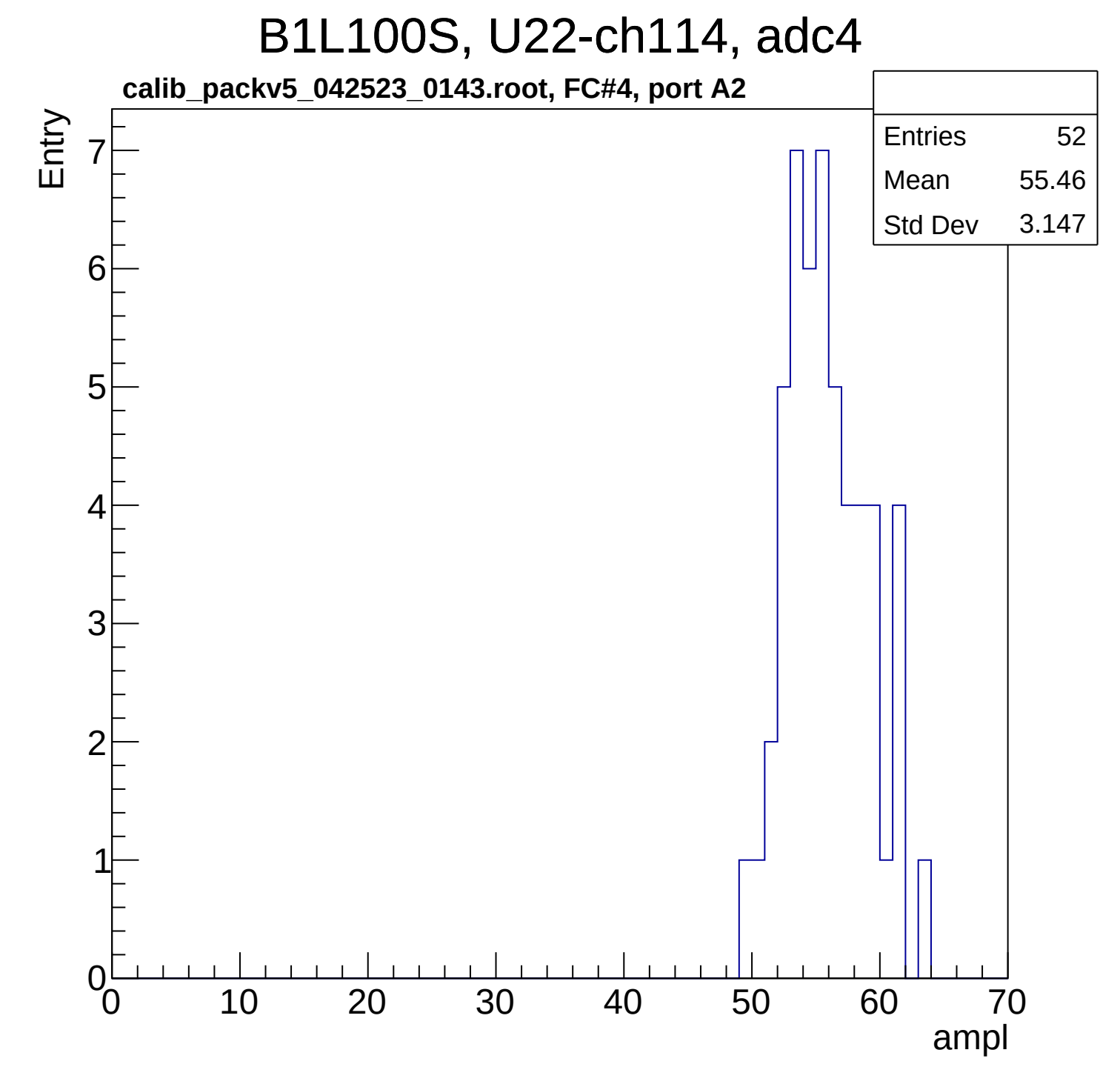
B1L100S, U22-ch114, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.46
Std Dev 3.147
Entry
ampl
0
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3
4
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6
7
0
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20
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40
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60
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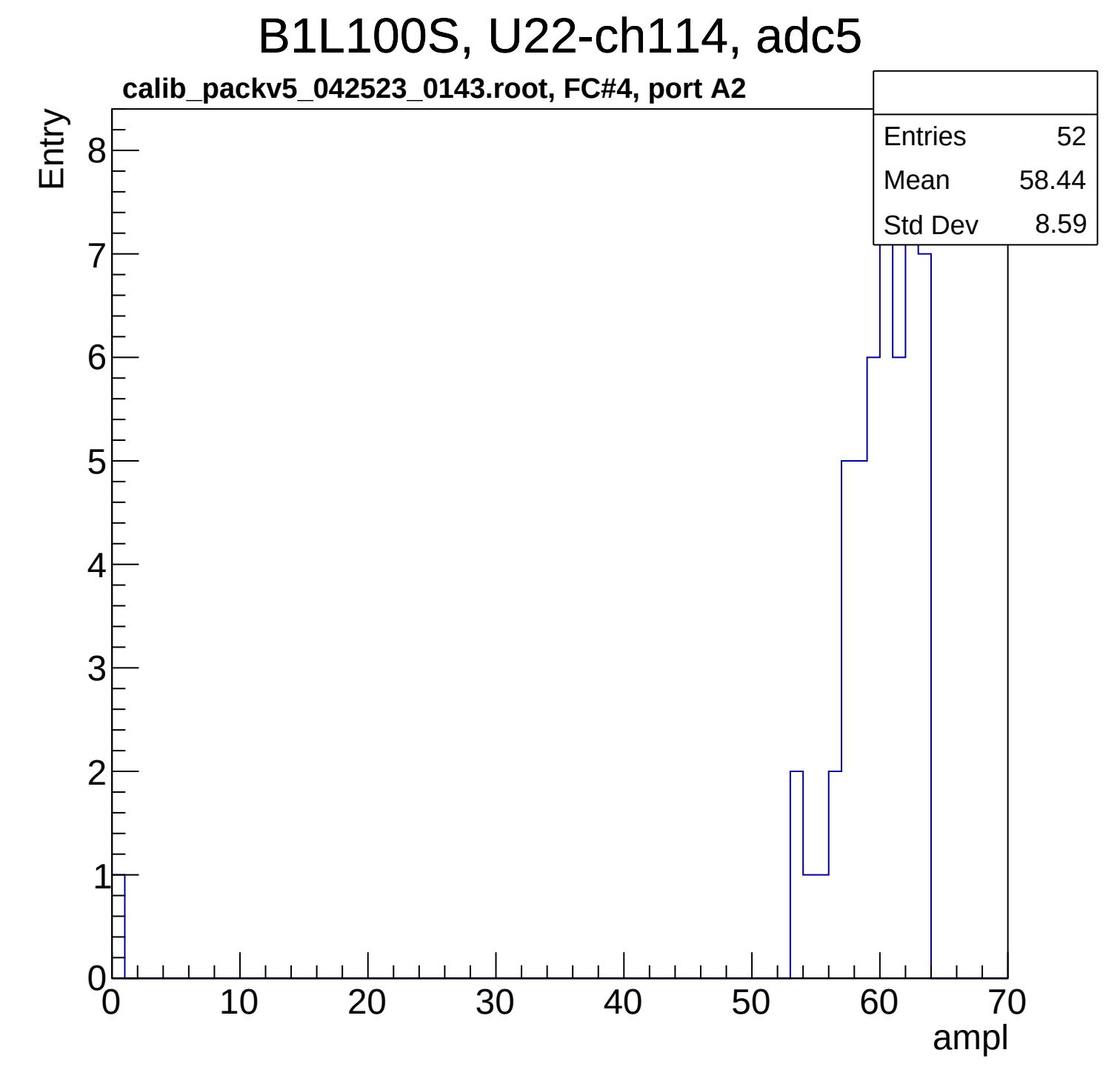
B1L100S, U22-ch114, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.44
Std Dev 8.59
Entry
ampl
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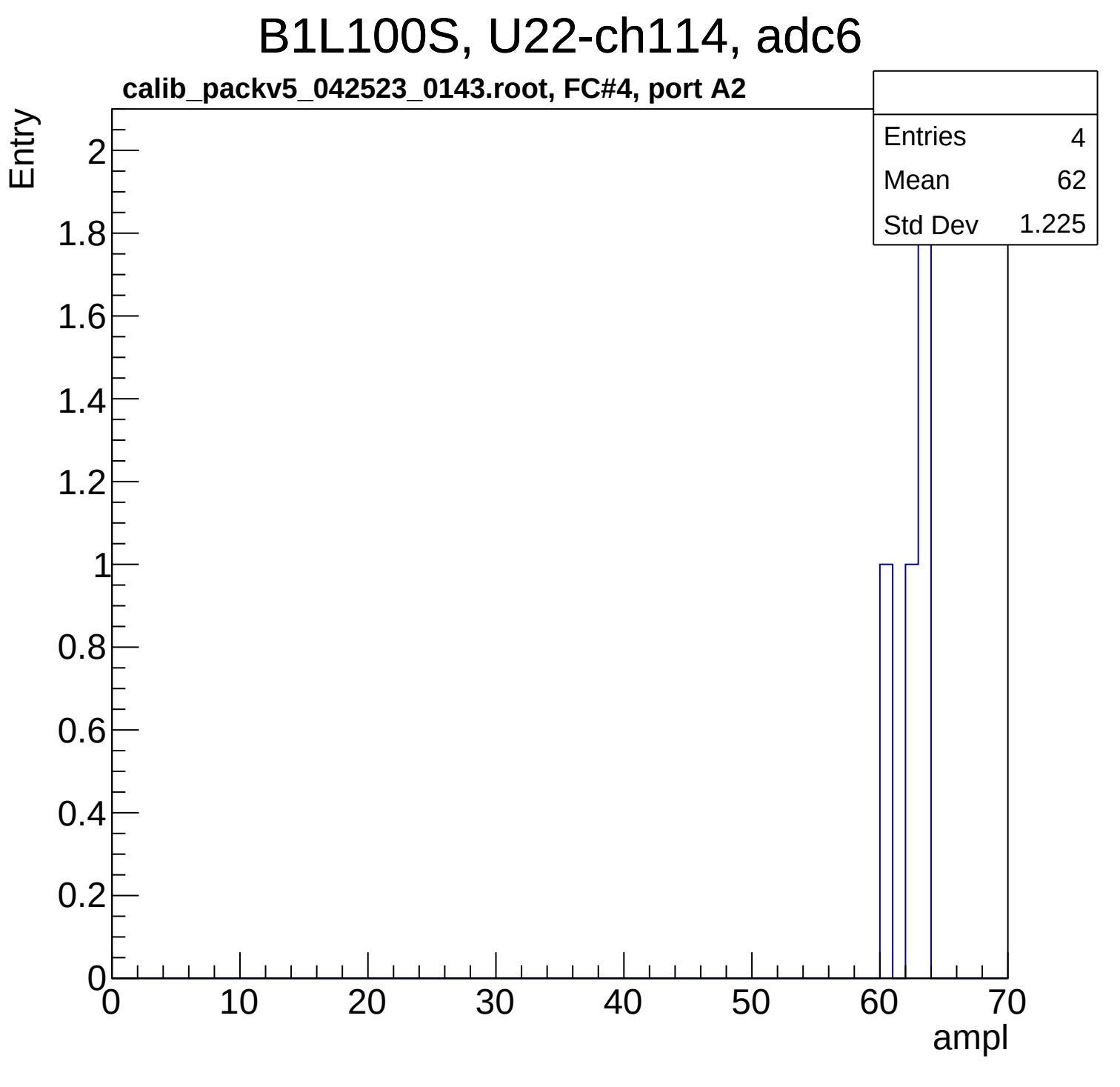
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch114, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62
Std Dev 1.225
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
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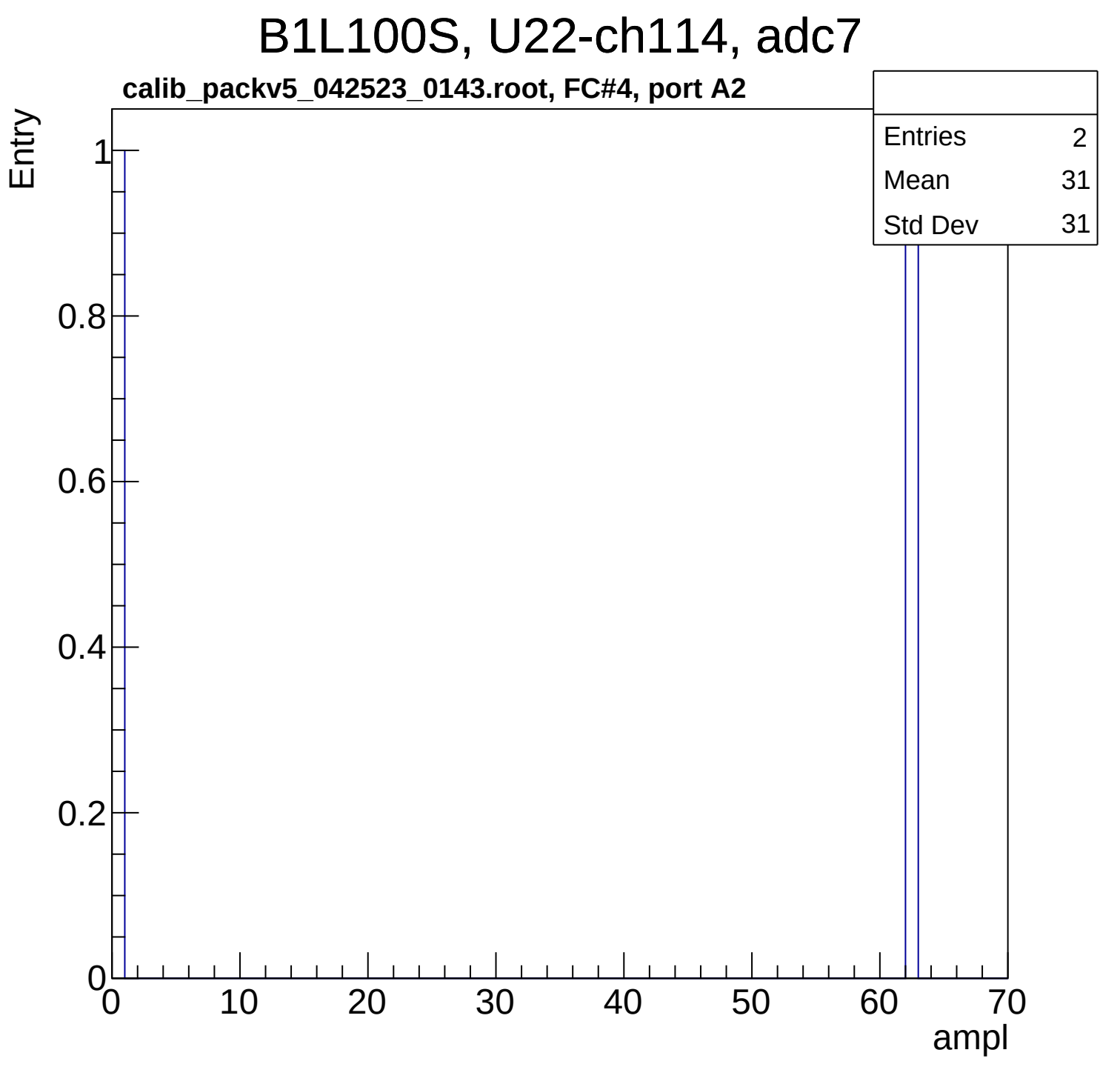
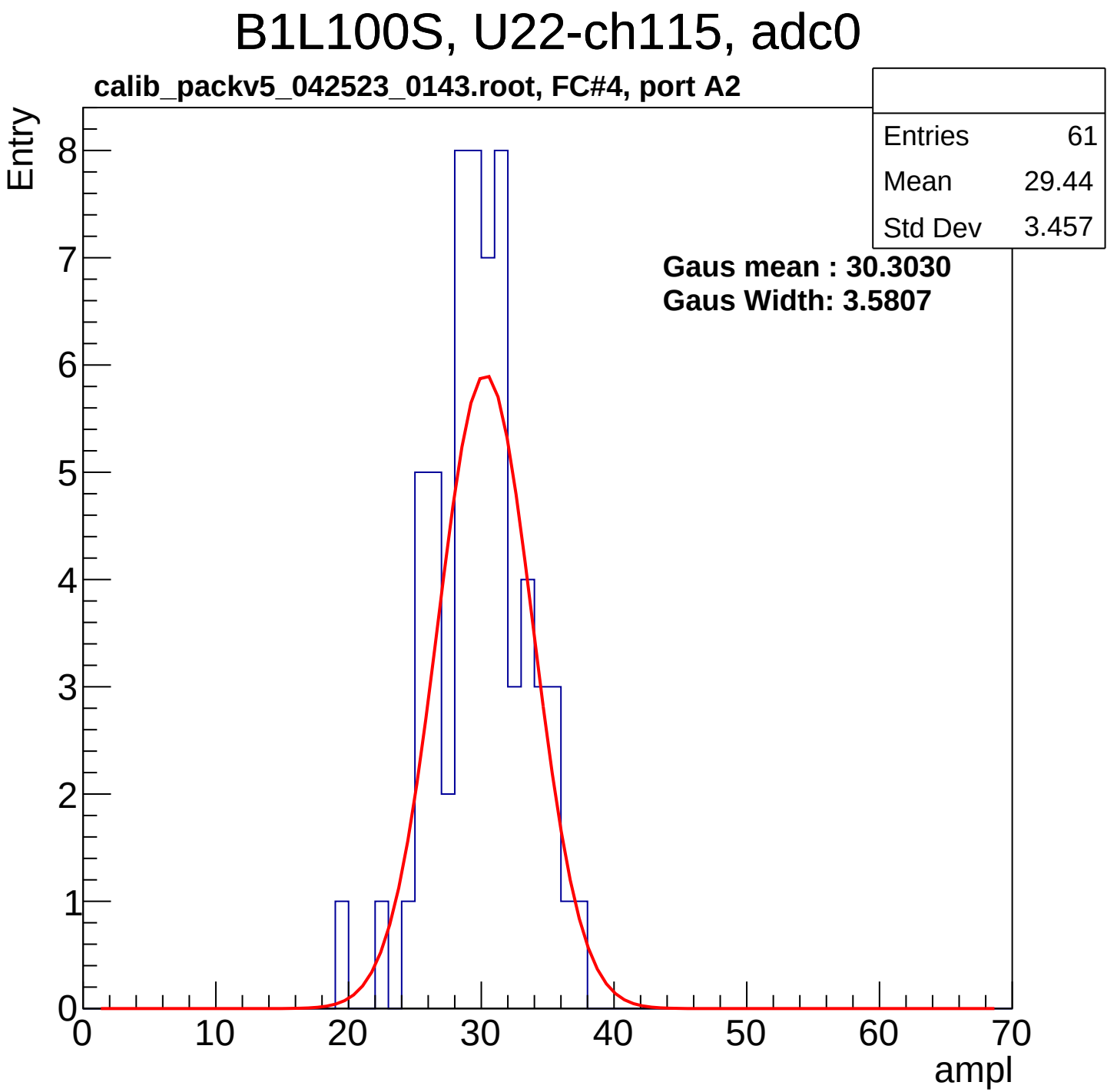
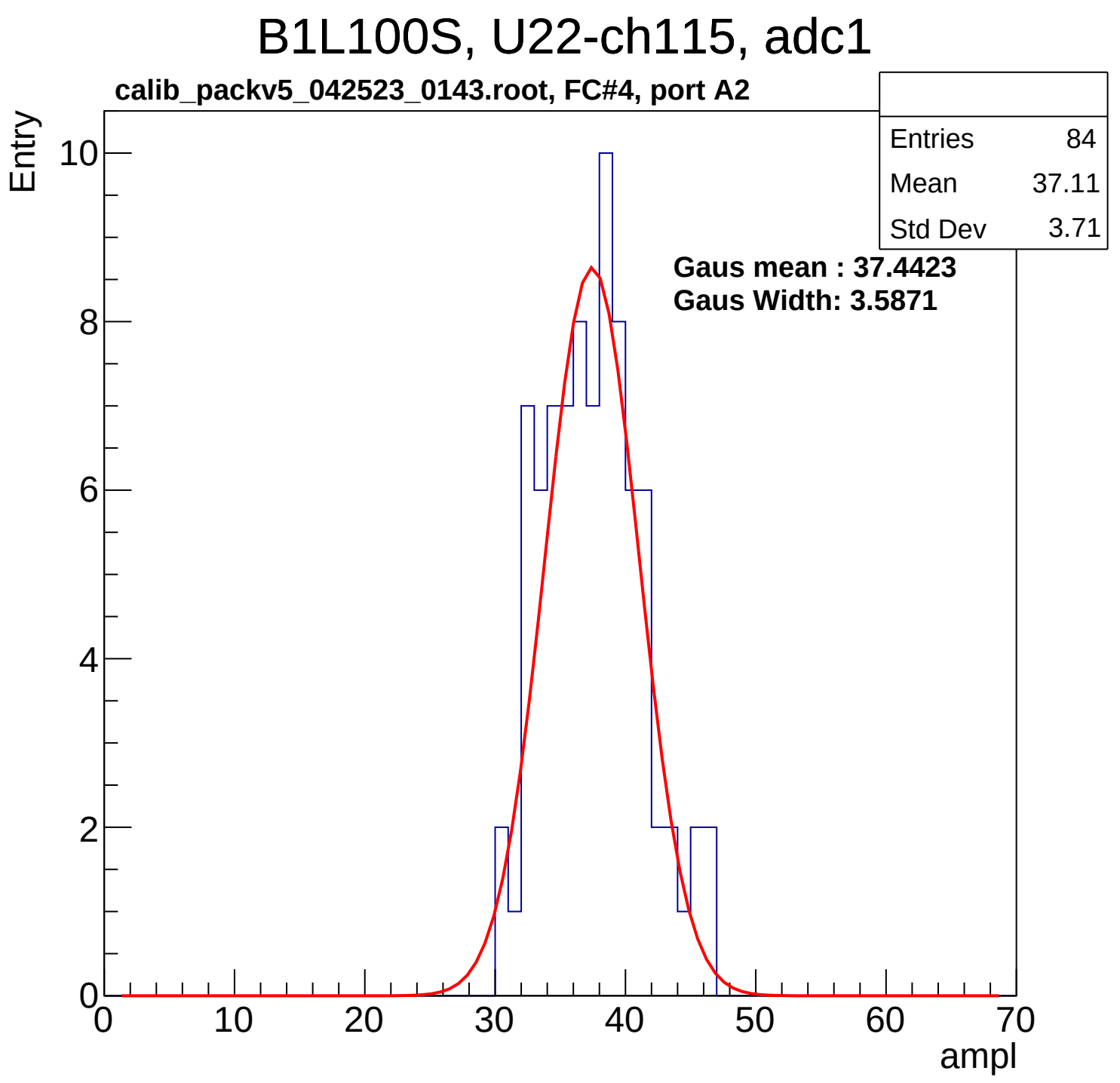
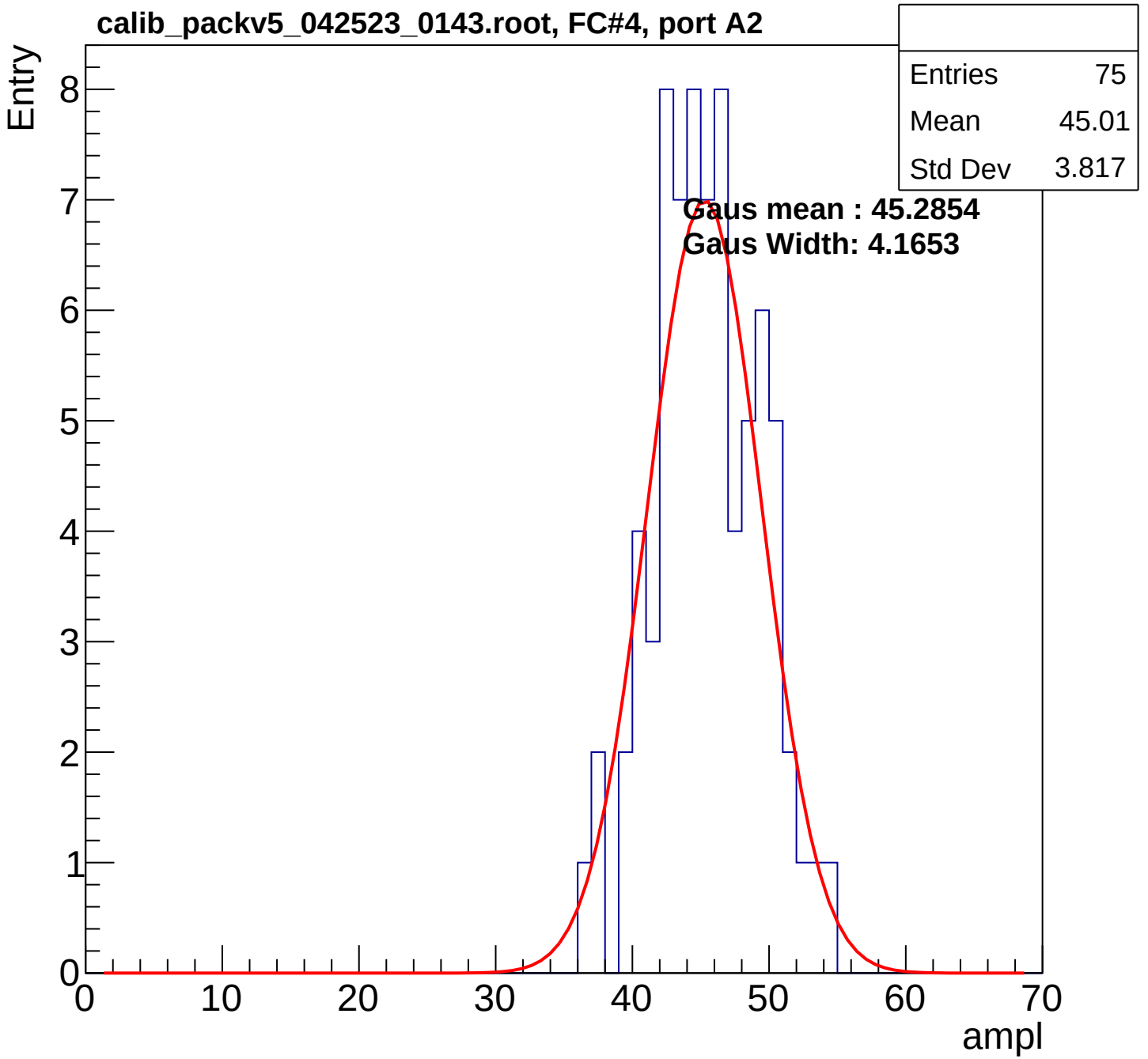
20
30
40
50
60
70


B1L100S, U22-ch114, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31
Std Dev 31
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch115, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 61
Mean 29.44
Std Dev 3.457
Gaus mean : 30.3030
Gaus Width: 3.5807
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch115, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 84
Mean 37.11
Std Dev 3.71
Gaus mean : 37.4423
Gaus Width: 3.5871
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch115, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 45.01
Std Dev 3.817
Gaus mean : 45.2854
Gaus Width: 4.1653
Entry
ampl
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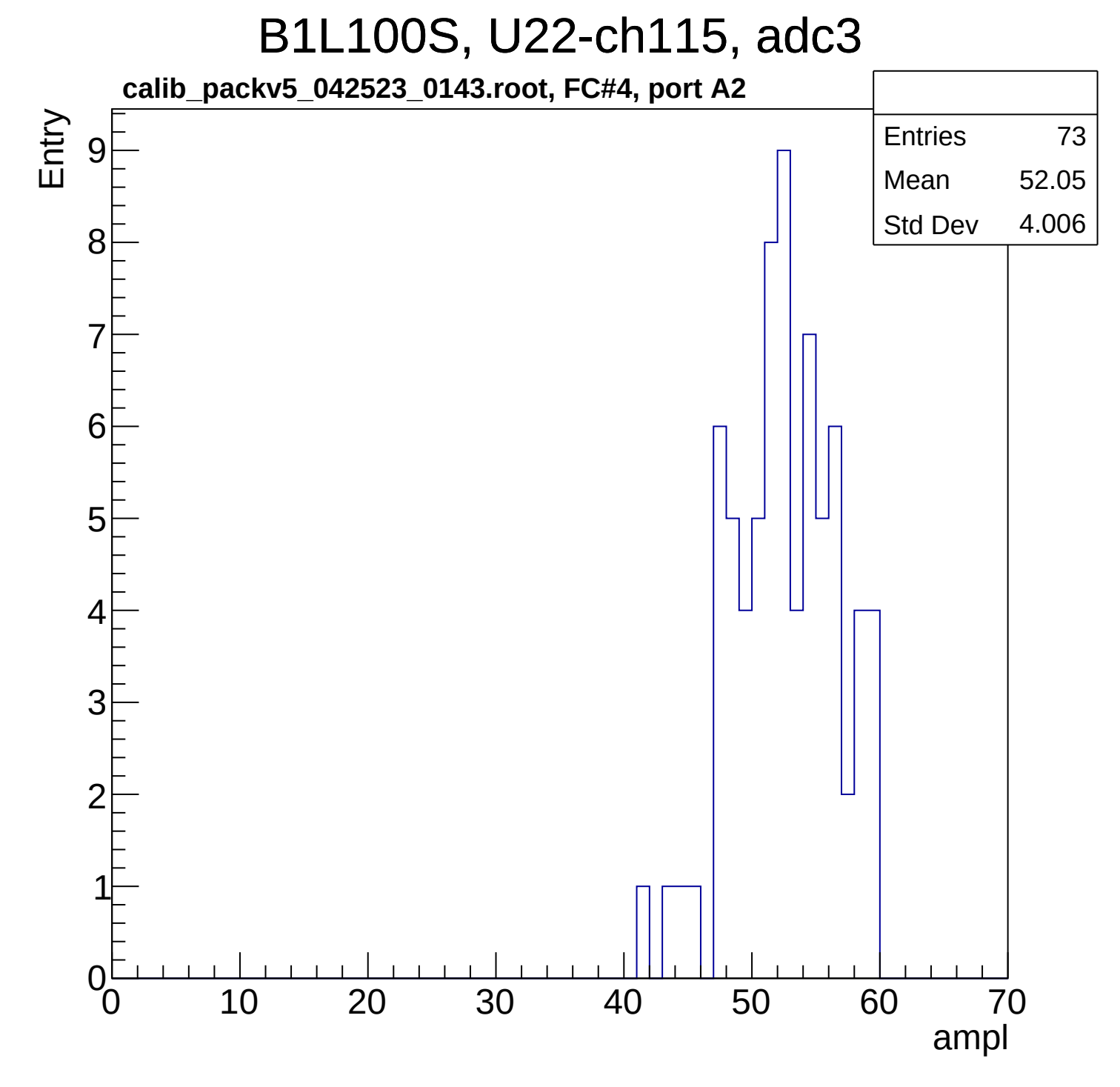
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch115, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 52.05
Std Dev 4.006
Entry
ampl
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2
3
4
5
6
7
8
9
0
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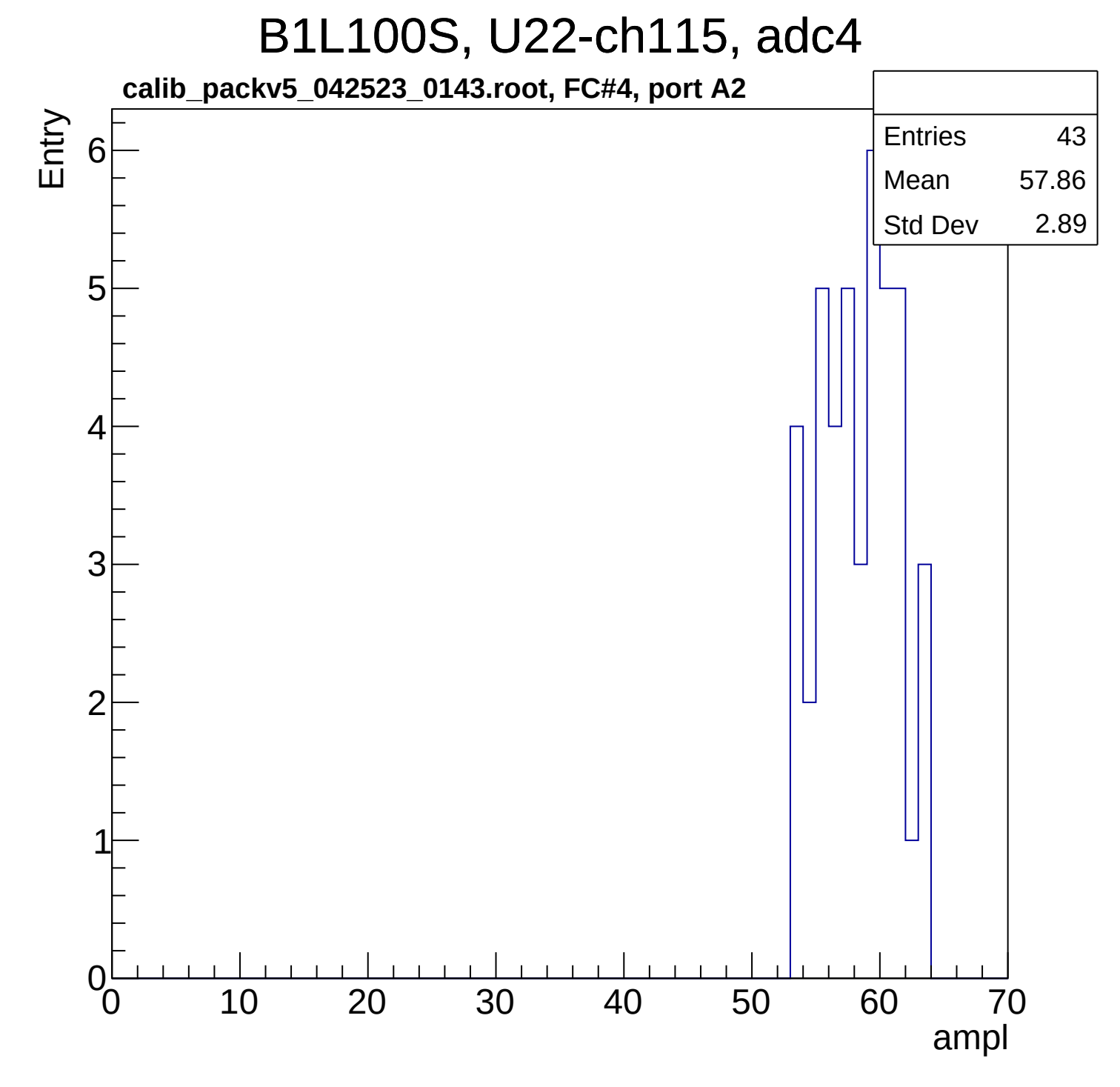
B1L100S, U22-ch115, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 57.86
Std Dev 2.89
Entry
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4
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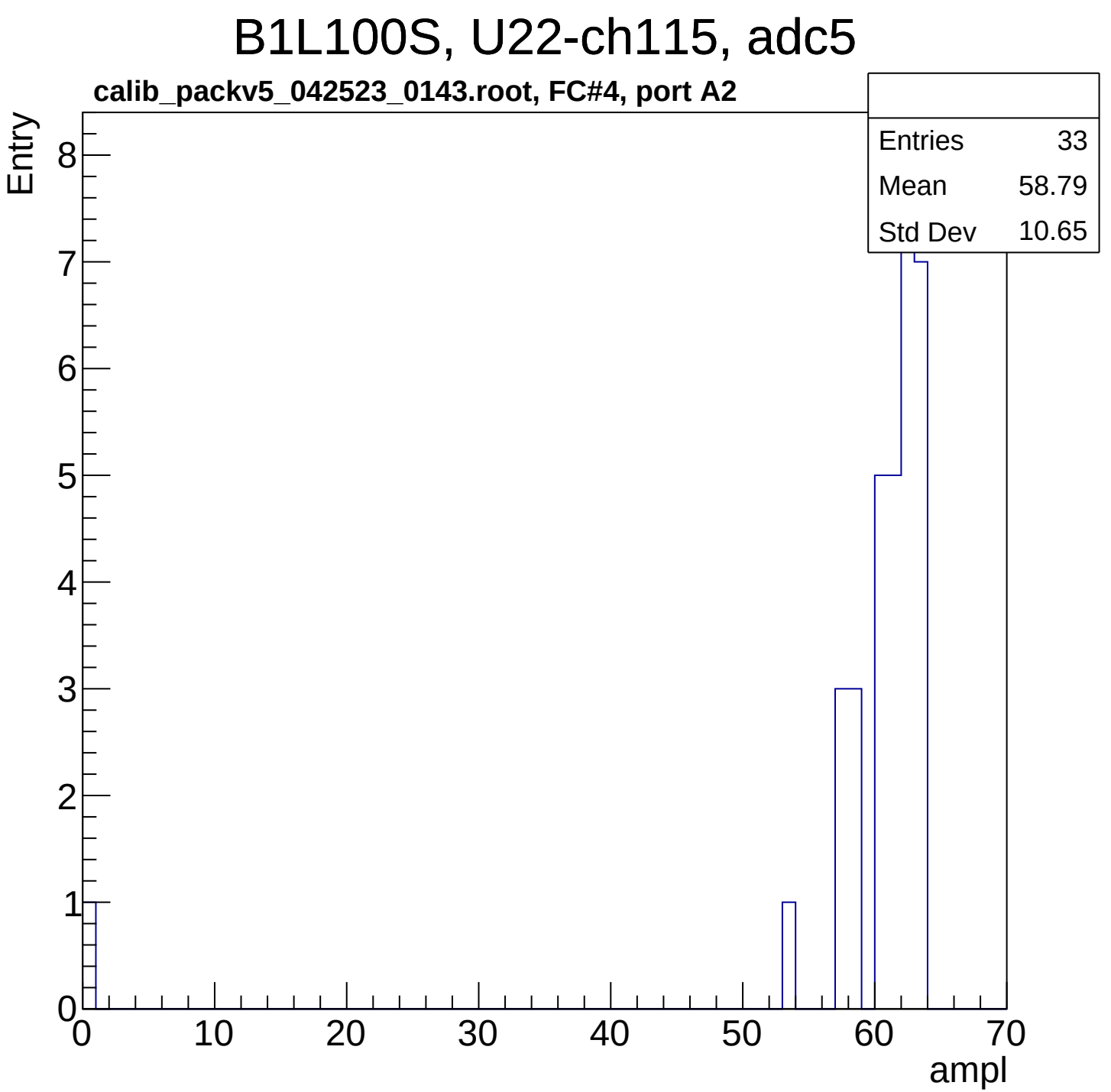
6
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch115, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 33
Mean 58.79
Std Dev 10.65
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
30
40
50
60
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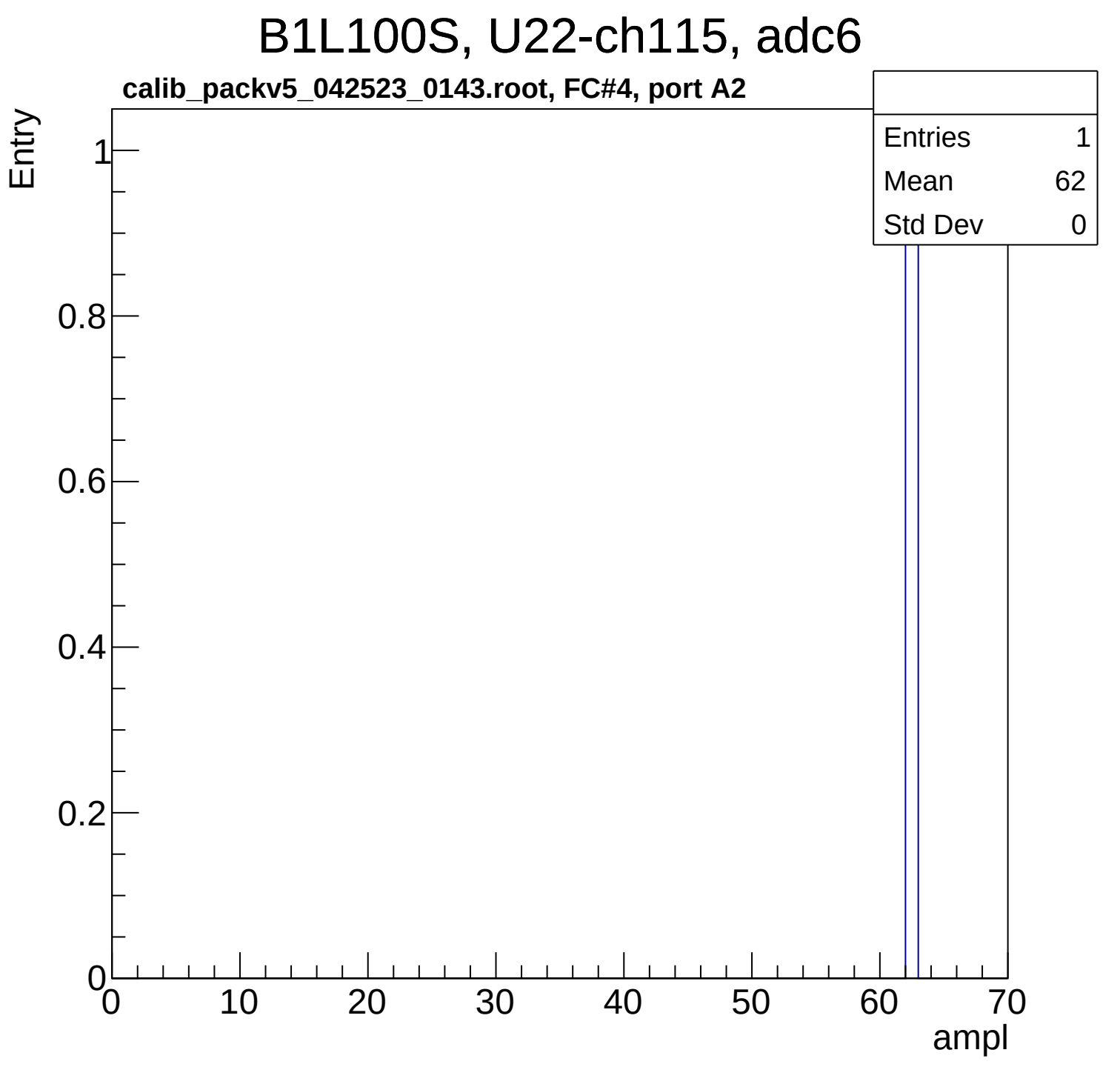
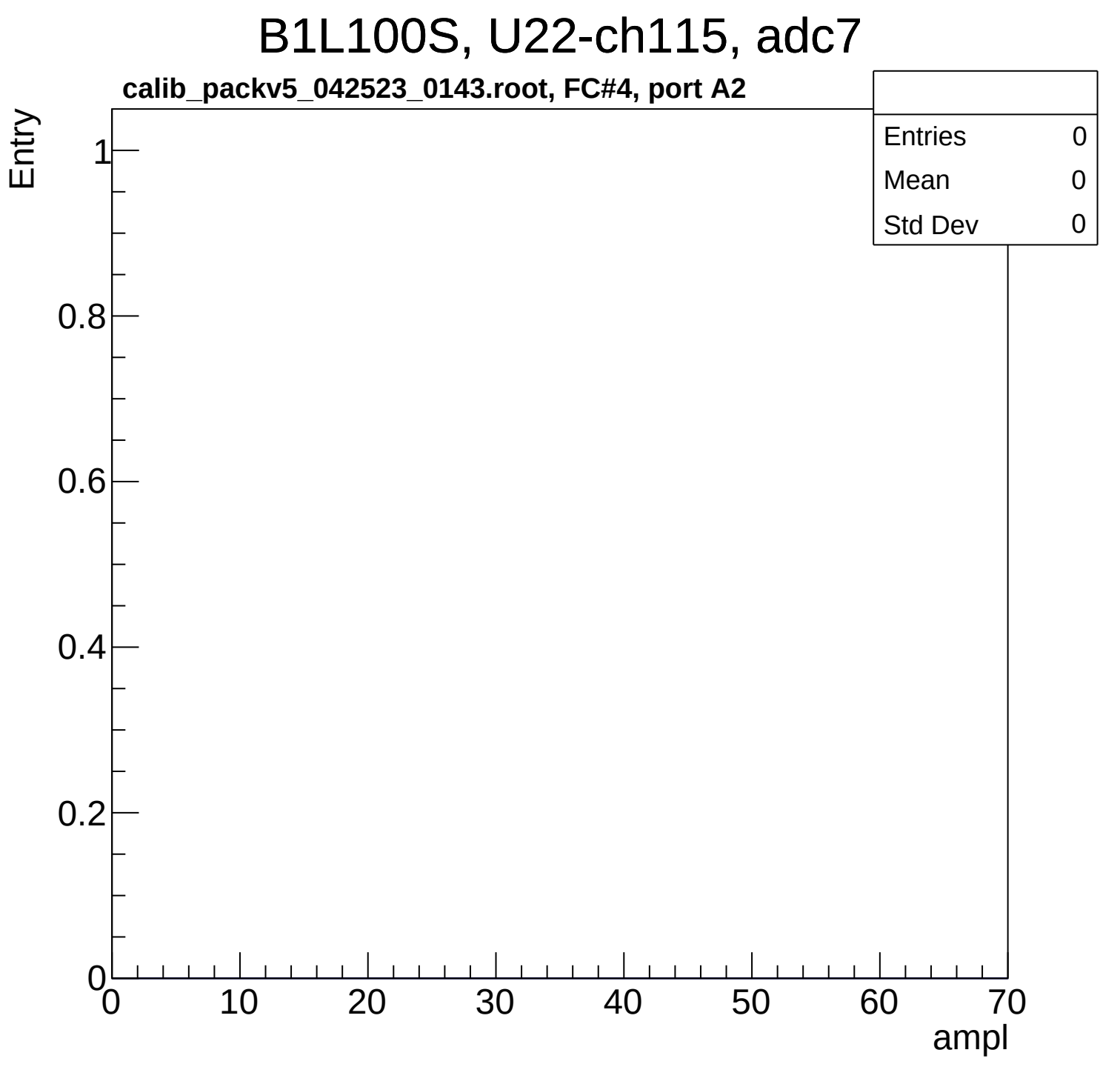

B1L100S, U22-ch115, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 62
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch115, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
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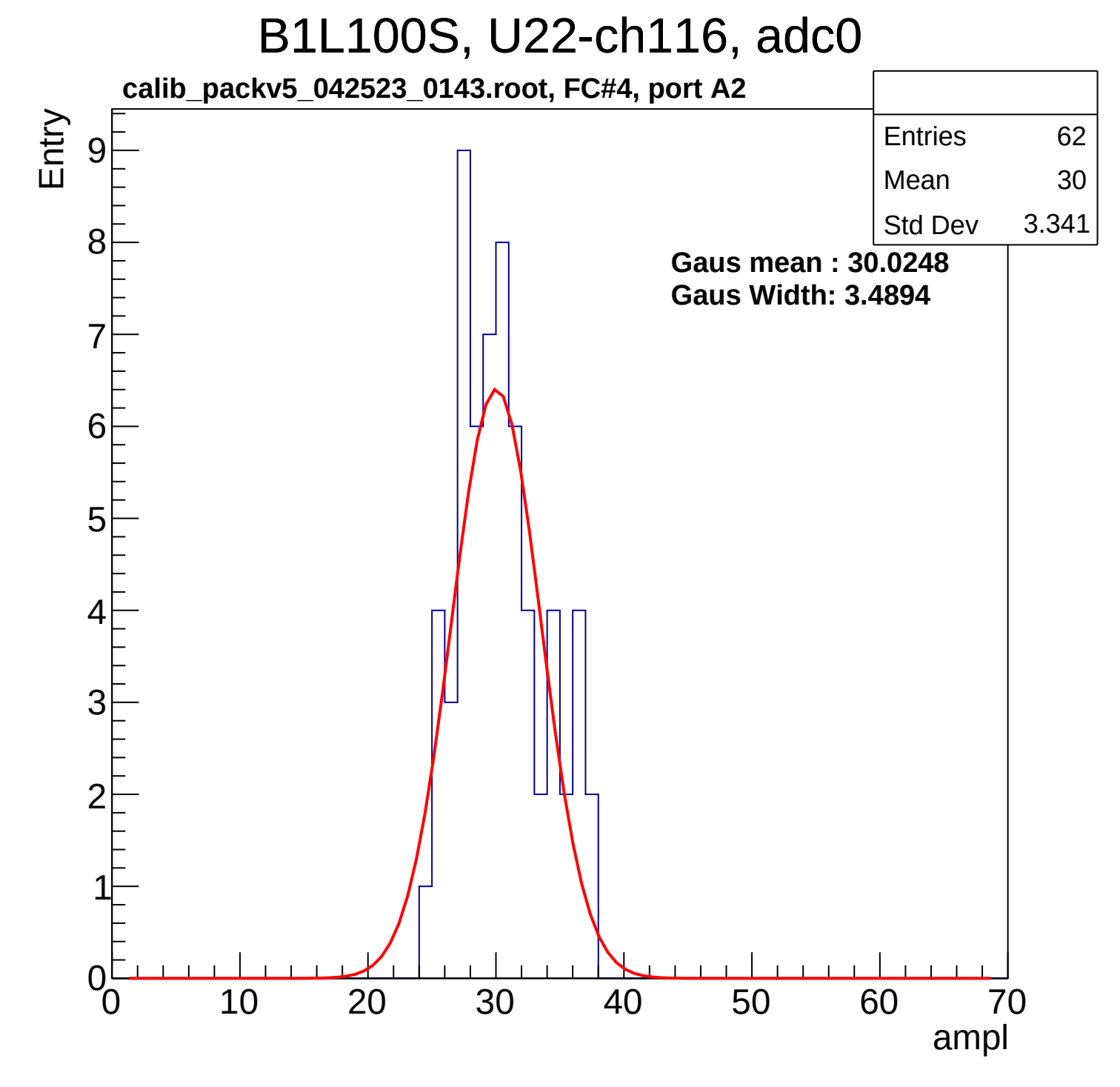
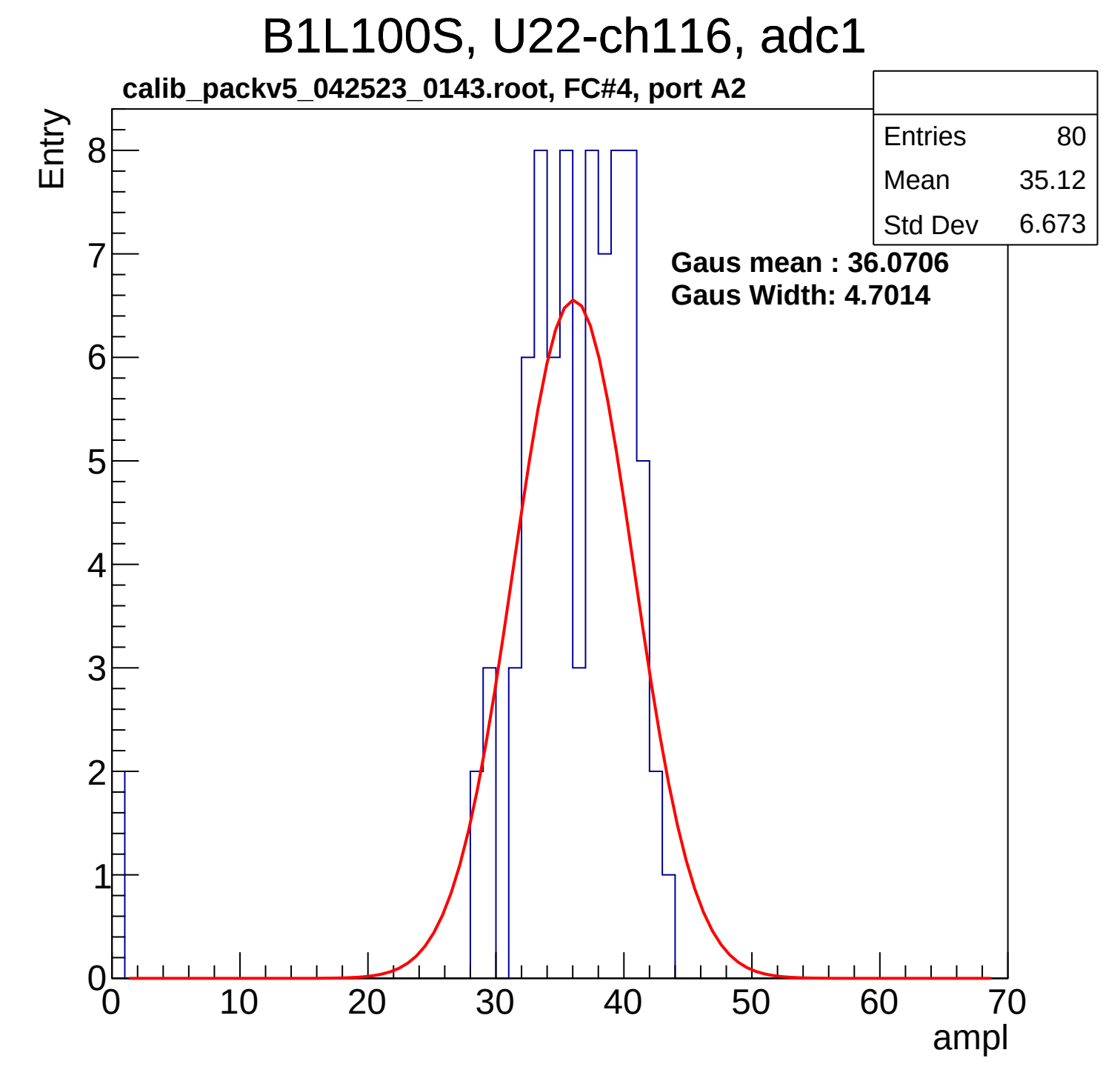
40
50
60
70
ampl


B1L100S, U22-ch116, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 30
Std Dev 3.341
Gaus mean : 30.0248
Gaus Width: 3.4894
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch116, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 35.12
Std Dev 6.673
Gaus mean : 36.0706
Gaus Width: 4.7014
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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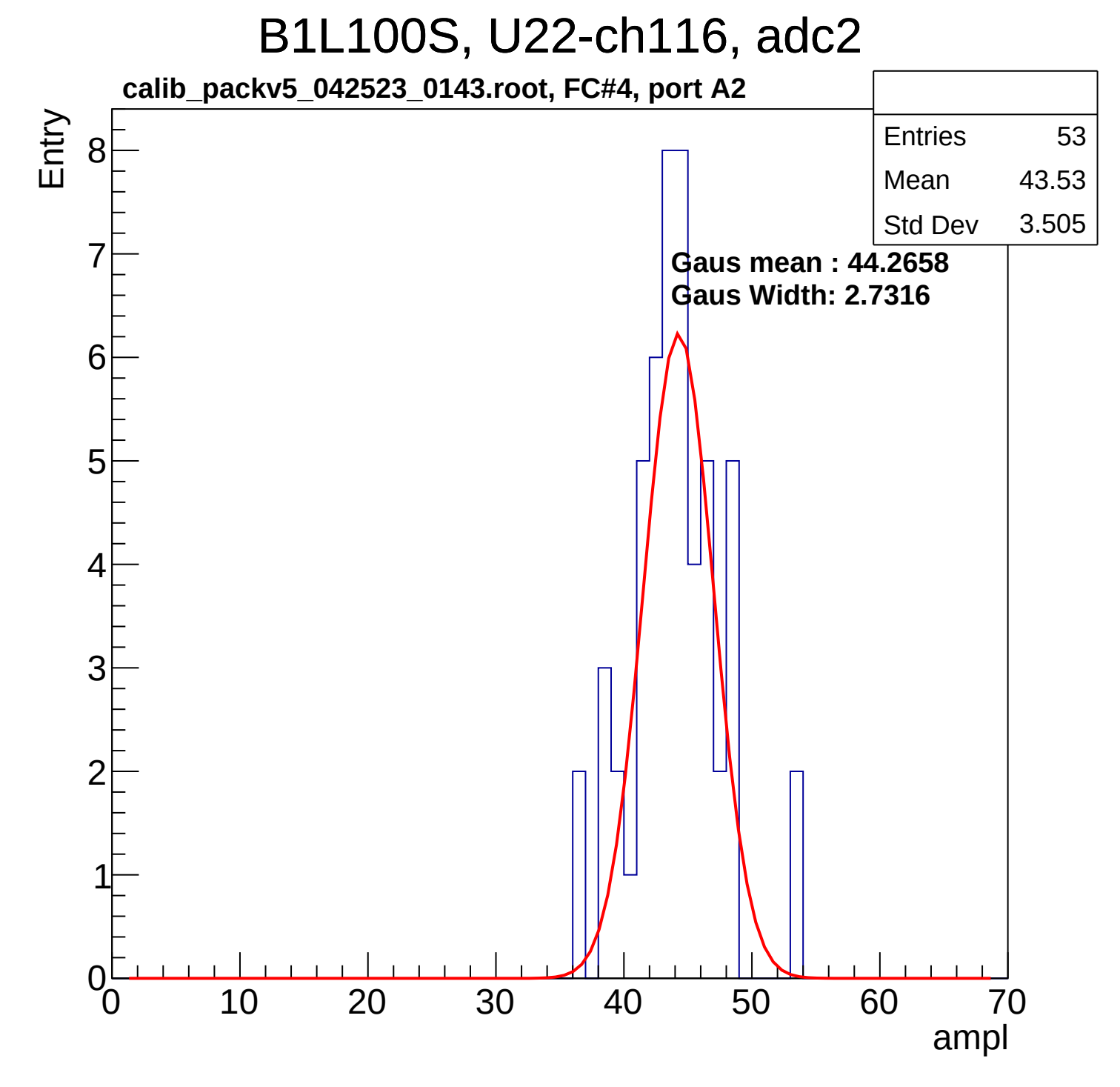
B1L100S, U22-ch116, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 43.53
Std Dev 3.505
Gaus mean : 44.2658
Gaus Width: 2.7316
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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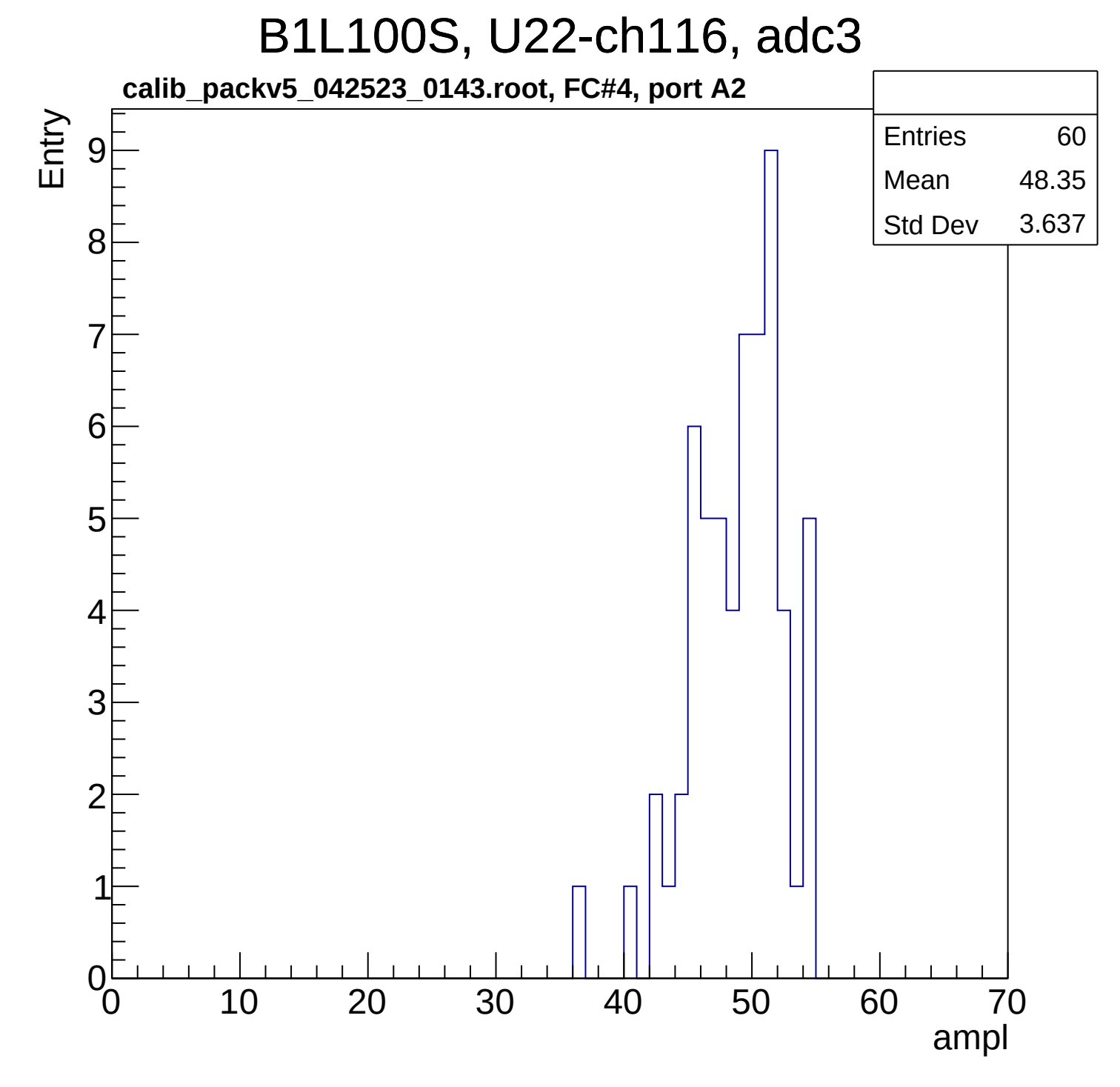
B1L100S, U22-ch116, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 48.35
Std Dev 3.637
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
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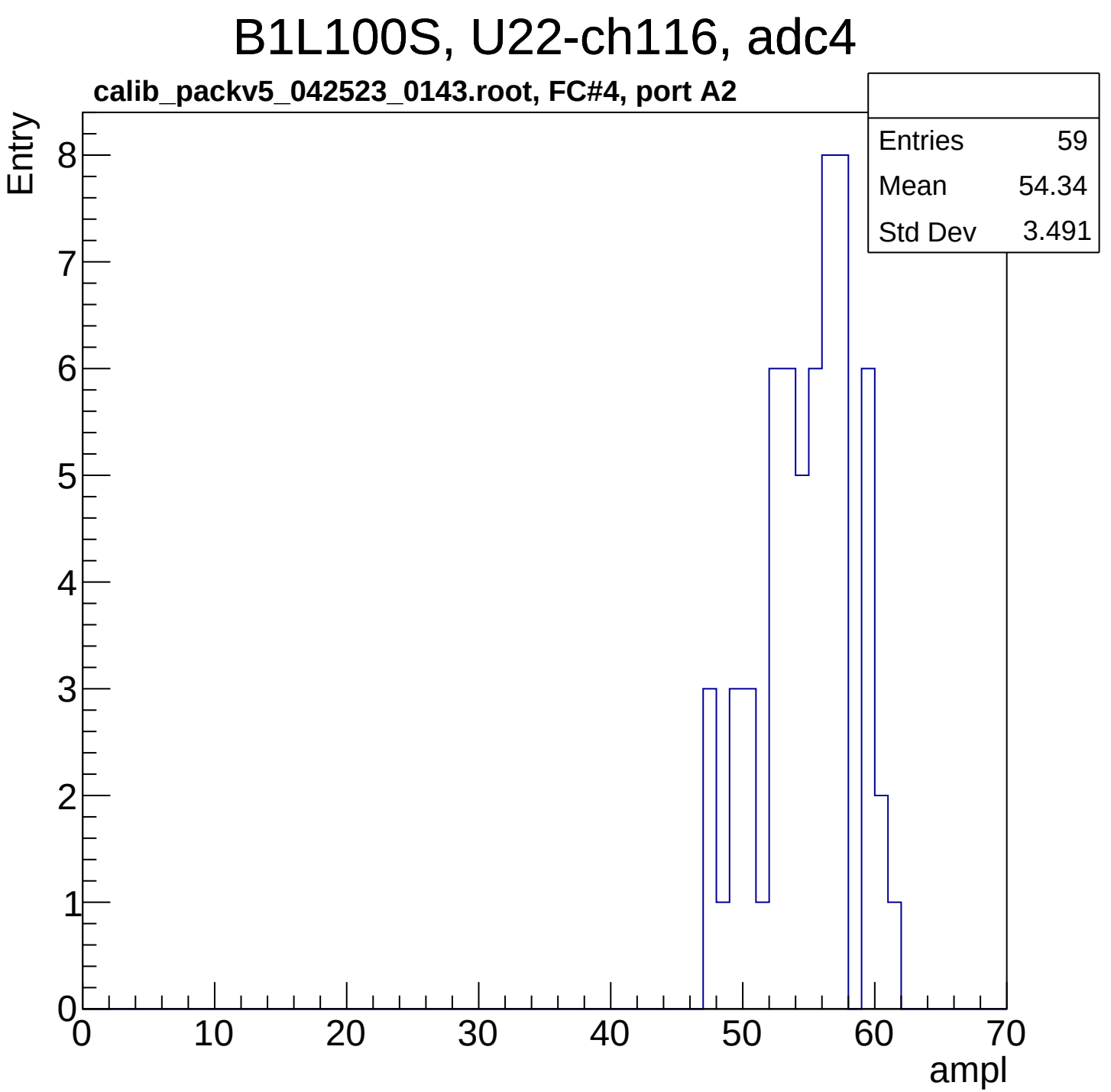
B1L100S, U22-ch116, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 54.34
Std Dev 3.491
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
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60
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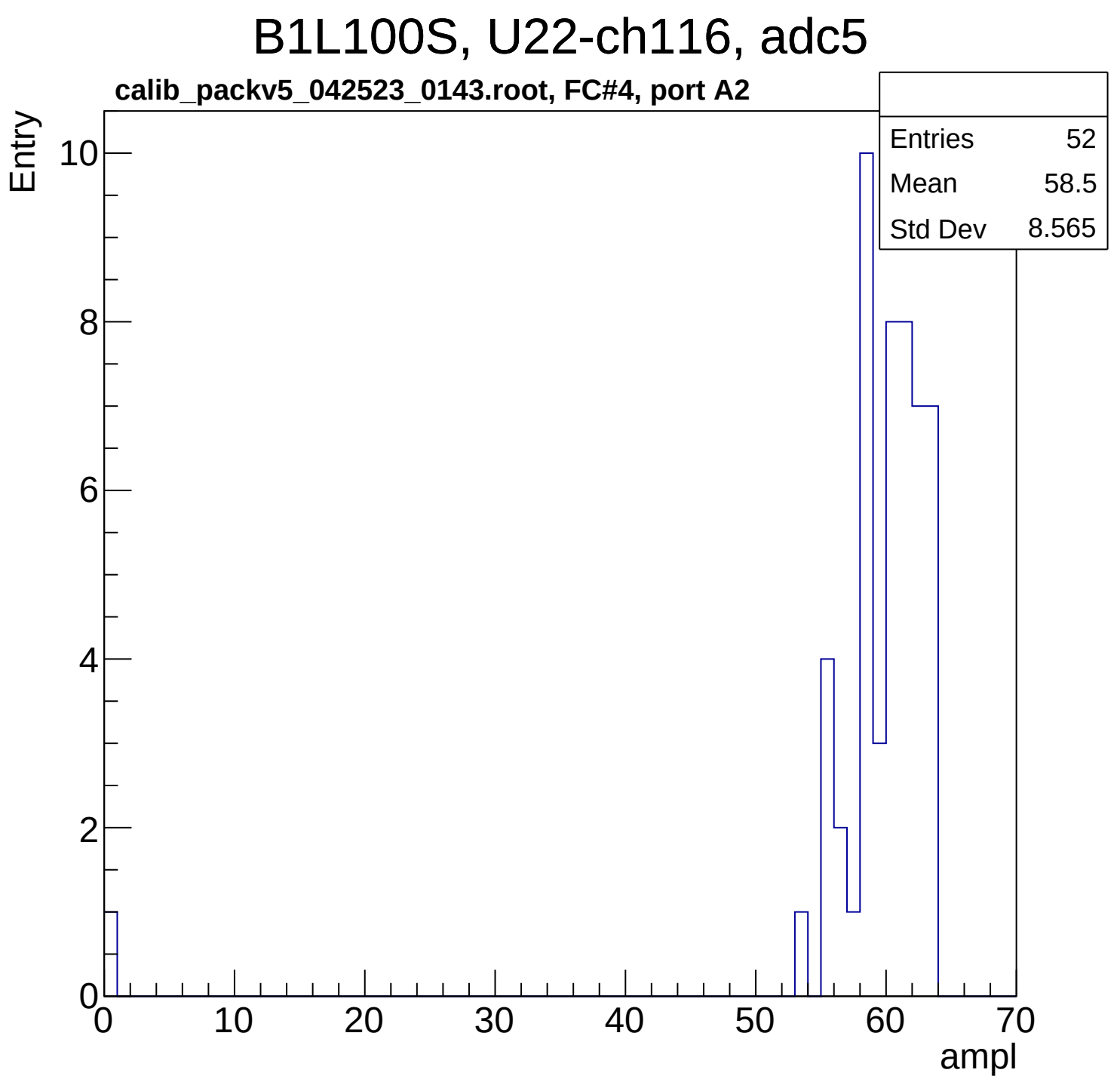
B1L100S, U22-ch116, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 58.5
Std Dev 8.565
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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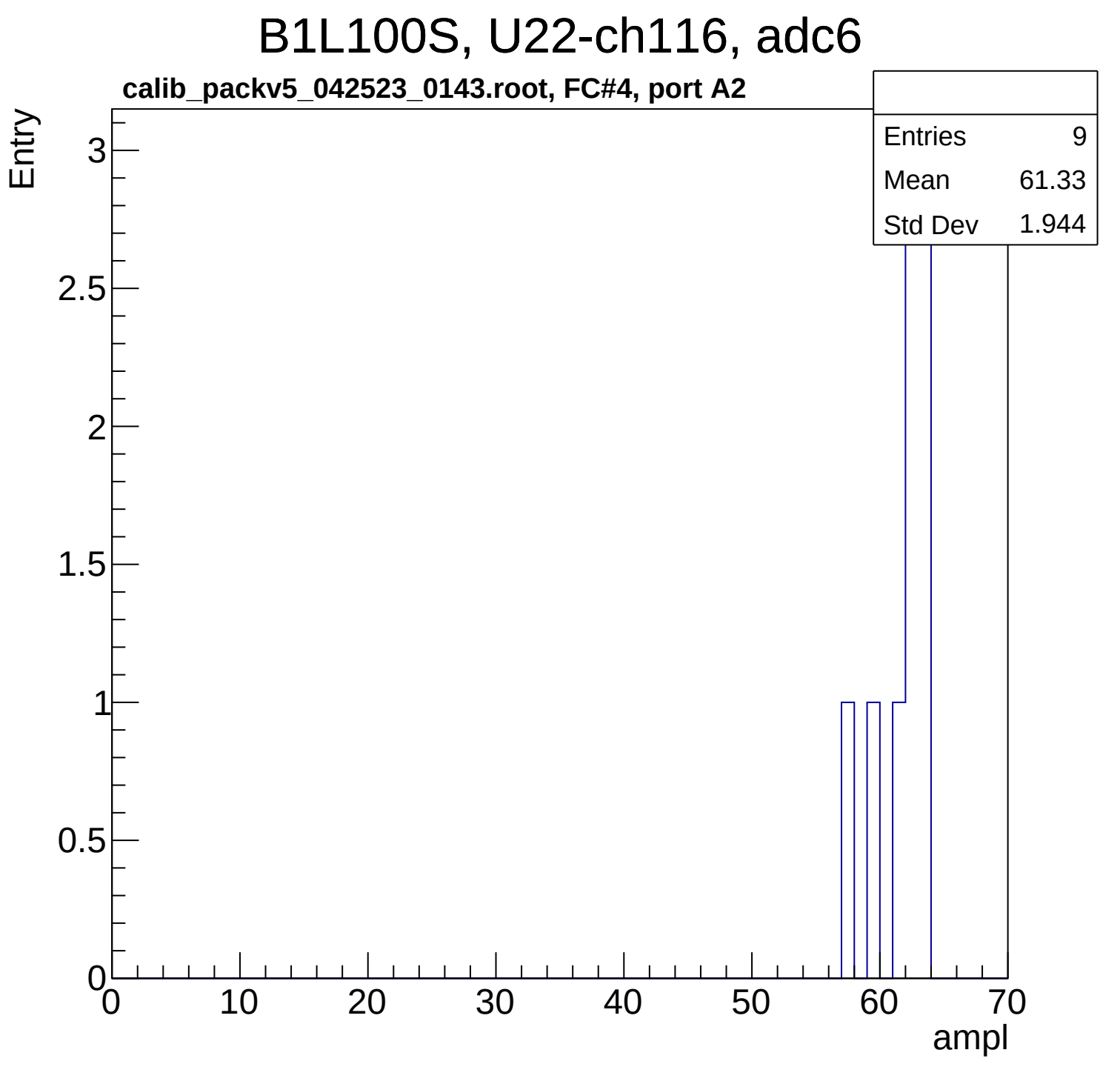
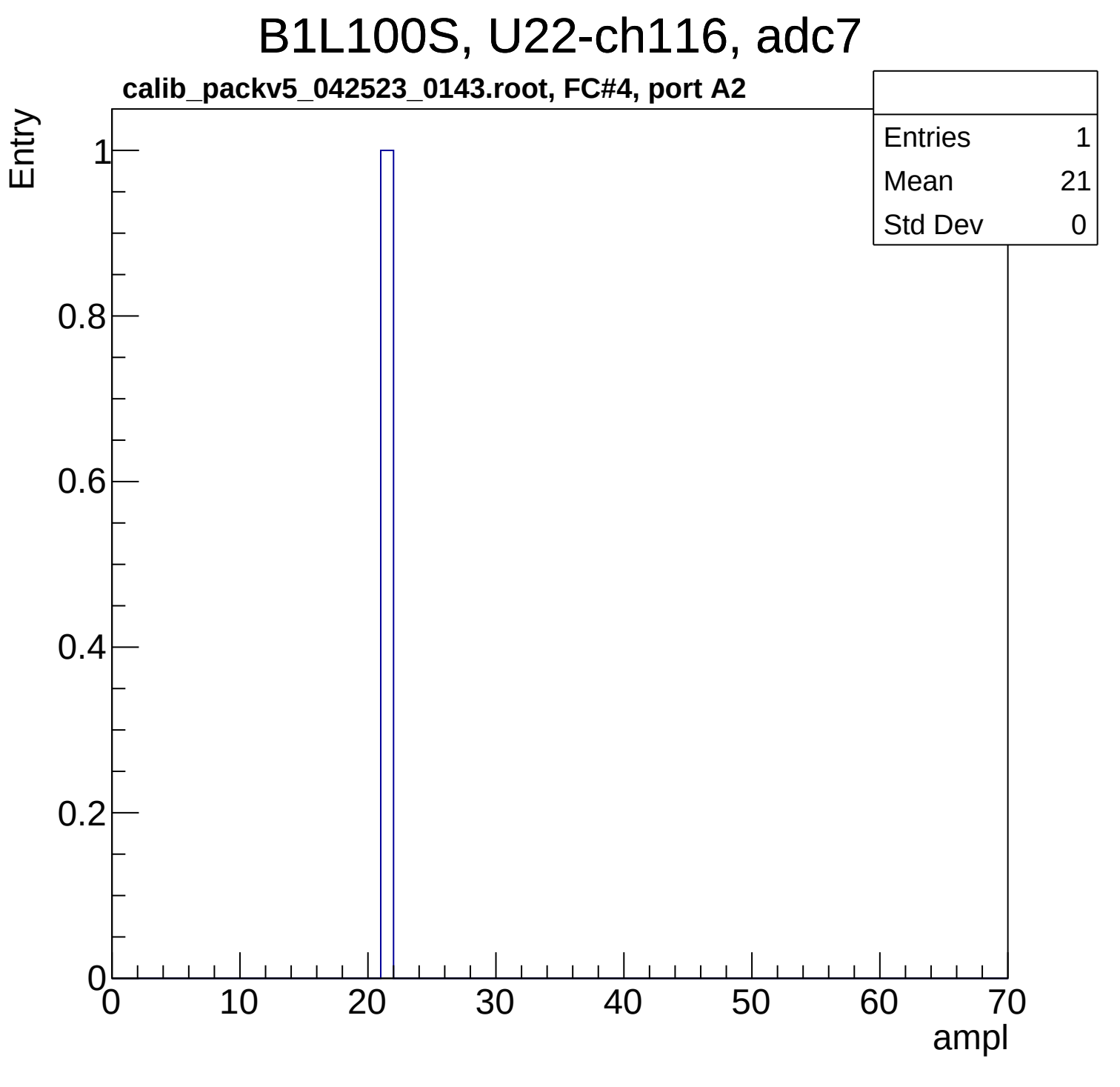
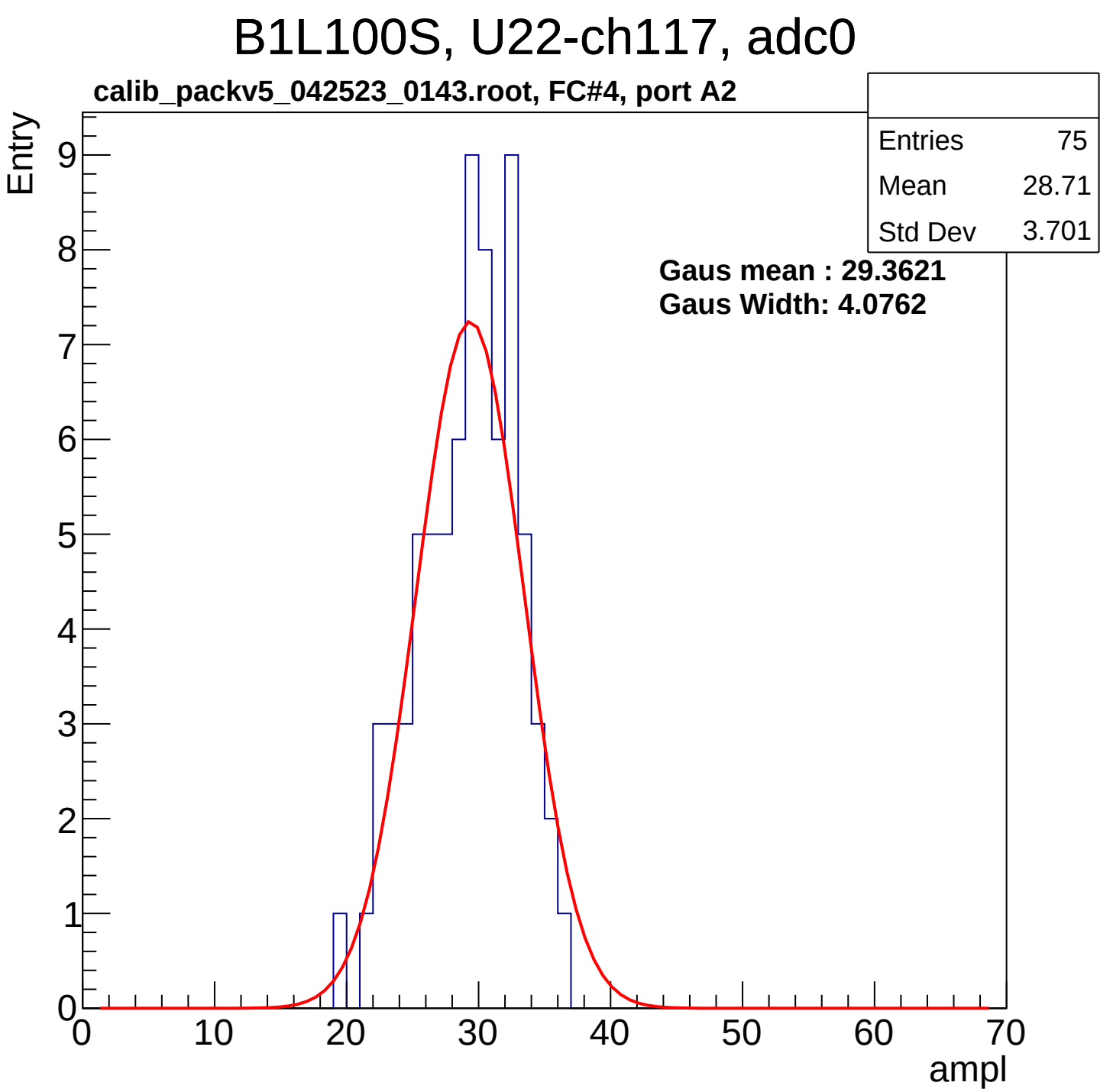
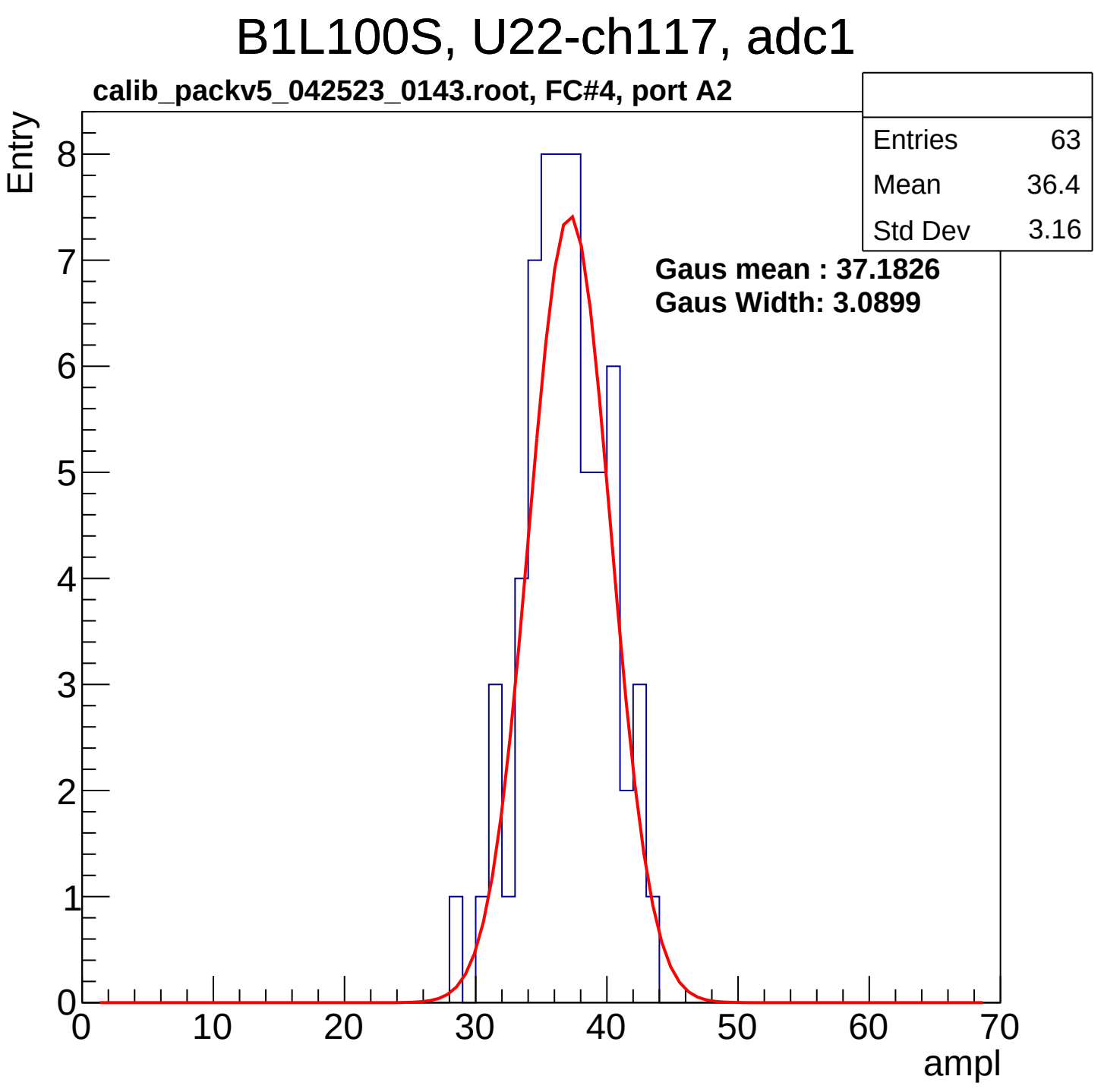
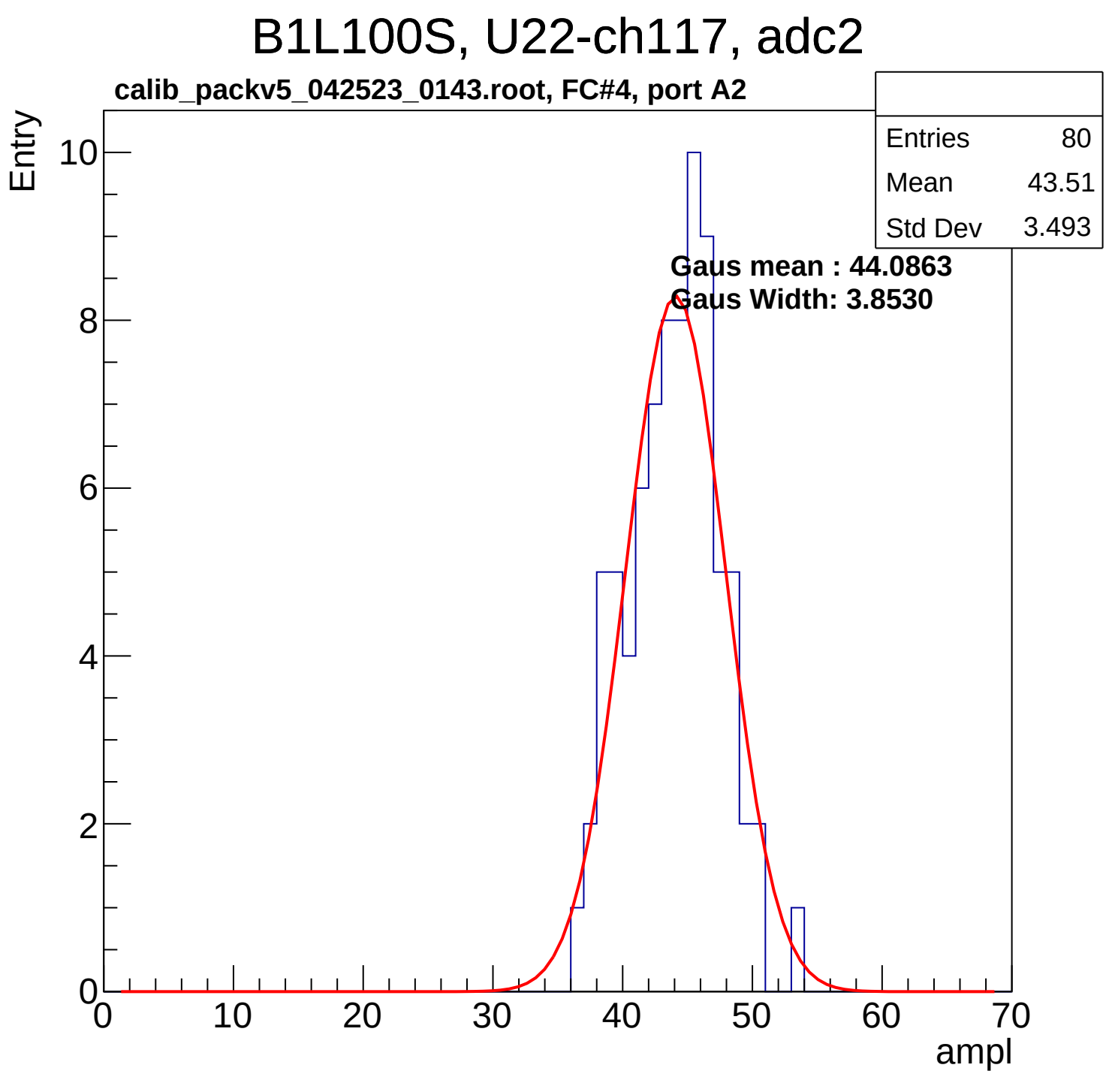

B1L100S, U22-ch116, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.33
Std Dev 1.944
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch116, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 21
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch117, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 28.71
Std Dev 3.701
Gaus mean : 29.3621
Gaus Width: 4.0762
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch117, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 36.4
Std Dev 3.16
Gaus mean : 37.1826
Gaus Width: 3.0899
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch117, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 43.51
Std Dev 3.493
Gaus mean : 44.0863
Gaus Width: 3.8530
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
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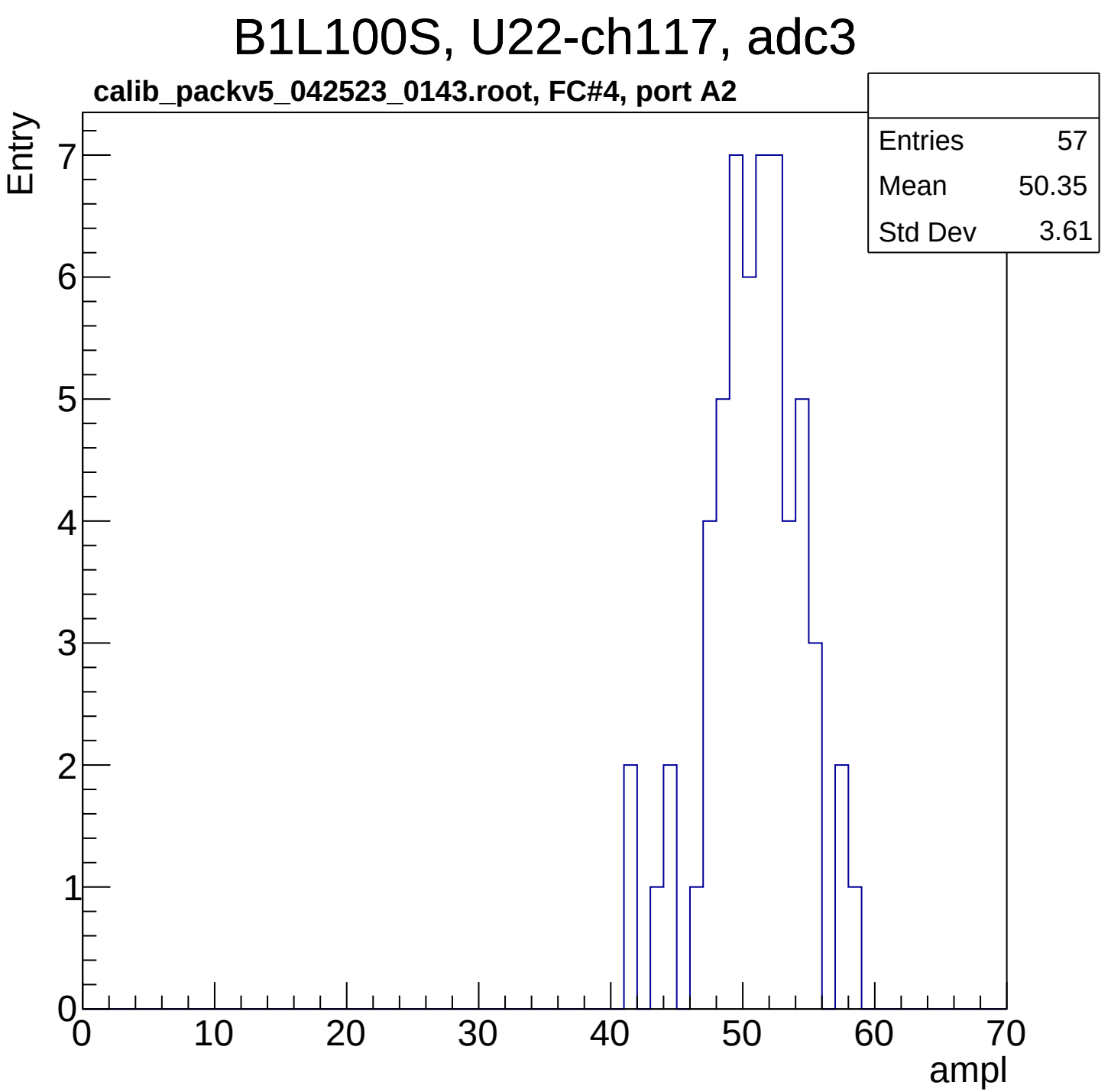
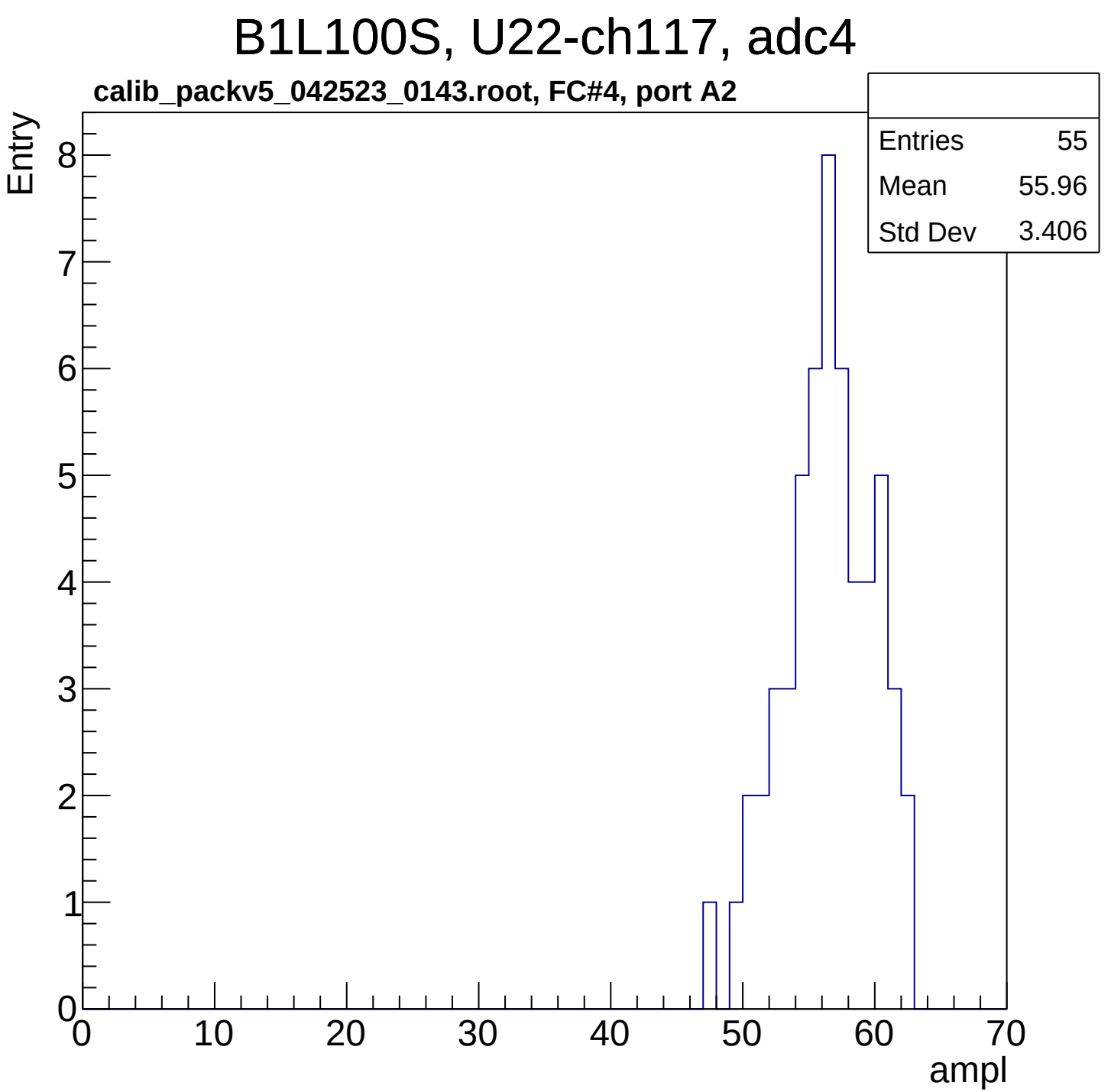
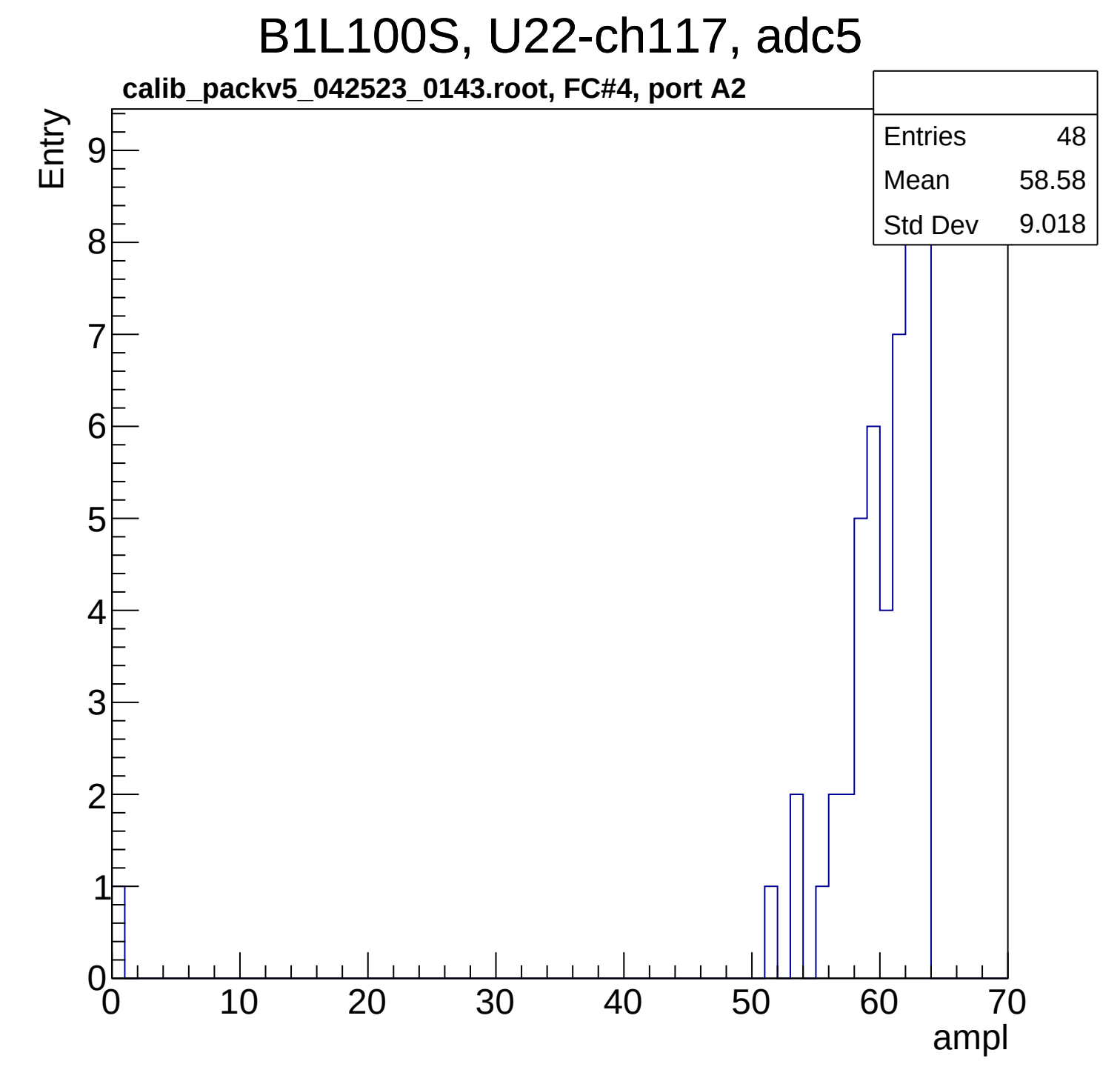
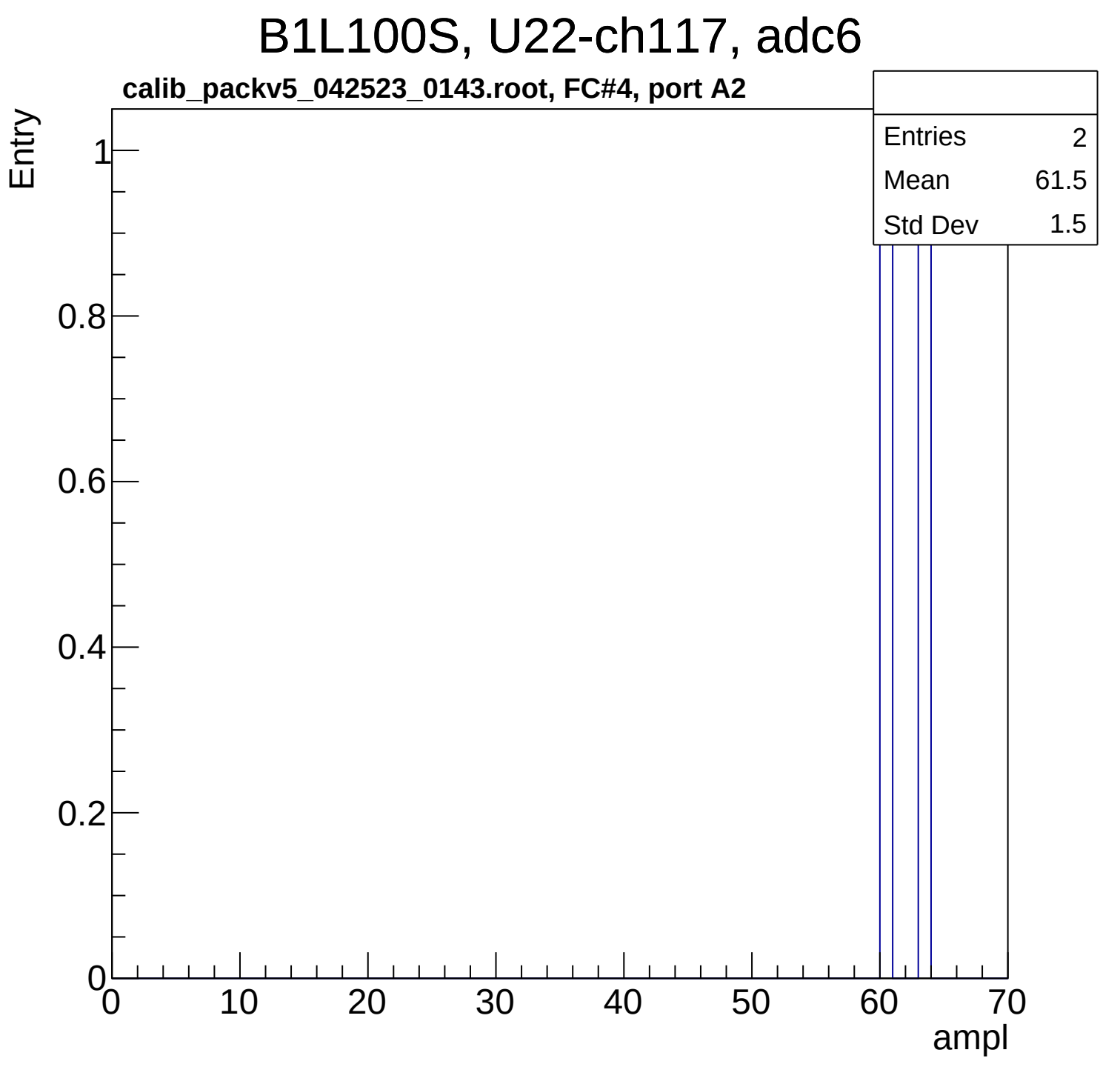
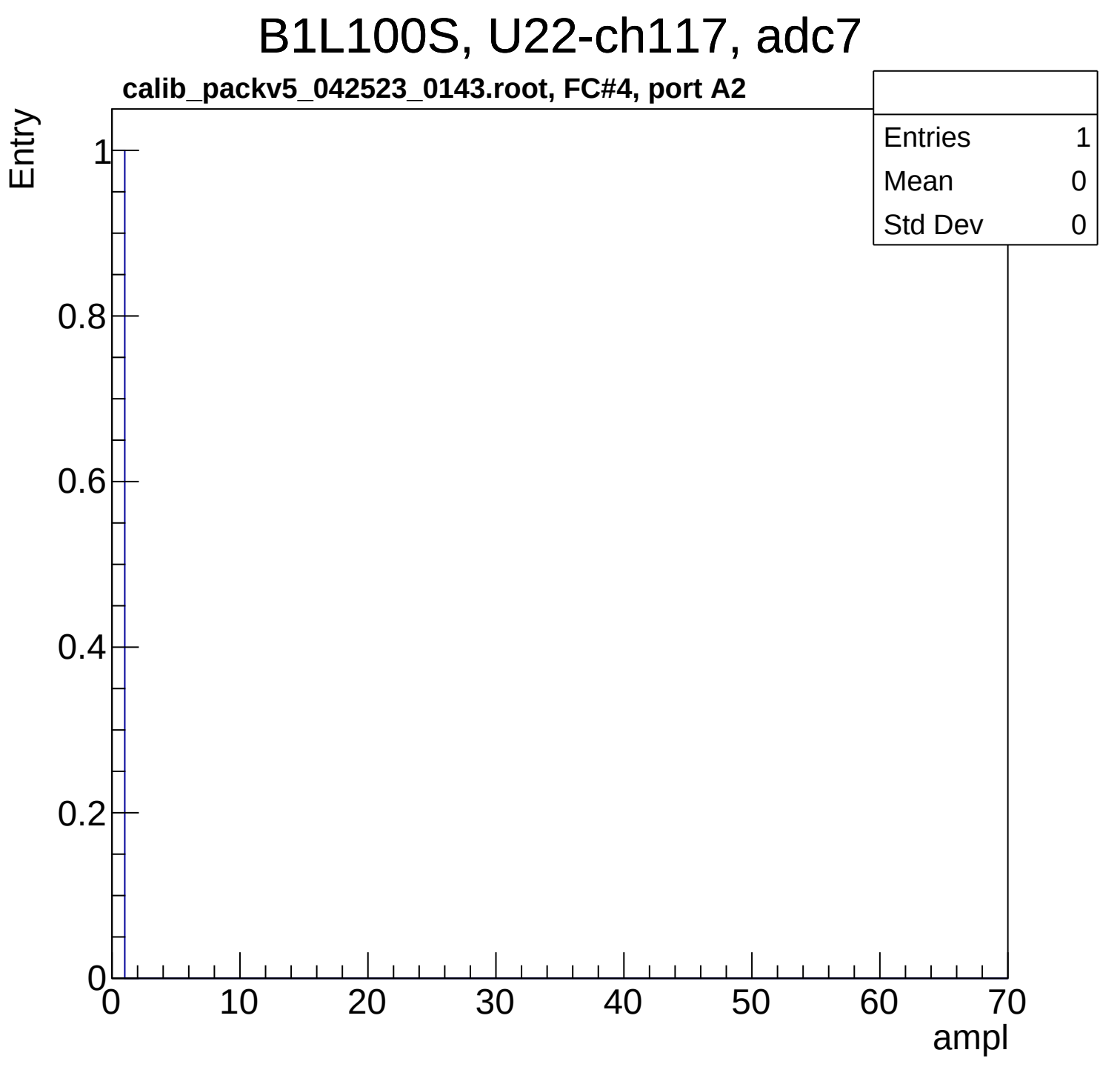
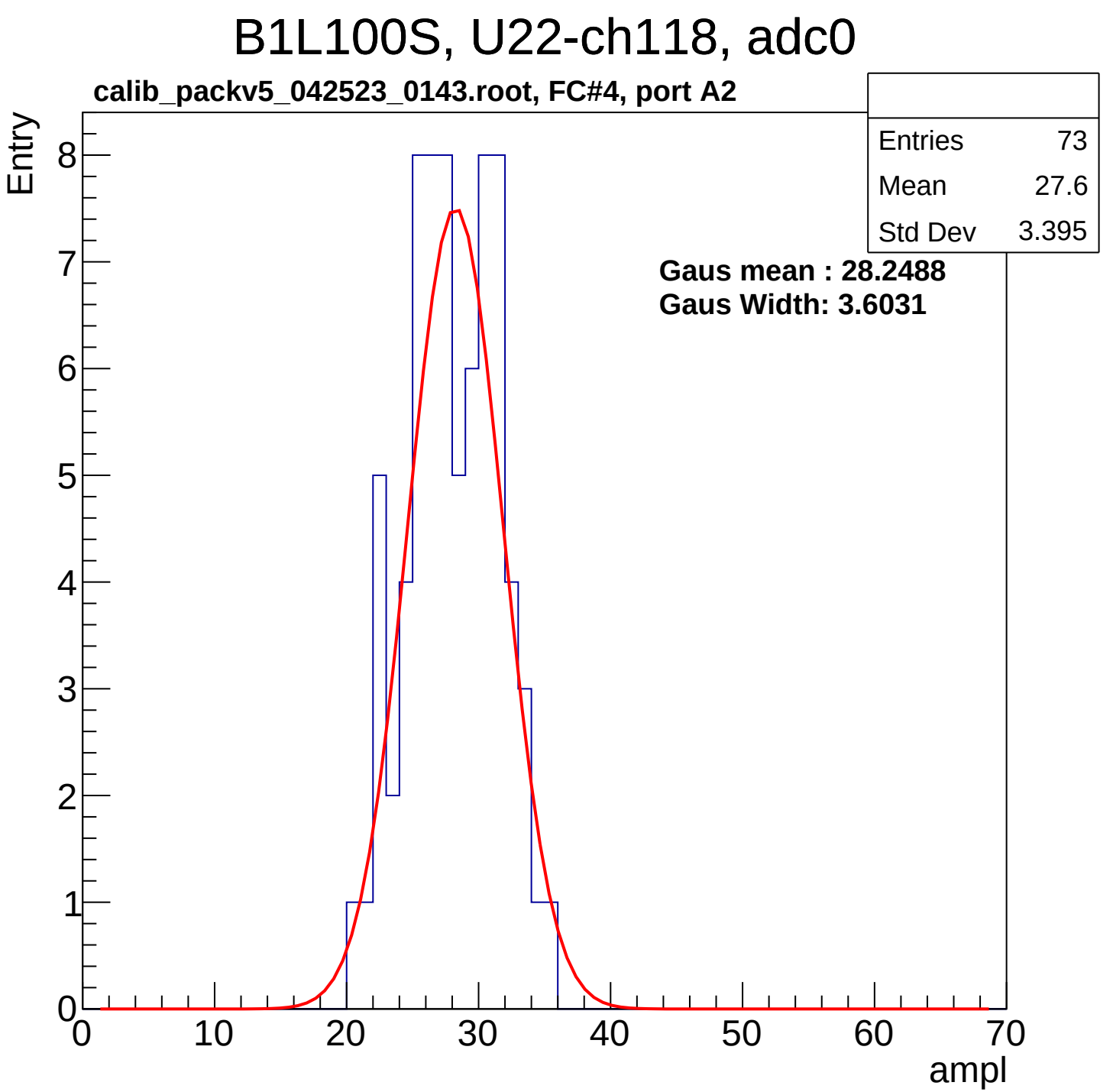

B1L100S, U22-ch117, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 50.35
Std Dev 3.61
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70

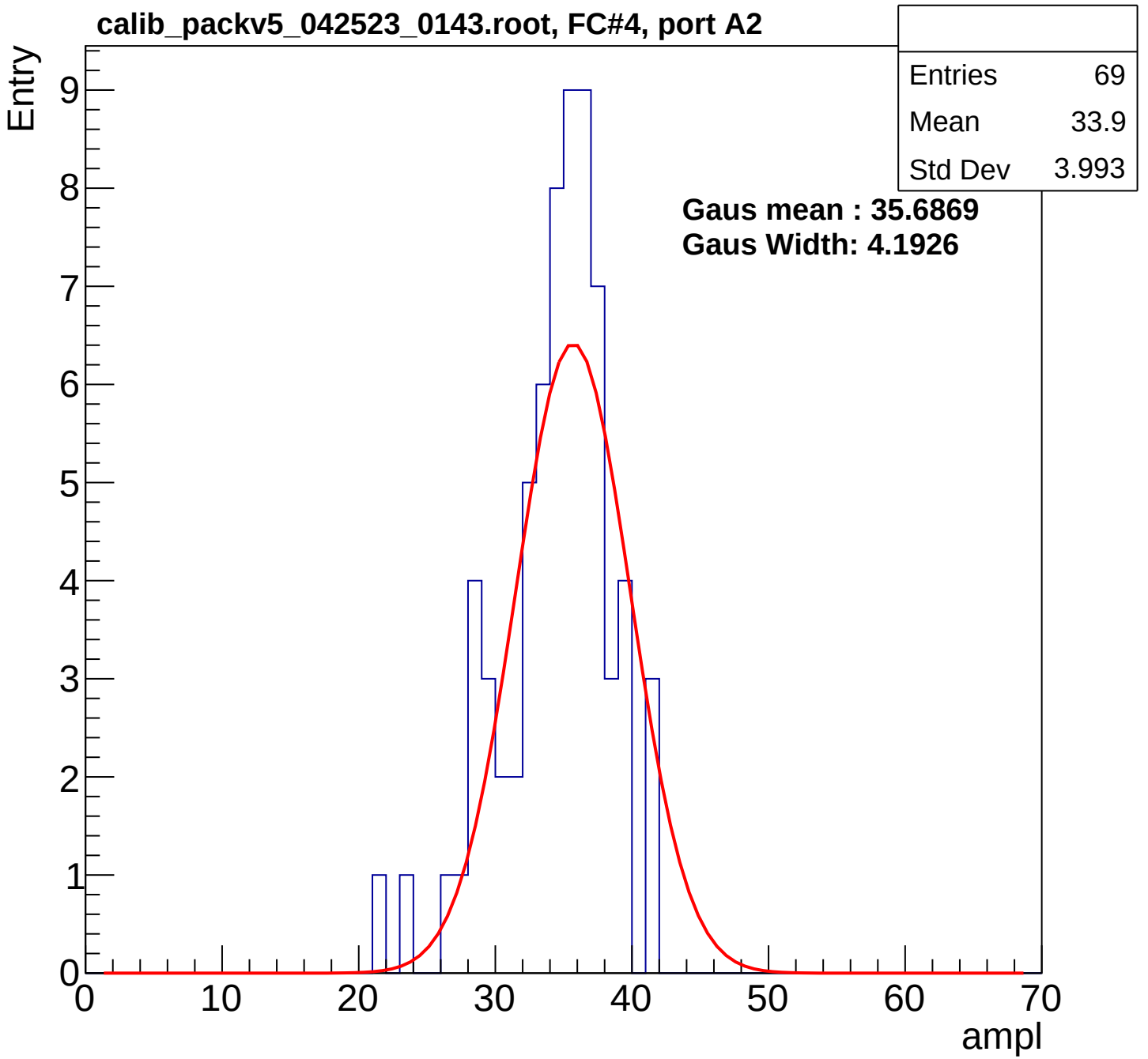

B1L100S, U22-ch117, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 55.96
Std Dev 3.406
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

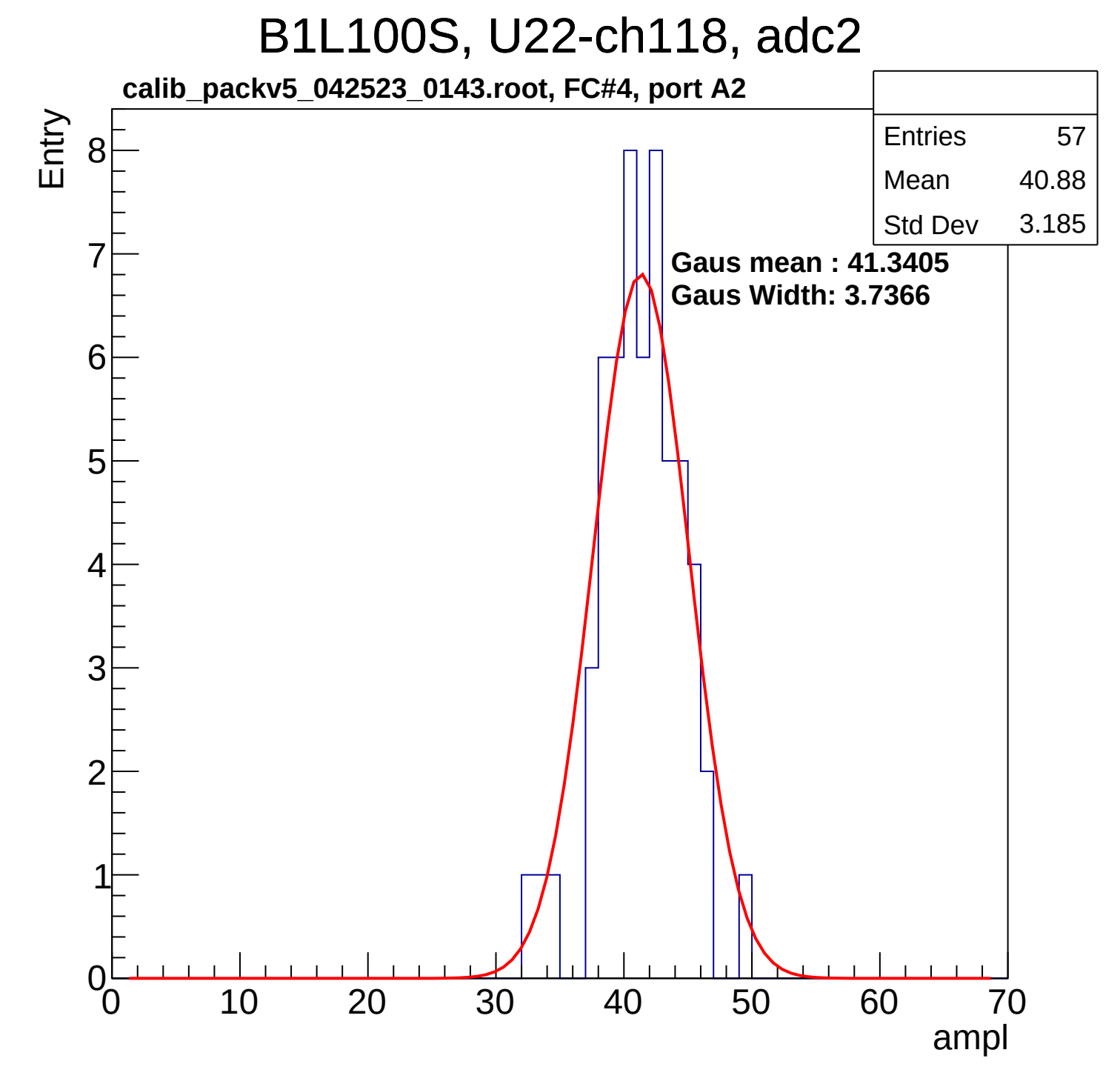

B1L100S, U22-ch117, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.58
Std Dev 9.018
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch117, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 61.5
Std Dev 1.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch117, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch118, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 27.6
Std Dev 3.395
Gaus mean : 28.2488
Gaus Width: 3.6031
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch118, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 33.9
Std Dev 3.993
Gaus mean : 35.6869
Gaus Width: 4.1926
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch118, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 40.88
Std Dev 3.185
Gaus mean : 41.3405
Gaus Width: 3.7366
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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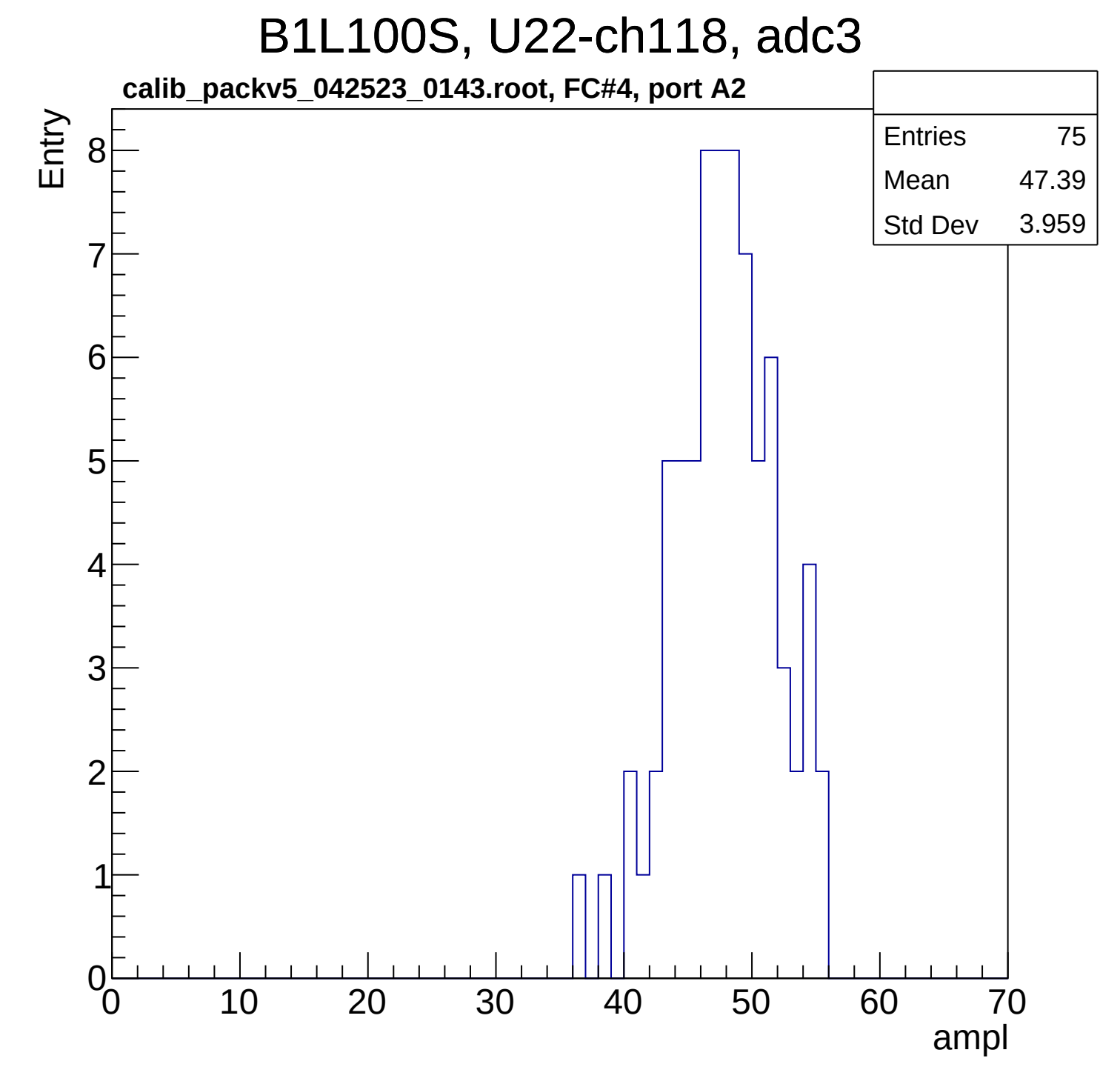
B1L100S, U22-ch118, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 47.39
Std Dev 3.959
Entry
ampl
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2
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4
5
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7
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0
10
20
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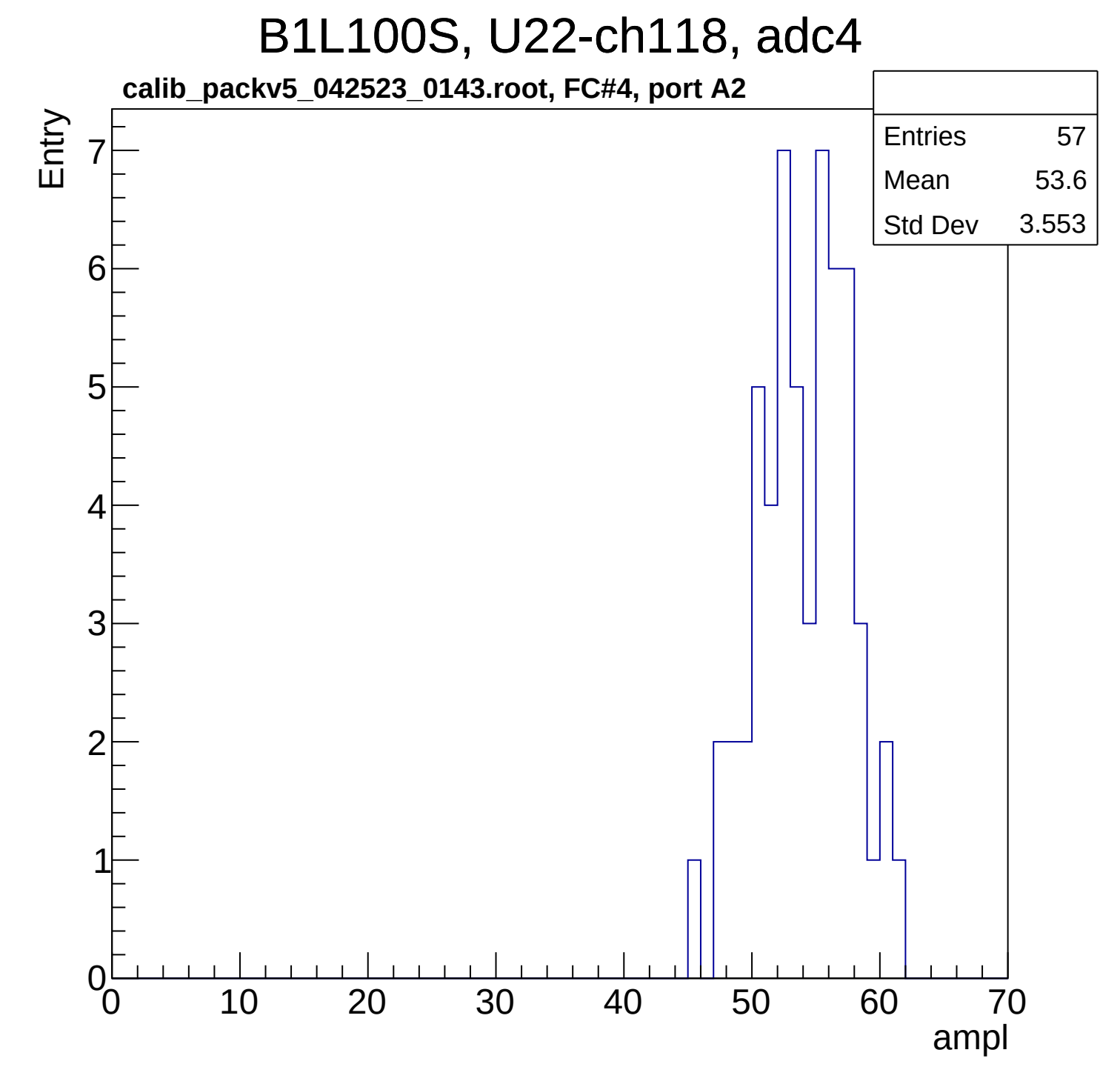
40
50
60
70


B1L100S, U22-ch118, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 57
Mean 53.6
Std Dev 3.553
Entry
ampl
0
1
2
3
4
5
6
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0
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20
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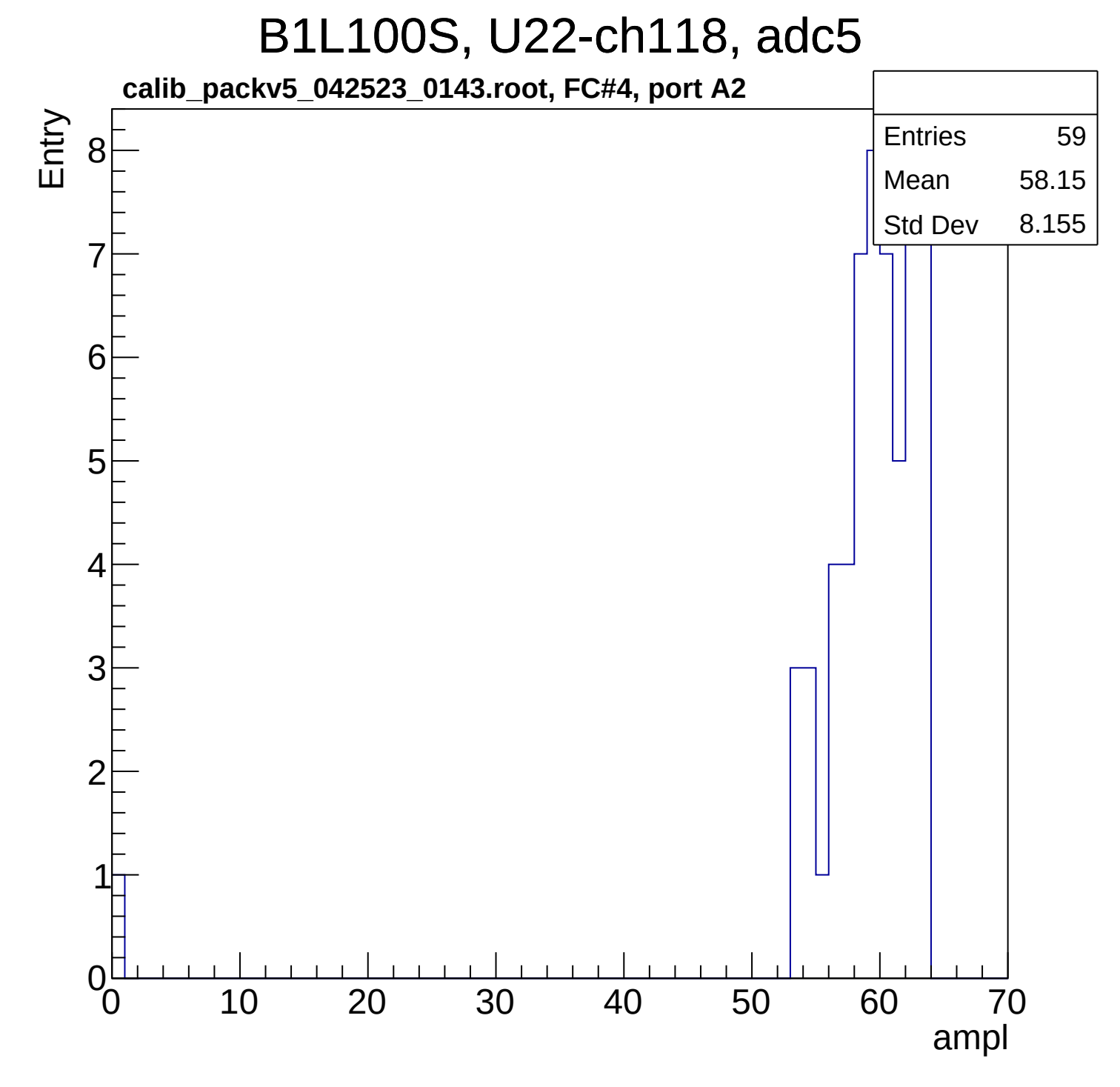
40
50
60
70


B1L100S, U22-ch118, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 59
Mean 58.15
Std Dev 8.155
Entry
ampl
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1
2
3
4
5
6
7
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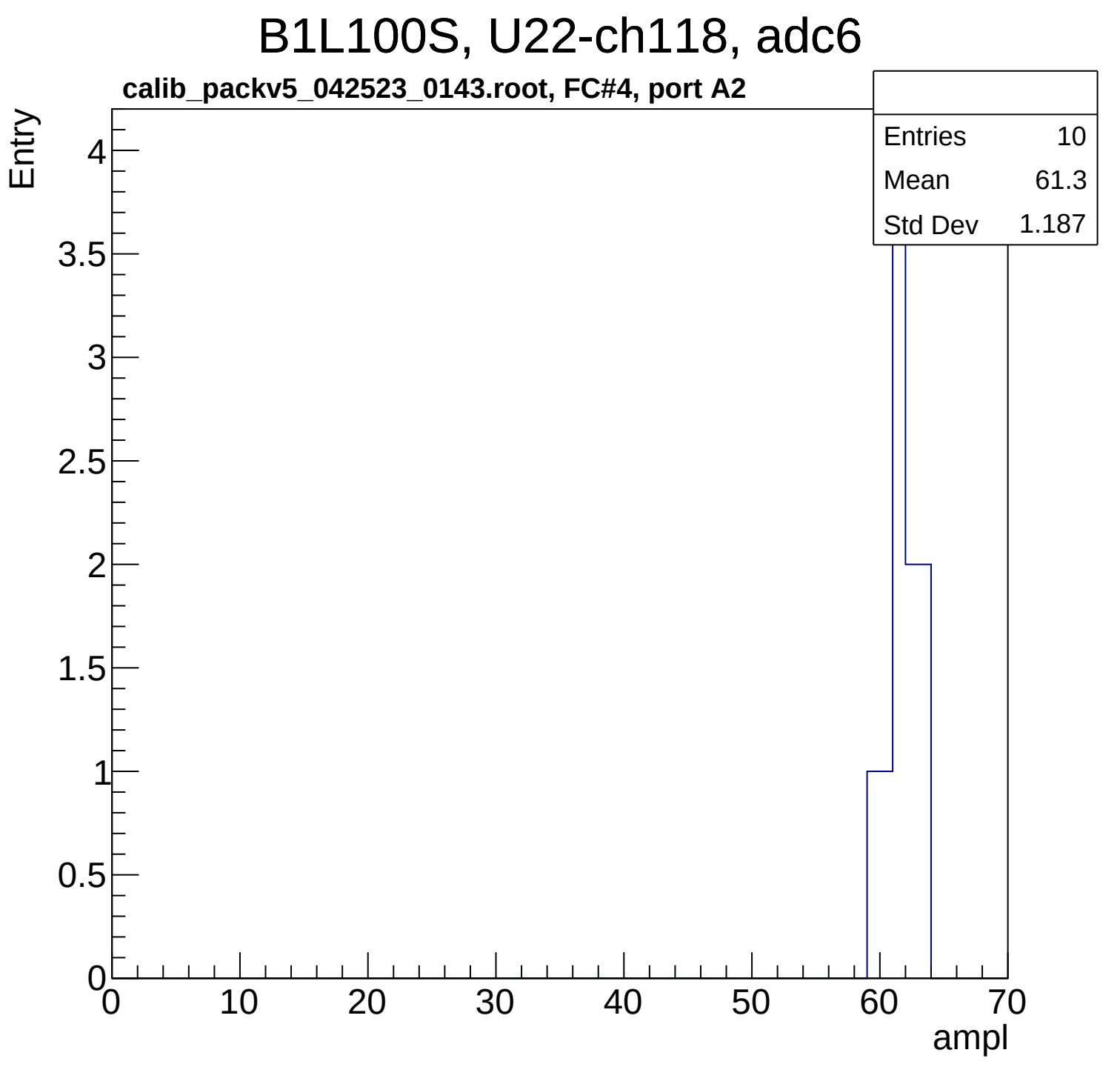
0
10
20
30
40
50
60
70


B1L100S, U22-ch118, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 10
Mean 61.3
Std Dev 1.187
Entry
ampl
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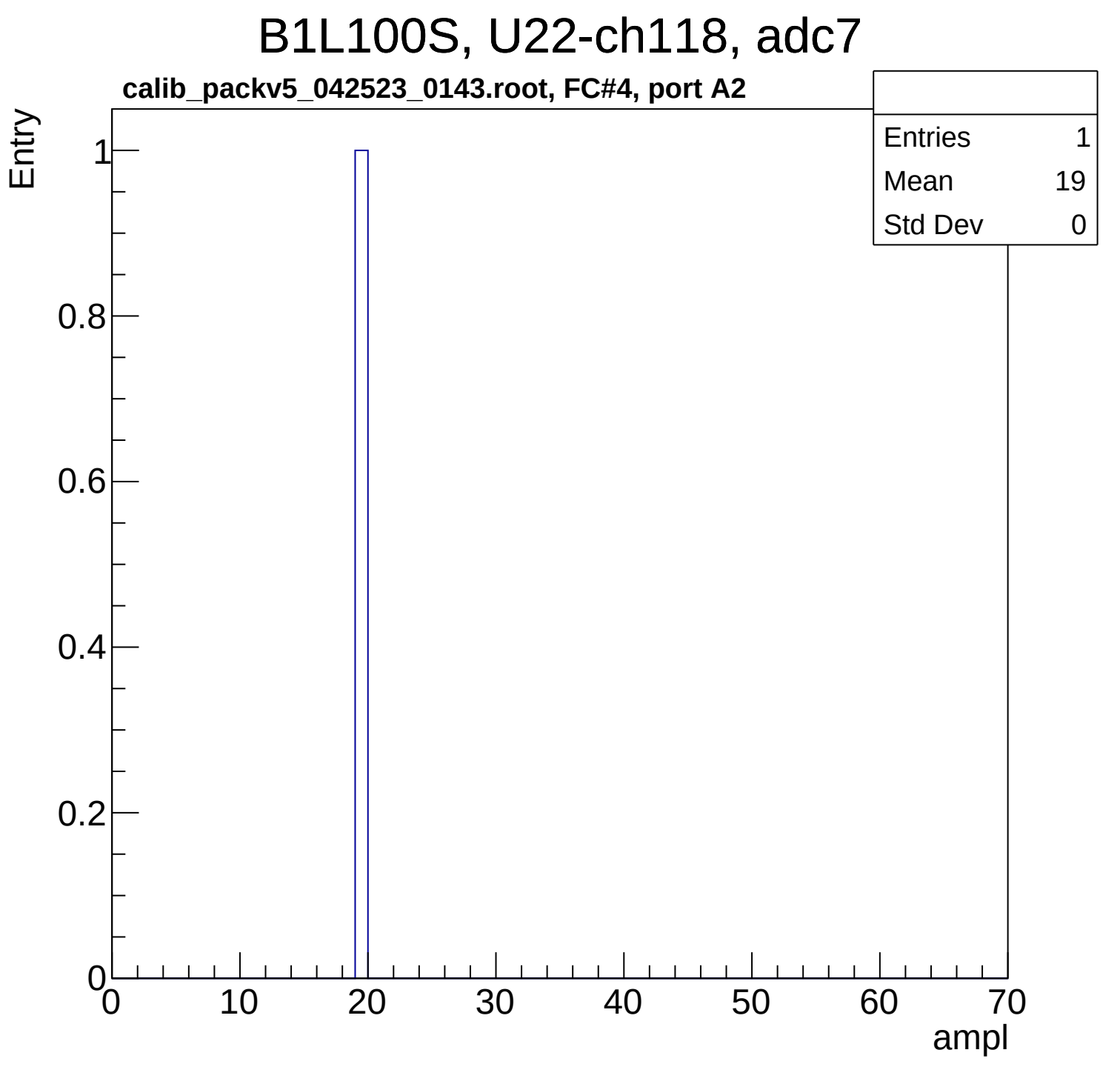
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch118, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 19
Std Dev 0
Entry
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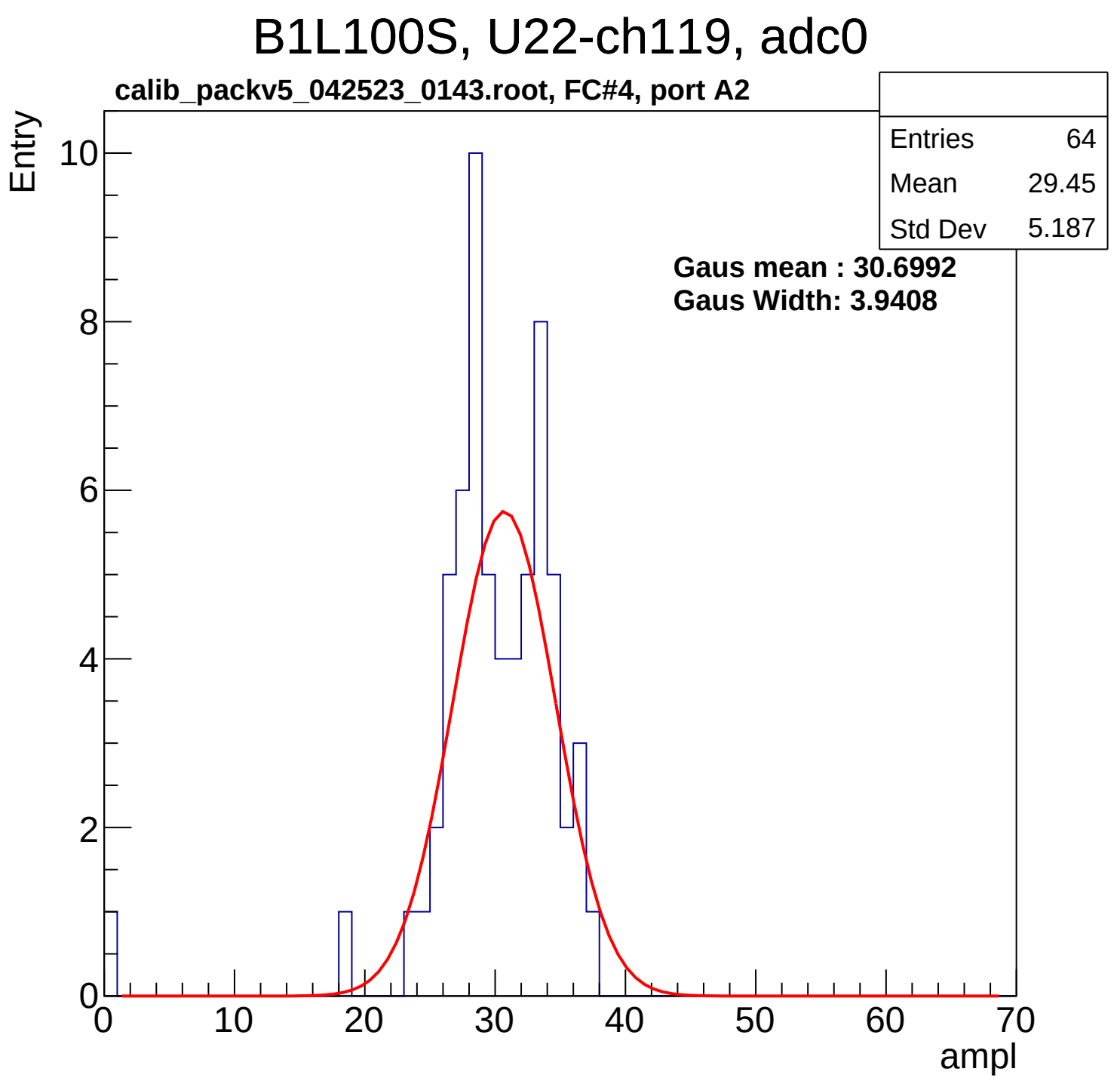
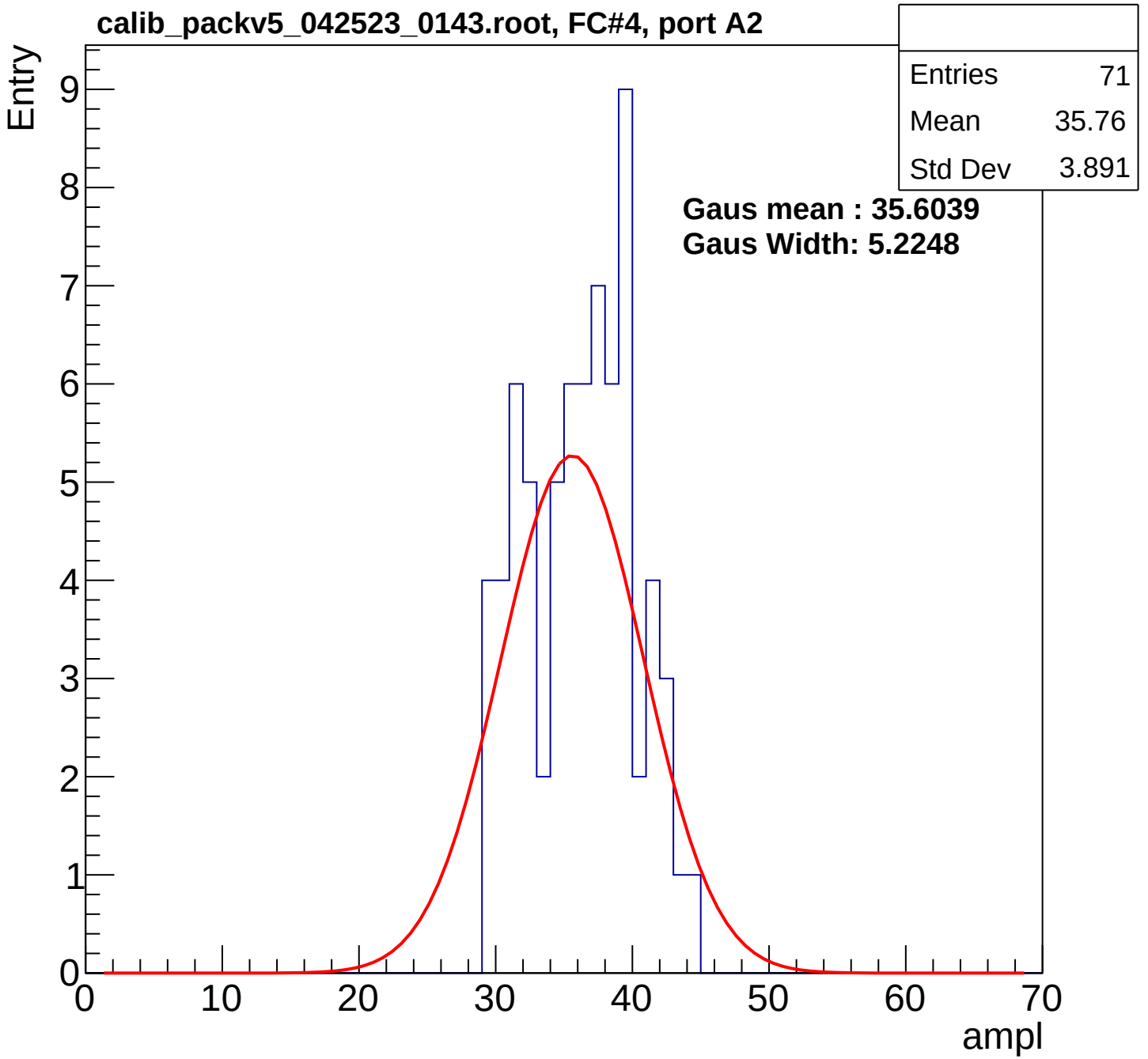
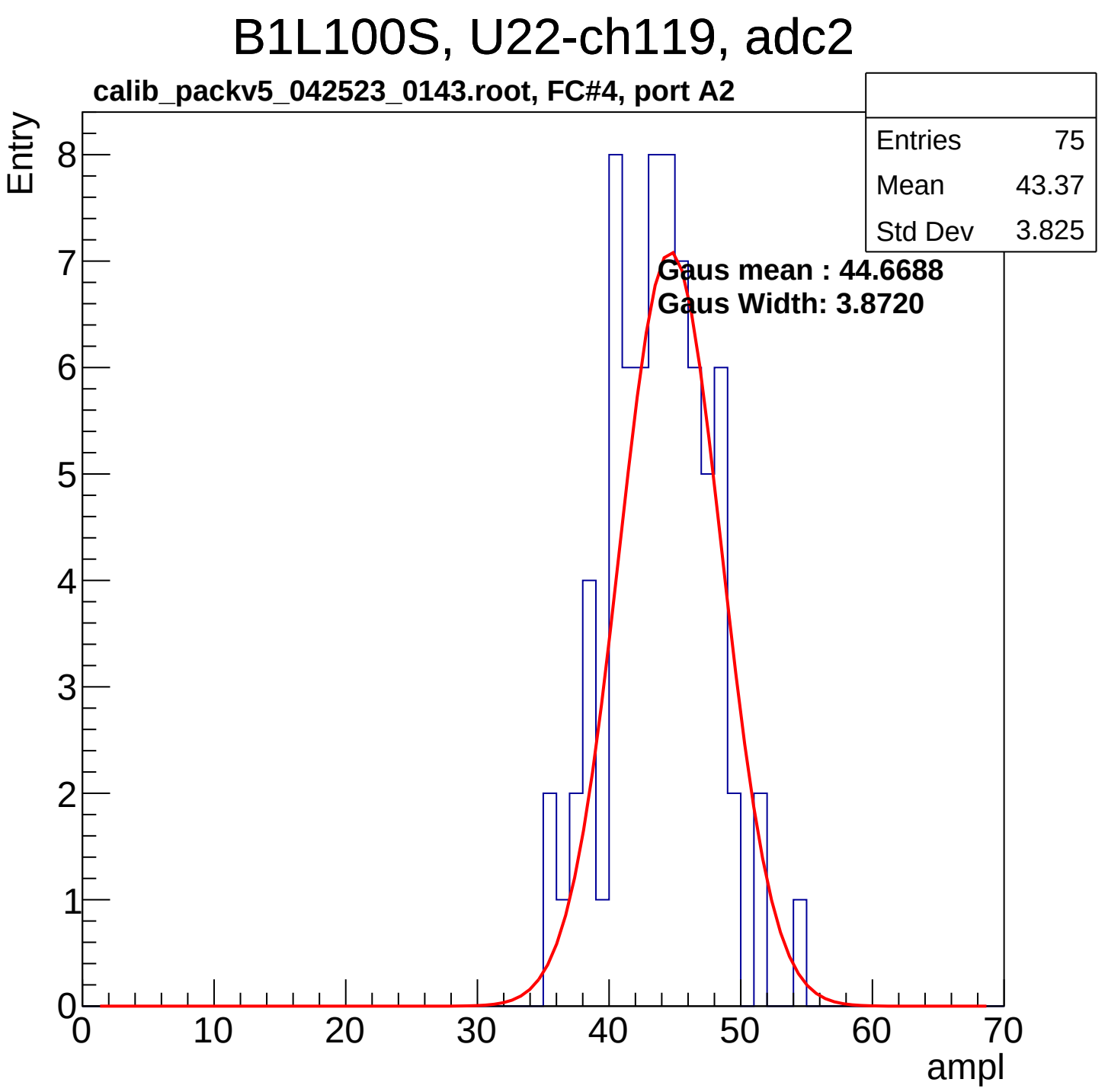
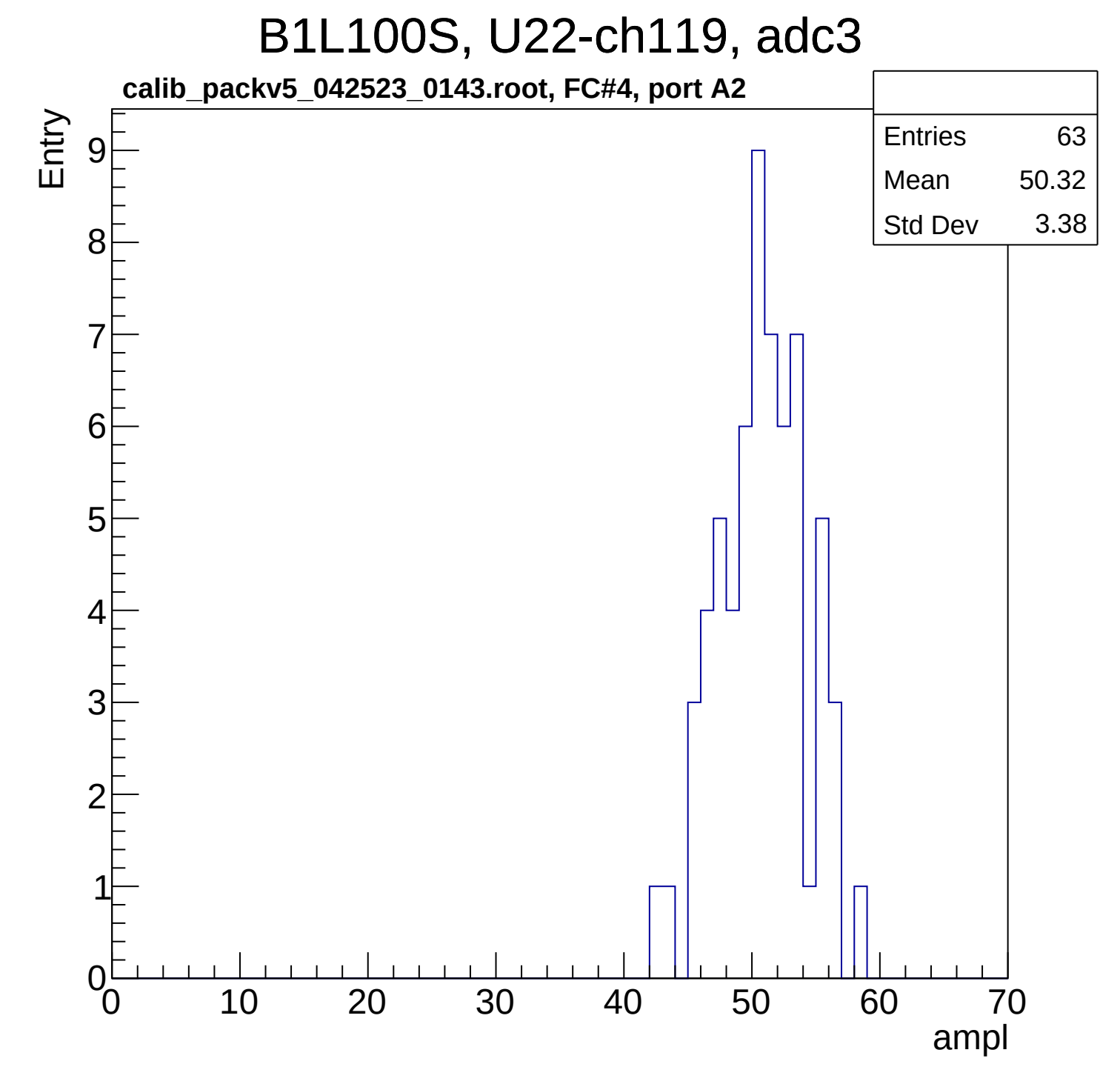
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch119, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 64
Mean 29.45
Std Dev 5.187
Gaus mean : 30.6992
Gaus Width: 3.9408
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch119, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 35.76
Std Dev 3.891
Gaus mean : 35.6039
Gaus Width: 5.2248
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch119, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 43.37
Std Dev 3.825
Gaus mean : 44.6688
Gaus Width: 3.8720
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch119, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 50.32
Std Dev 3.38
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
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40
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60
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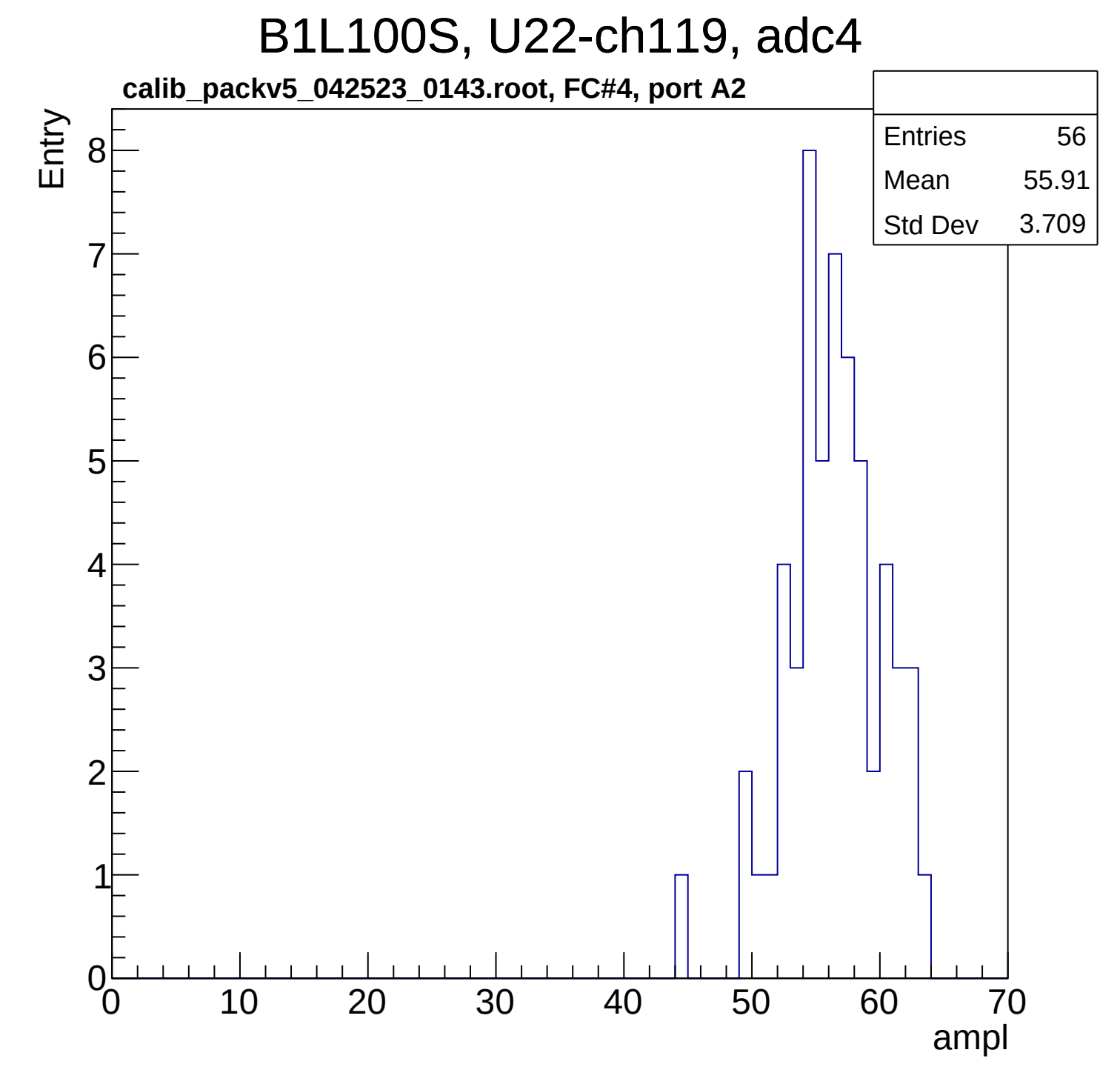
B1L100S, U22-ch119, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 56
Mean 55.91
Std Dev 3.709
Entry
ampl
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1
2
3
4
5
6
7
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0
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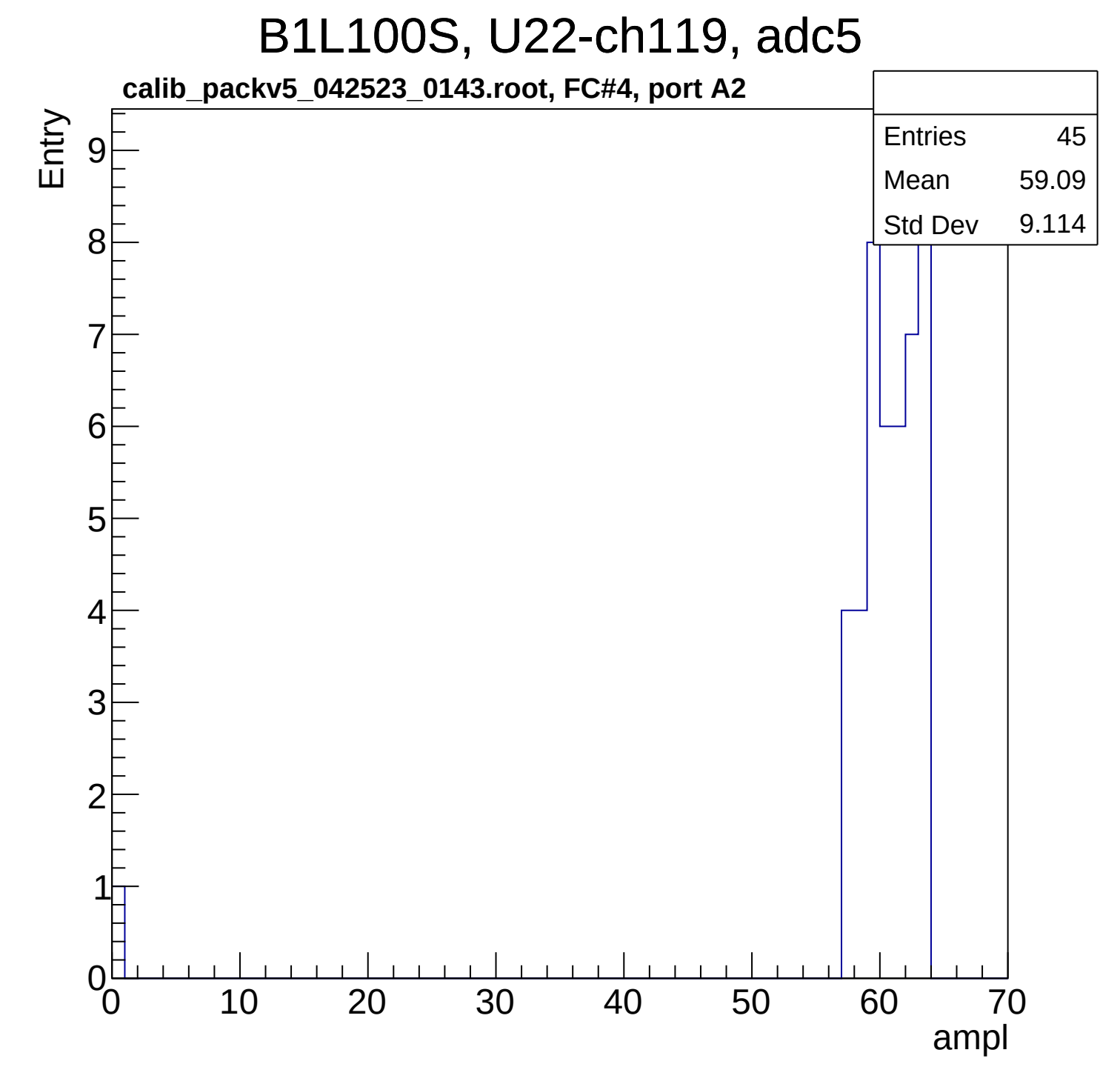
40
50
60
70


B1L100S, U22-ch119, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 59.09
Std Dev 9.114
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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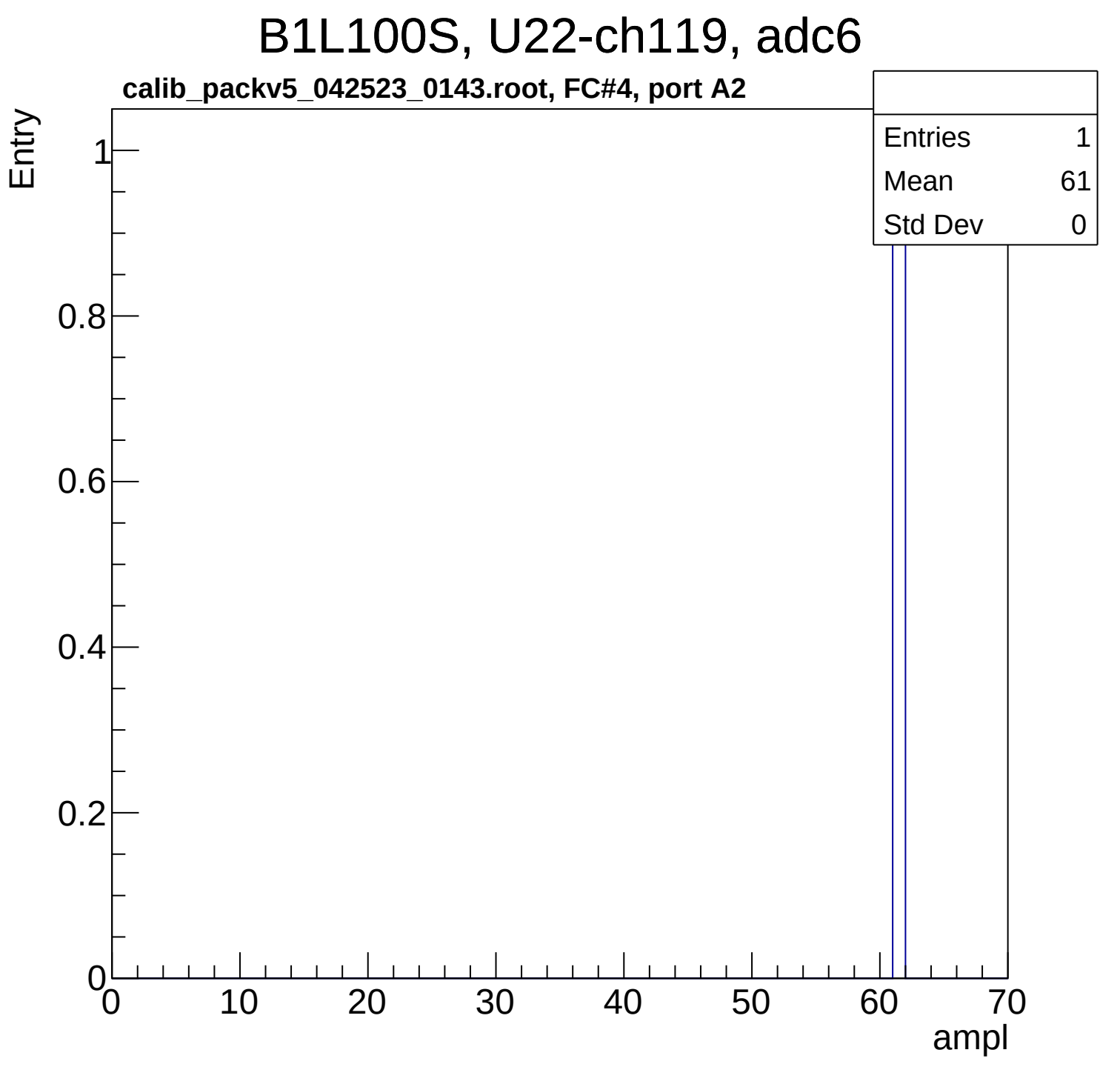
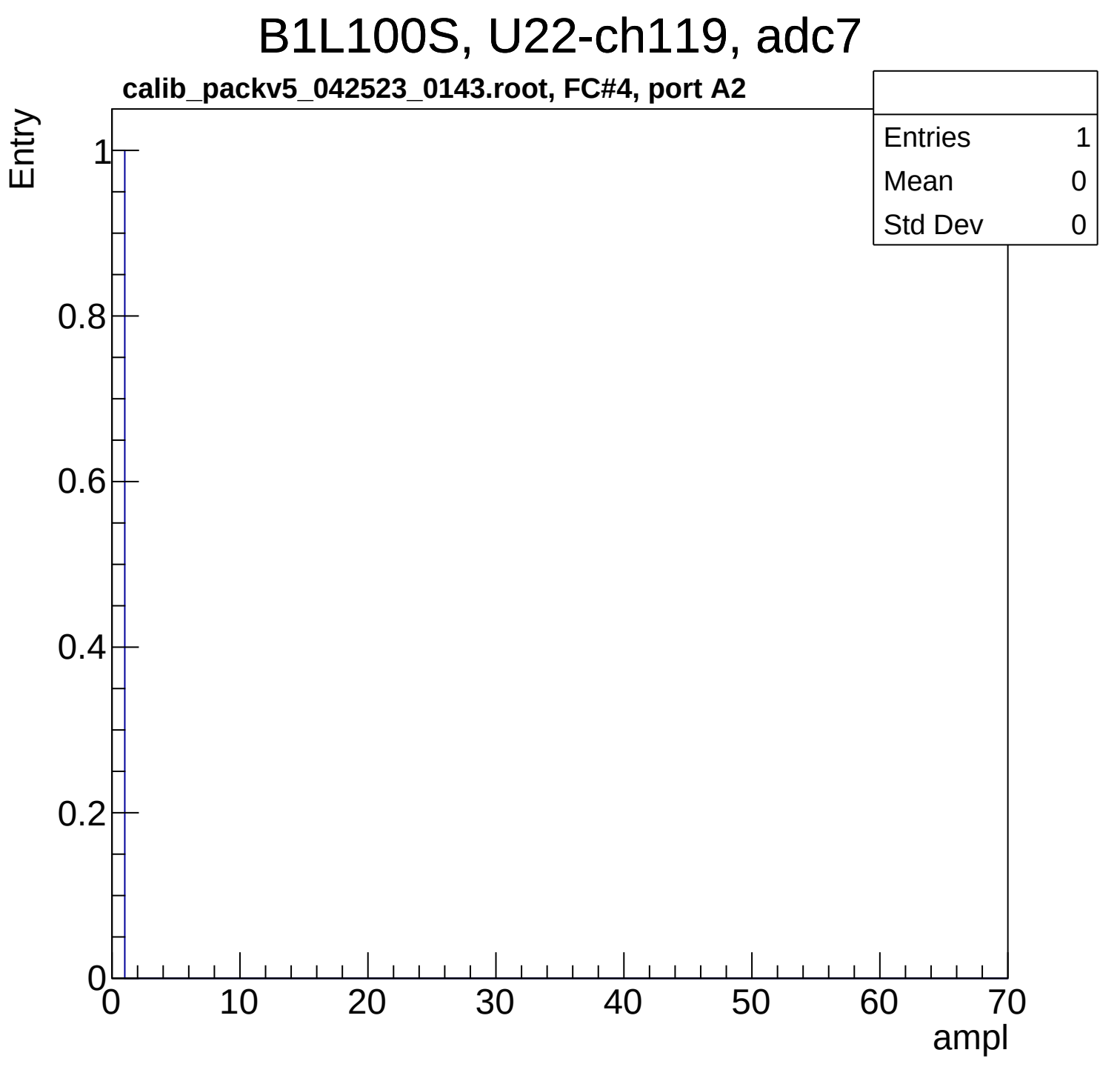

B1L100S, U22-ch119, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 61
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch119, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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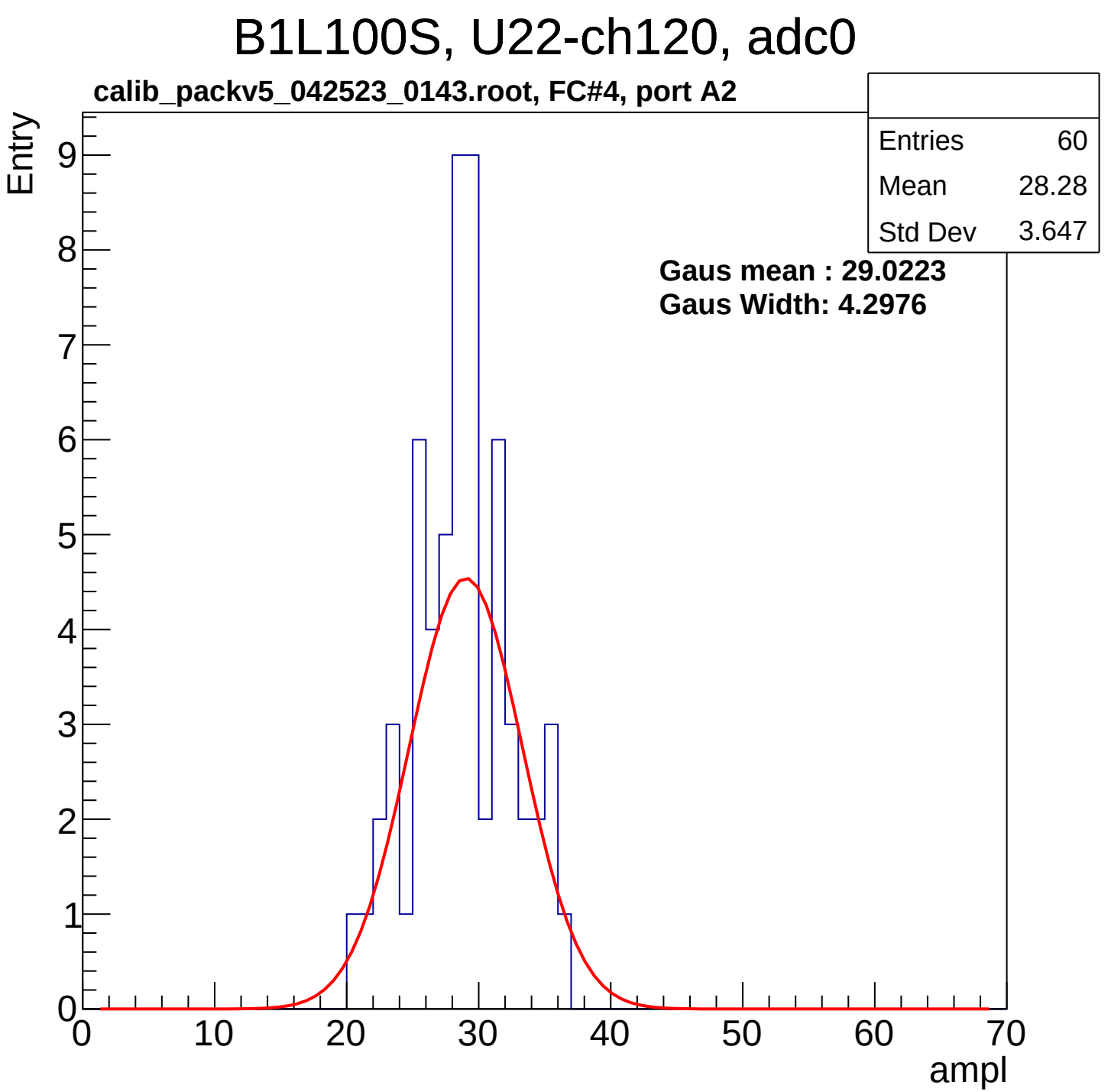
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch120, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 28.28
Std Dev 3.647
Gaus mean : 29.0223
Gaus Width: 4.2976
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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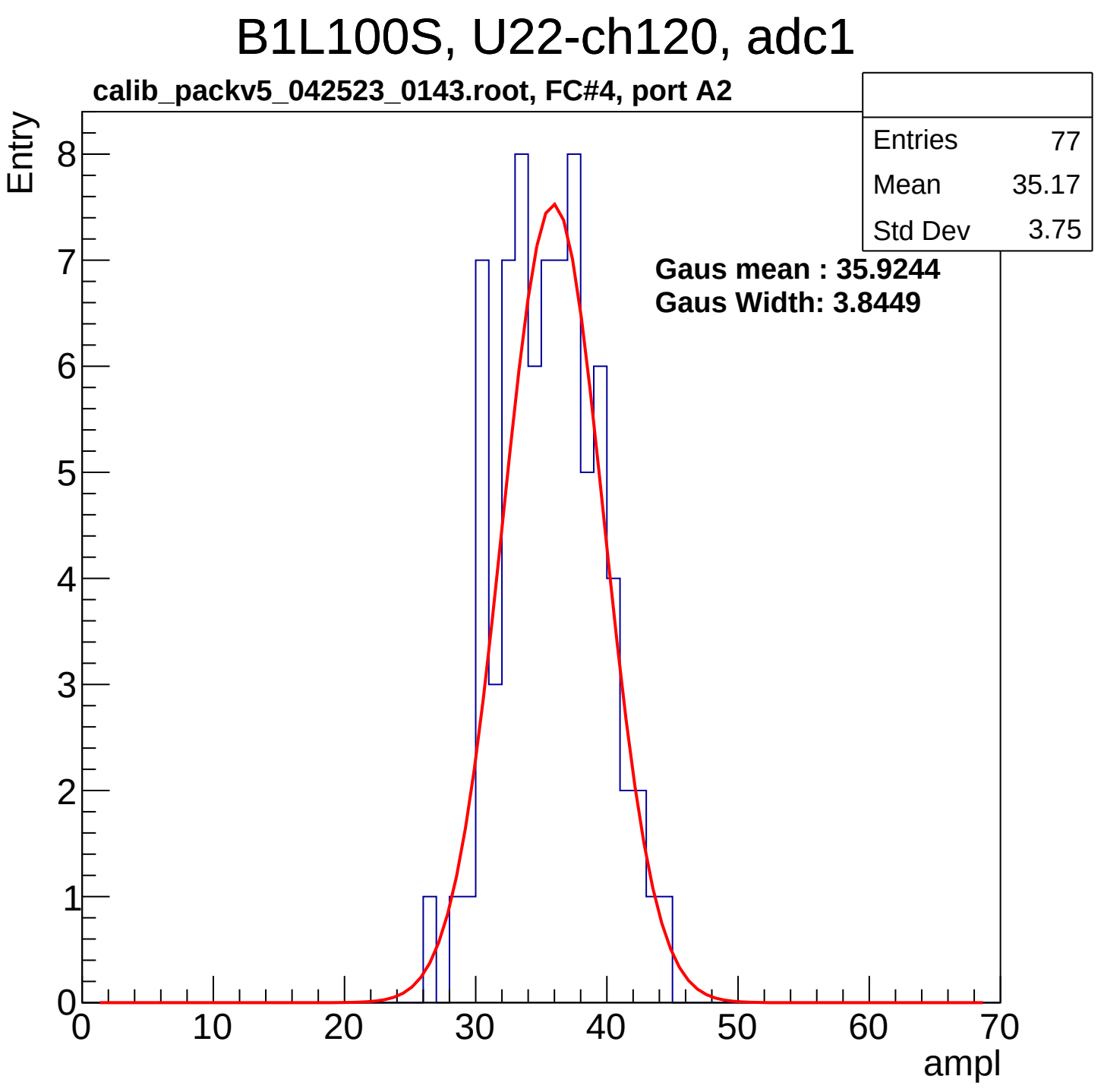
B1L100S, U22-ch120, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 77
Mean 35.17
Std Dev 3.75
Gaus mean : 35.9244
Gaus Width: 3.8449
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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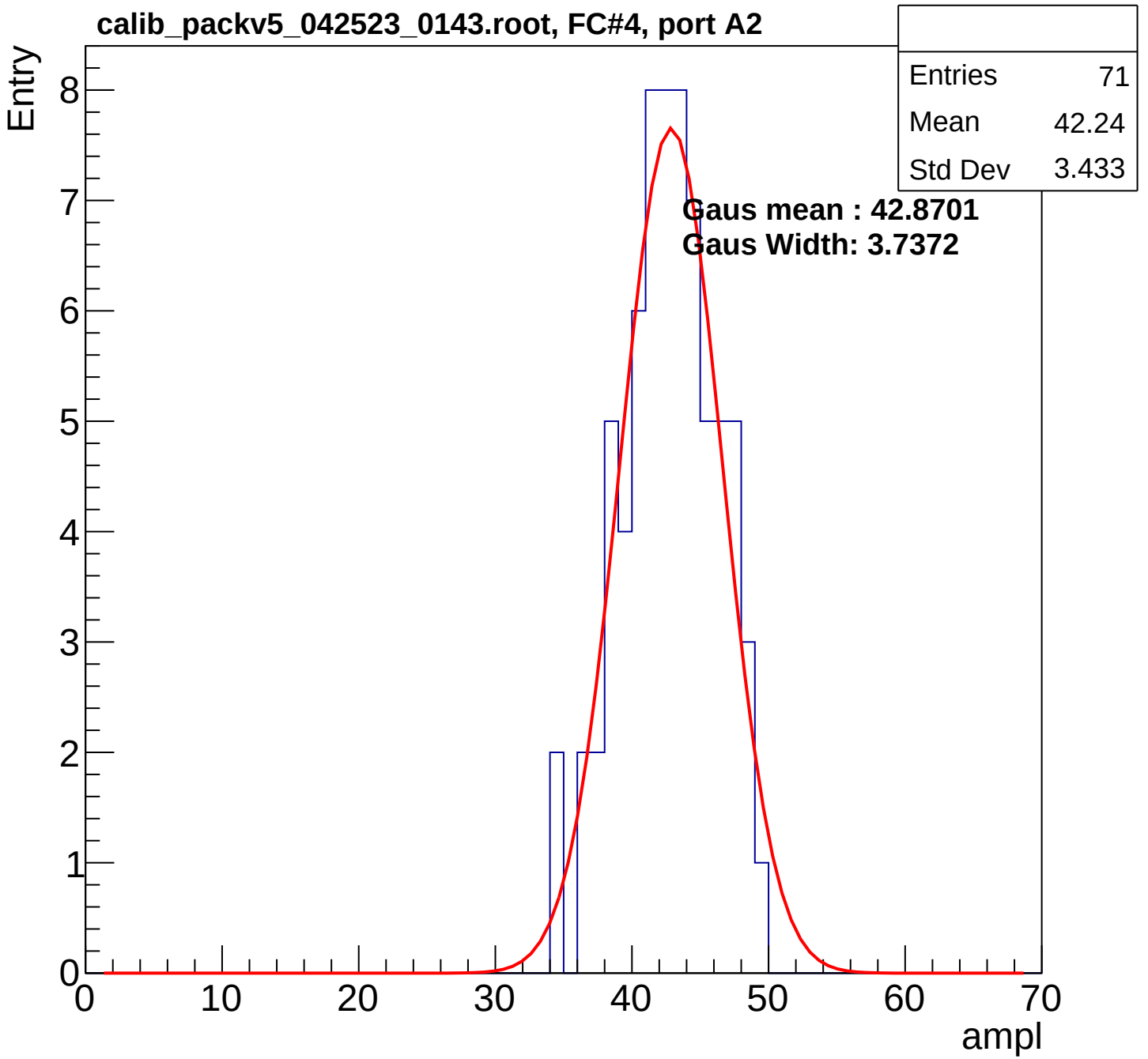
B1L100S, U22-ch120, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 71
Mean 42.24
Std Dev 3.433
Gaus mean : 42.8701
Gaus Width: 3.7372
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
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60
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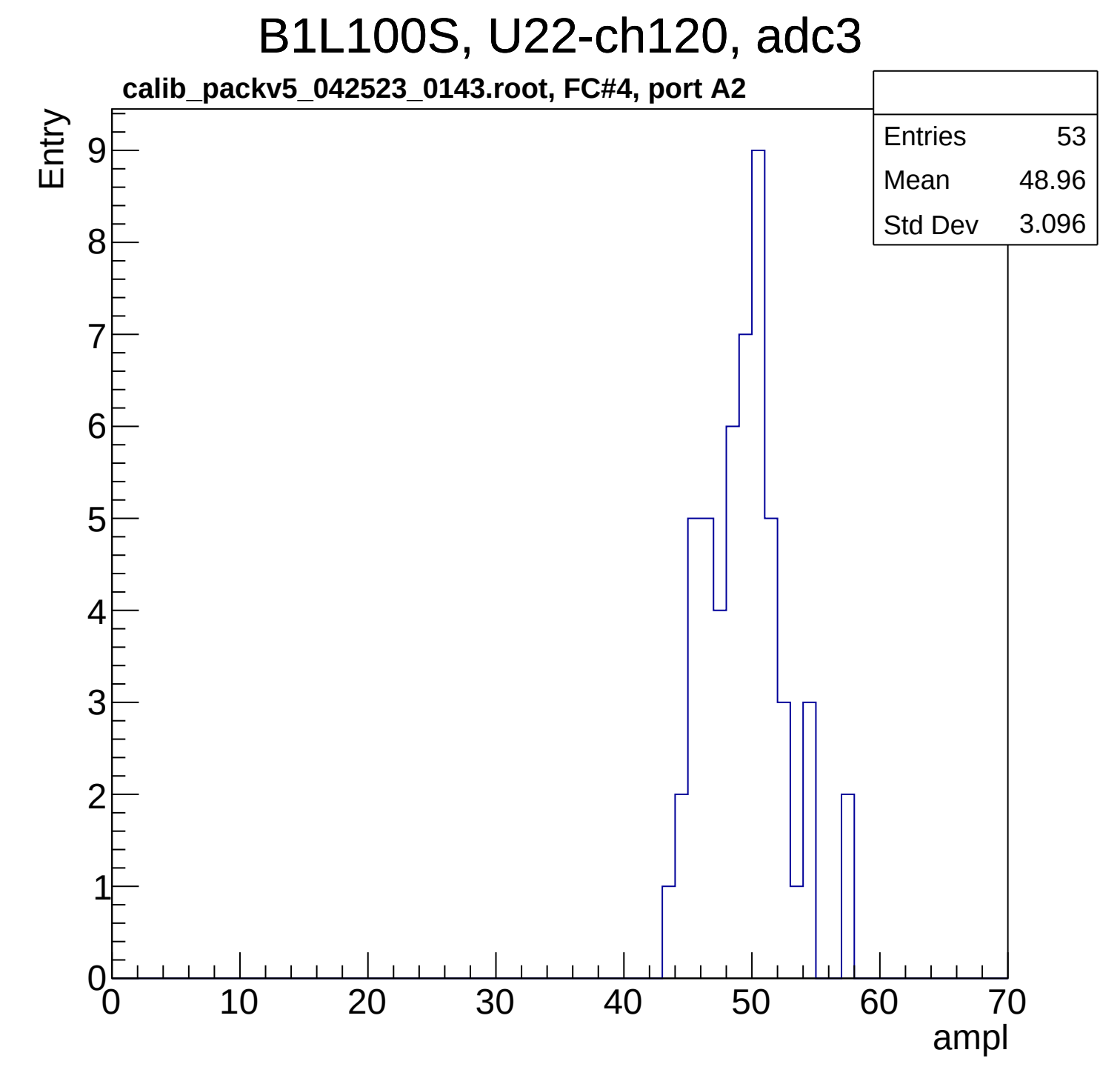
B1L100S, U22-ch120, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 53
Mean 48.96
Std Dev 3.096
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
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60
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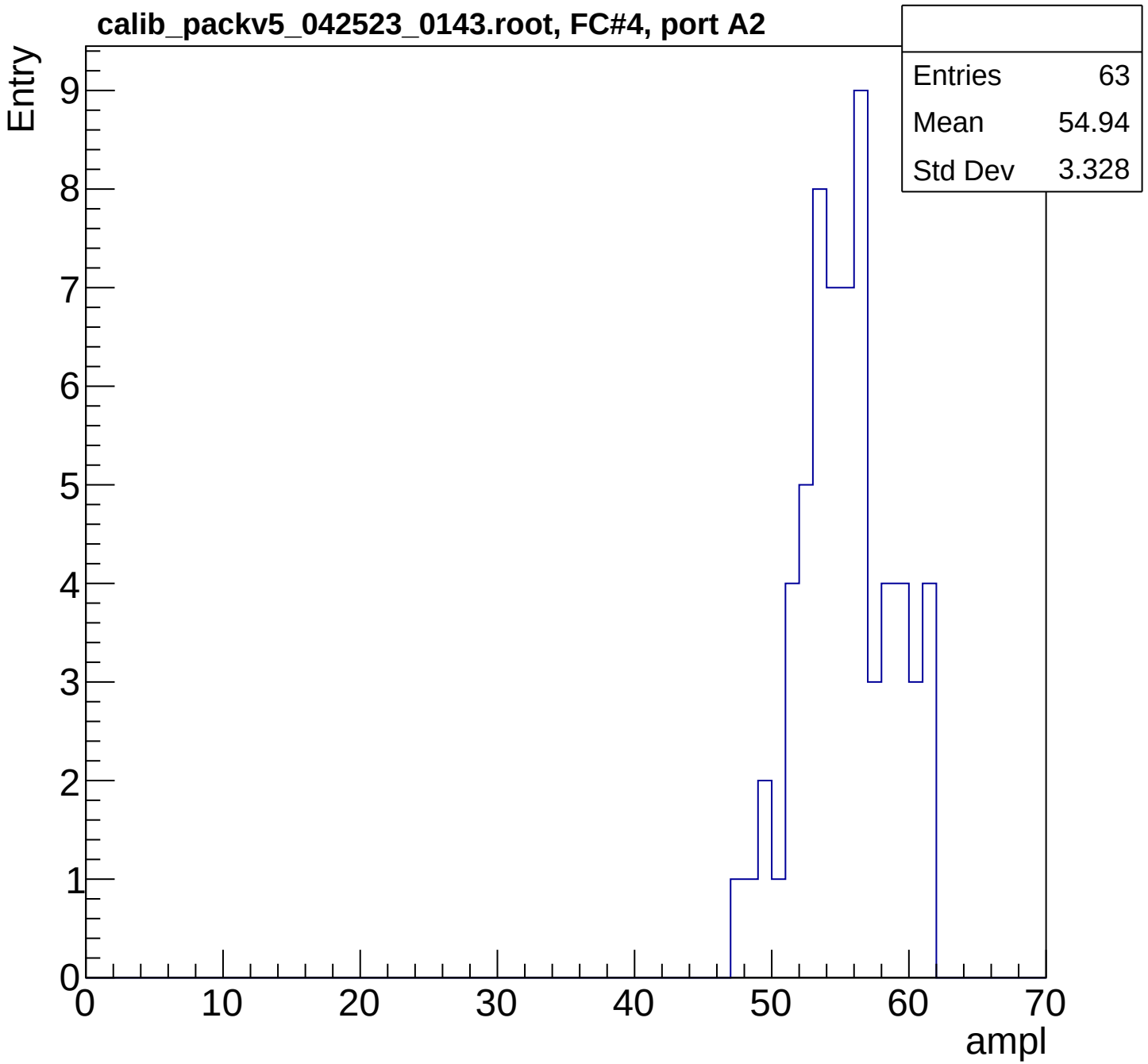
B1L100S, U22-ch120, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 54.94
Std Dev 3.328
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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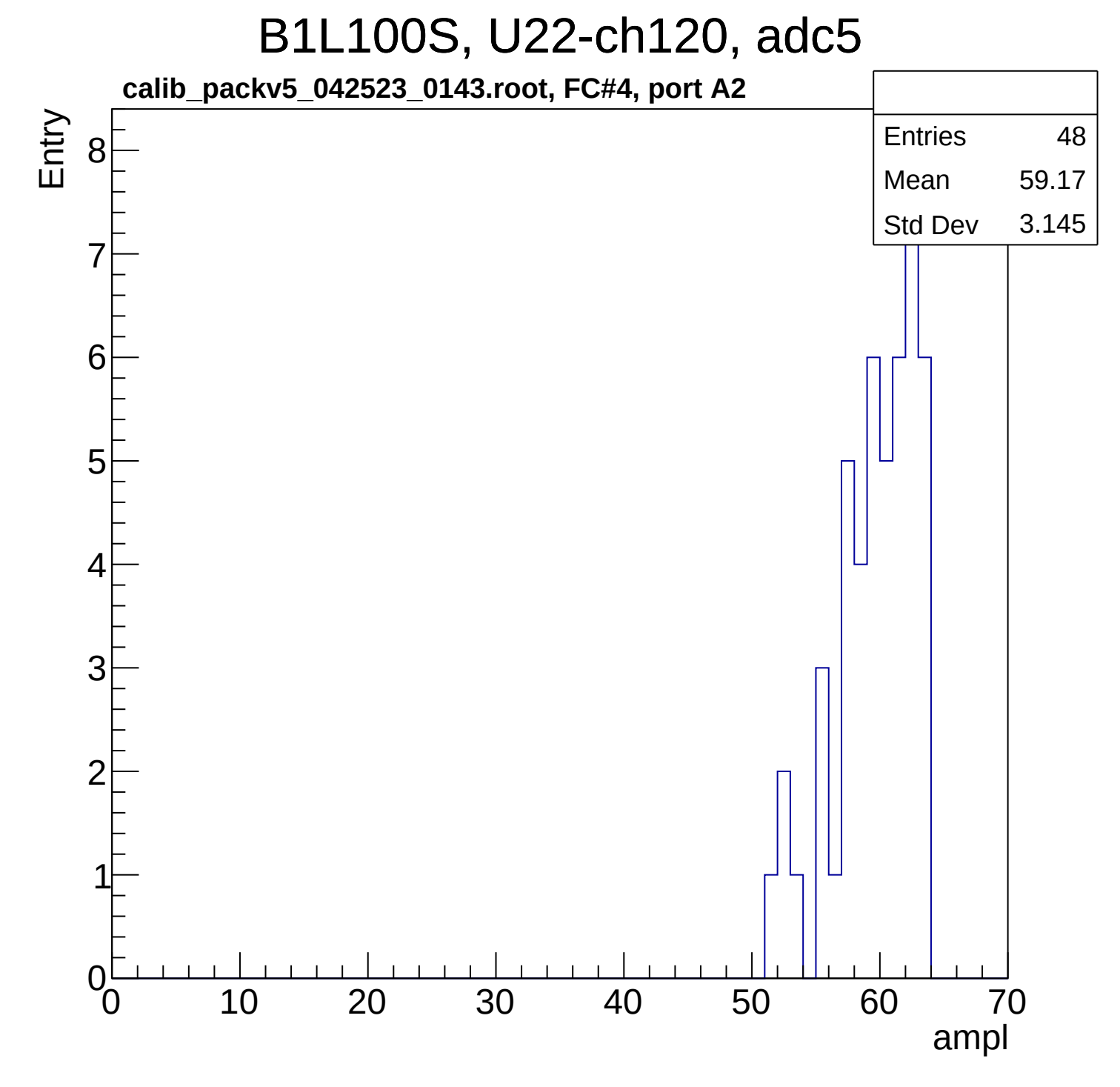
B1L100S, U22-ch120, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 59.17
Std Dev 3.145
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
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40
50
60
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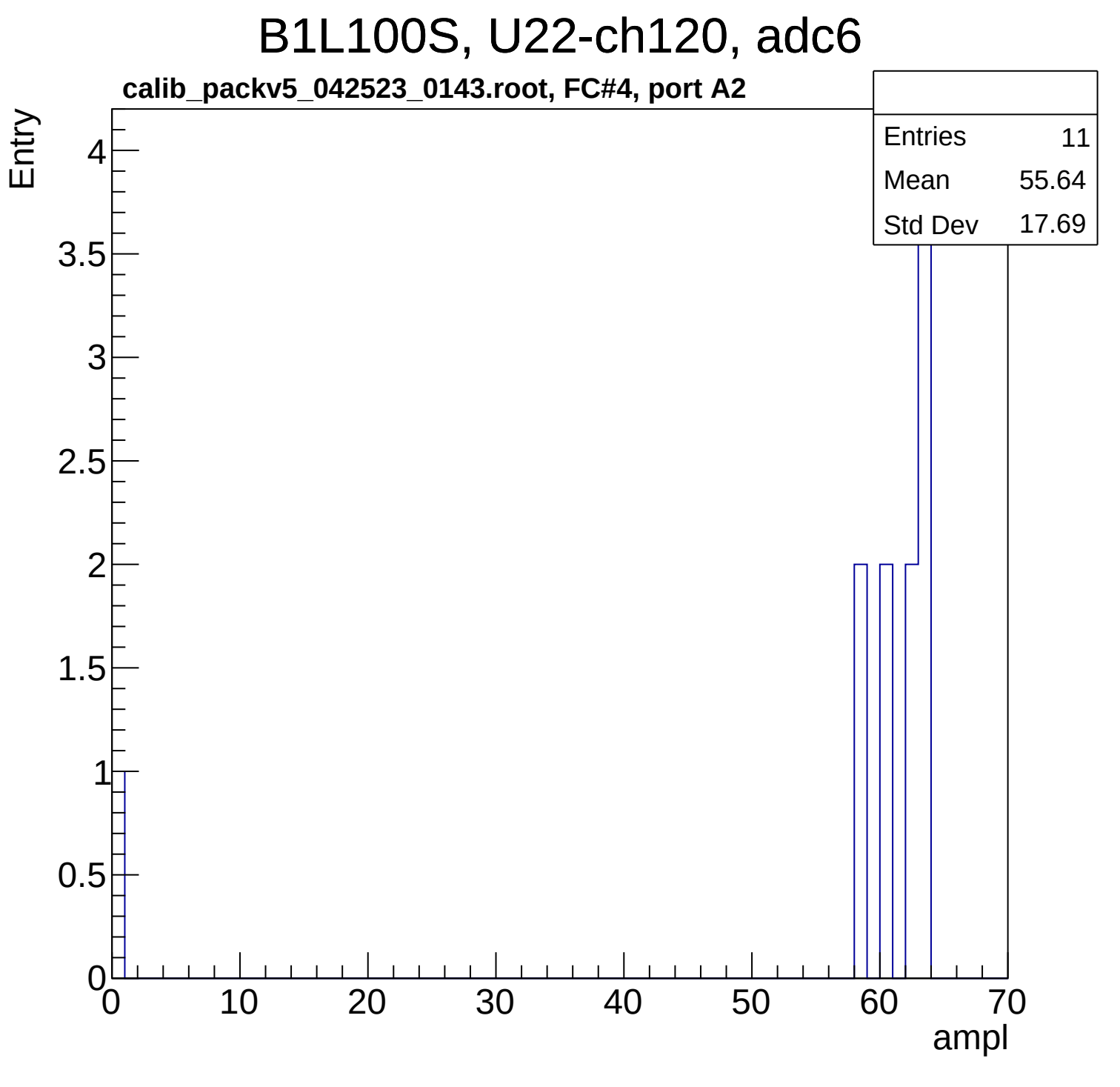
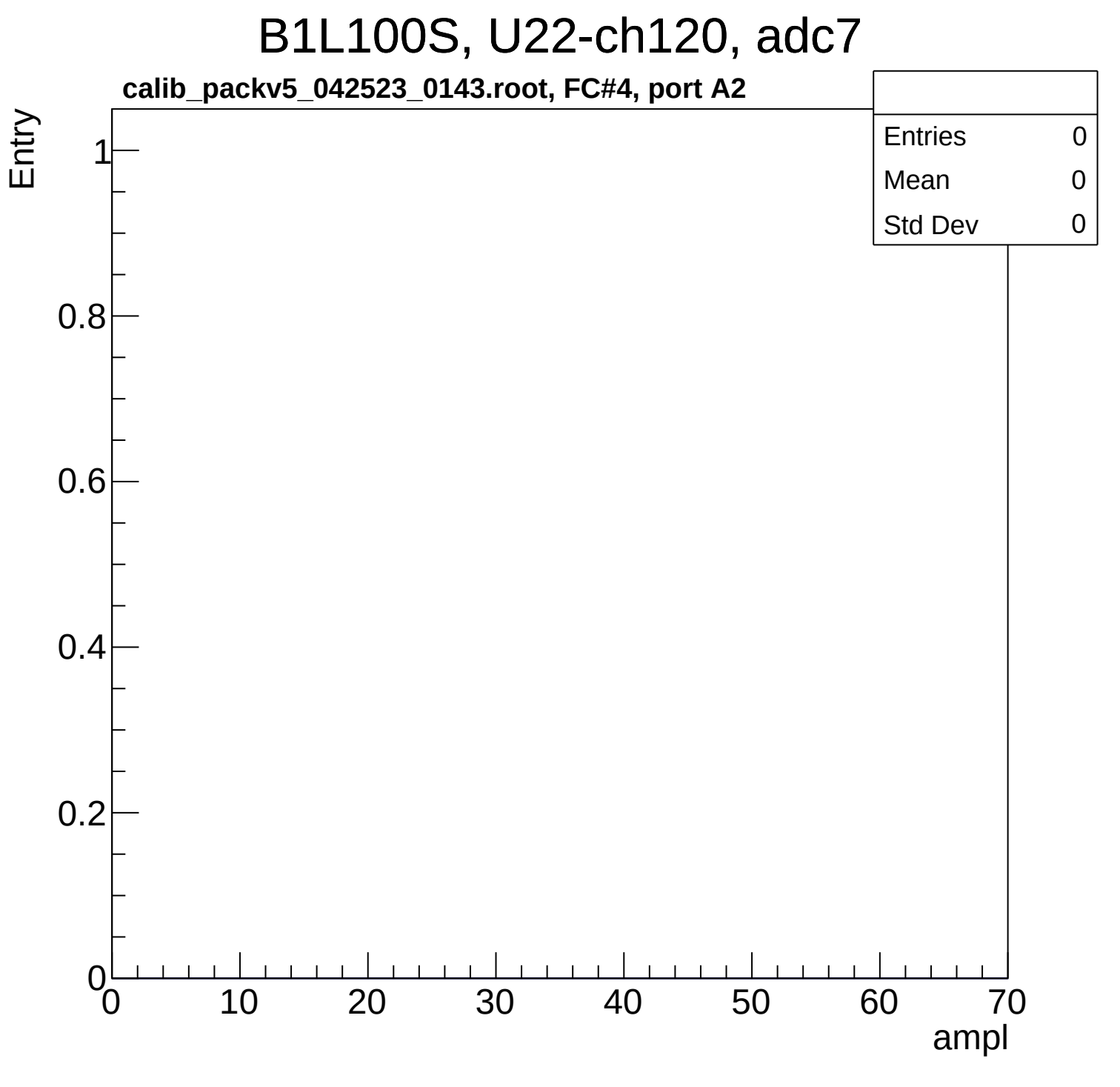
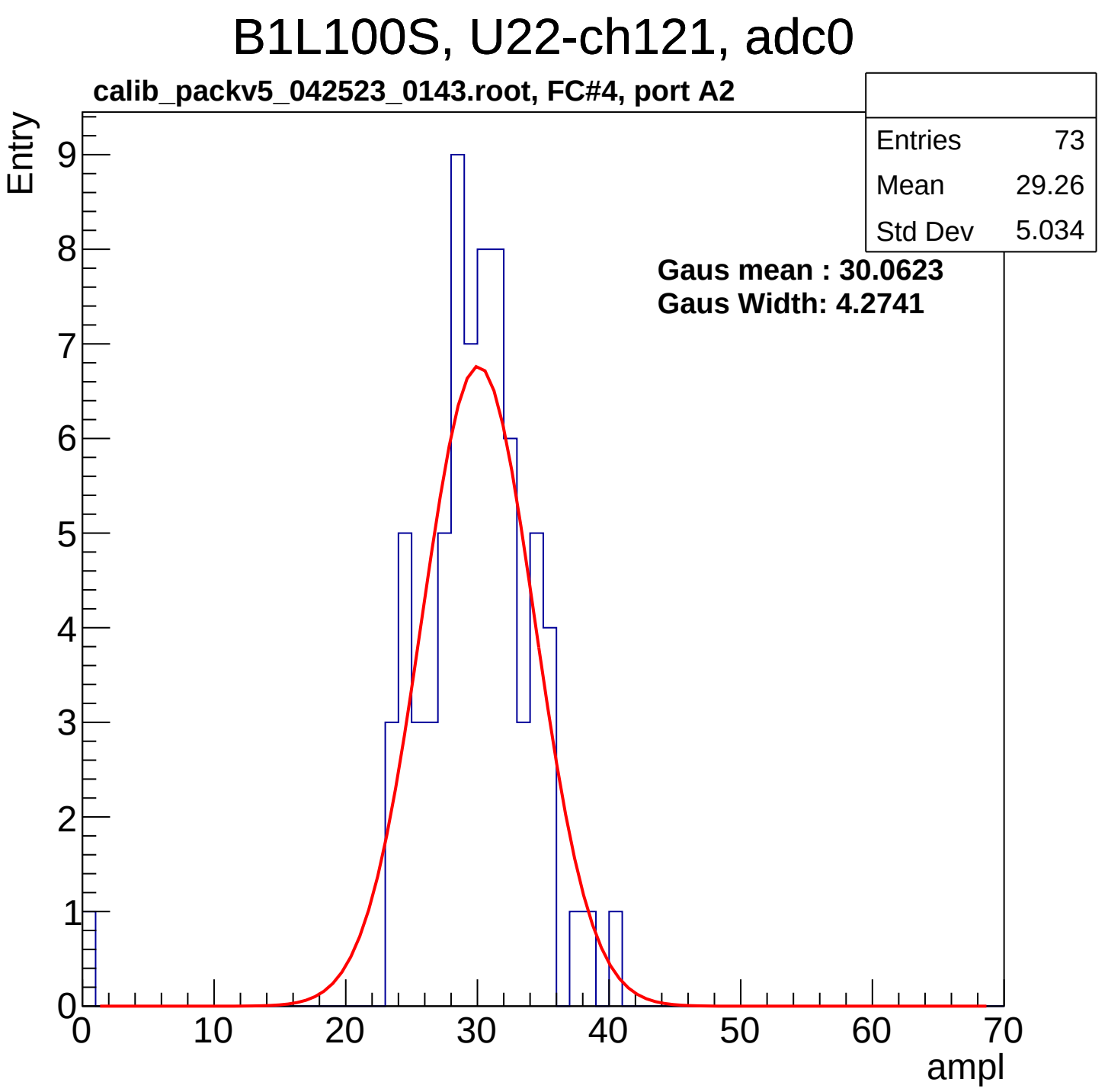

B1L100S, U22-ch120, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 11
Mean 55.64
Std Dev 17.69
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch120, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch121, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 73
Mean 29.26
Std Dev 5.034
Gaus mean : 30.0623
Gaus Width: 4.2741
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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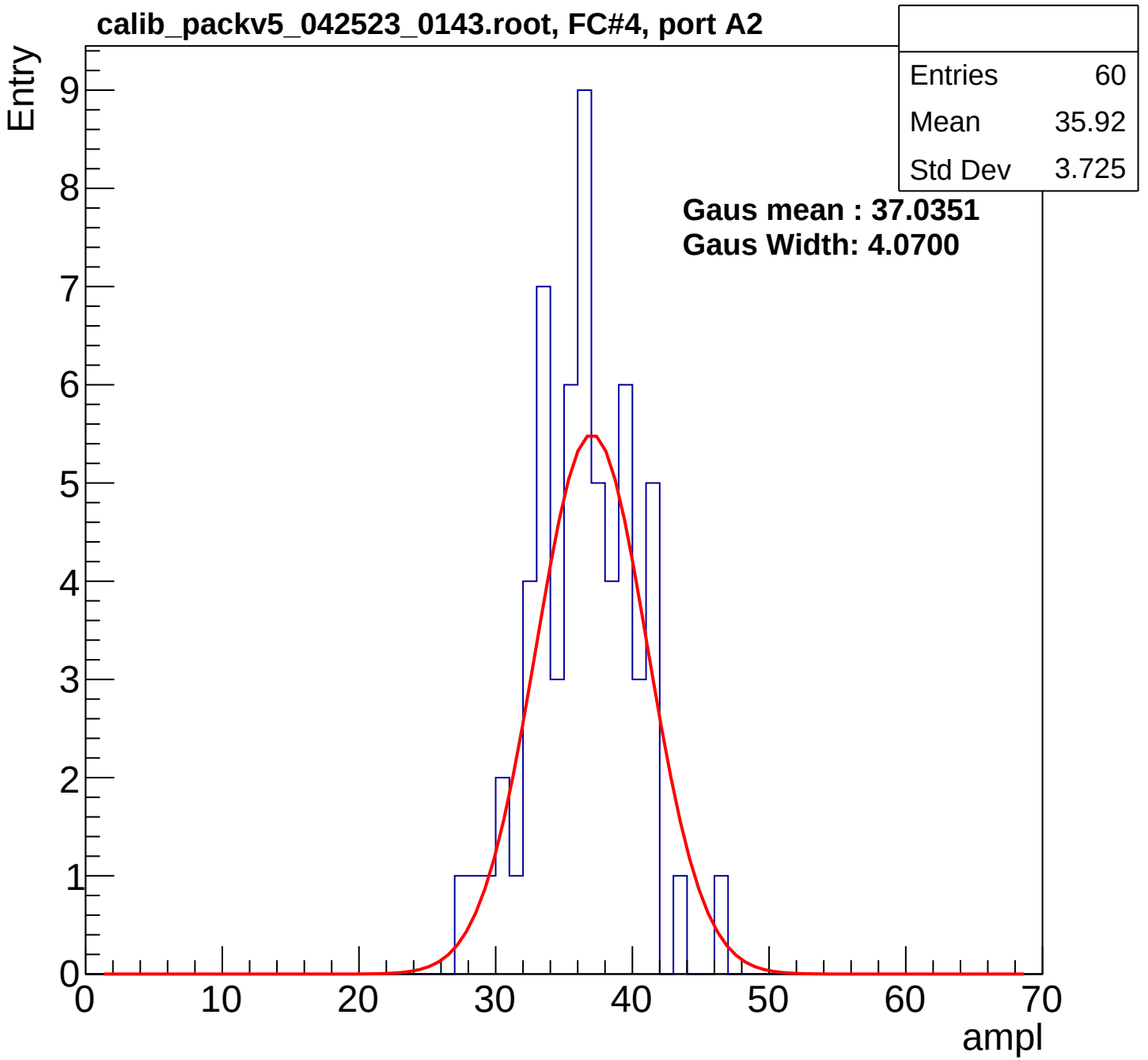
B1L100S, U22-ch121, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 60
Mean 35.92
Std Dev 3.725
Gaus mean : 37.0351
Gaus Width: 4.0700
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
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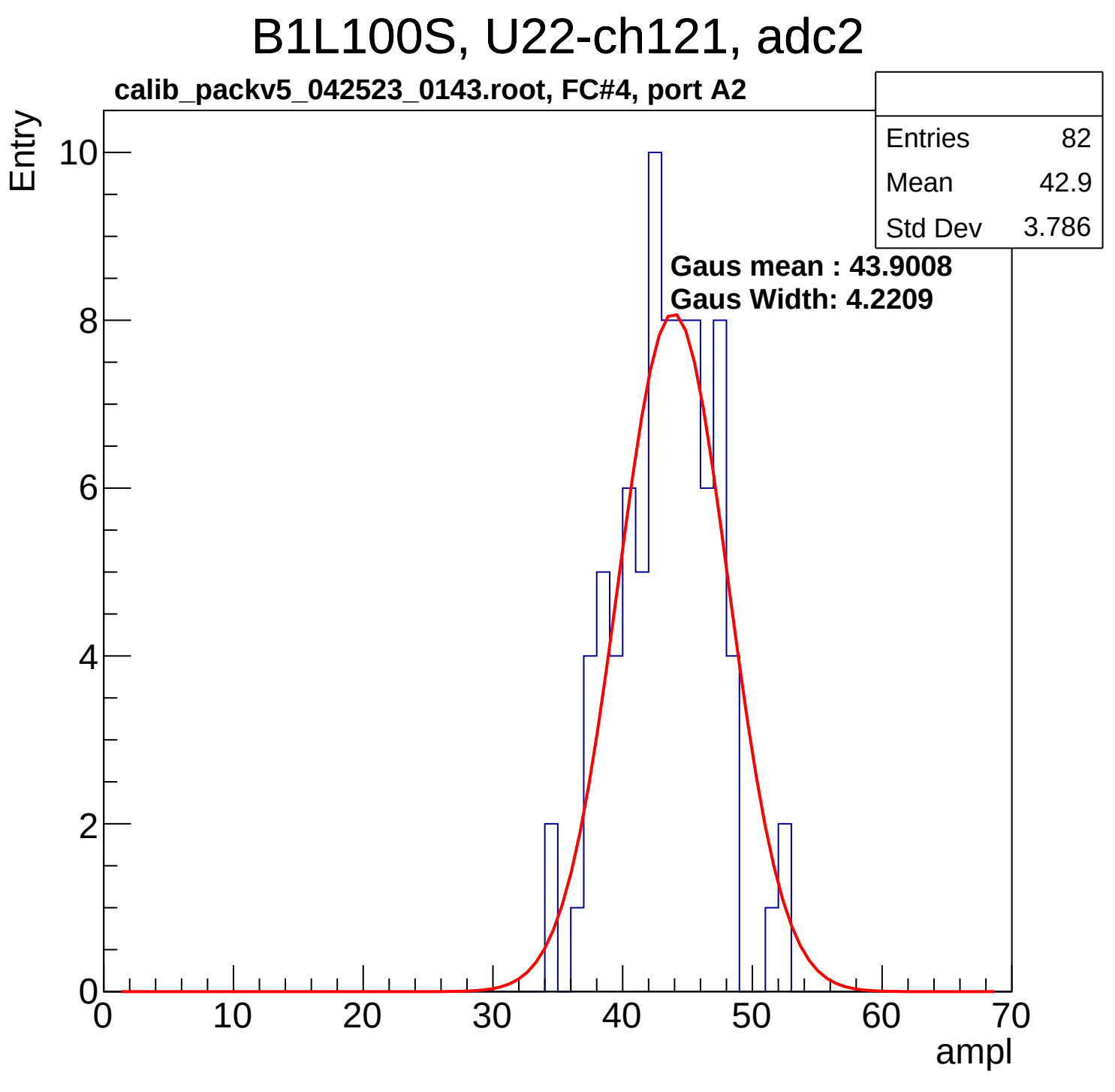
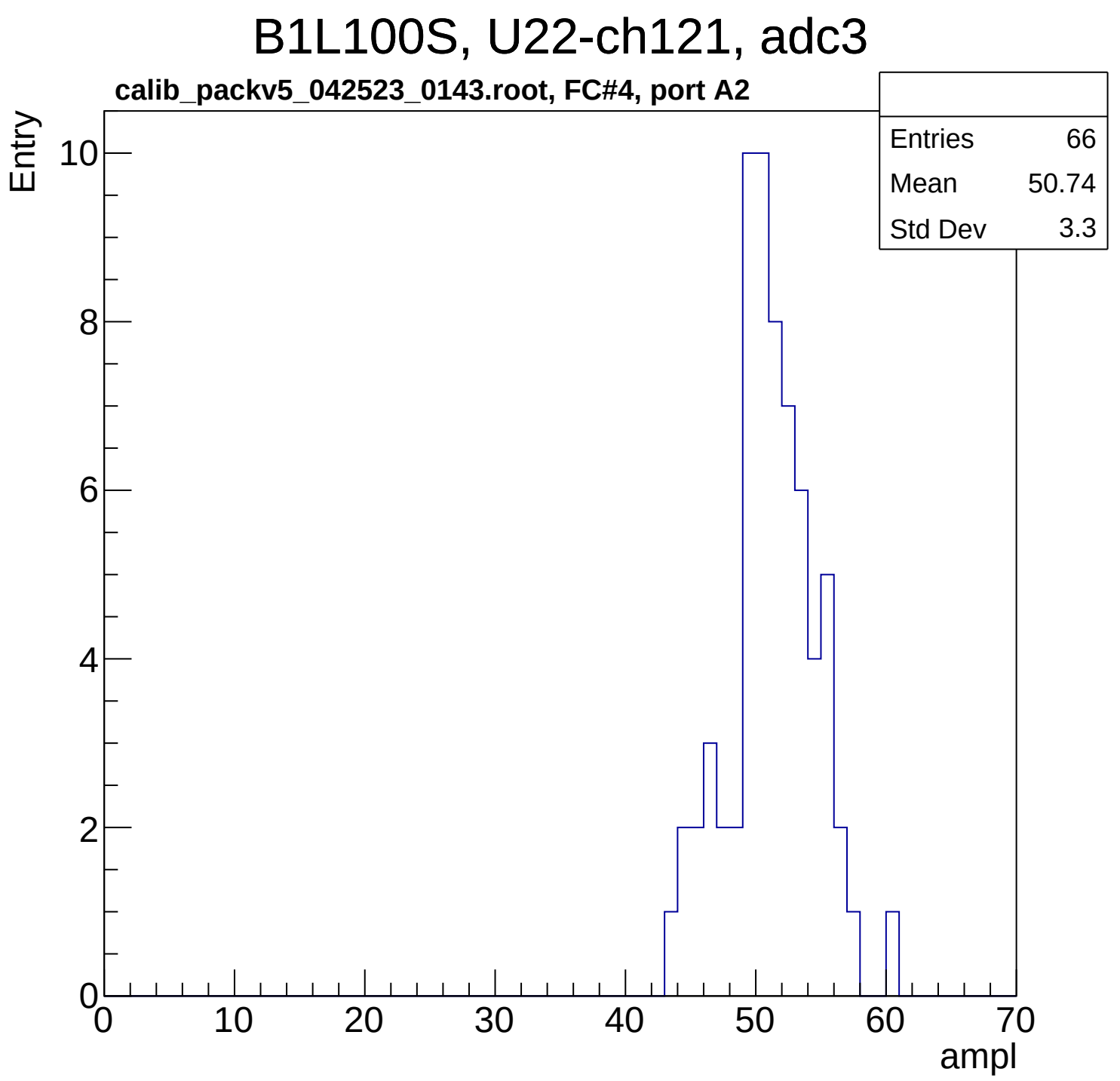
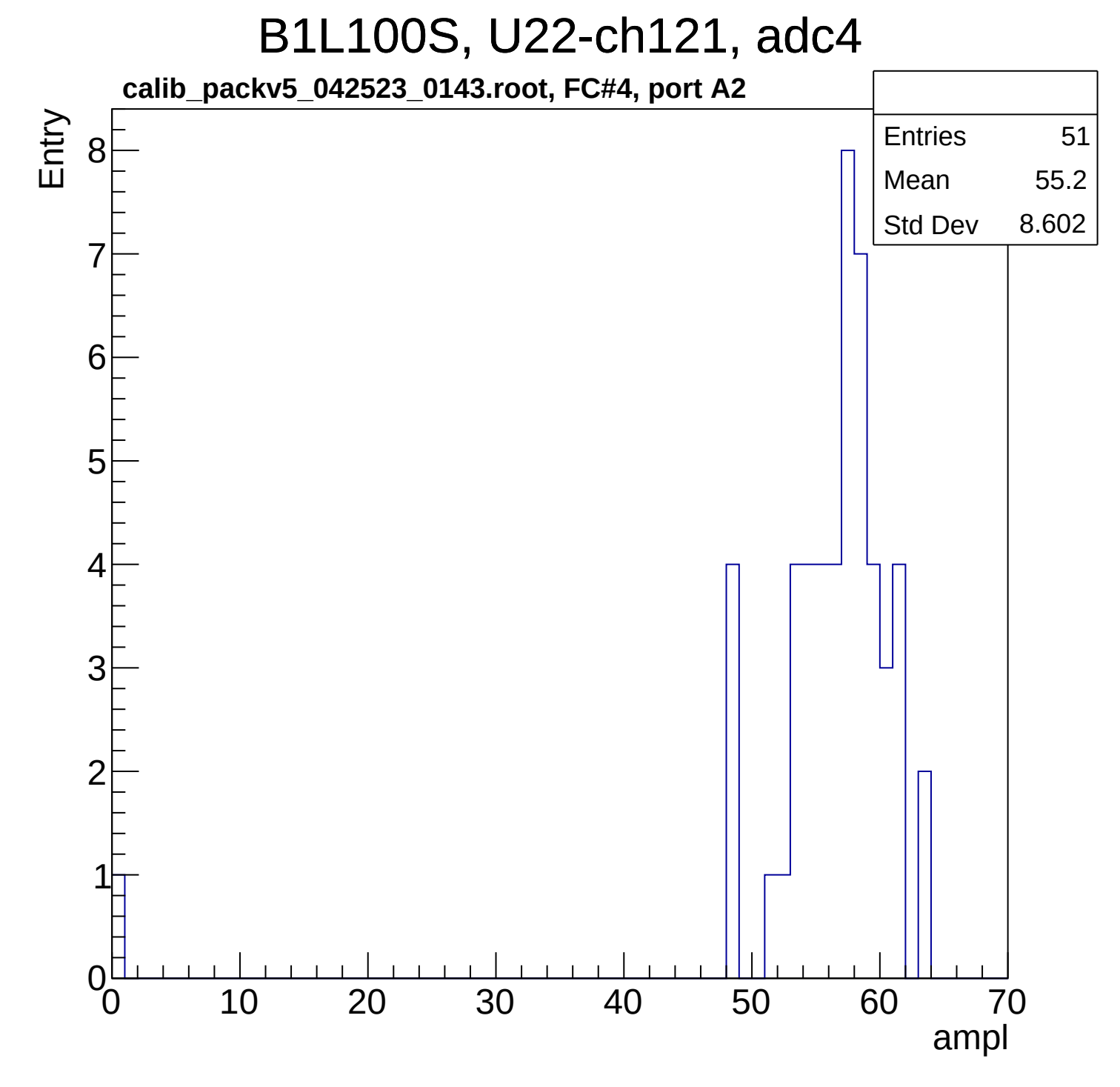

B1L100S, U22-ch121, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 42.9
Std Dev 3.786
Gaus mean : 43.9008
Gaus Width: 4.2209
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch121, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.74
Std Dev 3.3
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch121, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 51
Mean 55.2
Std Dev 8.602
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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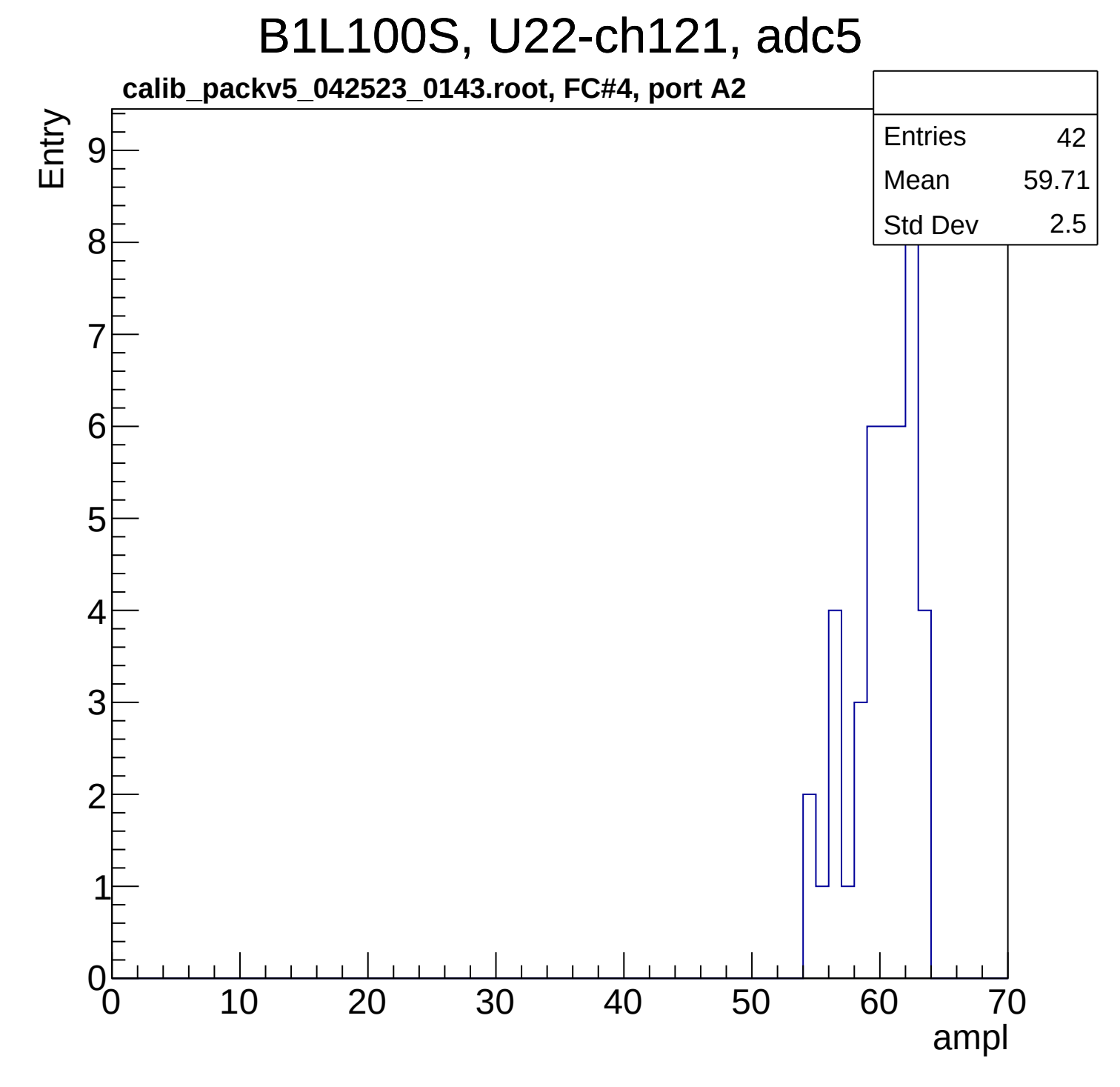
B1L100S, U22-ch121, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 59.71
Std Dev 2.5
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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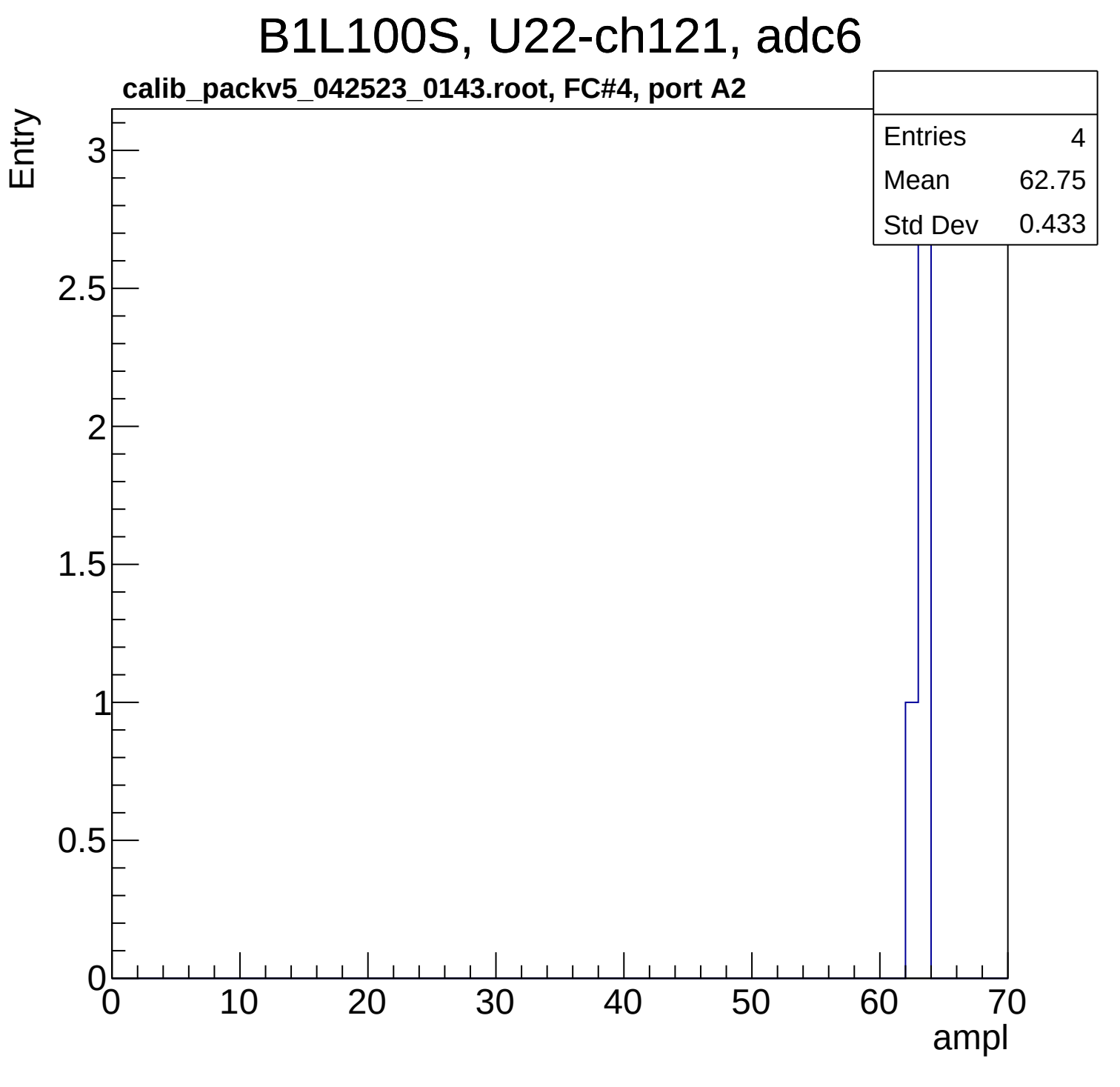
B1L100S, U22-ch121, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 4
Mean 62.75
Std Dev 0.433
Entry
ampl
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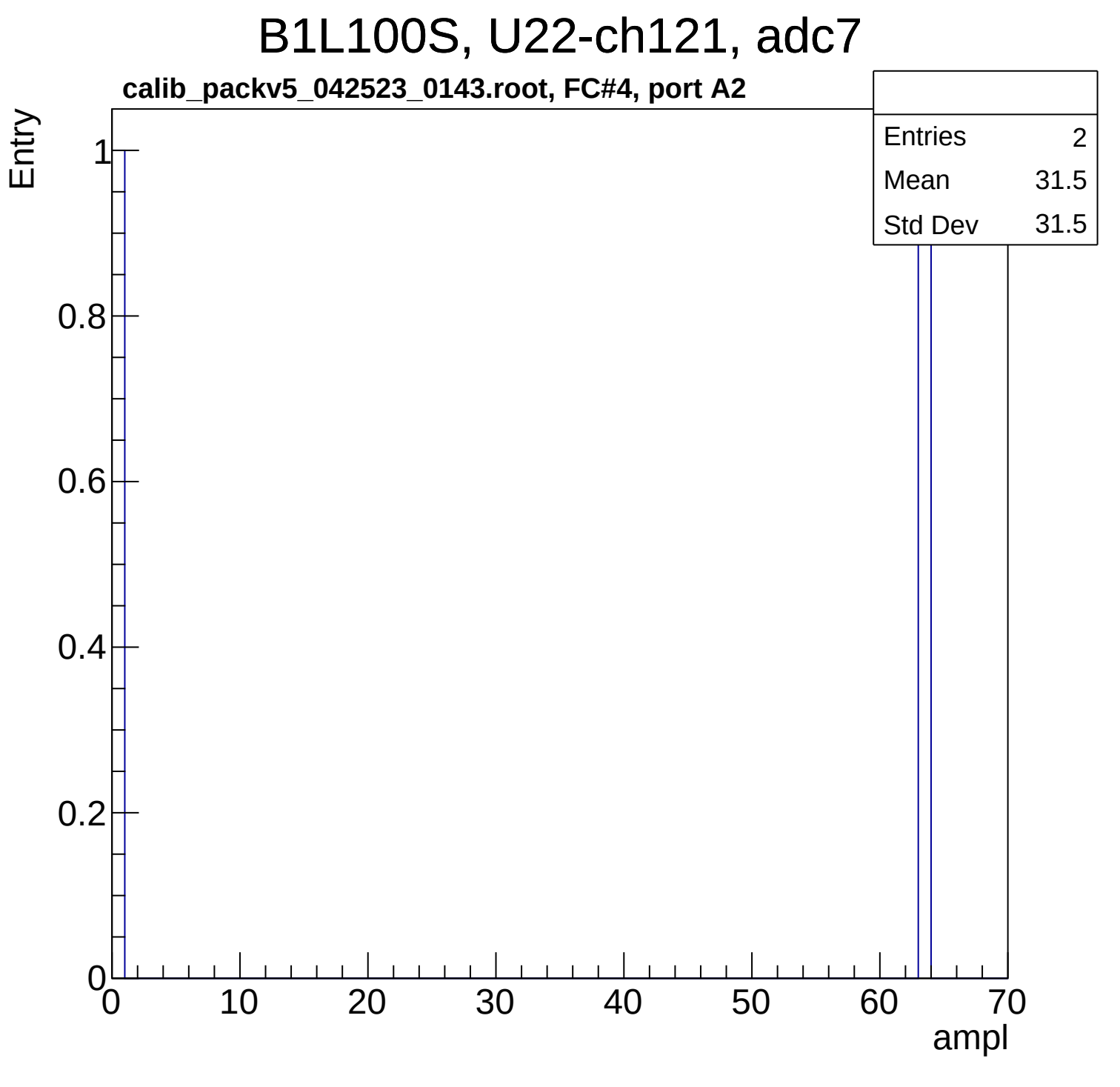
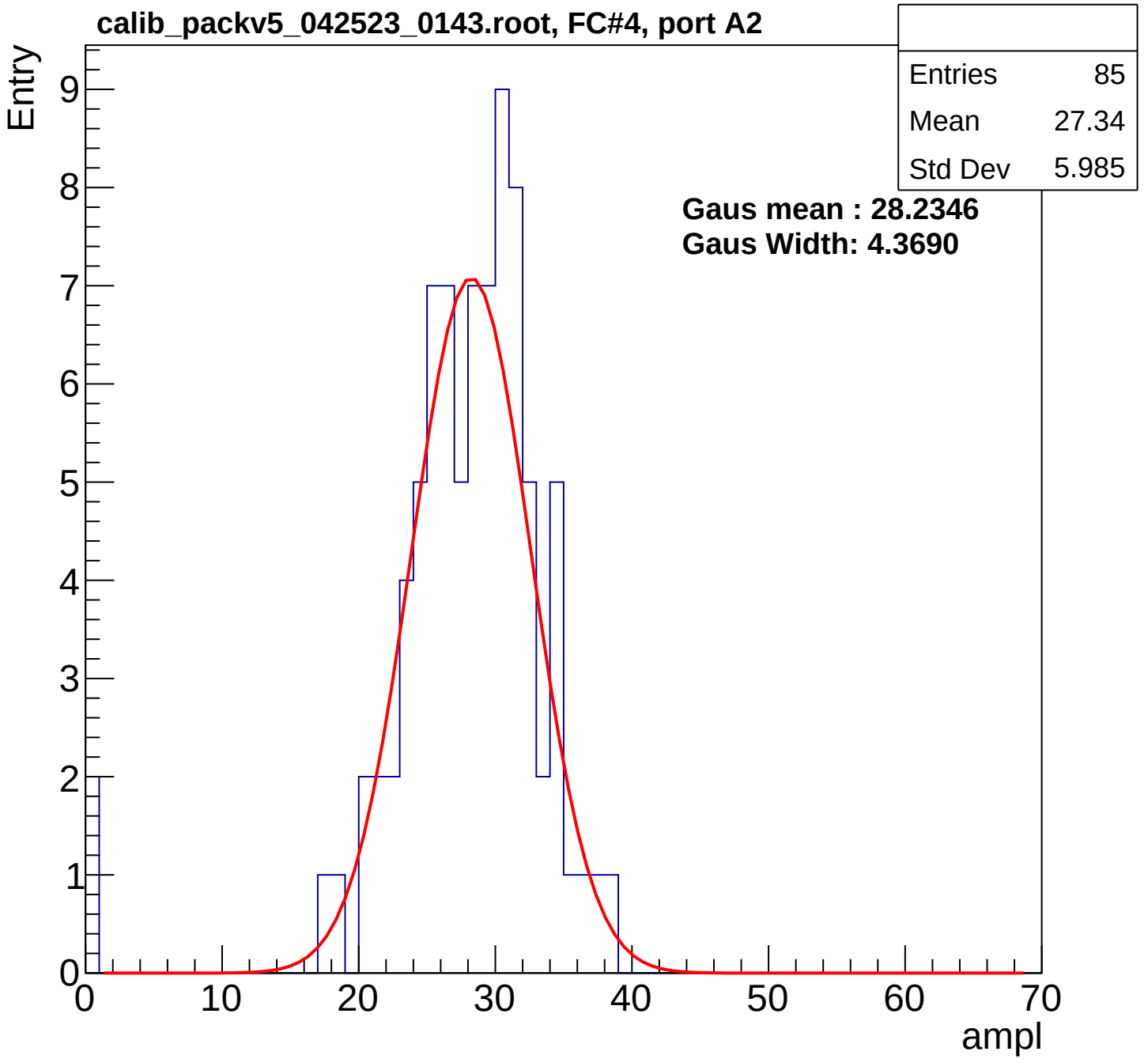
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch121, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 31.5
Std Dev 31.5
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch122, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 27.34
Std Dev 5.985
Gaus mean : 28.2346
Gaus Width: 4.3690
Entry
ampl
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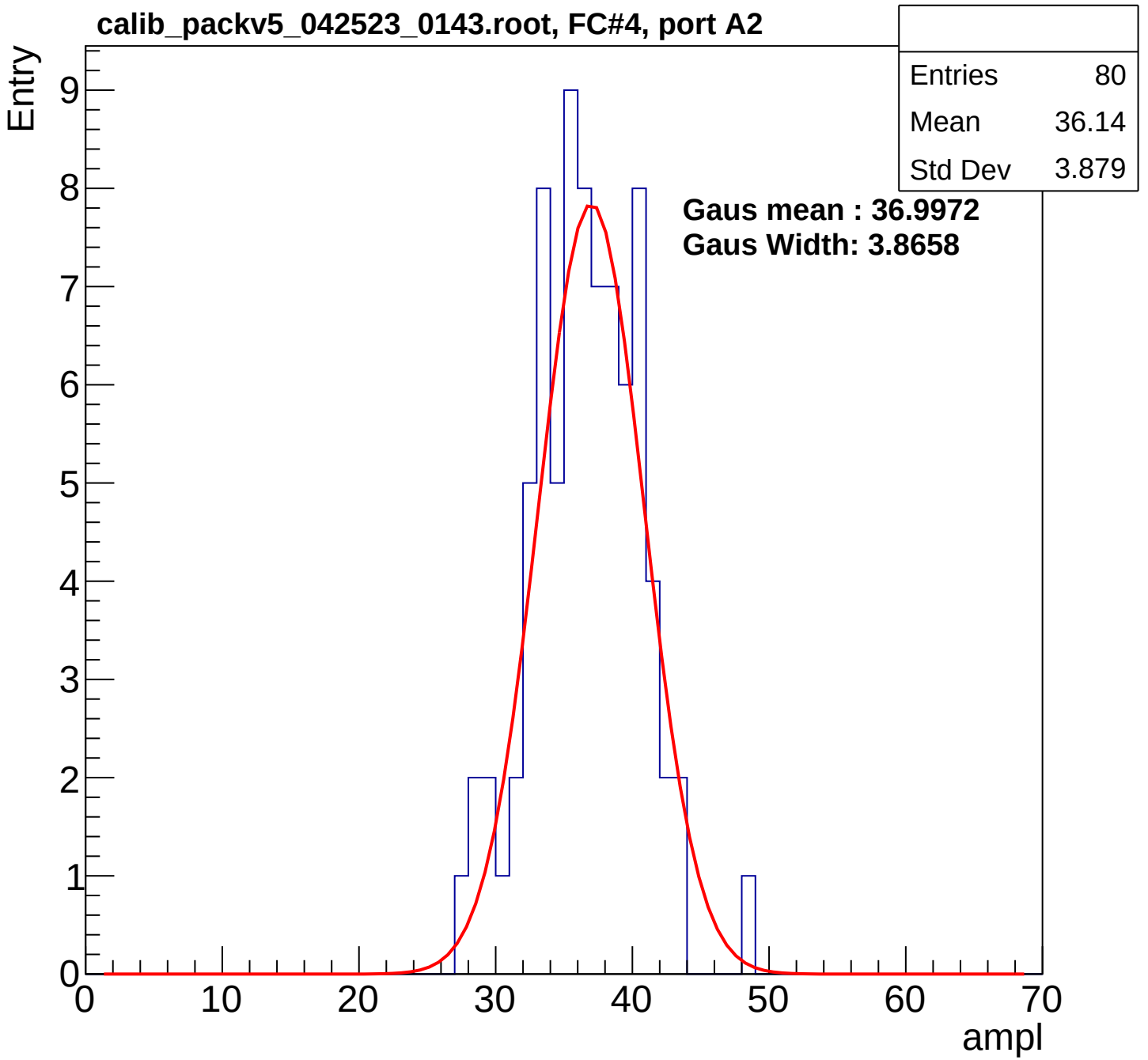
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch122, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 80
Mean 36.14
Std Dev 3.879
Gaus mean : 36.9972
Gaus Width: 3.8658
Entry
ampl
0
1
2
3
4
5
6
7
8
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0
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20
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40
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60
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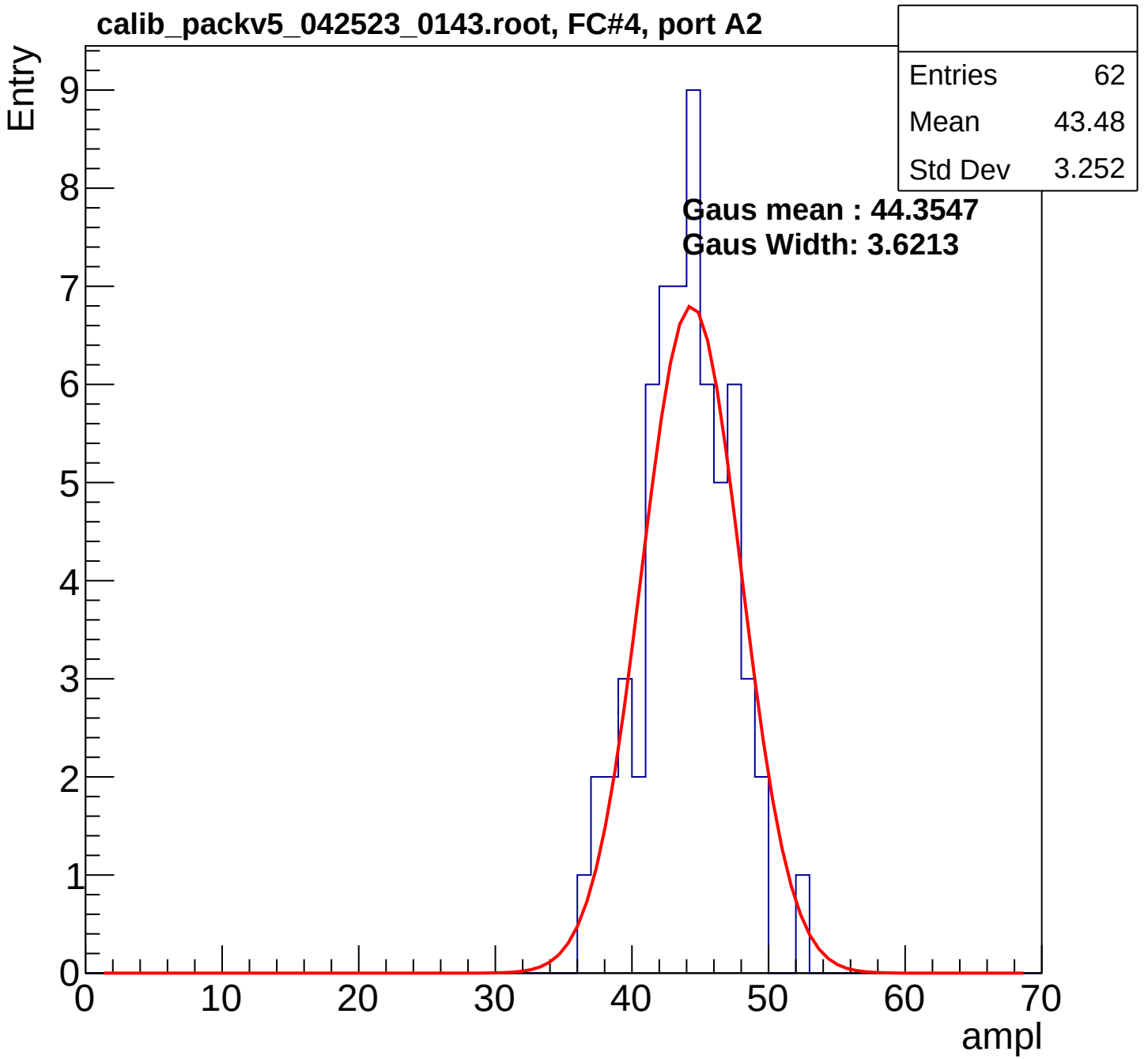
B1L100S, U22-ch122, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 43.48
Std Dev 3.252
Gaus mean : 44.3547
Gaus Width: 3.6213
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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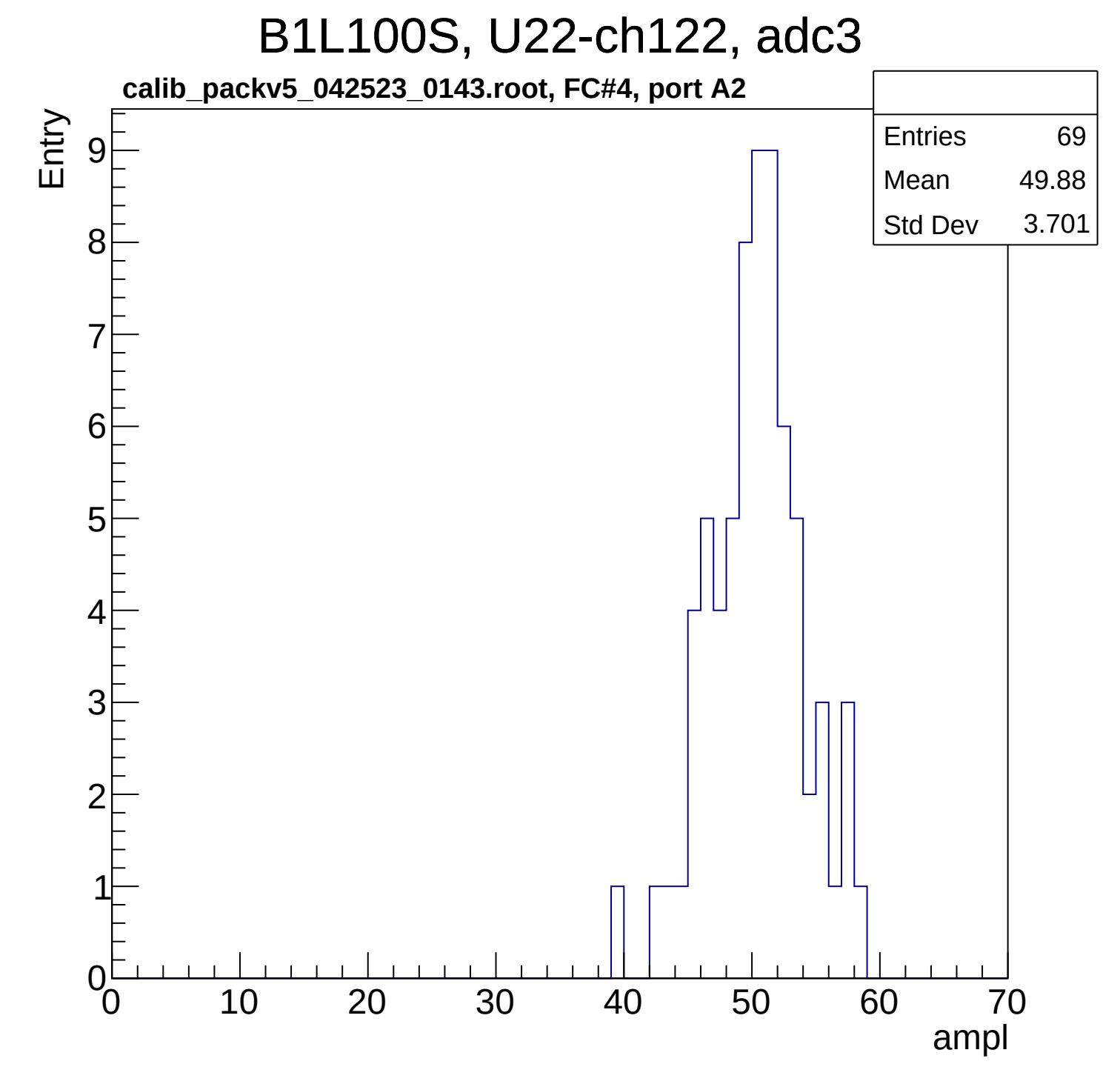
B1L100S, U22-ch122, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 49.88
Std Dev 3.701
Entry
ampl
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6
7
8
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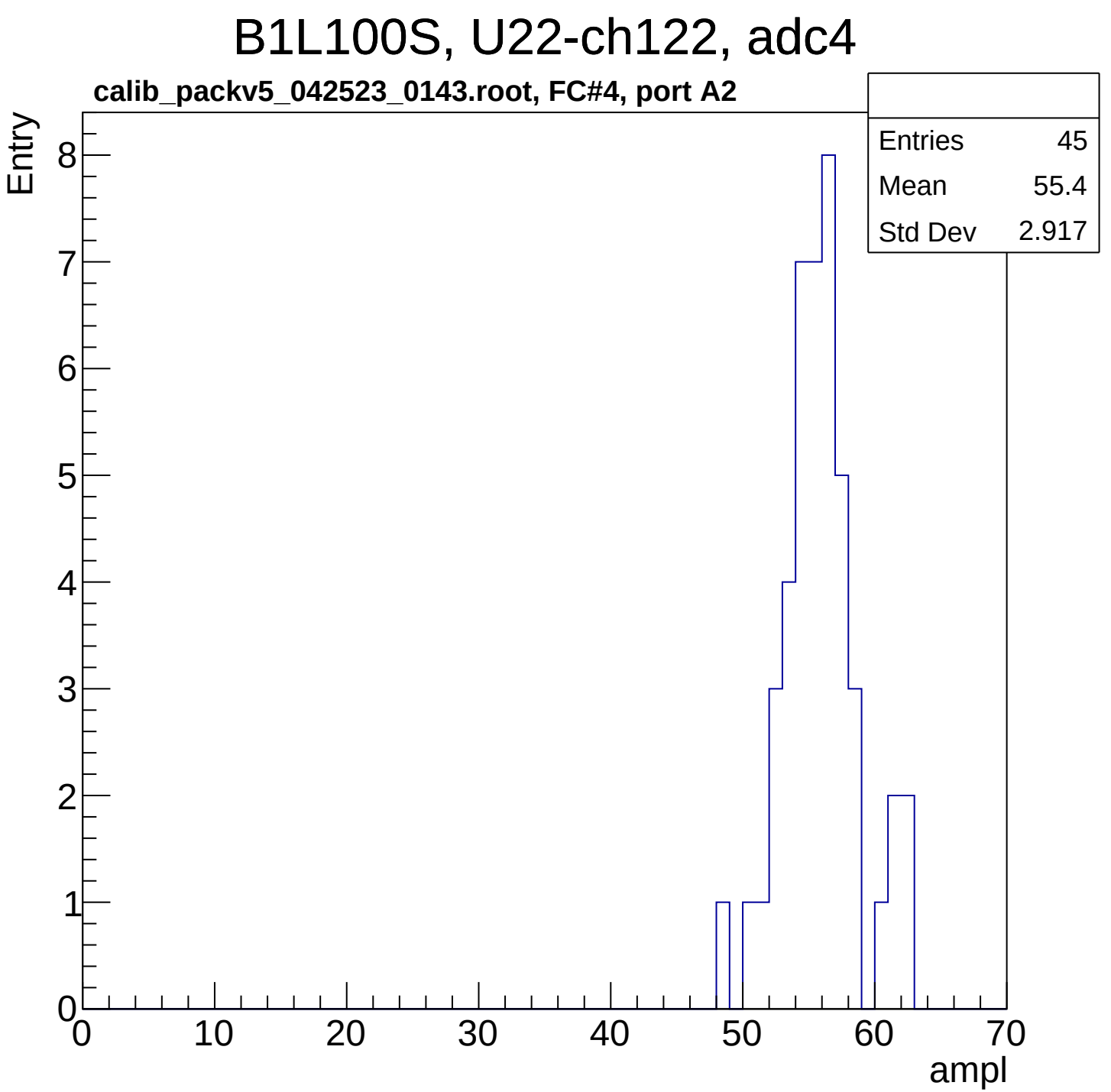
B1L100S, U22-ch122, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 45
Mean 55.4
Std Dev 2.917
Entry
ampl
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1
2
3
4
5
6
7
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0
10
20
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40
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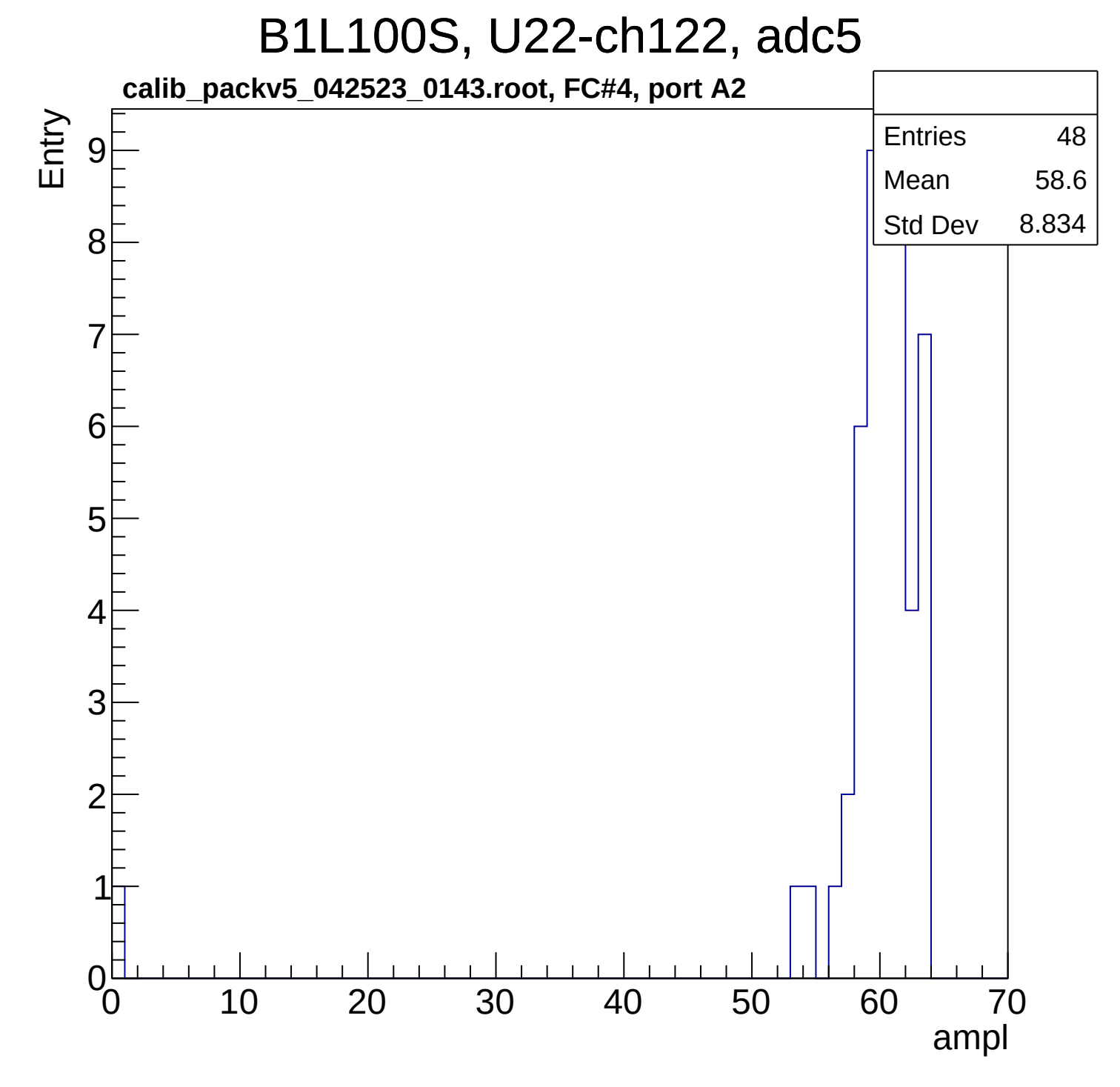
60
70


B1L100S, U22-ch122, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 48
Mean 58.6
Std Dev 8.834
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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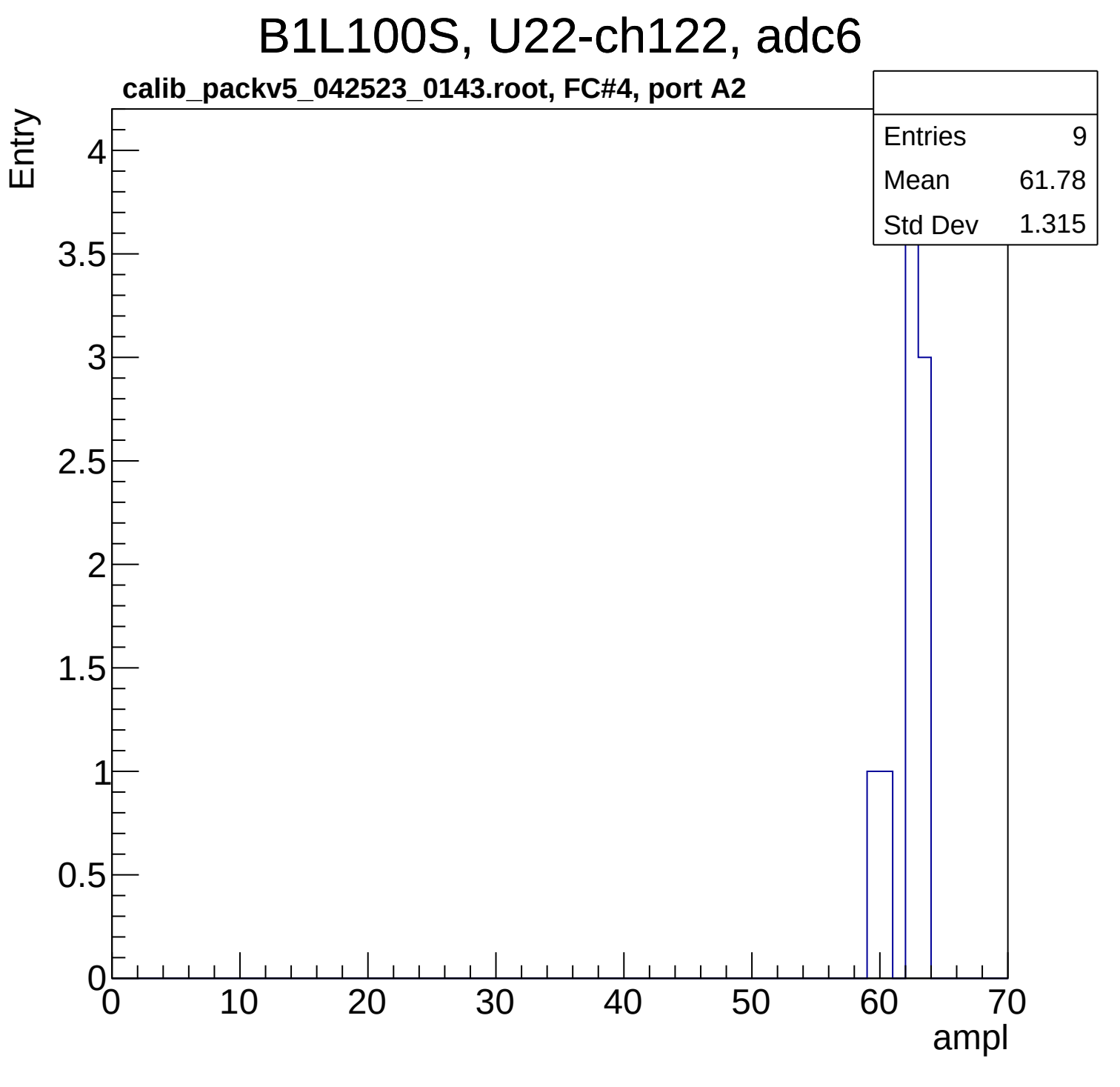
B1L100S, U22-ch122, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 9
Mean 61.78
Std Dev 1.315
Entry
ampl
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1.5
2
2.5
3
3.5
4
0
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20
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40
50
60
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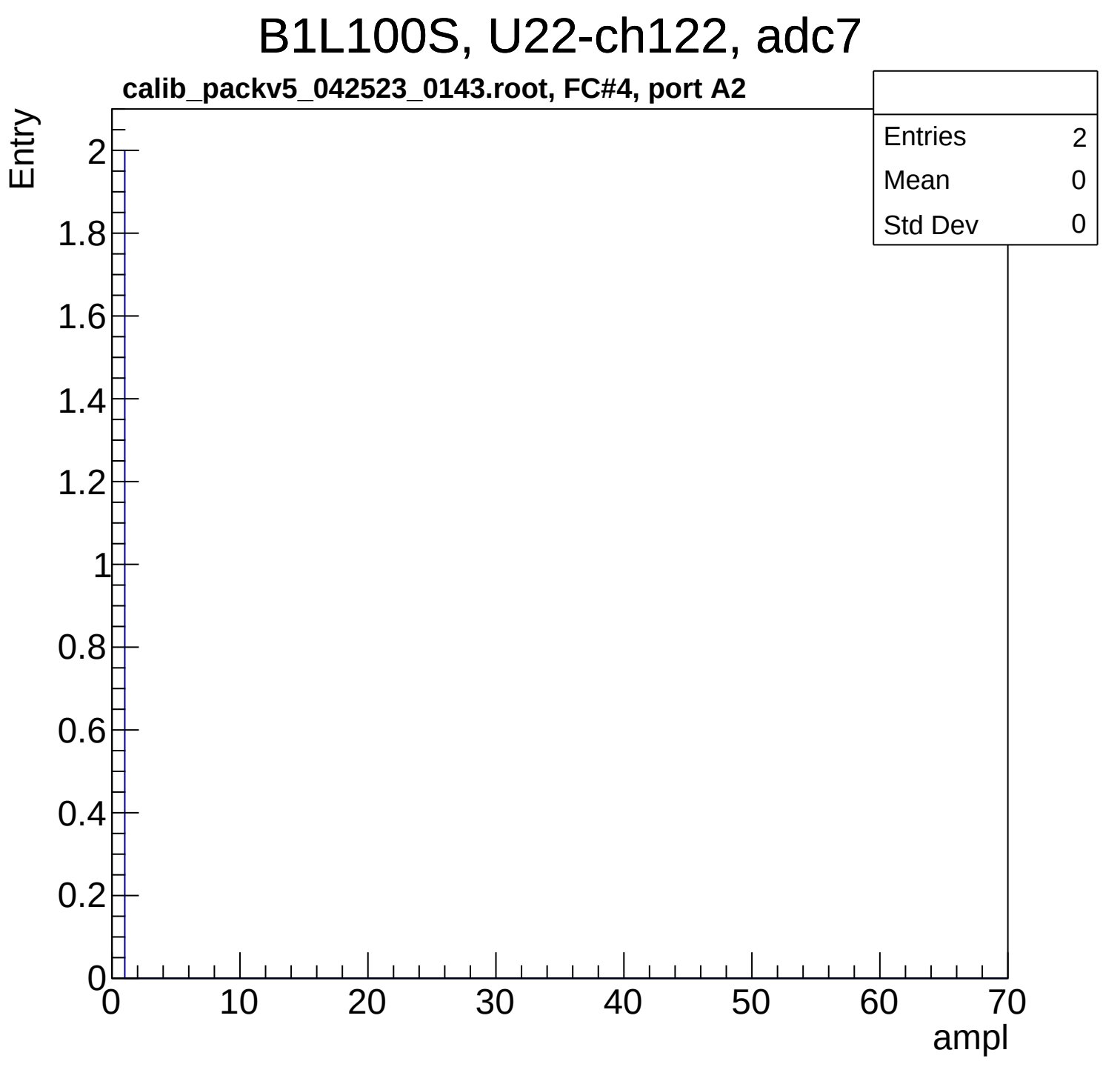
B1L100S, U22-ch122, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
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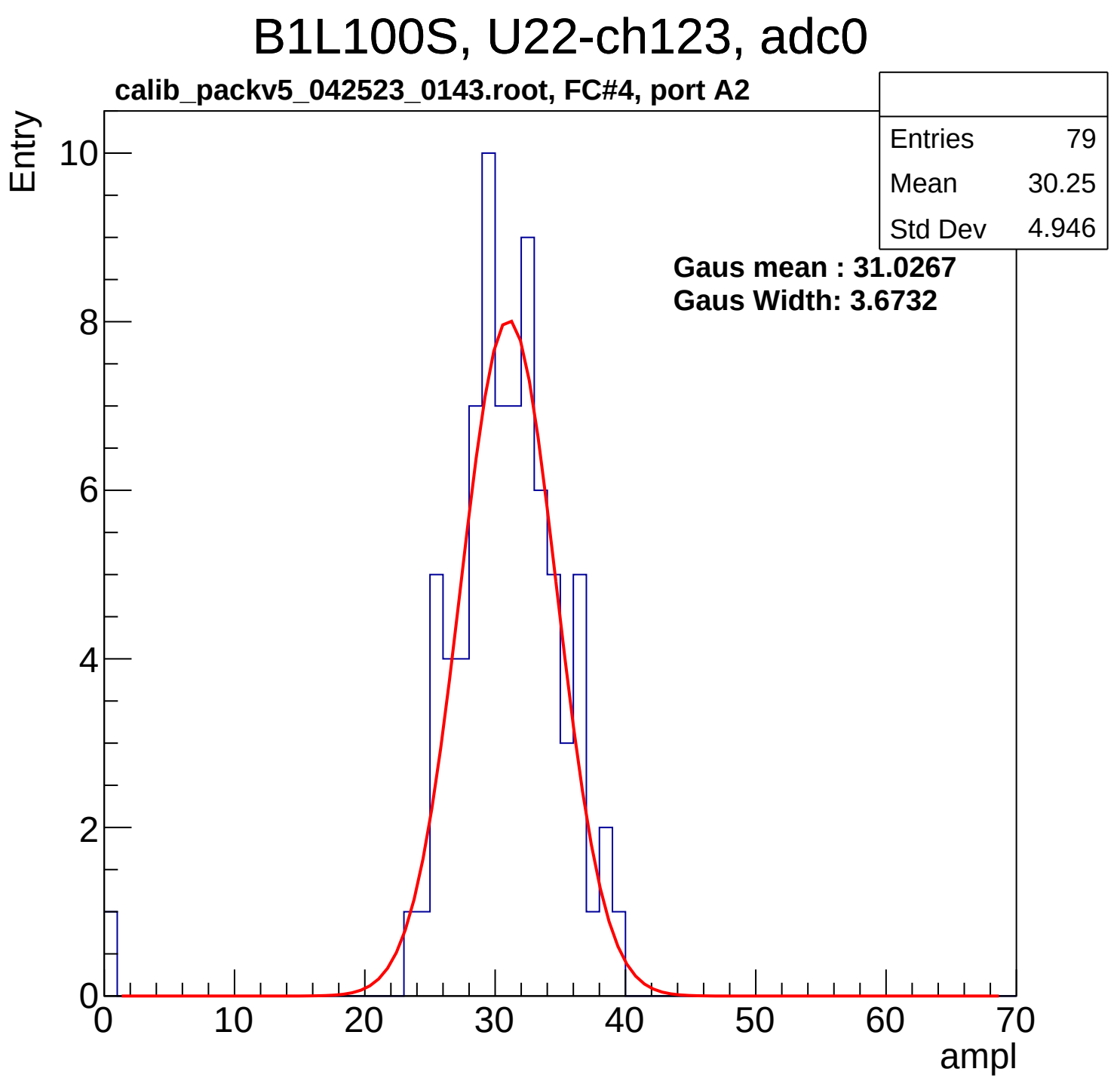
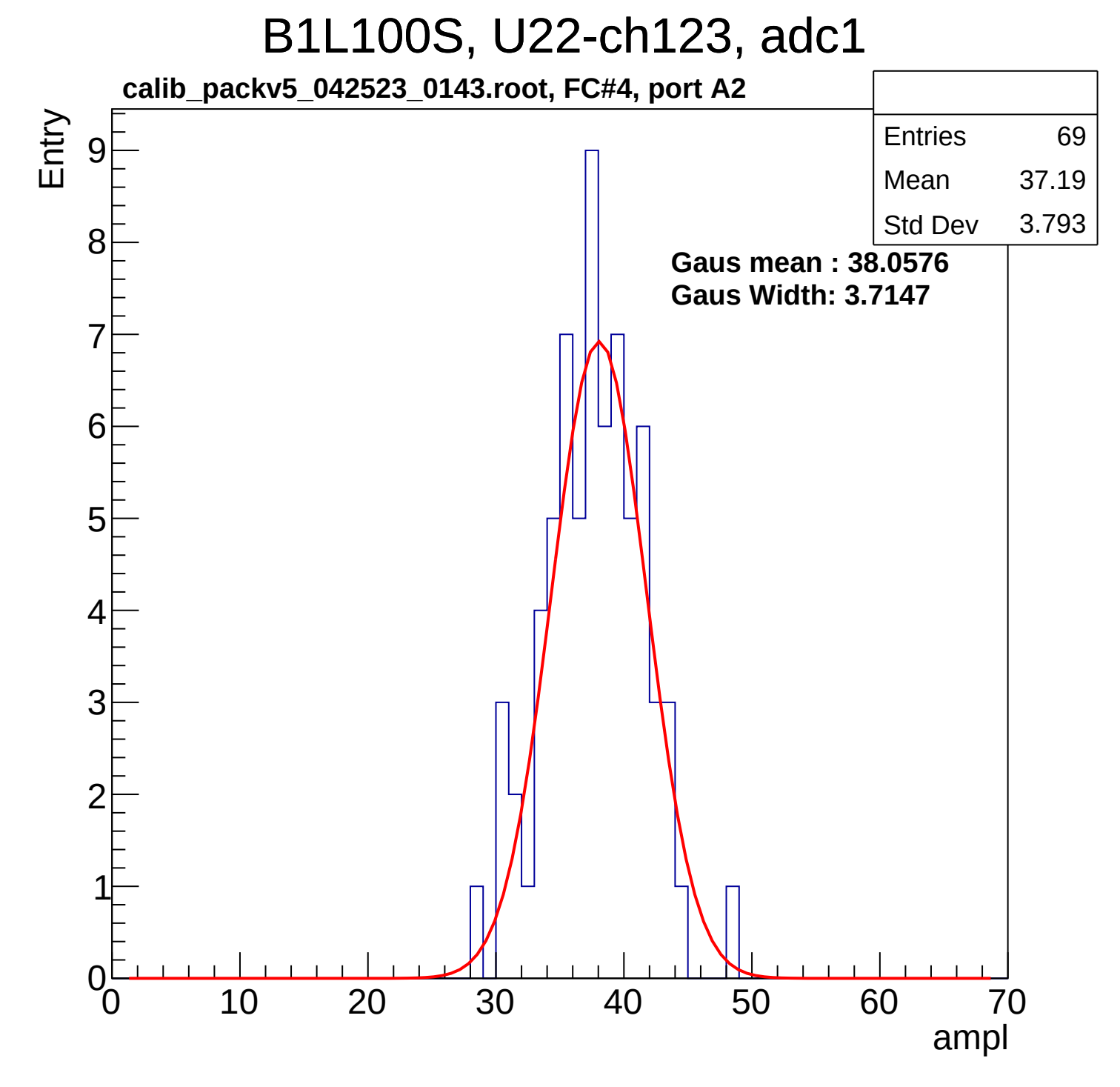
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch123, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 30.25
Std Dev 4.946
Gaus mean : 31.0267
Gaus Width: 3.6732
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch123, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.19
Std Dev 3.793
Gaus mean : 38.0576
Gaus Width: 3.7147
Entry
ampl
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2
3
4
5
6
7
8
9
0
10
20
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40
50
60
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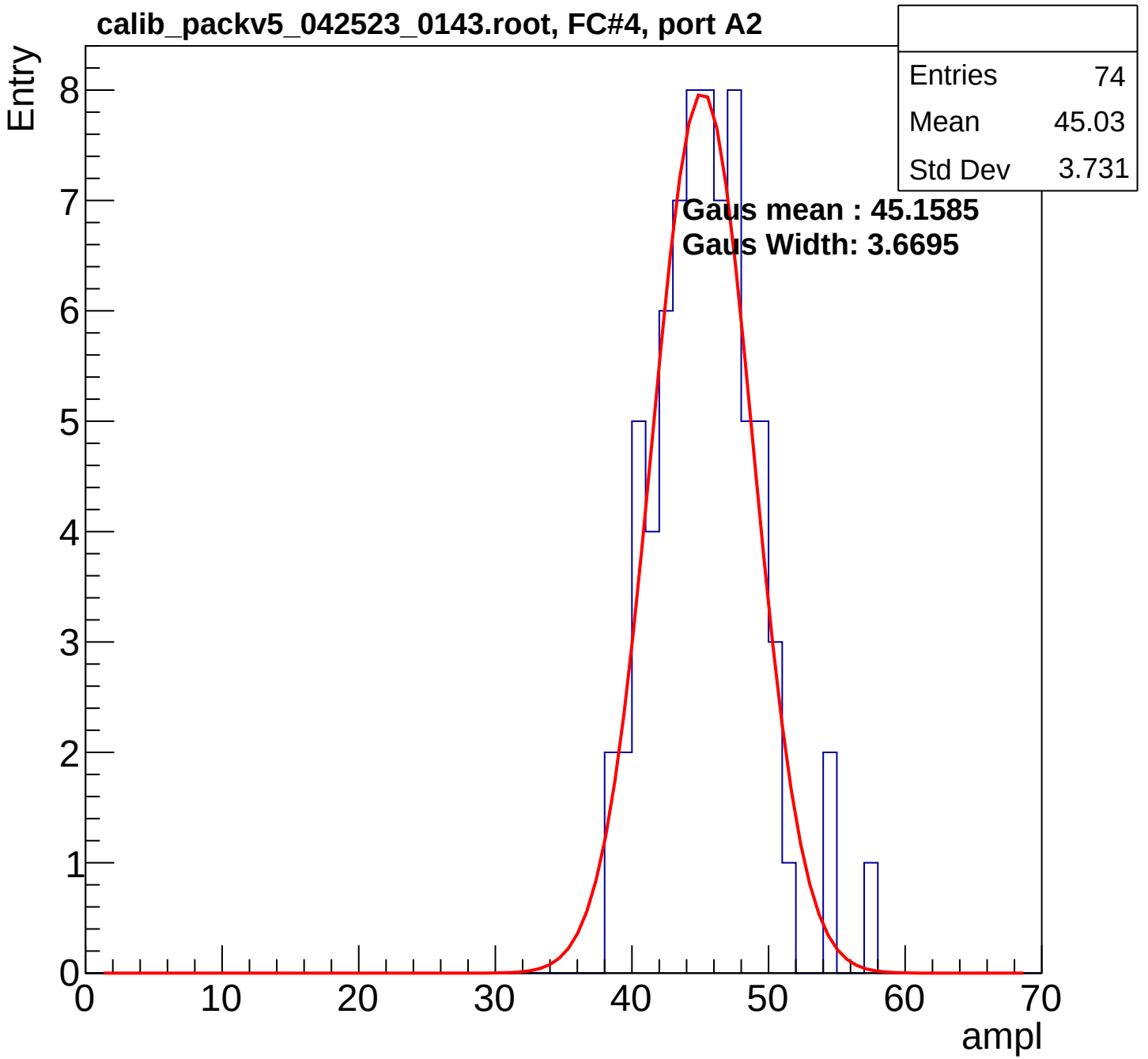
B1L100S, U22-ch123, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 74
Mean 45.03
Std Dev 3.731
Gaus mean : 45.1585
Gaus Width: 3.6695
Entry
ampl
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1
2
3
4
5
6
7
8
0
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40
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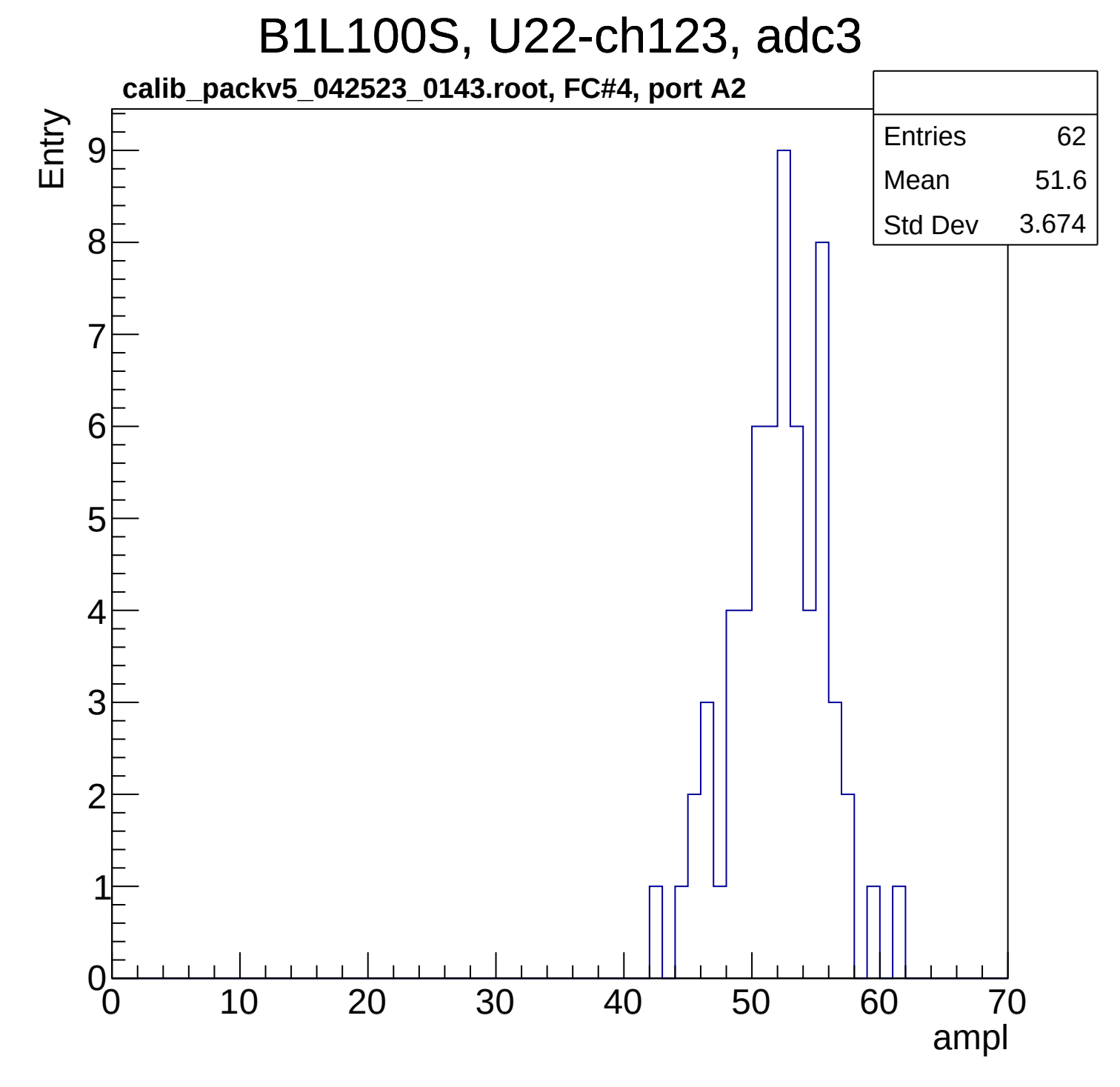
B1L100S, U22-ch123, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 62
Mean 51.6
Std Dev 3.674
Entry
ampl
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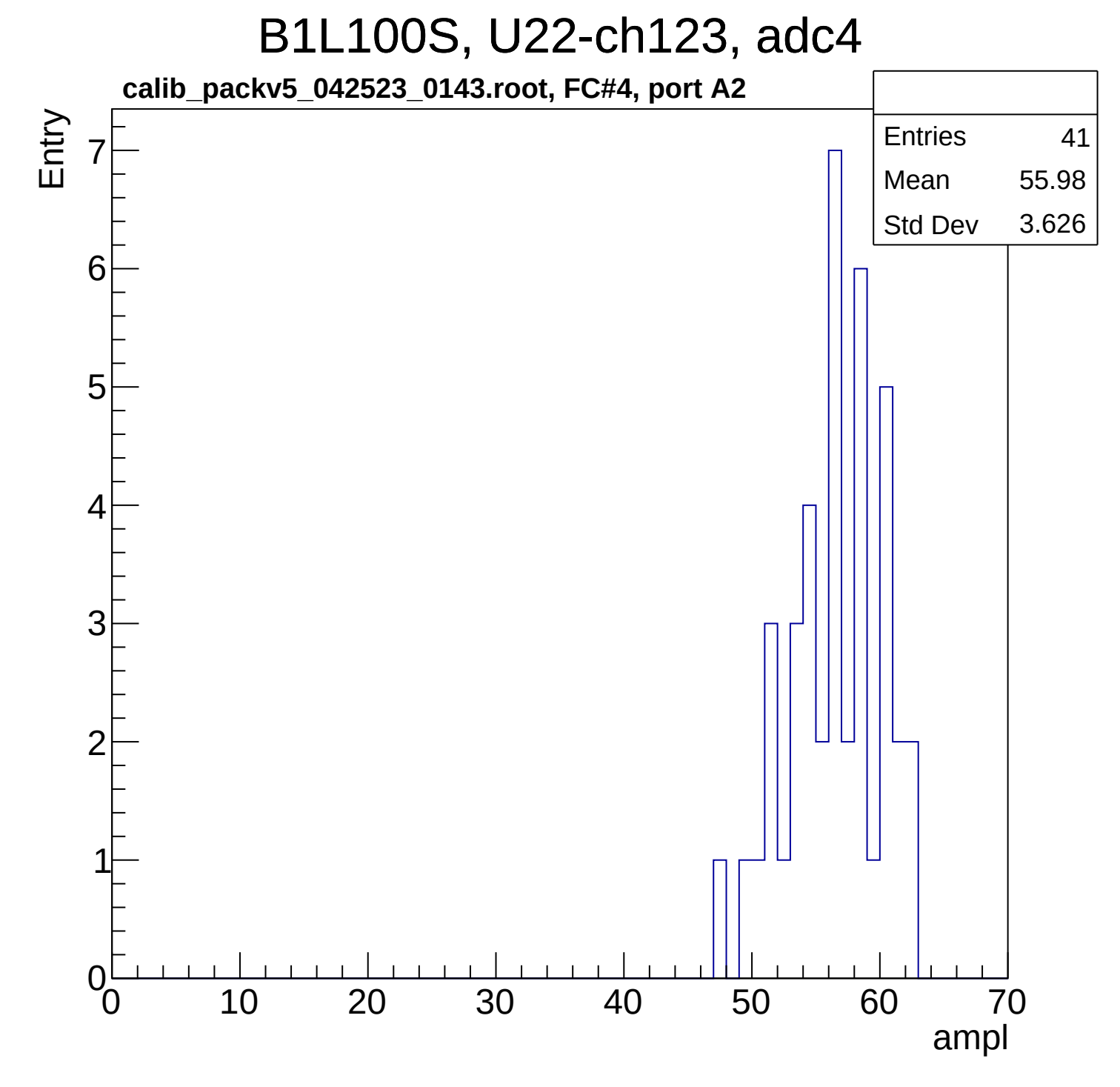
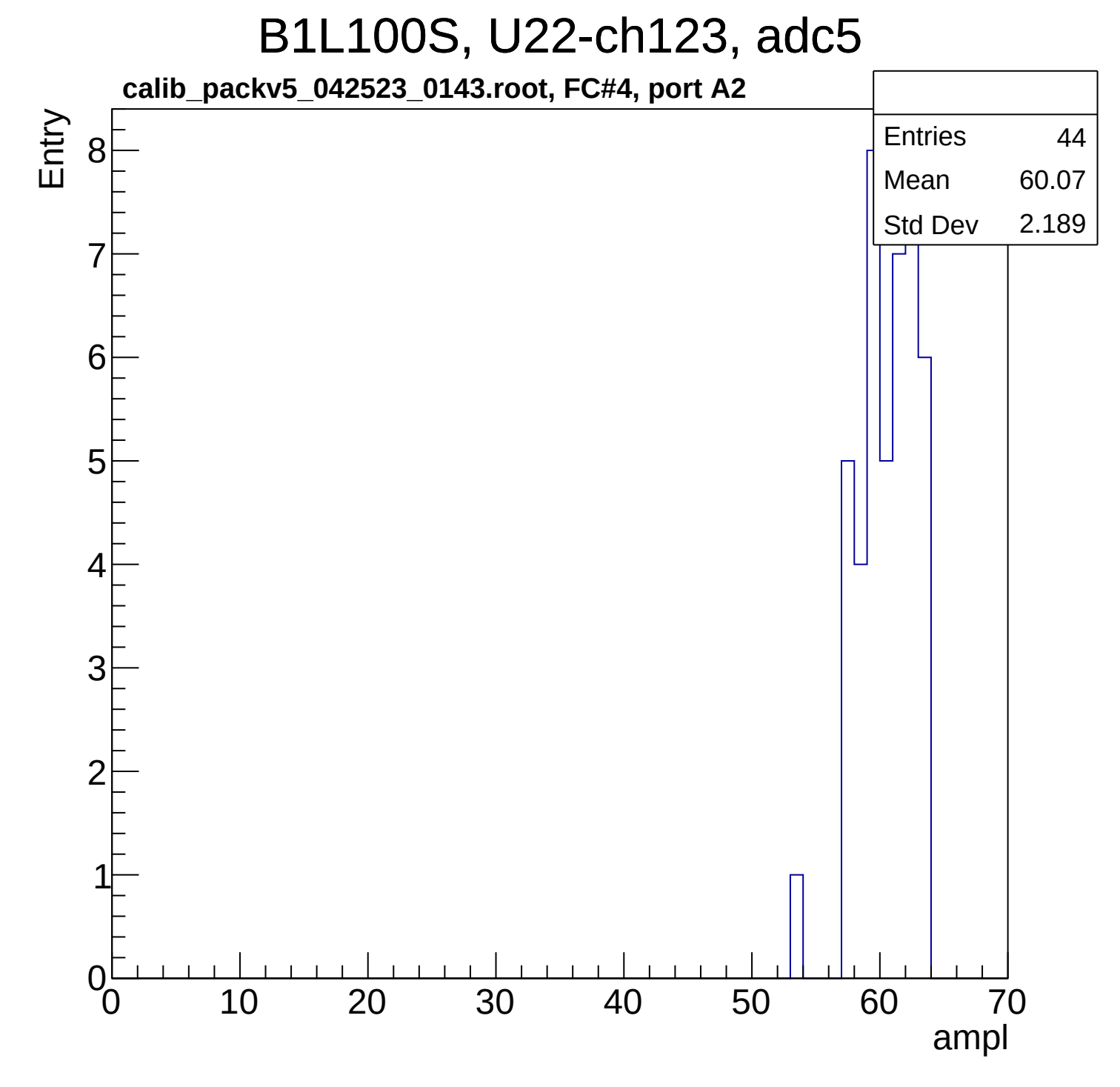

B1L100S, U22-ch123, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 41
Mean 55.98
Std Dev 3.626
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch123, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 44
Mean 60.07
Std Dev 2.189
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
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40
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60
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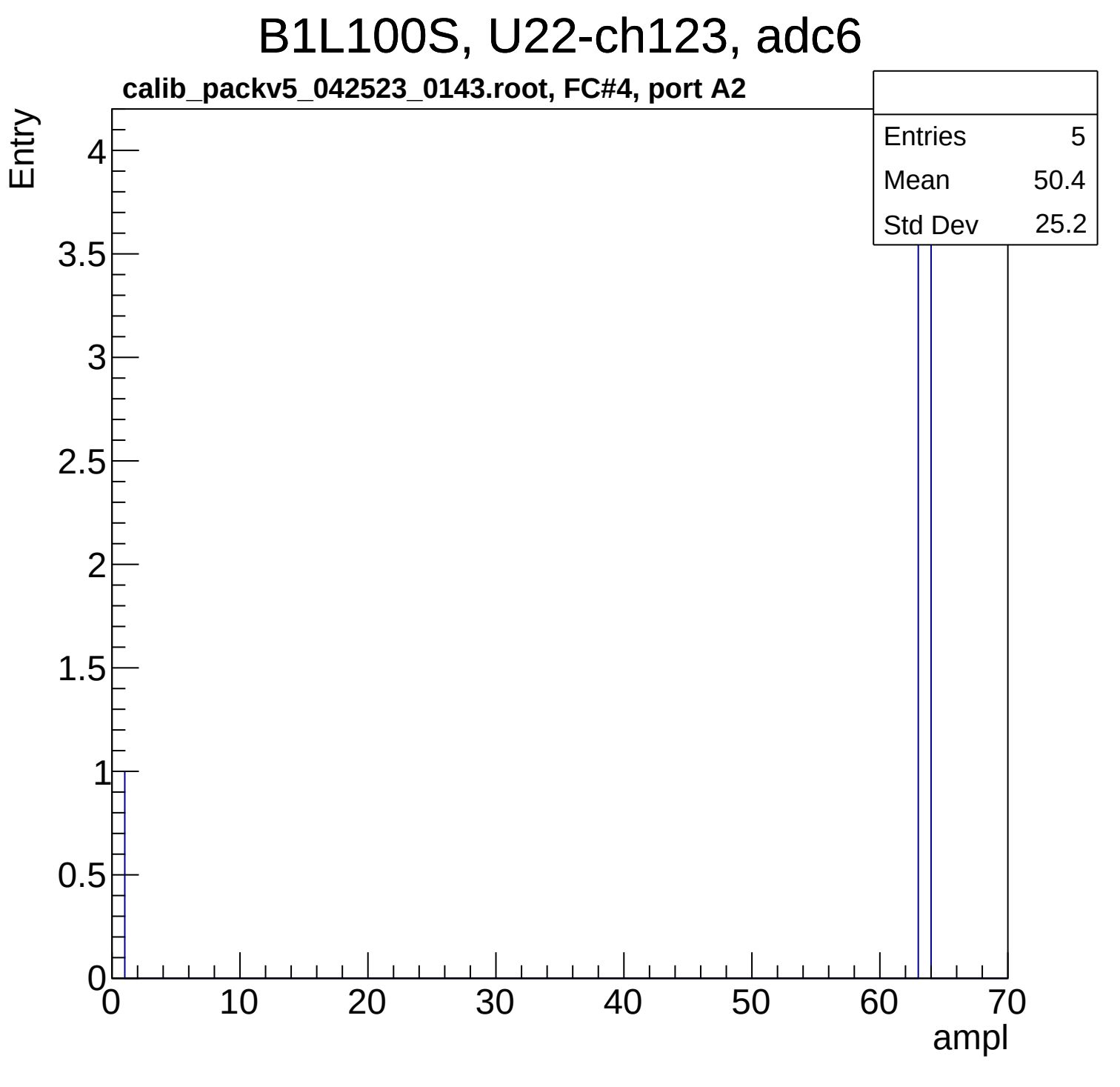
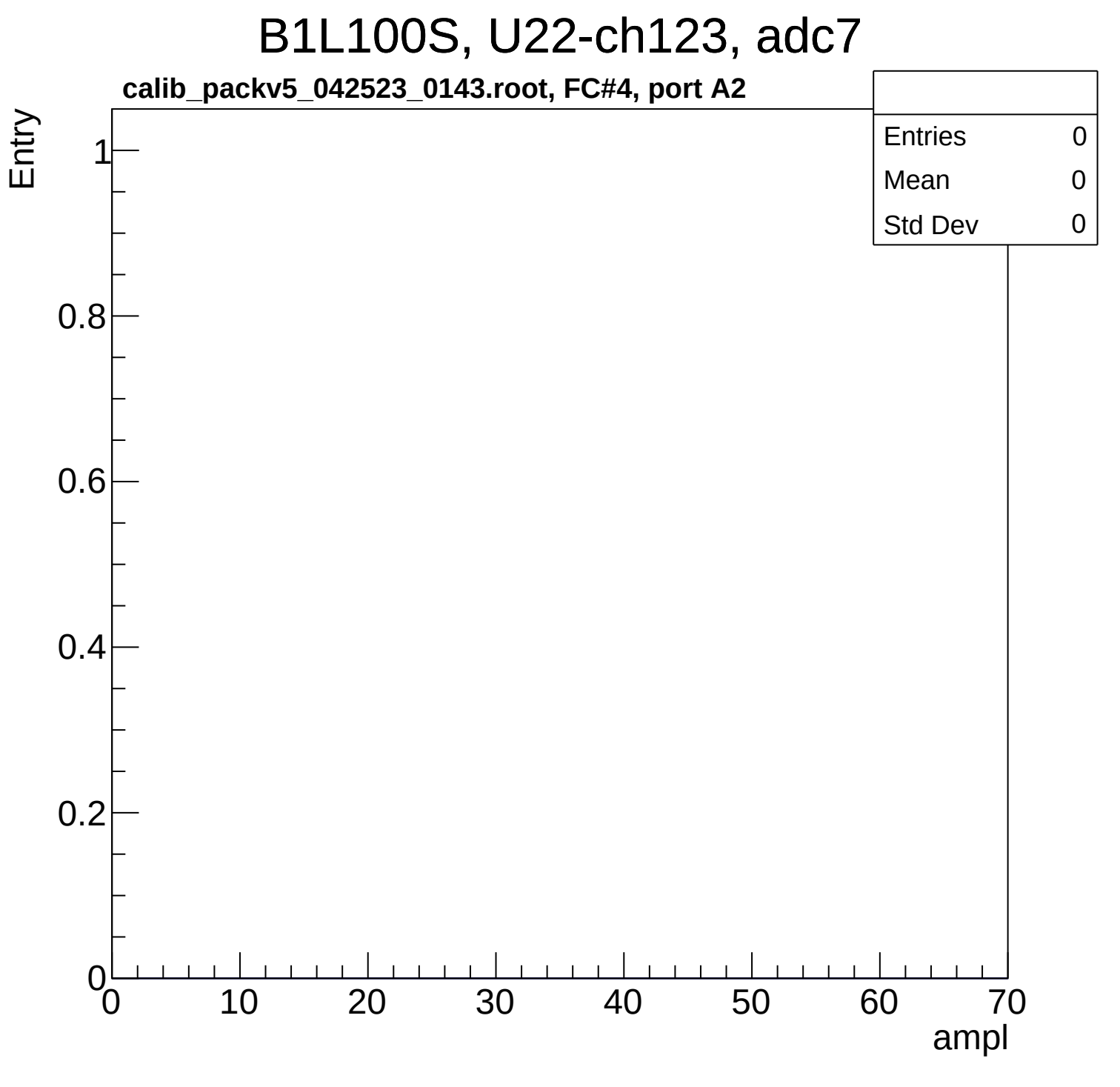
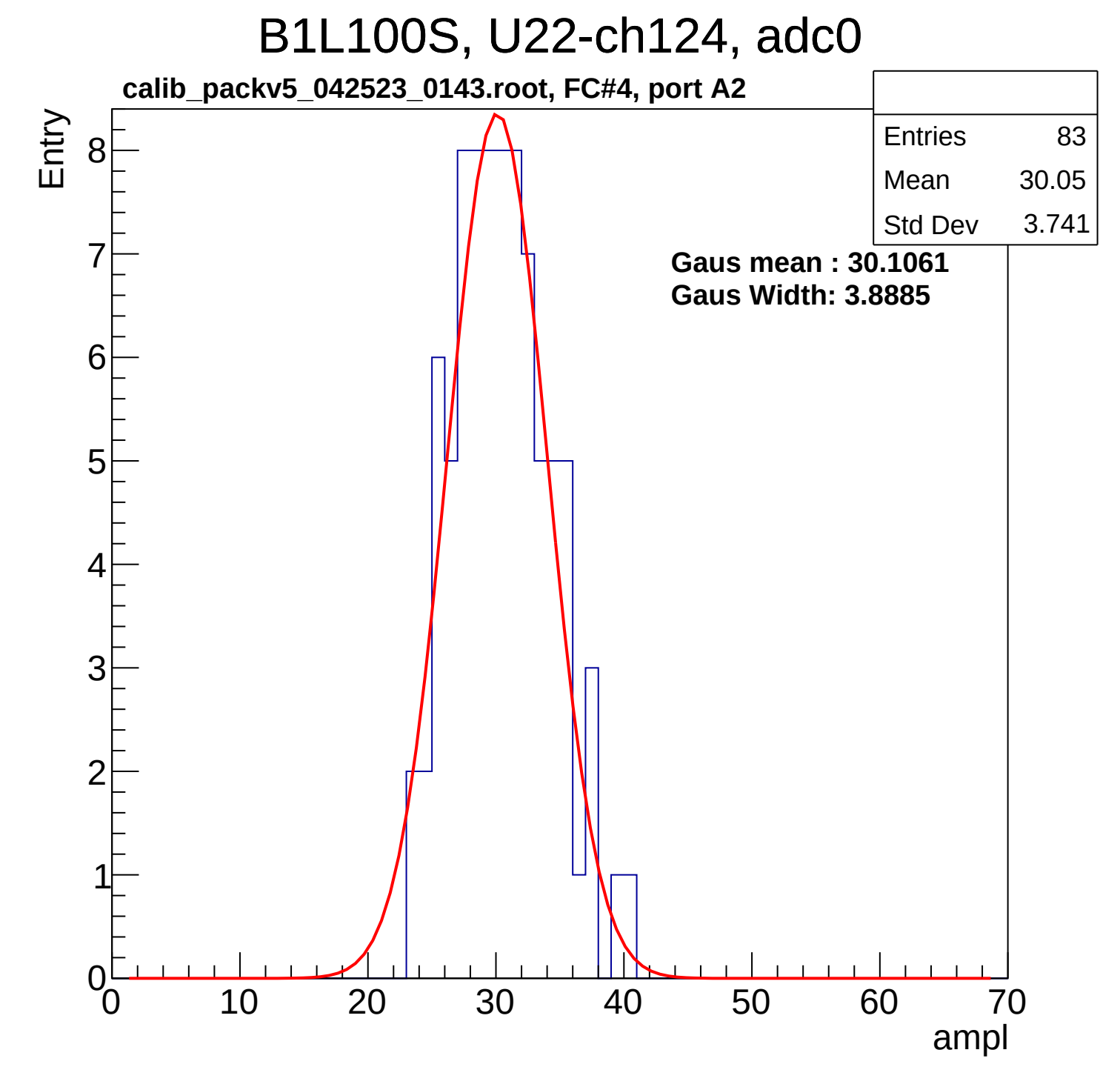

B1L100S, U22-ch123, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 50.4
Std Dev 25.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch123, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 0
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch124, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 83
Mean 30.05
Std Dev 3.741
Gaus mean : 30.1061
Gaus Width: 3.8885
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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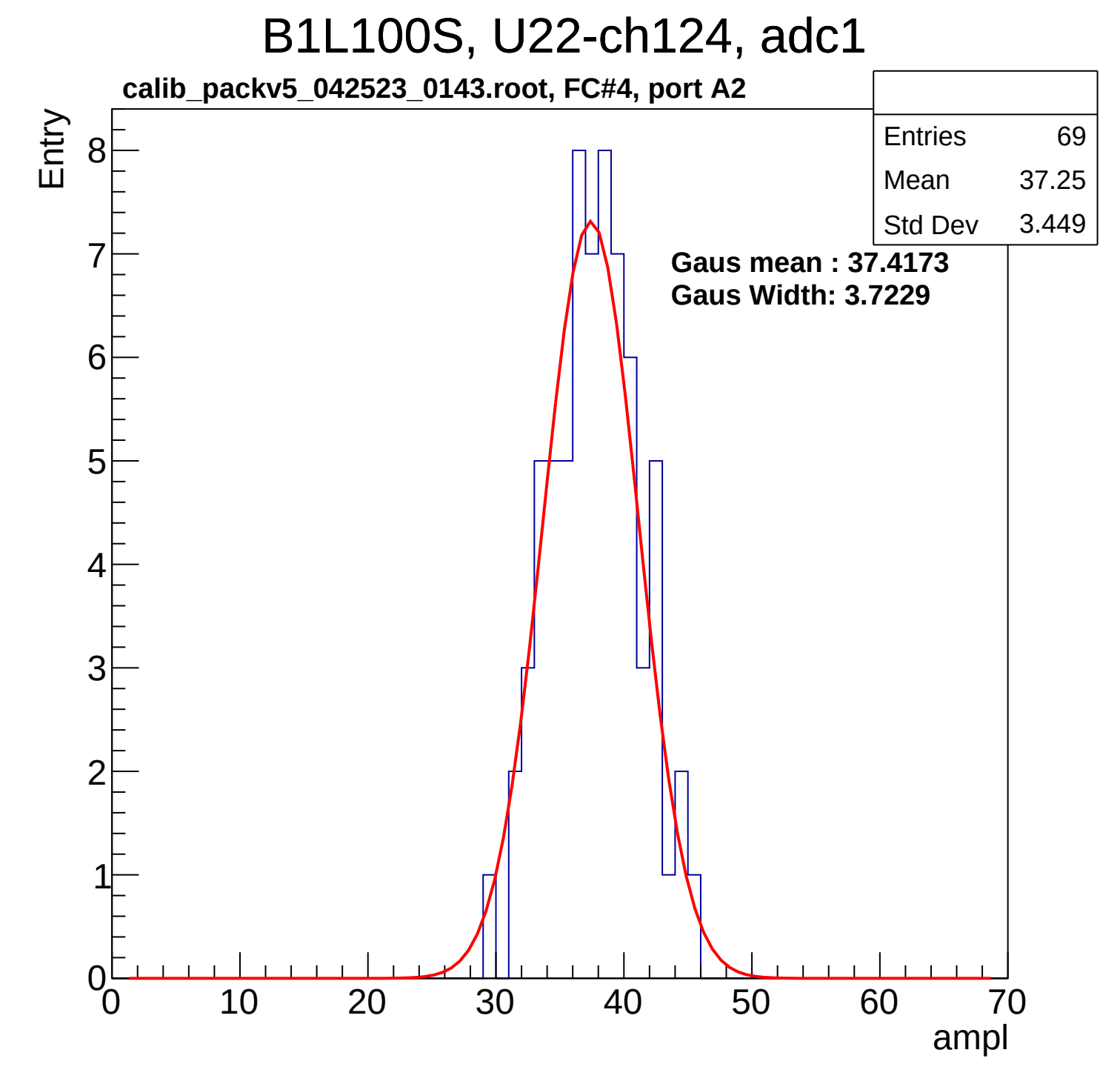
B1L100S, U22-ch124, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 69
Mean 37.25
Std Dev 3.449
Gaus mean : 37.4173
Gaus Width: 3.7229
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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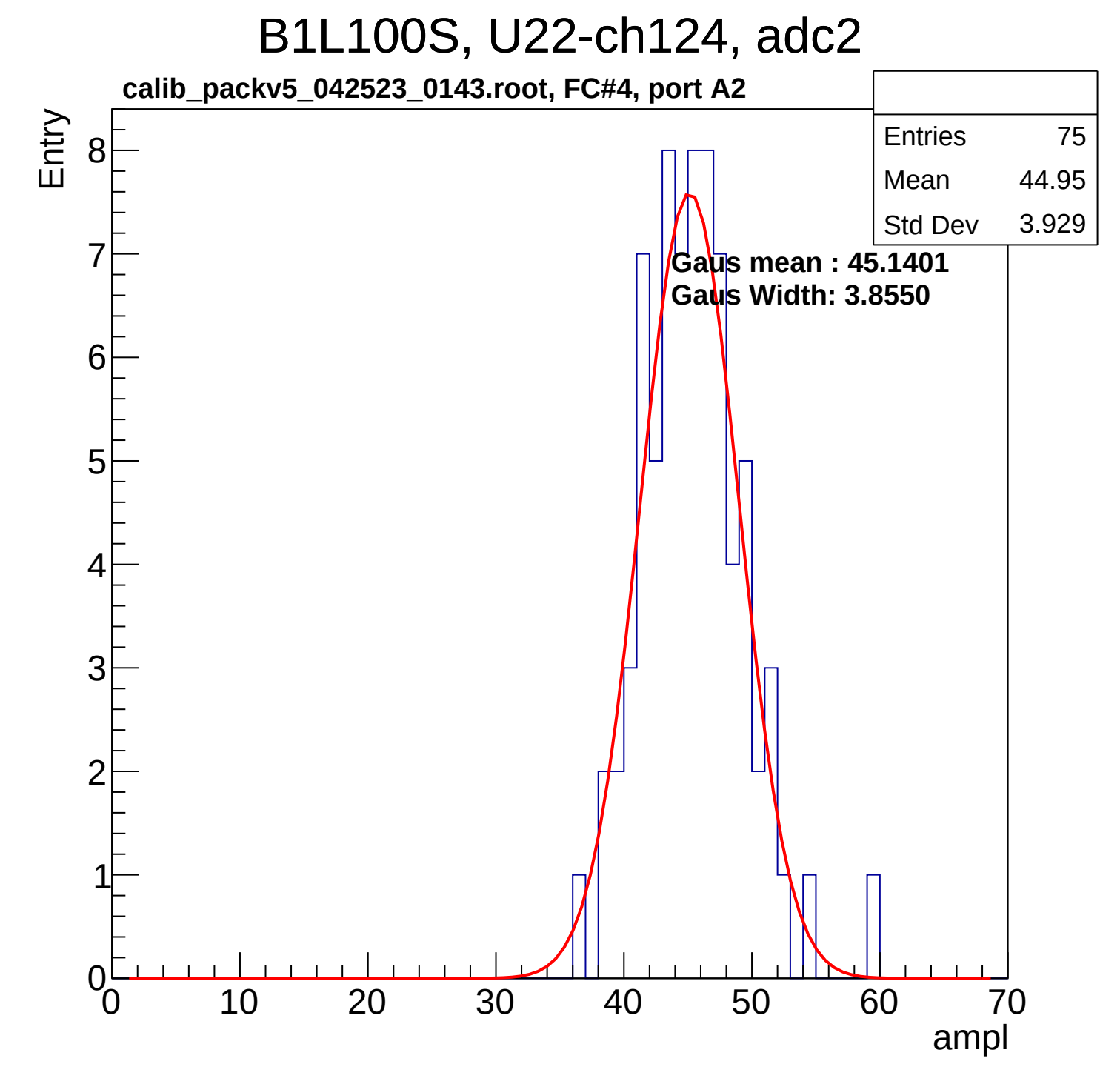
B1L100S, U22-ch124, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 44.95
Std Dev 3.929
Gaus mean : 45.1401
Gaus Width: 3.8550
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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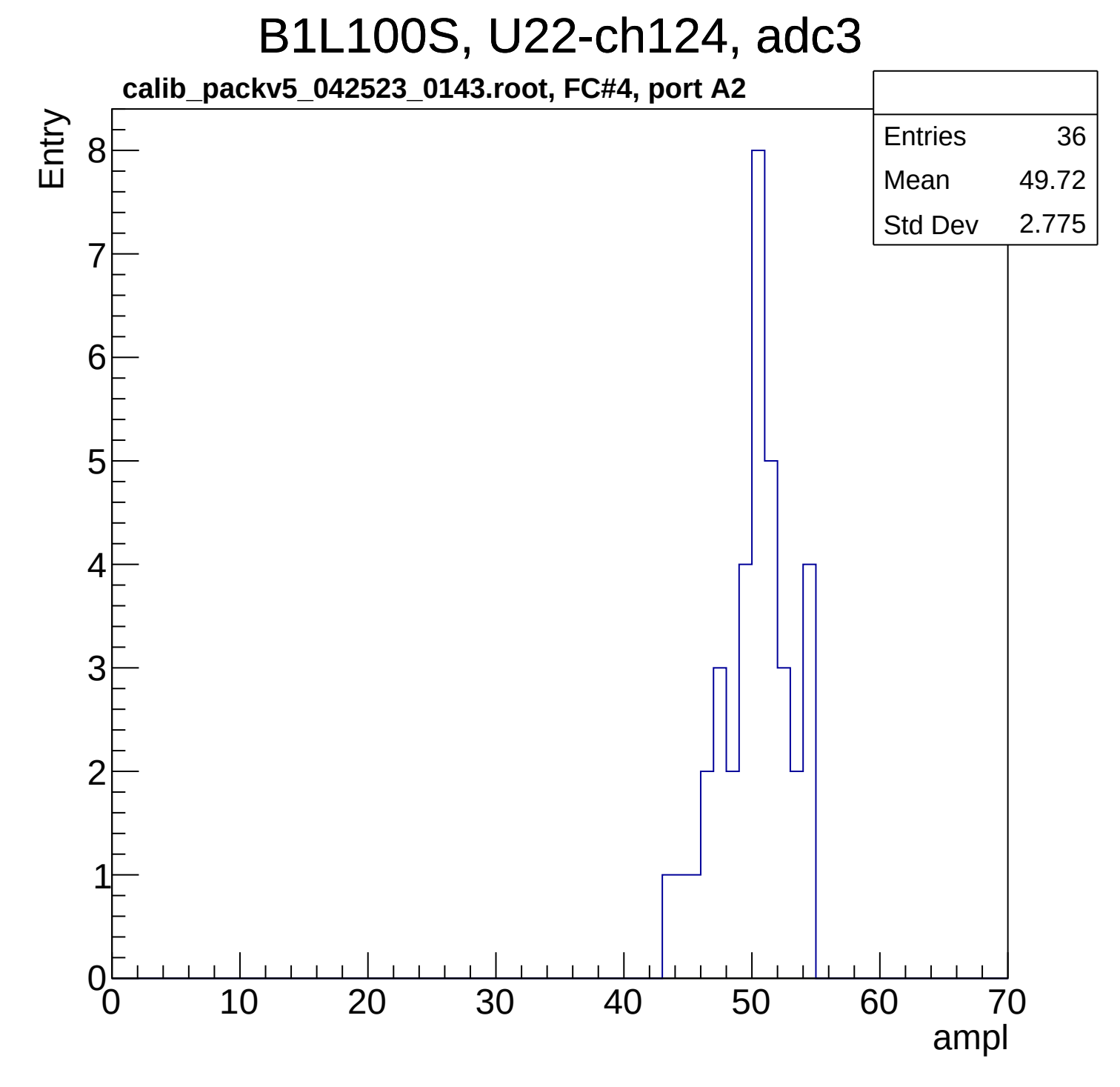
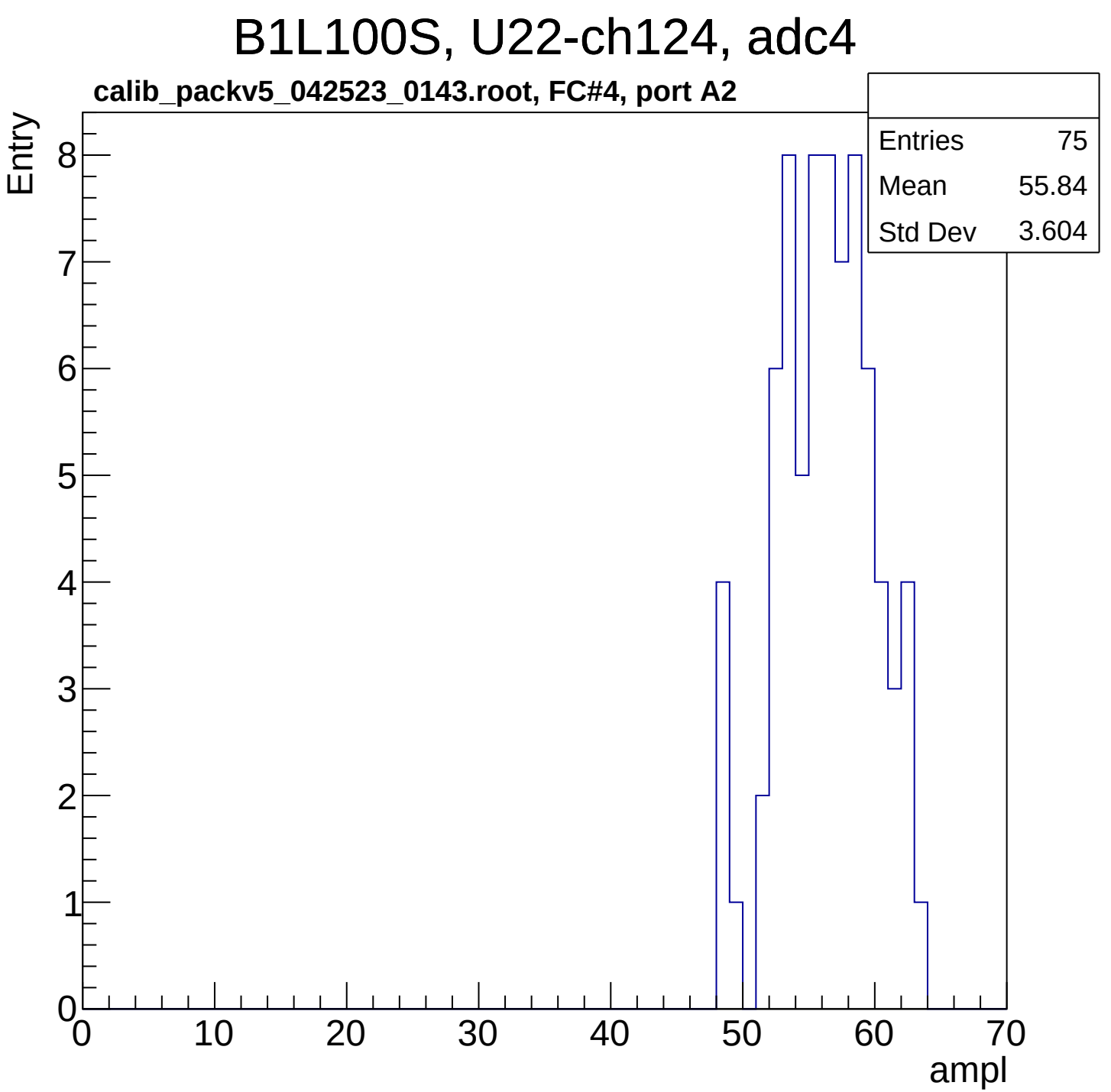
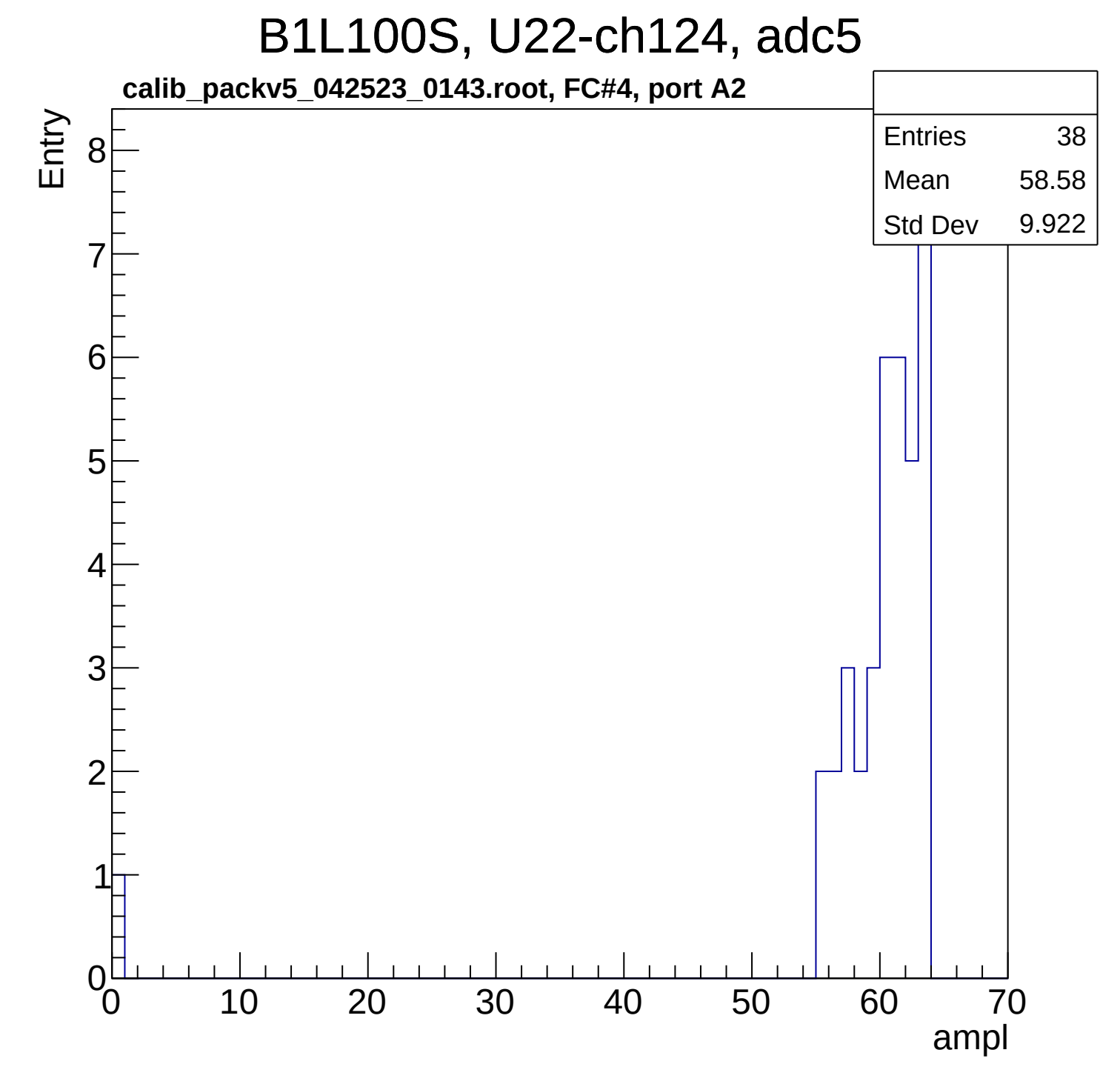
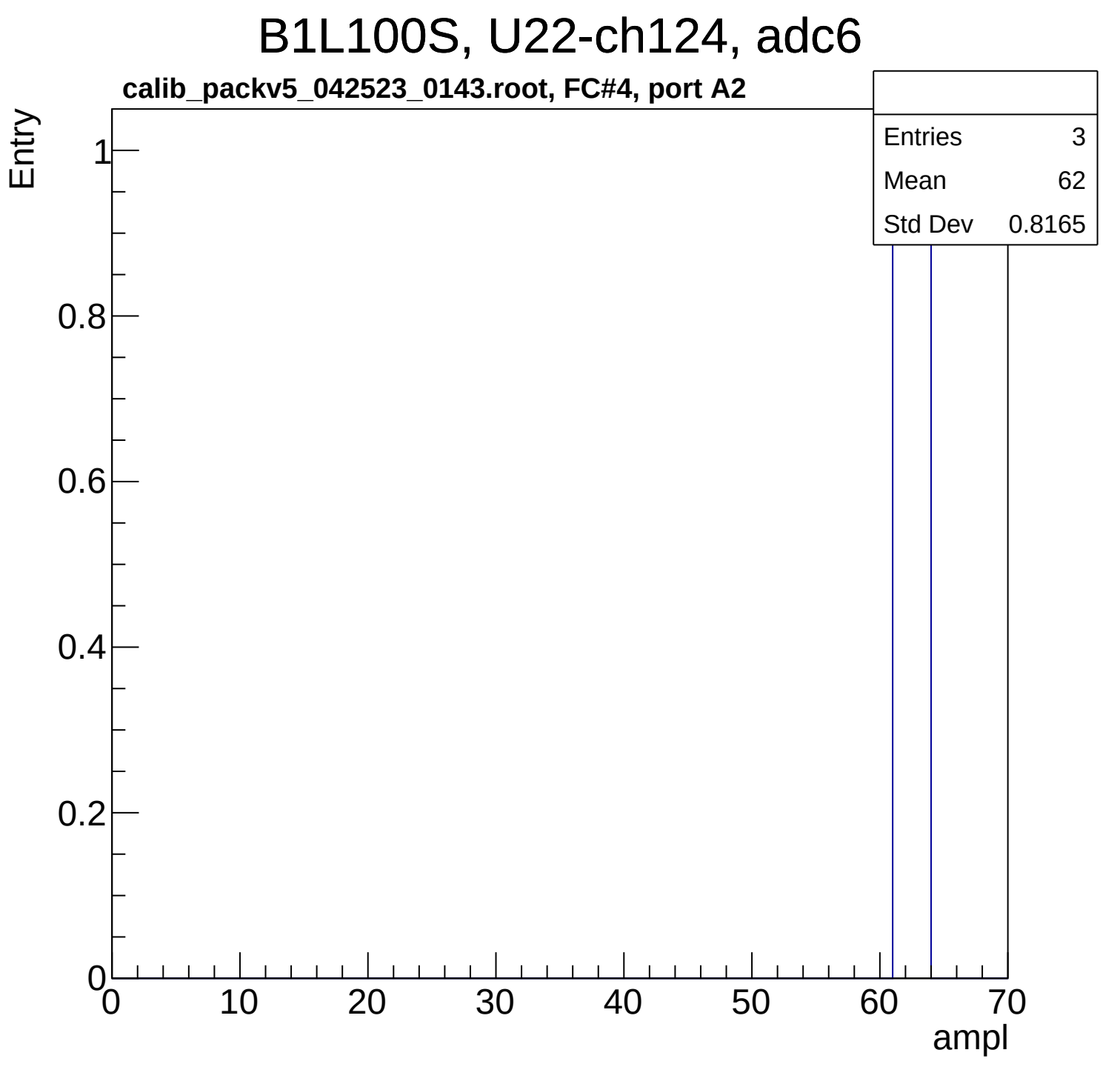
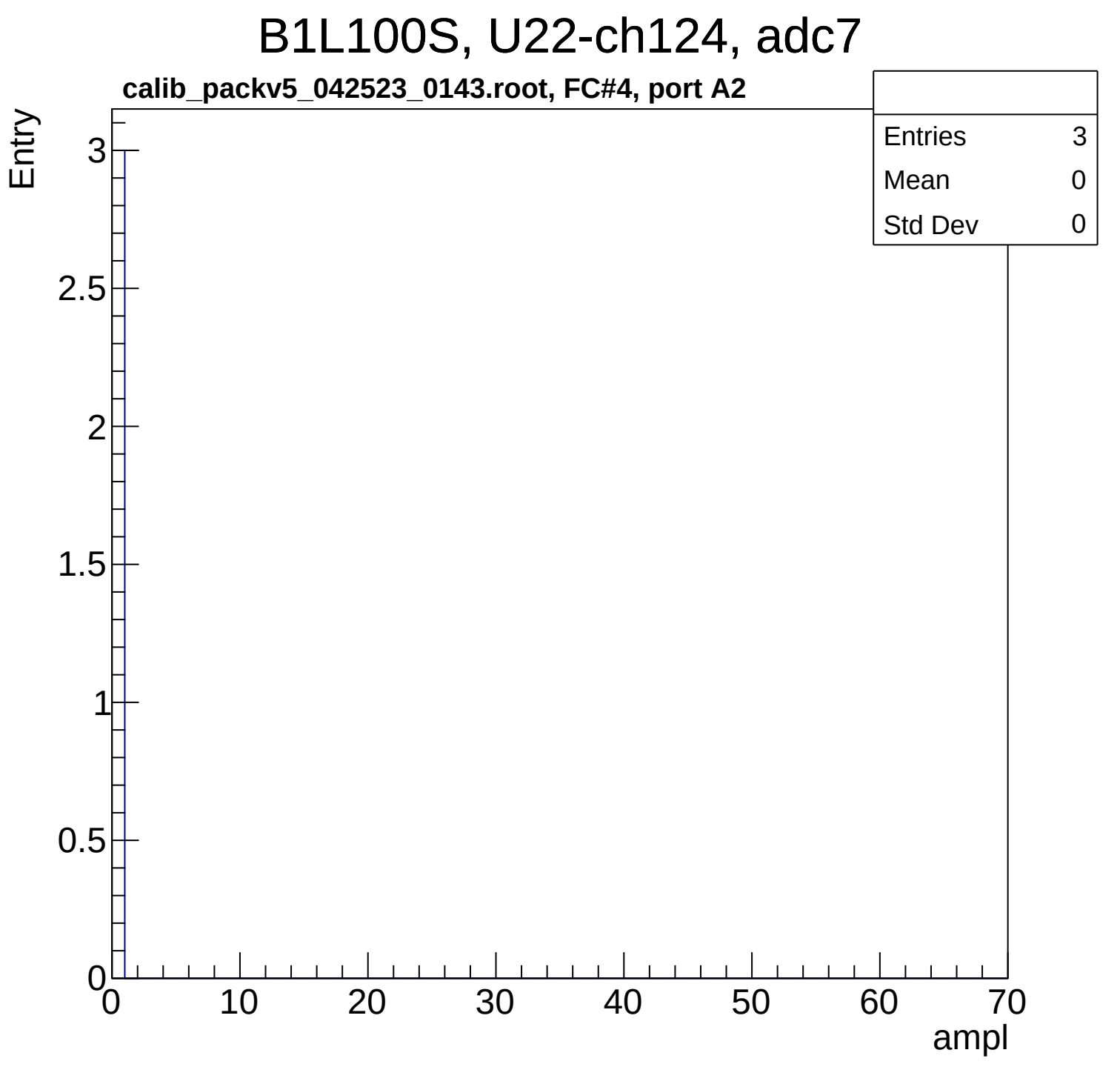
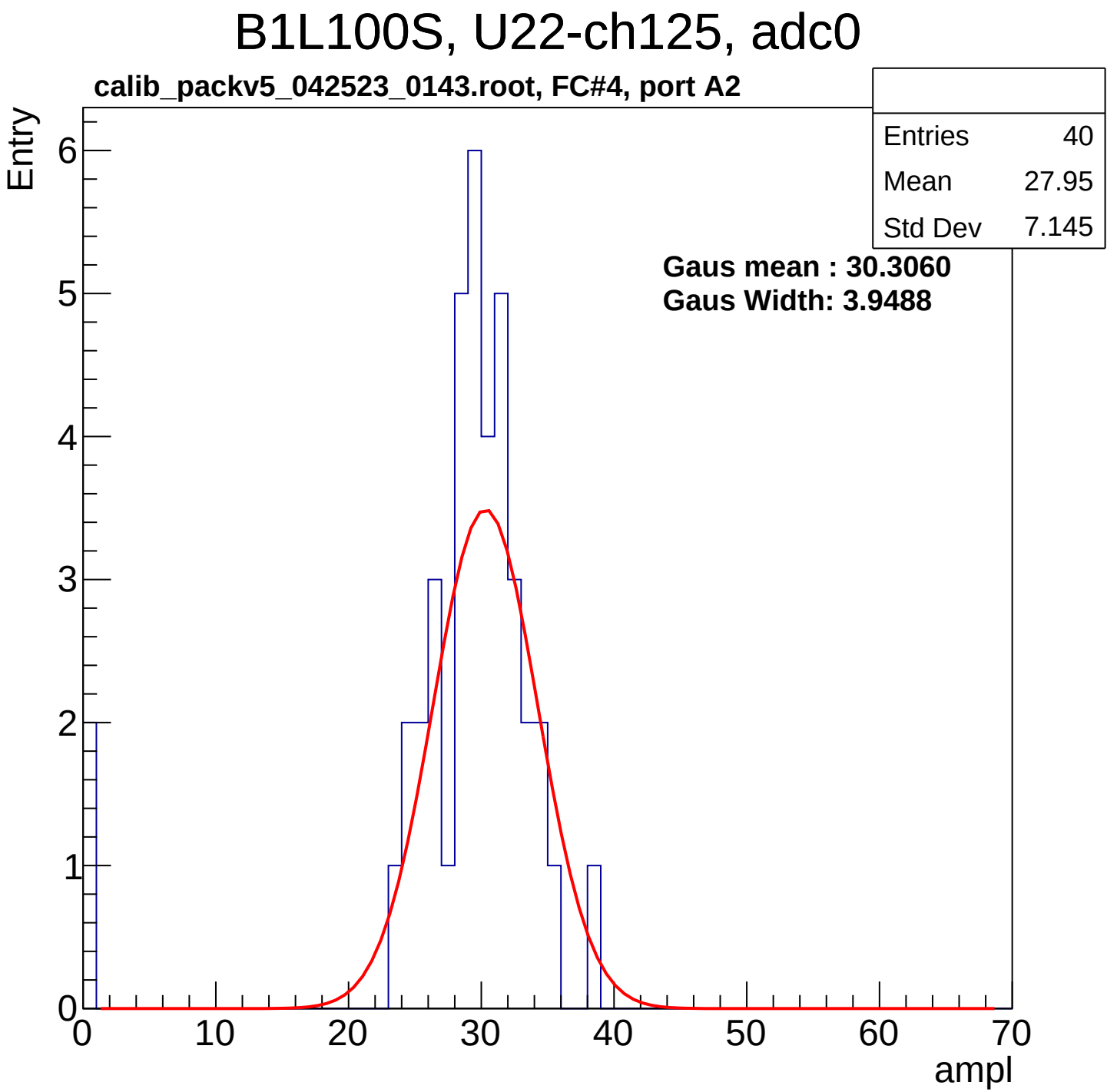

B1L100S, U22-ch124, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 36
Mean 49.72
Std Dev 2.775
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

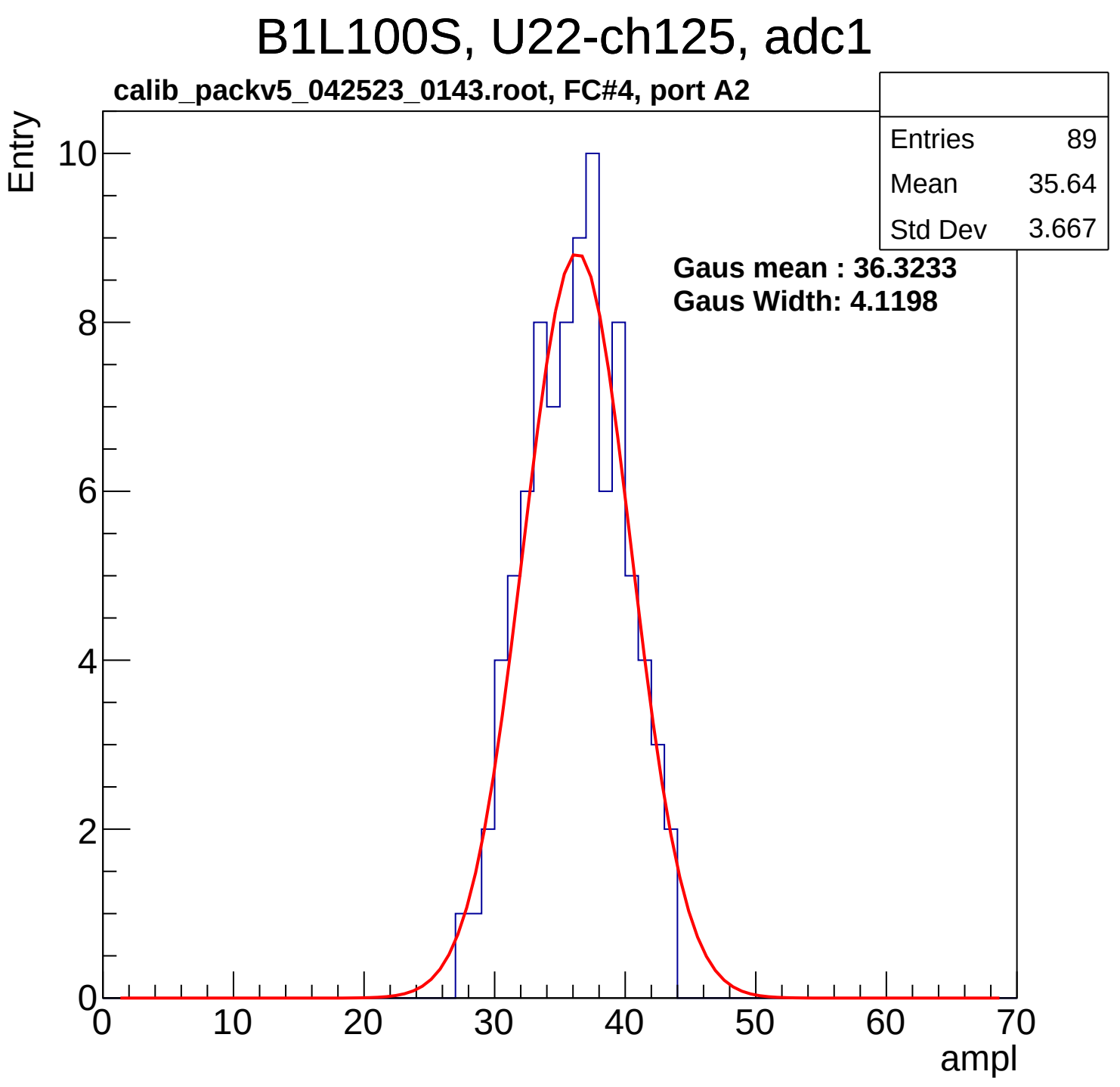

B1L100S, U22-ch124, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 75
Mean 55.84
Std Dev 3.604
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70

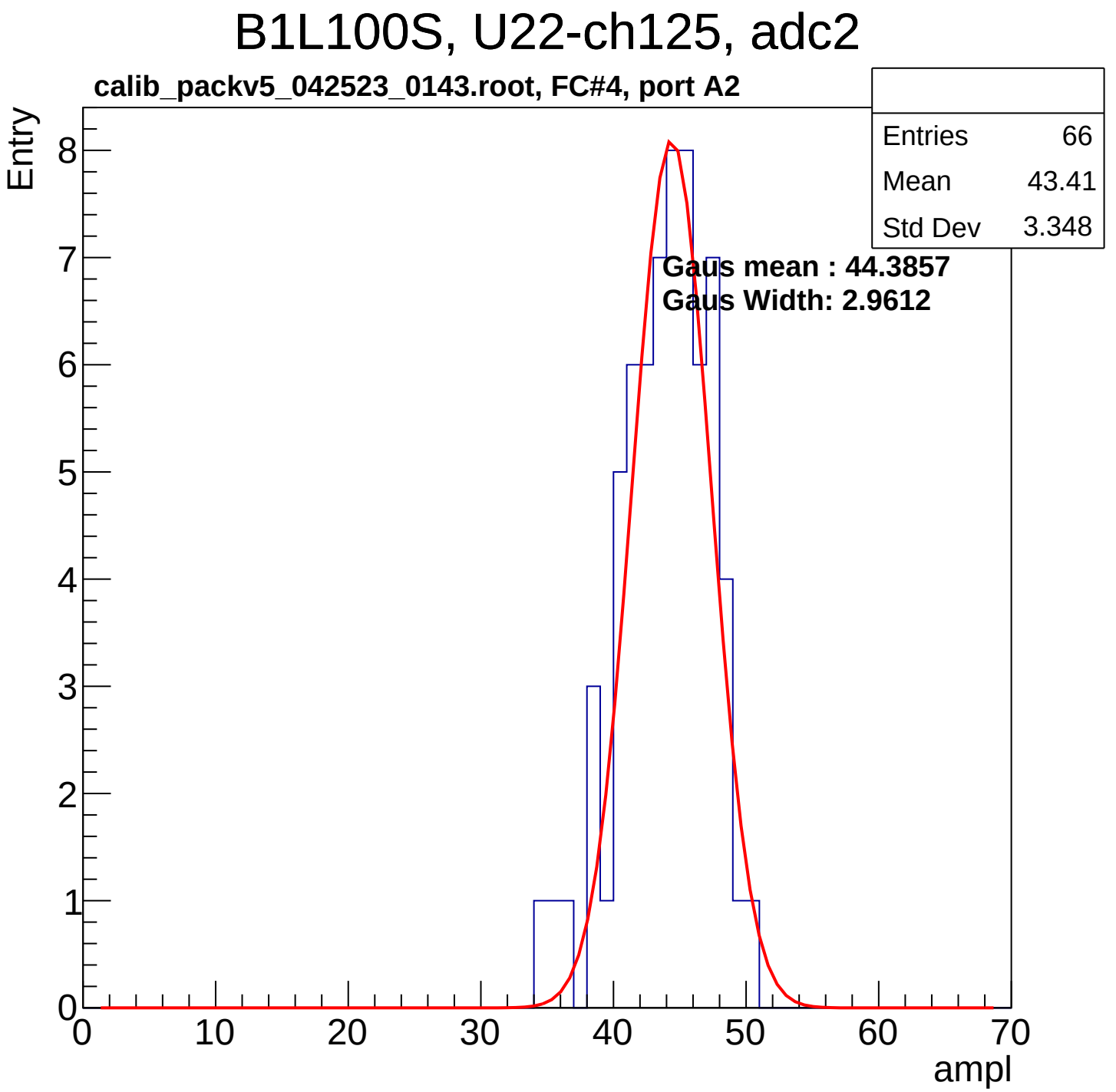

B1L100S, U22-ch124, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 38
Mean 58.58
Std Dev 9.922
Entry
ampl
0
1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
70


B1L100S, U22-ch124, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 62
Std Dev 0.8165
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch124, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 3
Mean 0
Std Dev 0
Entry
ampl
0
0.5
1
1.5
2
2.5
3
0
10
20
30
40
50
60
70


B1L100S, U22-ch125, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 40
Mean 27.95
Std Dev 7.145
Gaus mean : 30.3060
Gaus Width: 3.9488
Entry
ampl
0
1
2
3
4
5
6
0
10
20
30
40
50
60
70


B1L100S, U22-ch125, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 89
Mean 35.64
Std Dev 3.667
Gaus mean : 36.3233
Gaus Width: 4.1198
Entry
ampl
0
2
4
6
8
10
0
10
20
30
40
50
60
70


B1L100S, U22-ch125, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 43.41
Std Dev 3.348
Gaus mean : 44.3857
Gaus Width: 2.9612
Entry
ampl
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1
2
3
4
5
6
7
8
0
10
20
30
40
50
60
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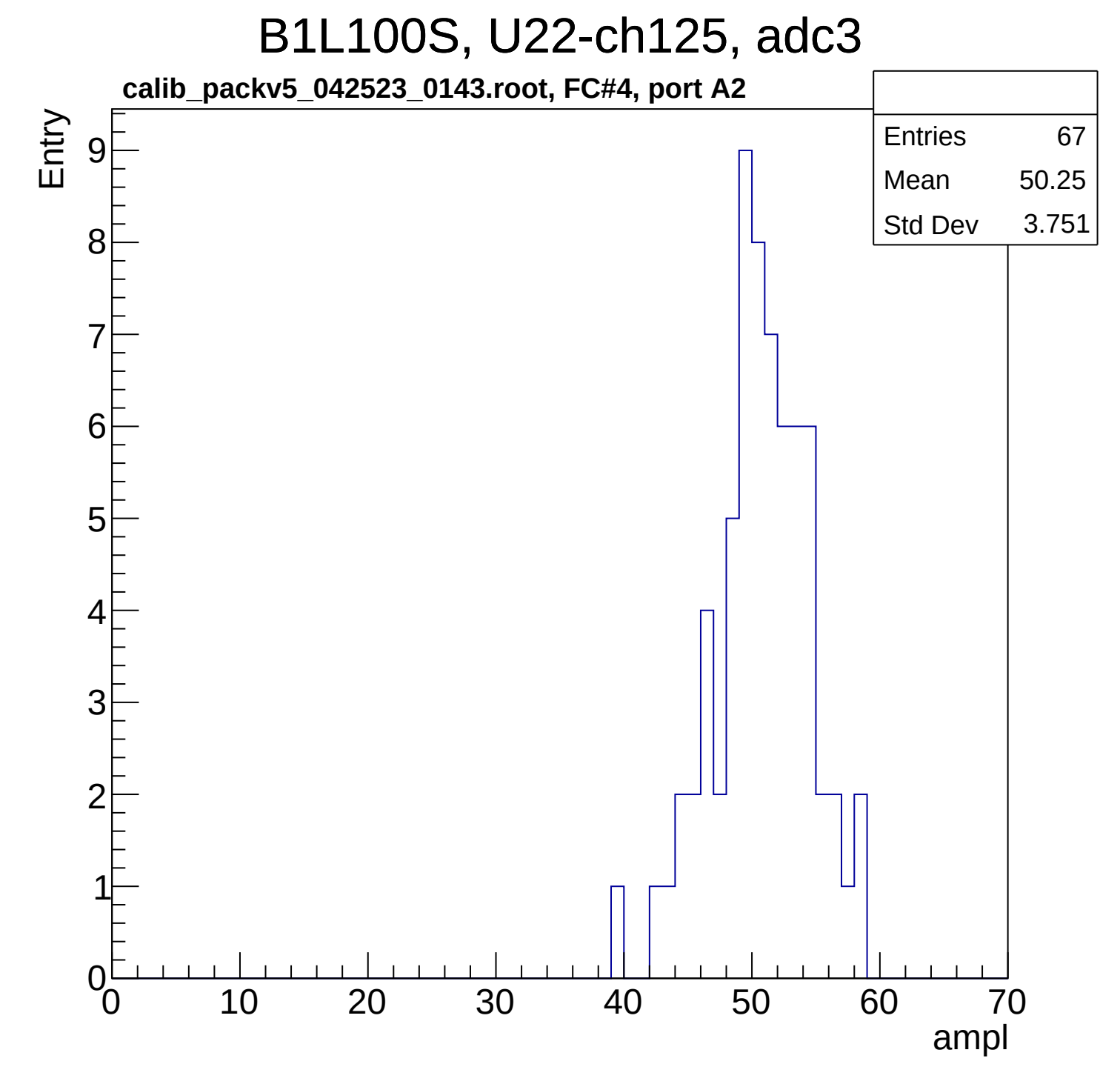
B1L100S, U22-ch125, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 67
Mean 50.25
Std Dev 3.751
Entry
ampl
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1
2
3
4
5
6
7
8
9
0
10
20
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40
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60
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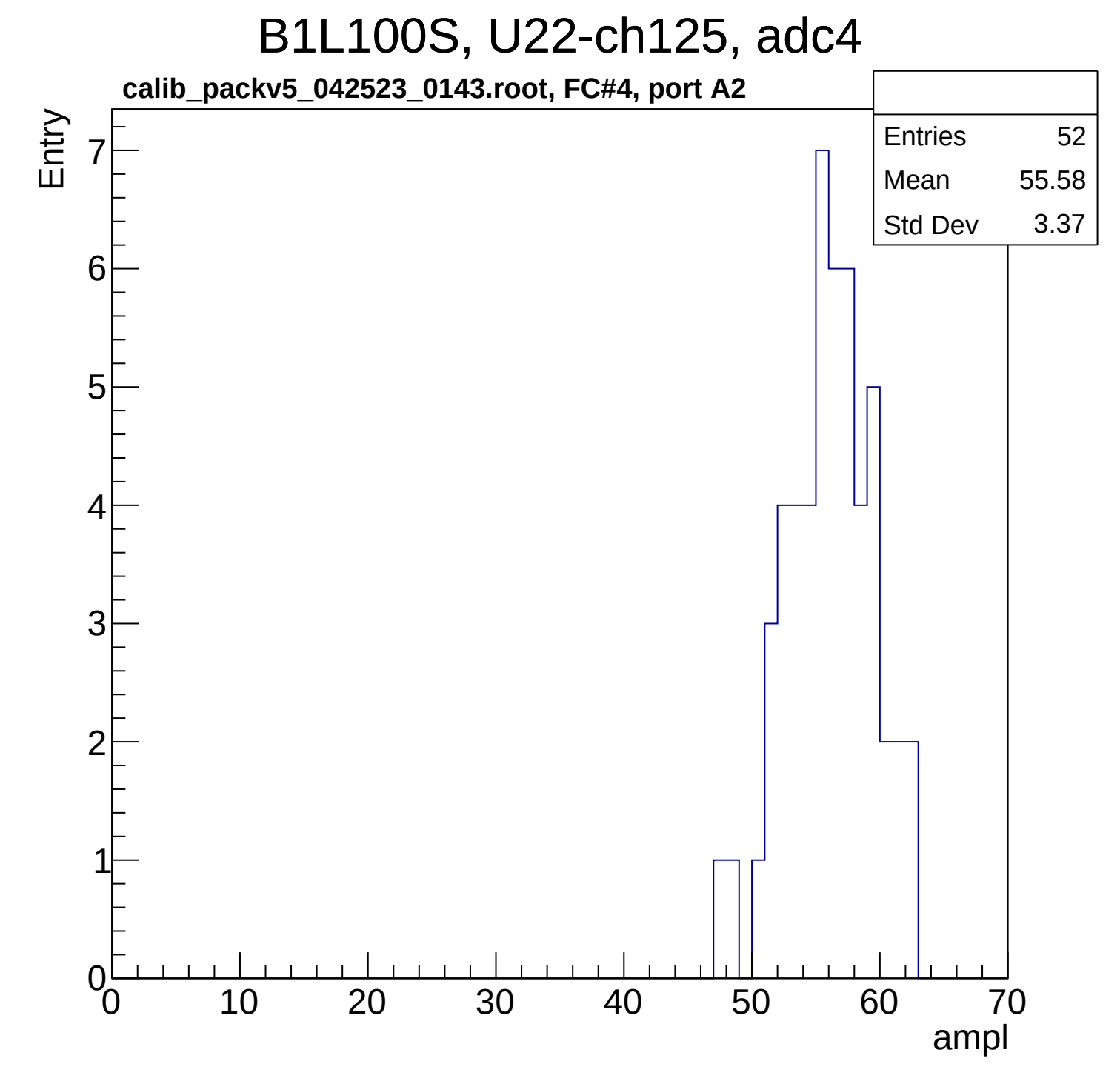
B1L100S, U22-ch125, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 55.58
Std Dev 3.37
Entry
ampl
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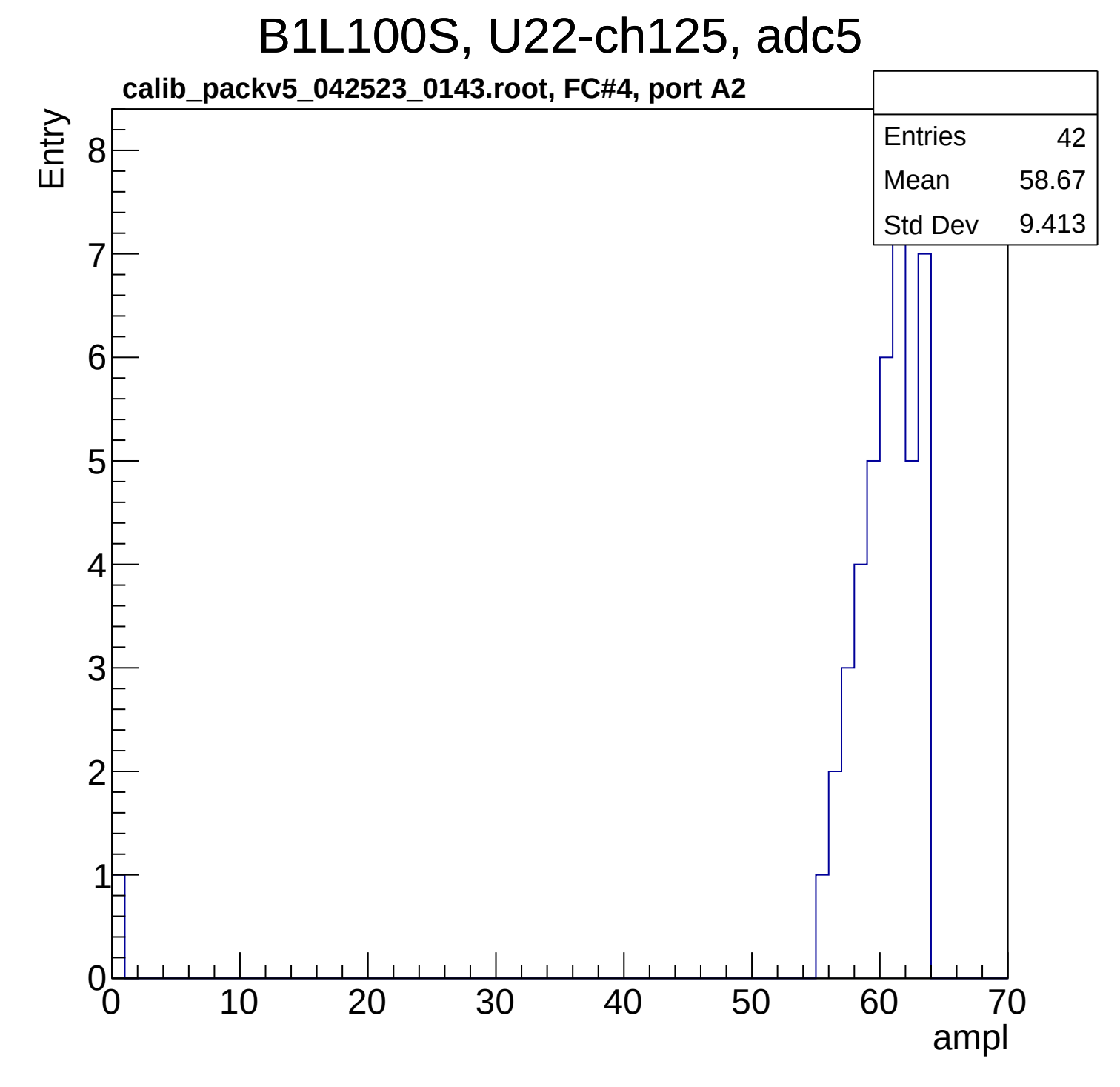
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch125, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 42
Mean 58.67
Std Dev 9.413
Entry
ampl
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2
3
4
5
6
7
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60
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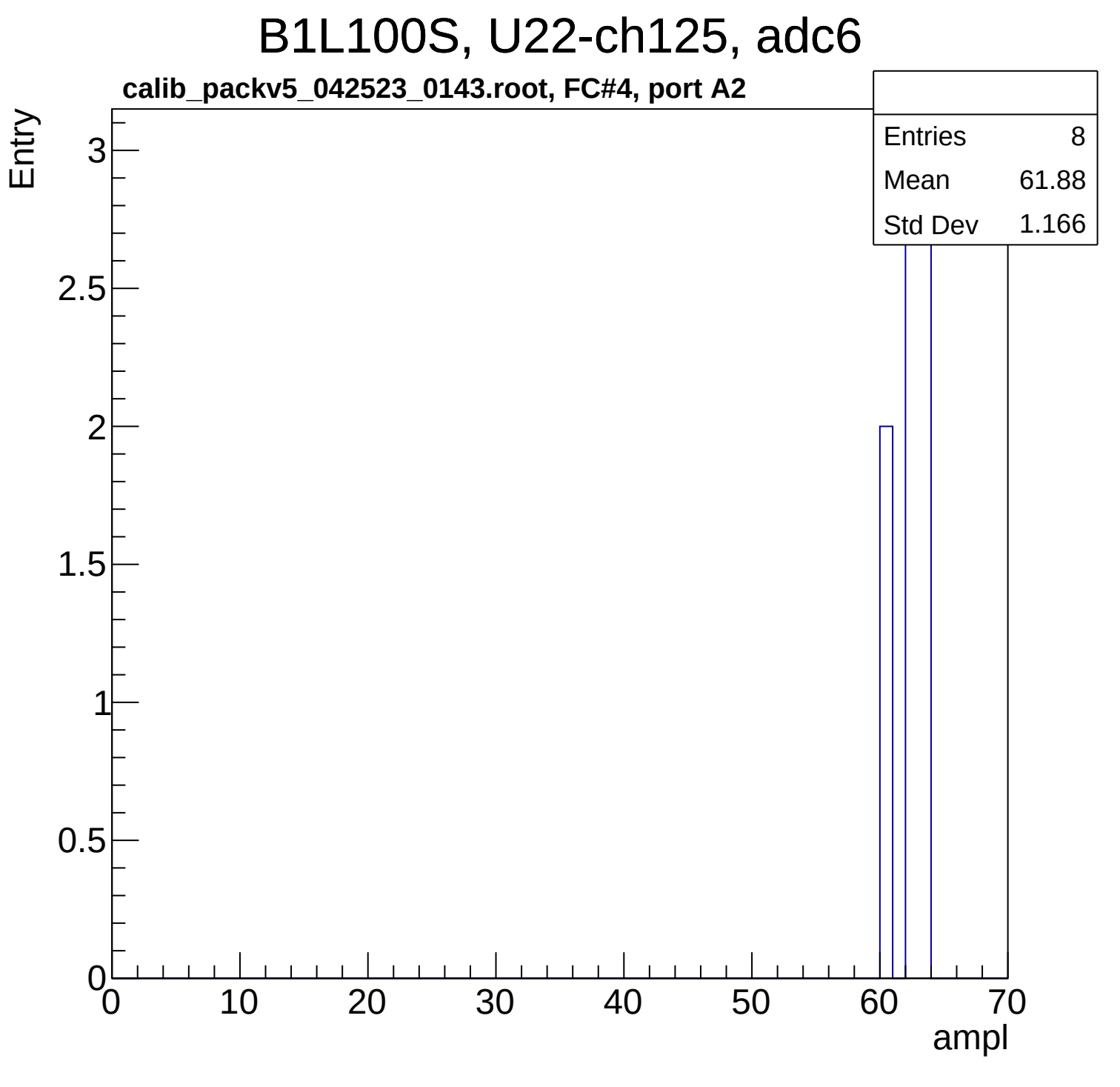
B1L100S, U22-ch125, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 8
Mean 61.88
Std Dev 1.166
Entry
ampl
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0.5
1
1.5
2
2.5
3
0
10
20
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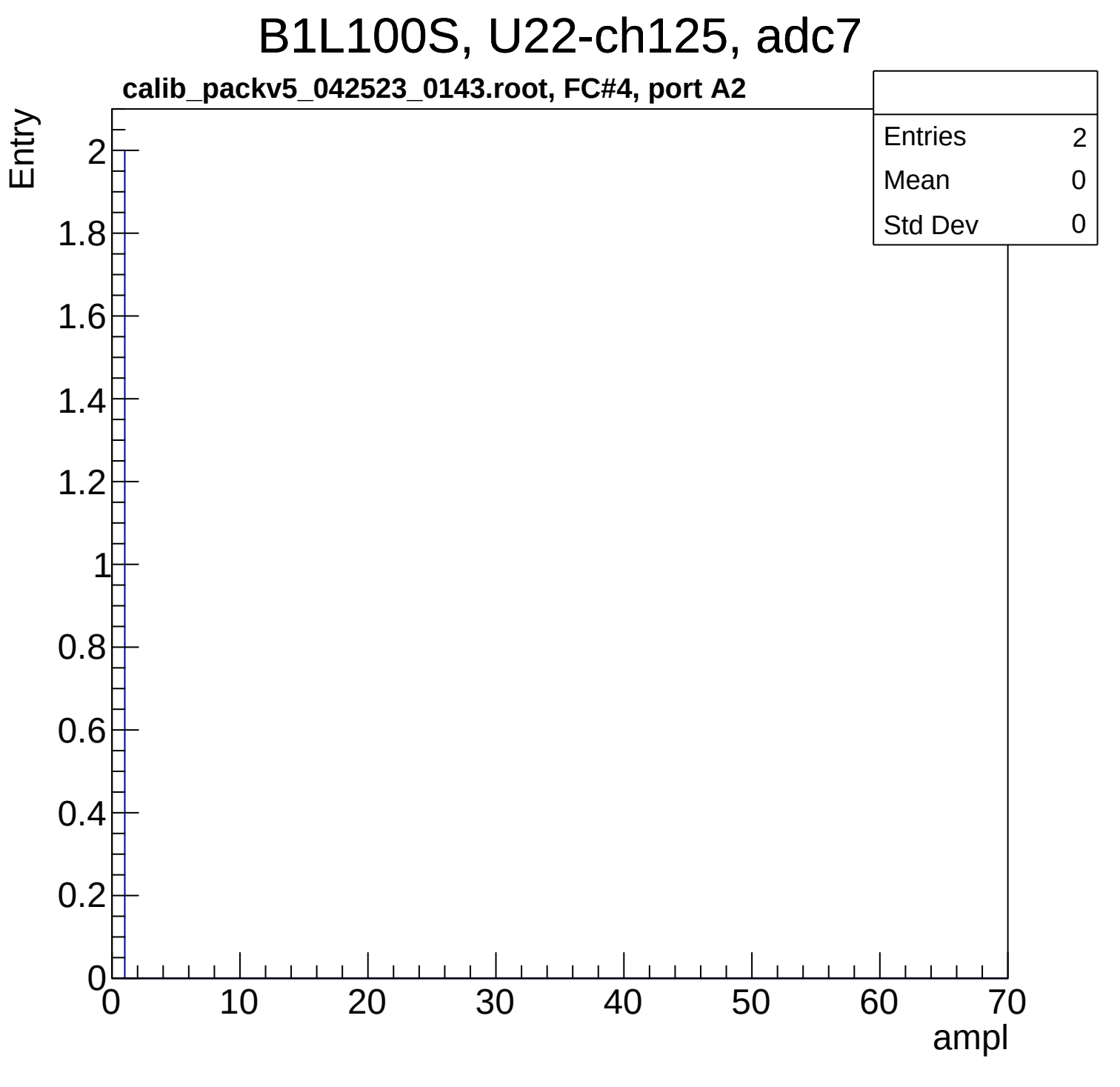
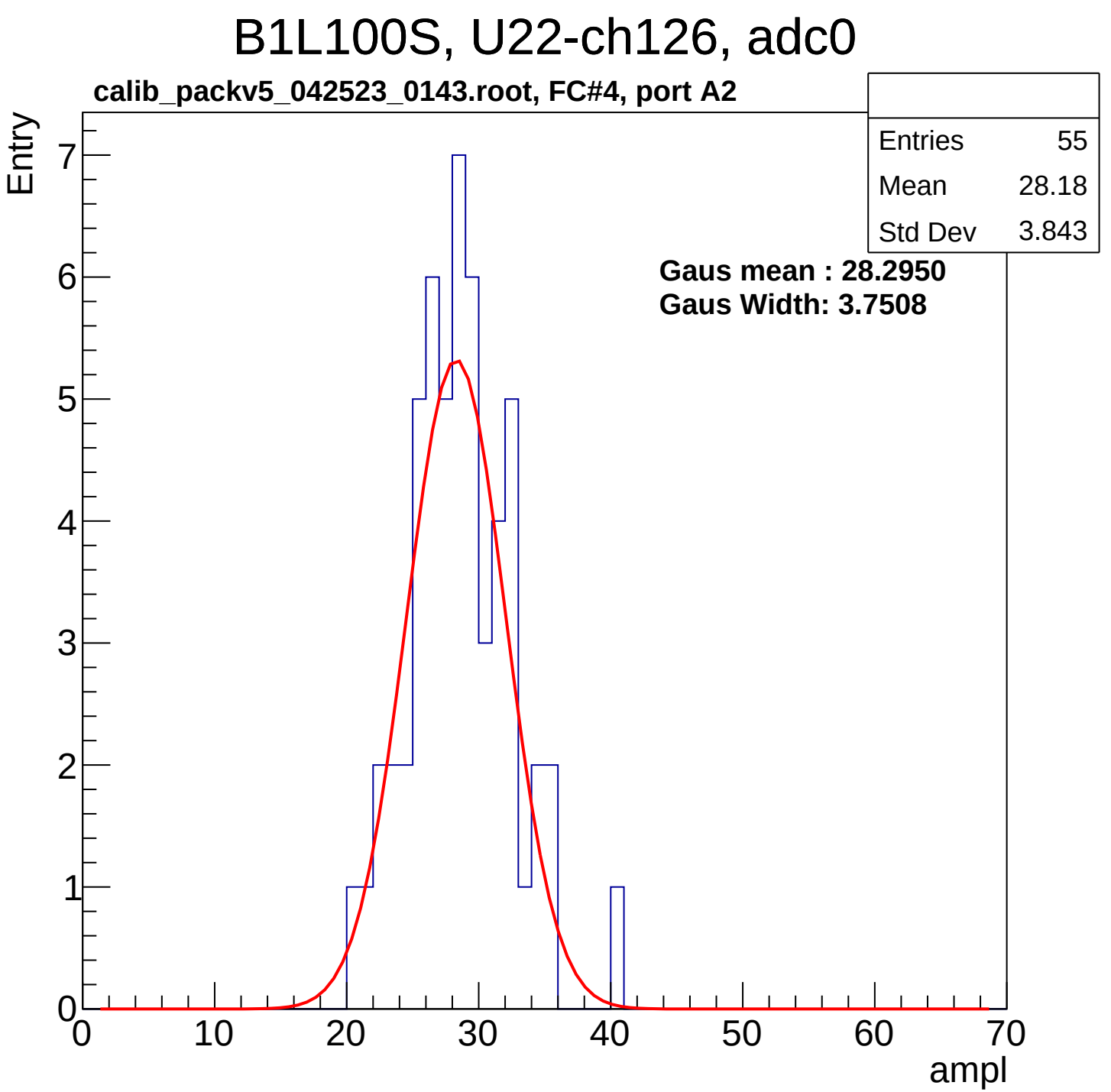
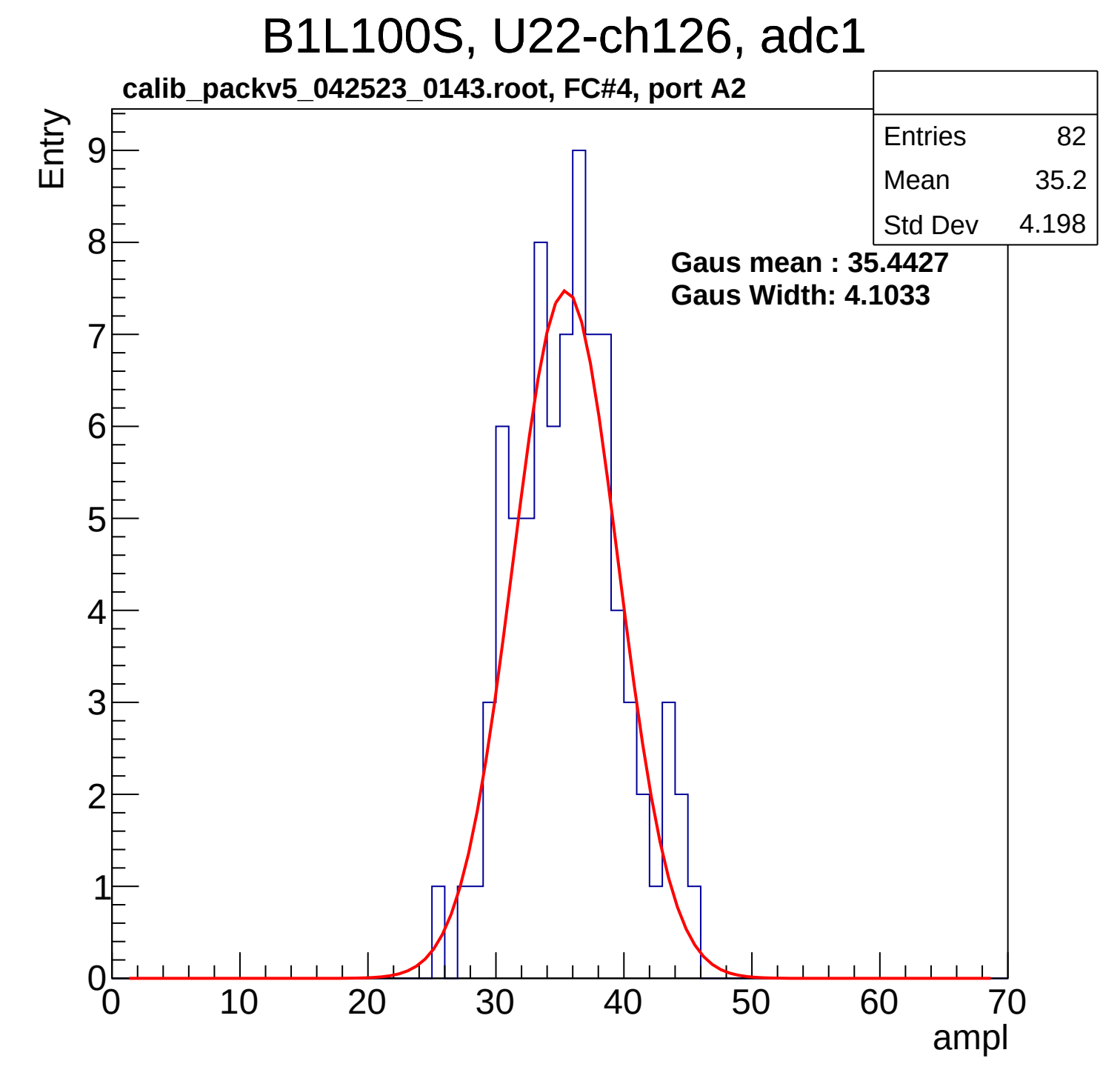
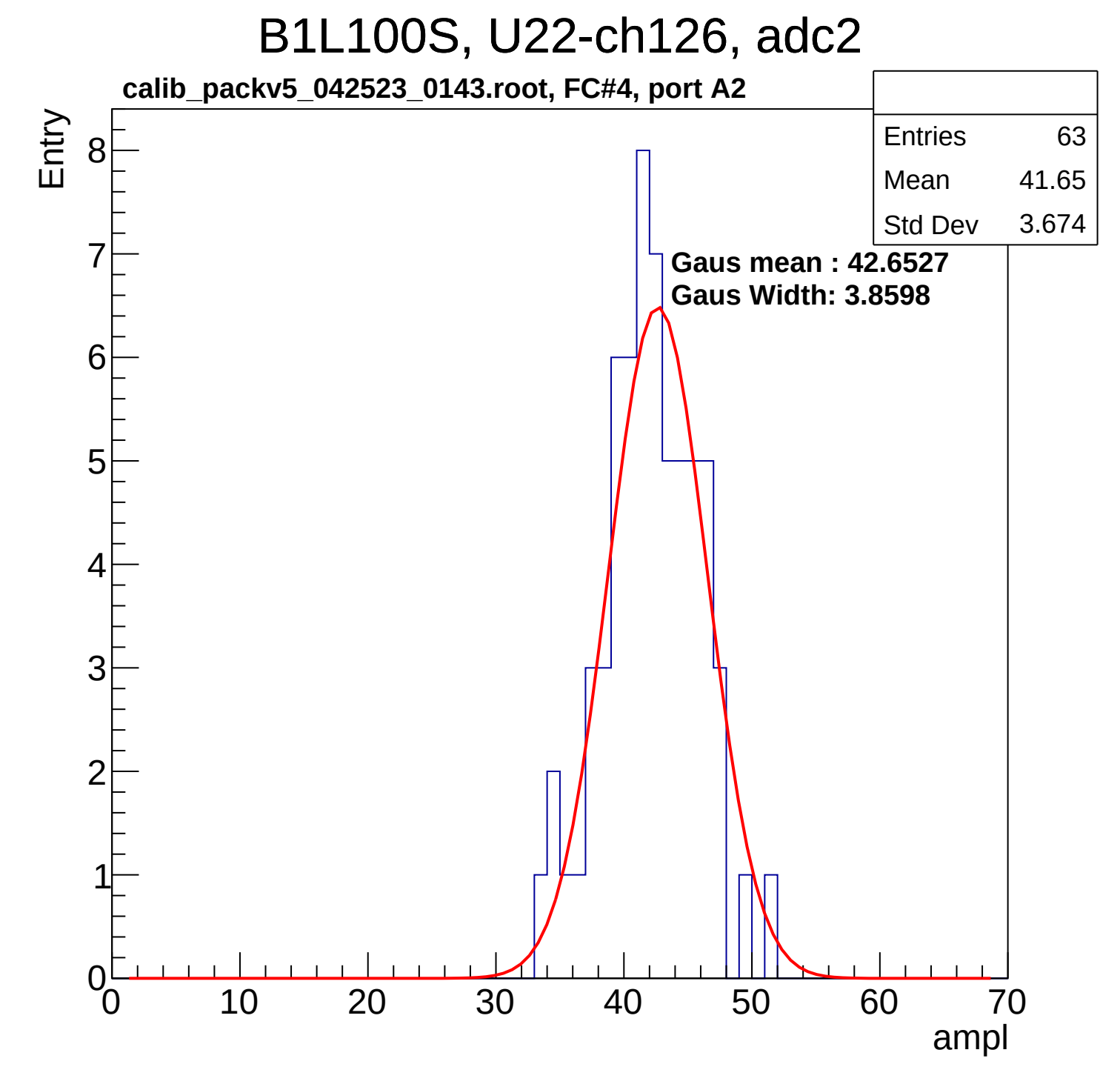
40
50
60
70


B1L100S, U22-ch125, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 2
Mean 0
Std Dev 0
Entry
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch126, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 55
Mean 28.18
Std Dev 3.843
Gaus mean : 28.2950
Gaus Width: 3.7508
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch126, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 82
Mean 35.2
Std Dev 4.198
Gaus mean : 35.4427
Gaus Width: 4.1033
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch126, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 41.65
Std Dev 3.674
Gaus mean : 42.6527
Gaus Width: 3.8598
Entry
ampl
0
1
2
3
4
5
6
7
8
0
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20
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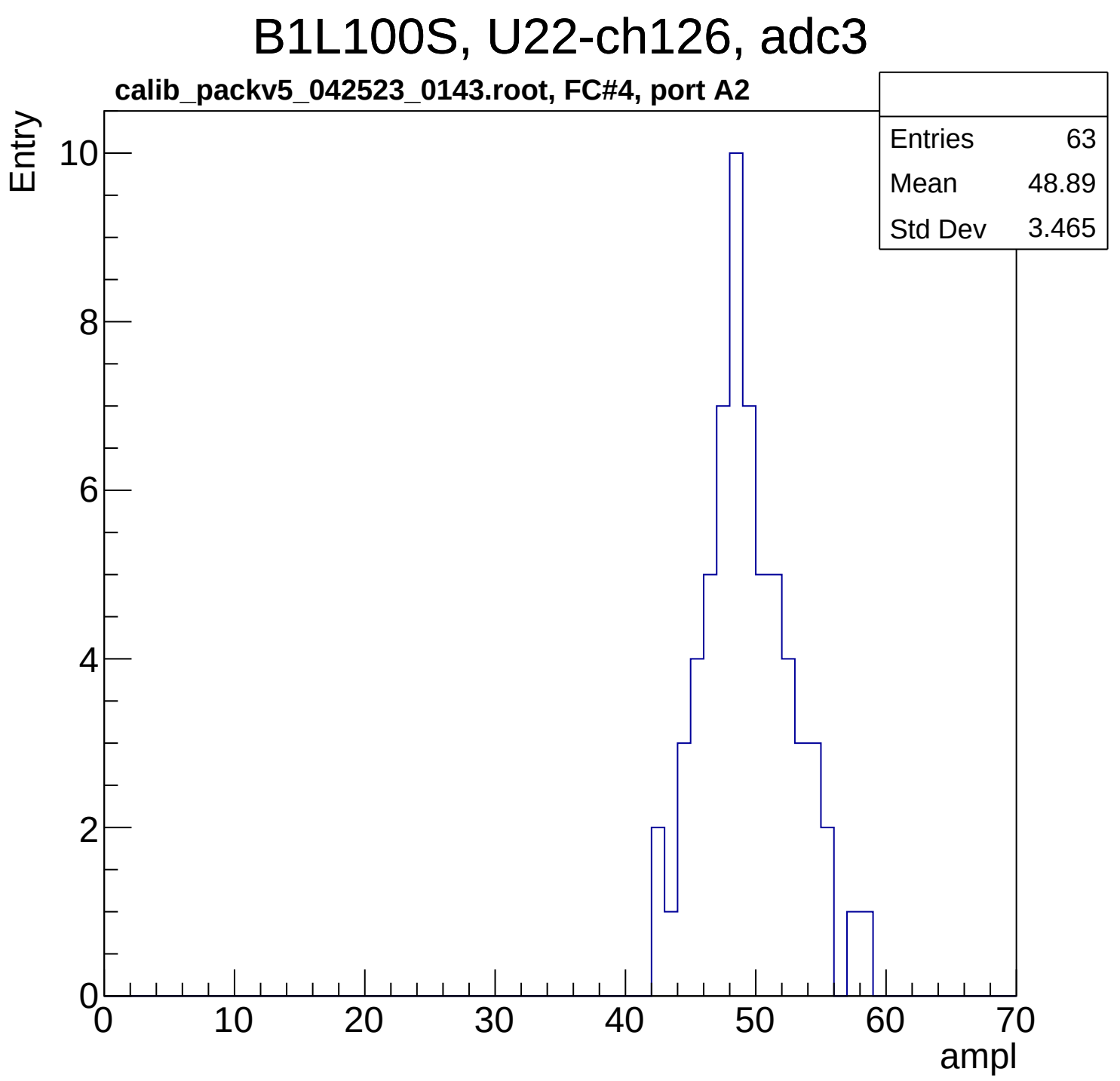
40
50
60
70


B1L100S, U22-ch126, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 63
Mean 48.89
Std Dev 3.465
Entry
ampl
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2
4
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10
0
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20
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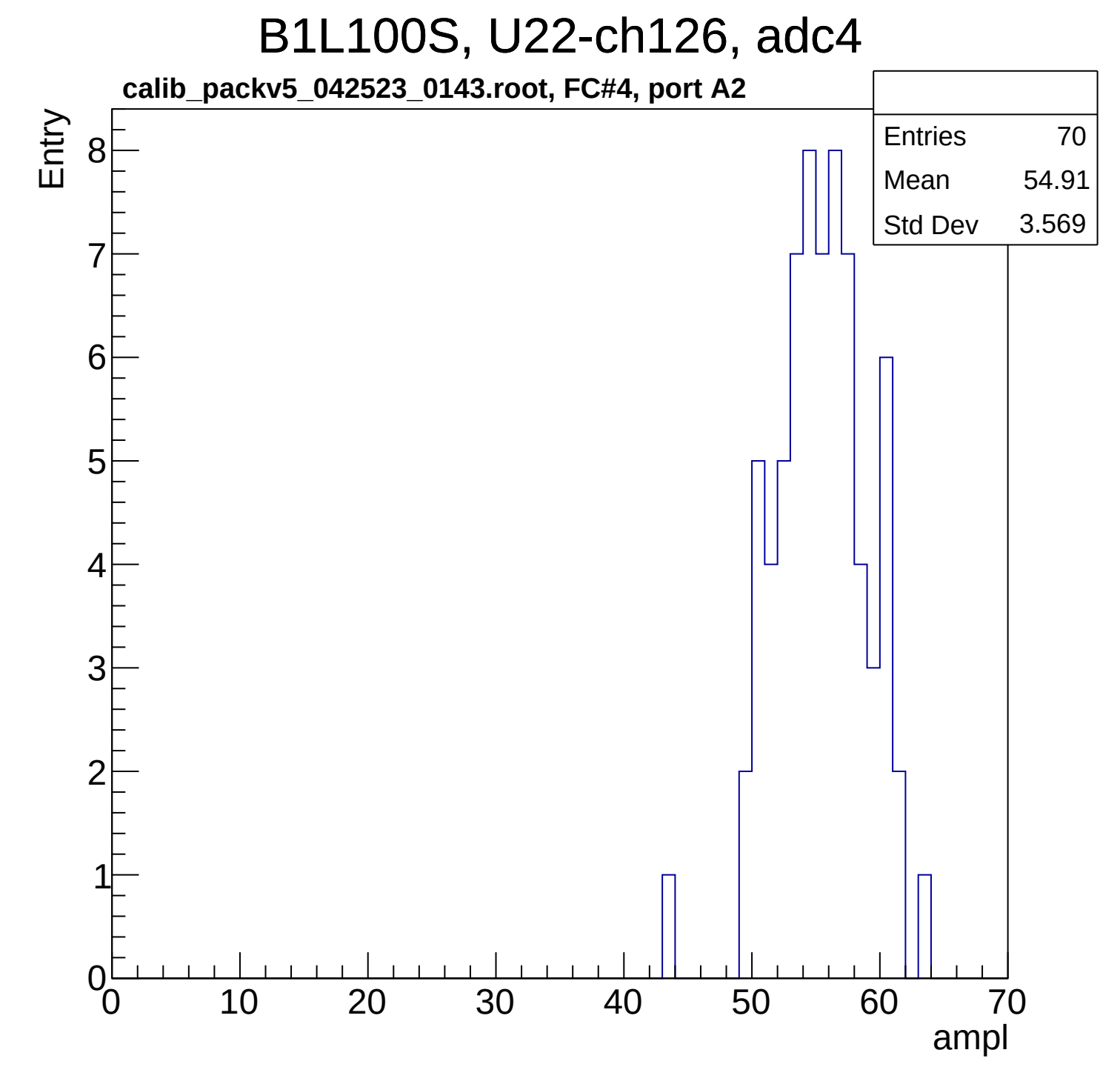
40
50
60
70


B1L100S, U22-ch126, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 70
Mean 54.91
Std Dev 3.569
Entry
ampl
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1
2
3
4
5
6
7
8
0
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20
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40
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60
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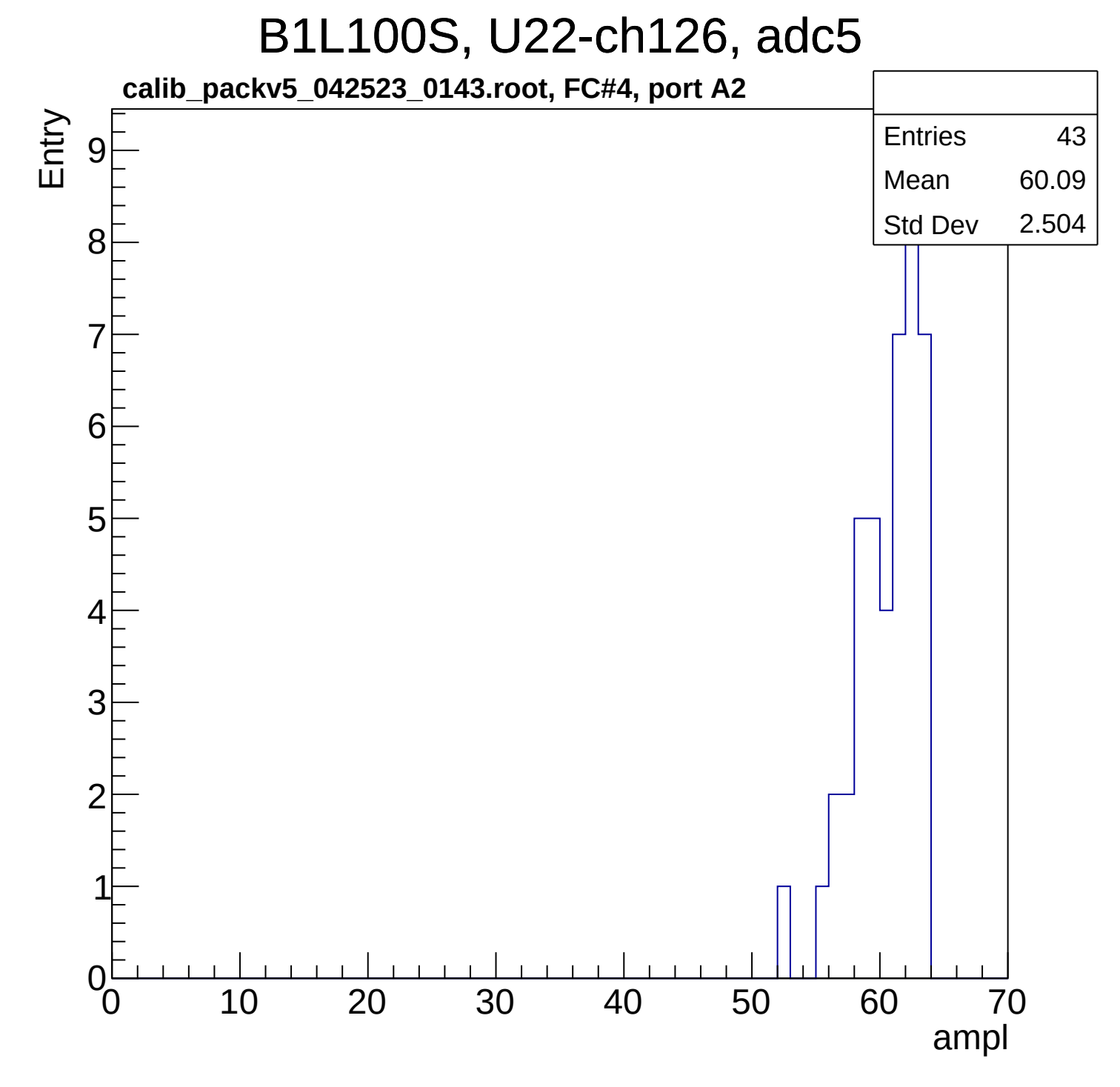
B1L100S, U22-ch126, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 60.09
Std Dev 2.504
Entry
ampl
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2
3
4
5
6
7
8
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0
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60
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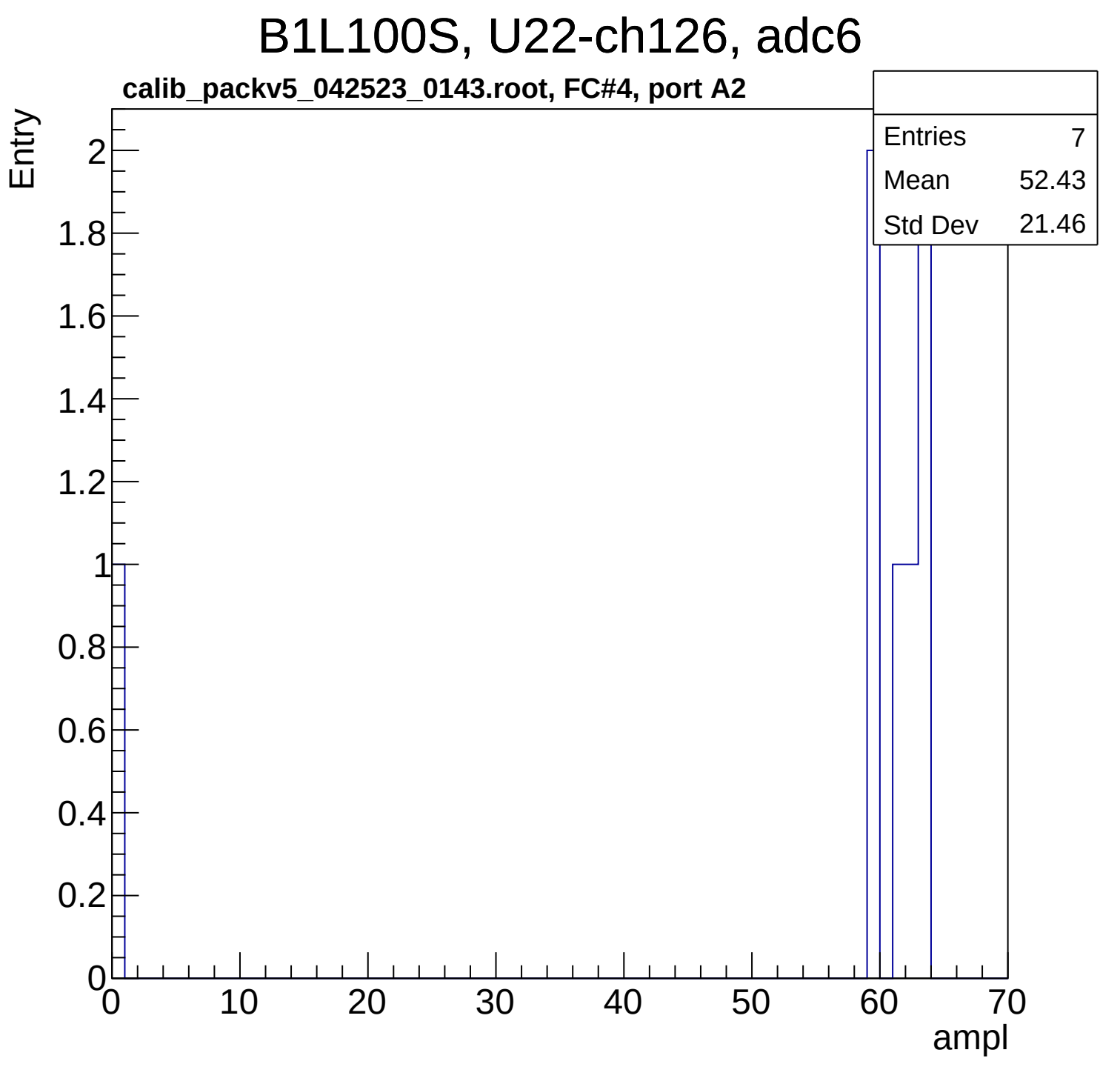
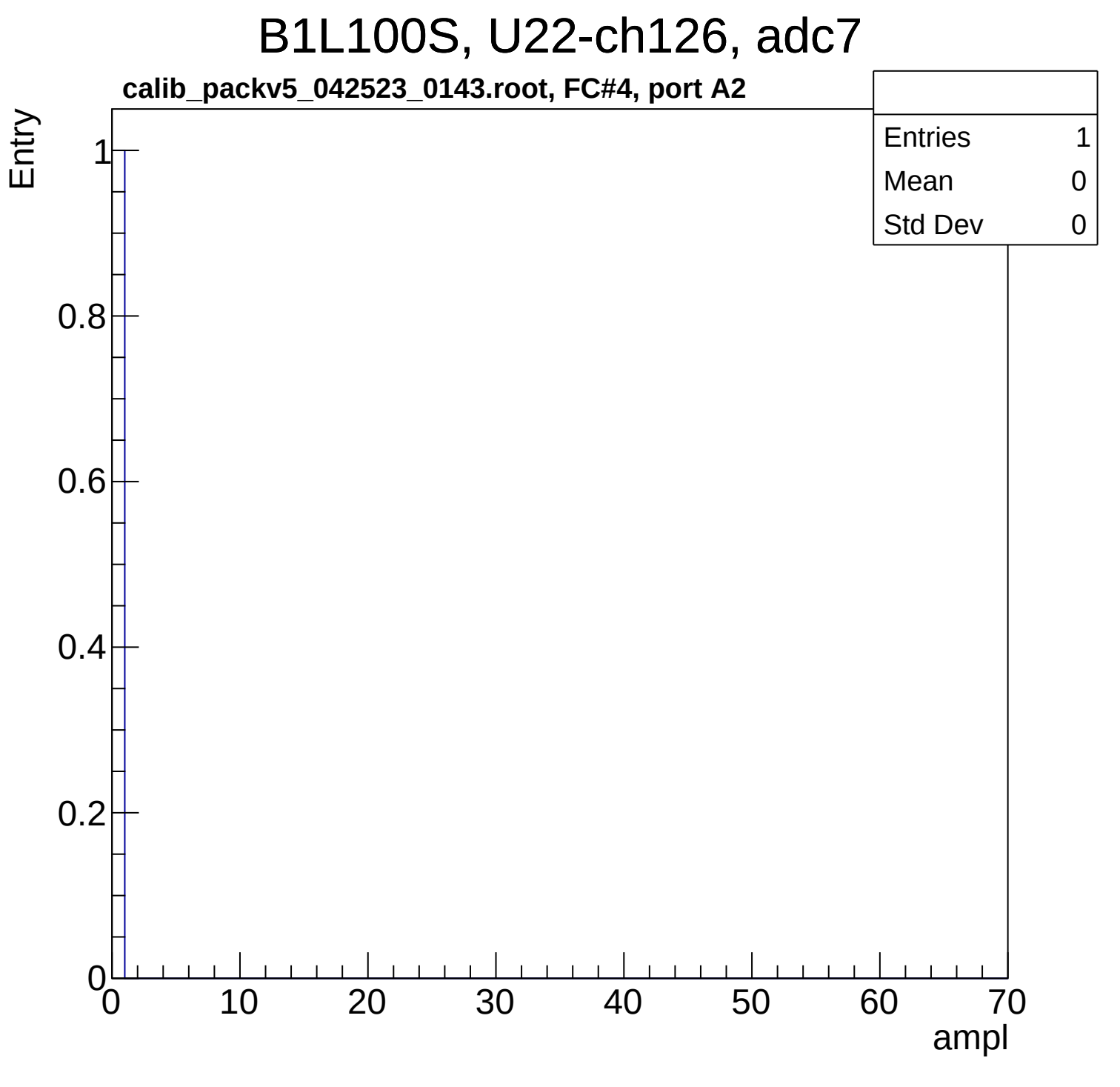

B1L100S, U22-ch126, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 7
Mean 52.43
Std Dev 21.46
Entry
ampl
0
0.2
0.4
0.6
0.8
1
1.2
1.4
1.6
1.8
2
0
10
20
30
40
50
60
70


B1L100S, U22-ch126, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
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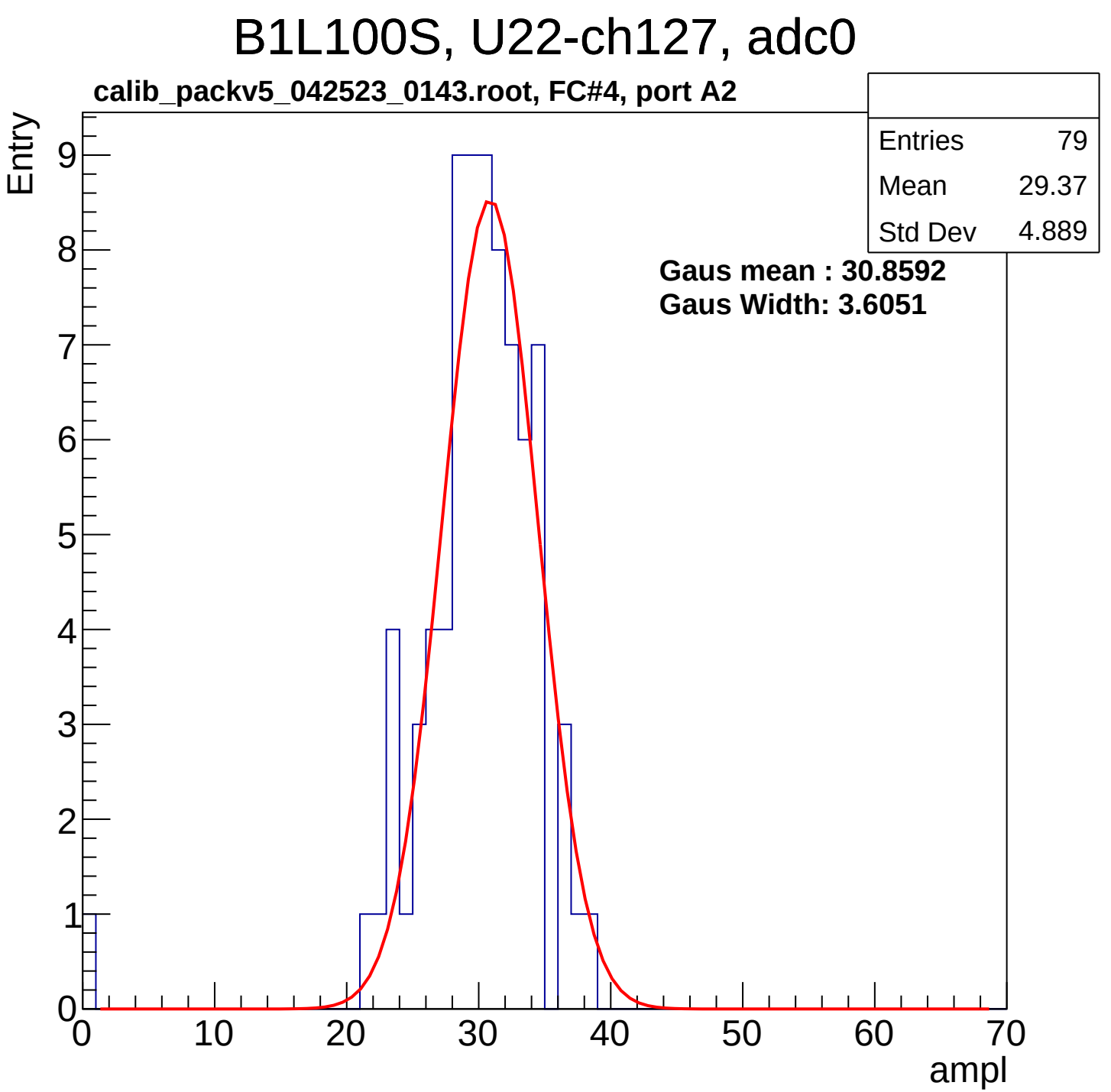
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch127, adc0
calib_packv5_042523_0143.root, FC#4, port A2
Entries 79
Mean 29.37
Std Dev 4.889
Gaus mean : 30.8592
Gaus Width: 3.6051
Entry
ampl
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2
3
4
5
6
7
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0
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60
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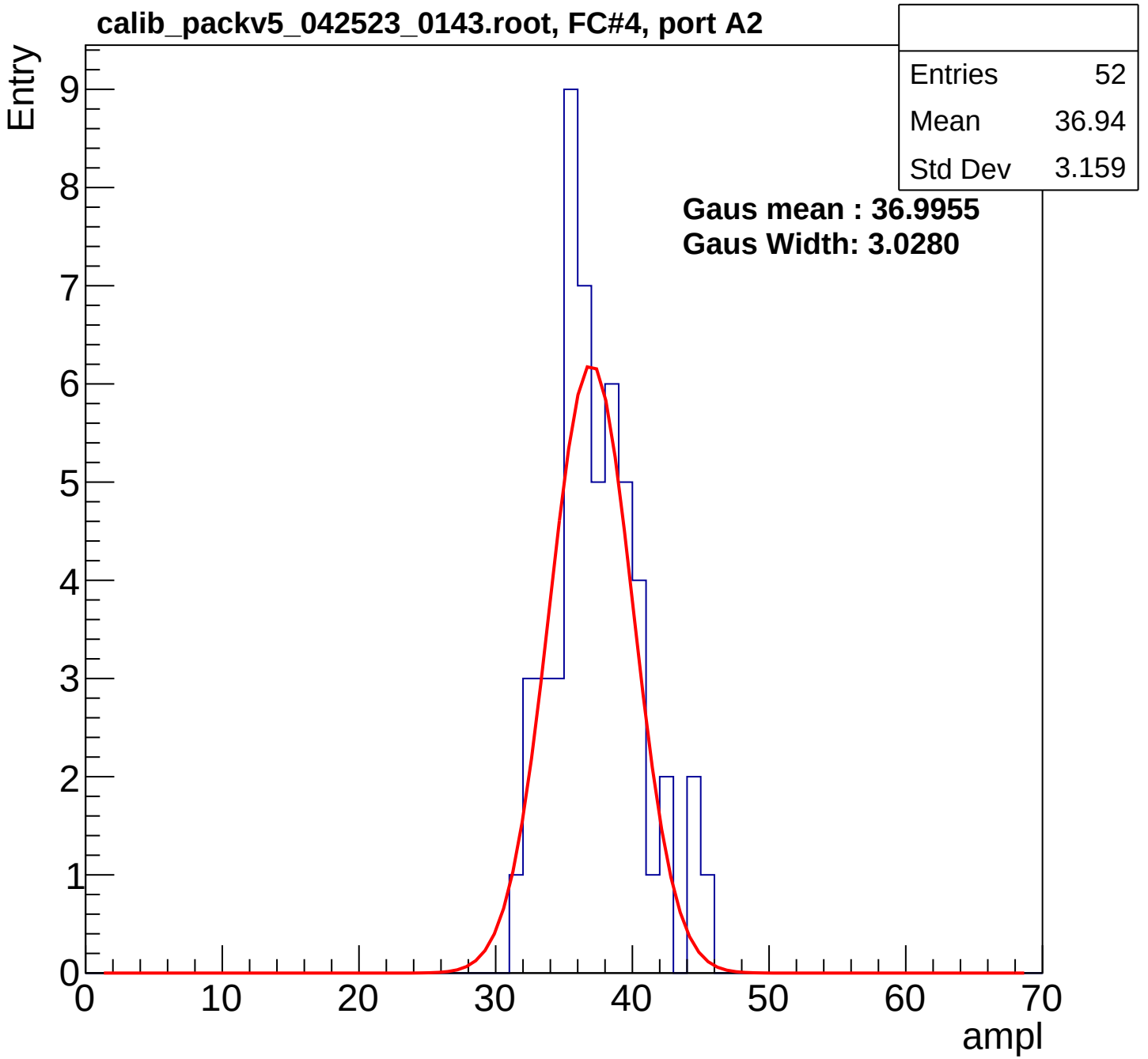
B1L100S, U22-ch127, adc1
calib_packv5_042523_0143.root, FC#4, port A2
Entries 52
Mean 36.94
Std Dev 3.159
Gaus mean : 36.9955
Gaus Width: 3.0280
Entry
ampl
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3
4
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6
7
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0
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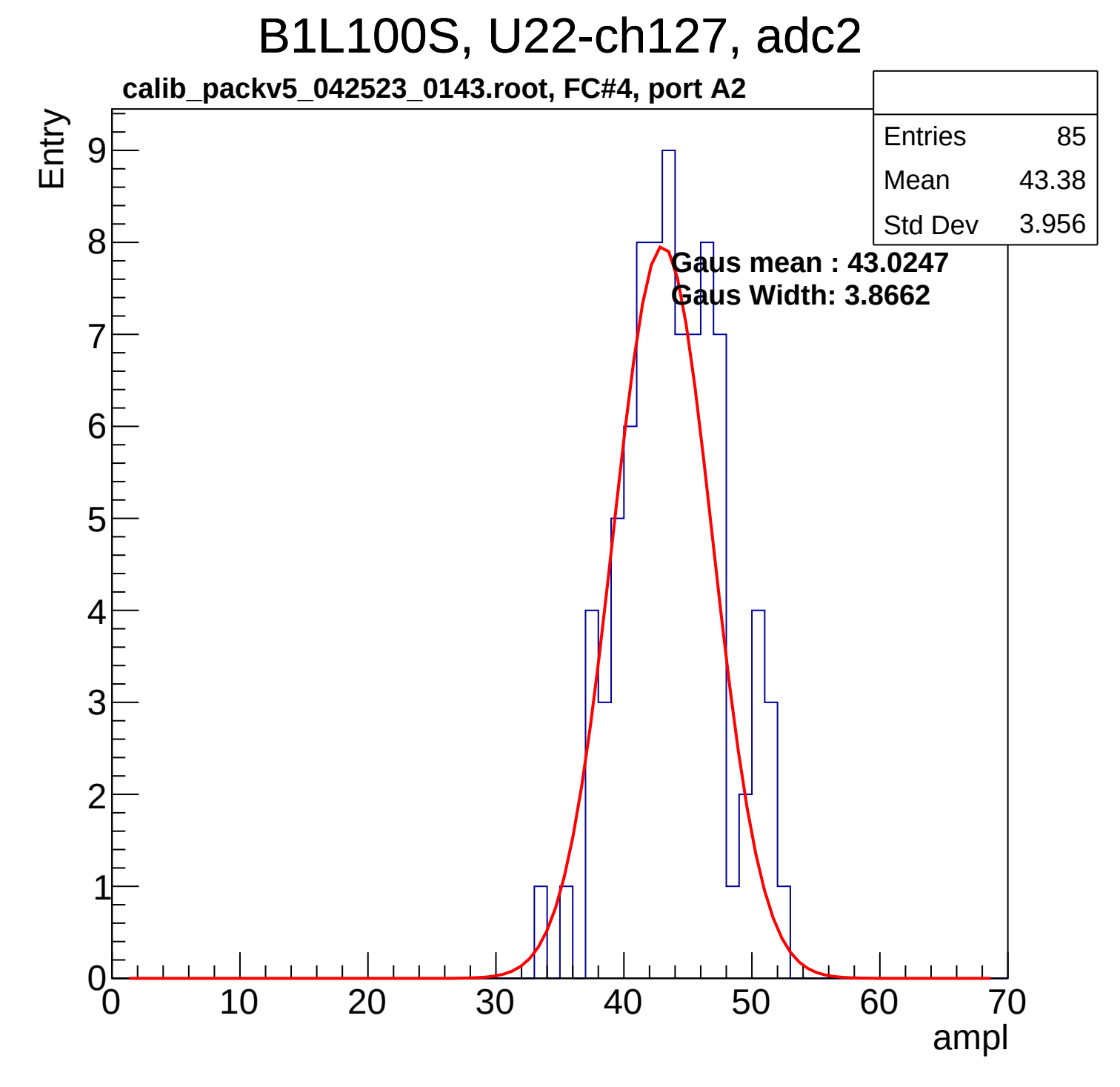
B1L100S, U22-ch127, adc2
calib_packv5_042523_0143.root, FC#4, port A2
Entries 85
Mean 43.38
Std Dev 3.956
Gaus mean : 43.0247
Gaus Width: 3.8662
Entry
ampl
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2
3
4
5
6
7
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0
10
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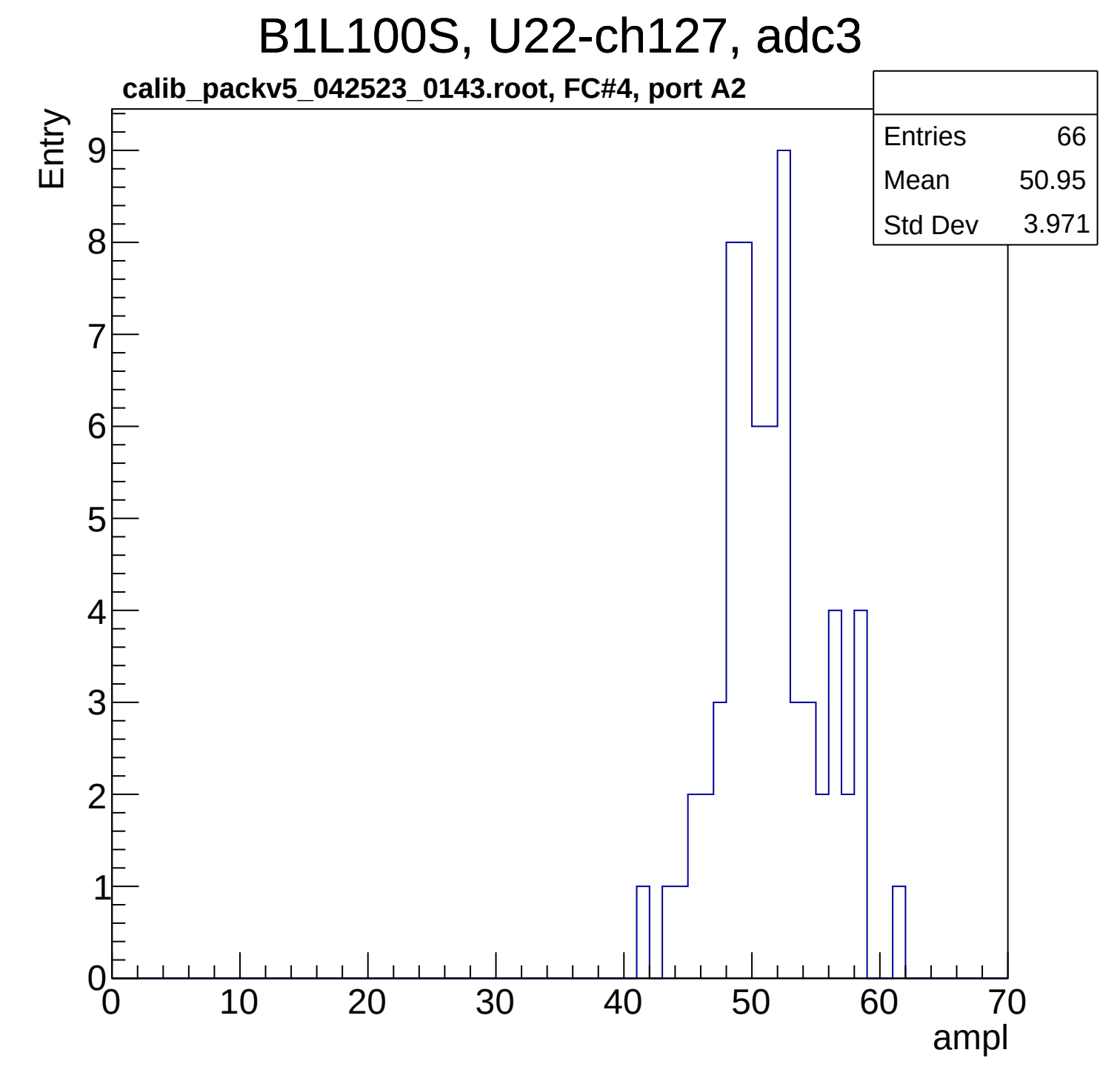
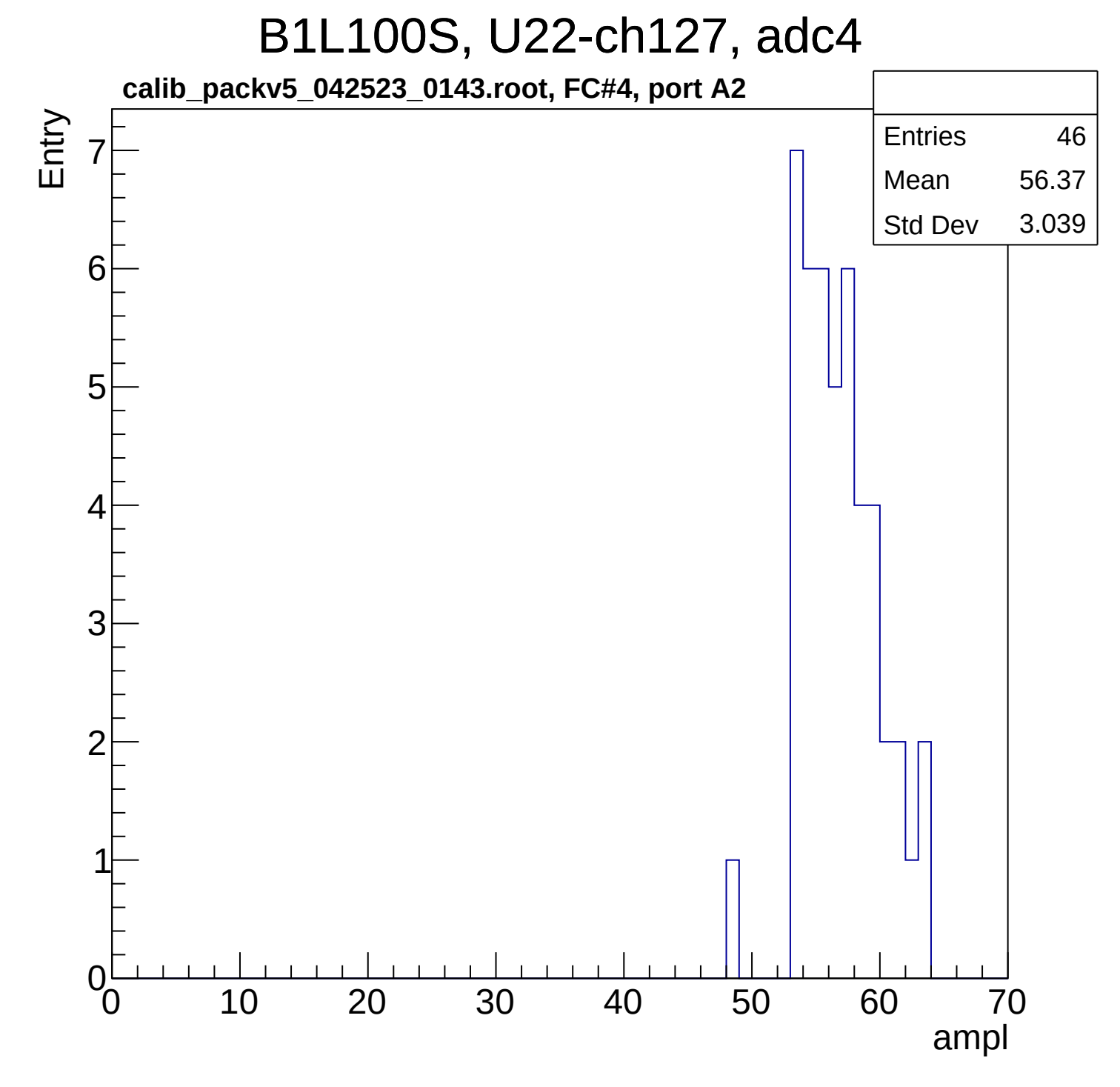
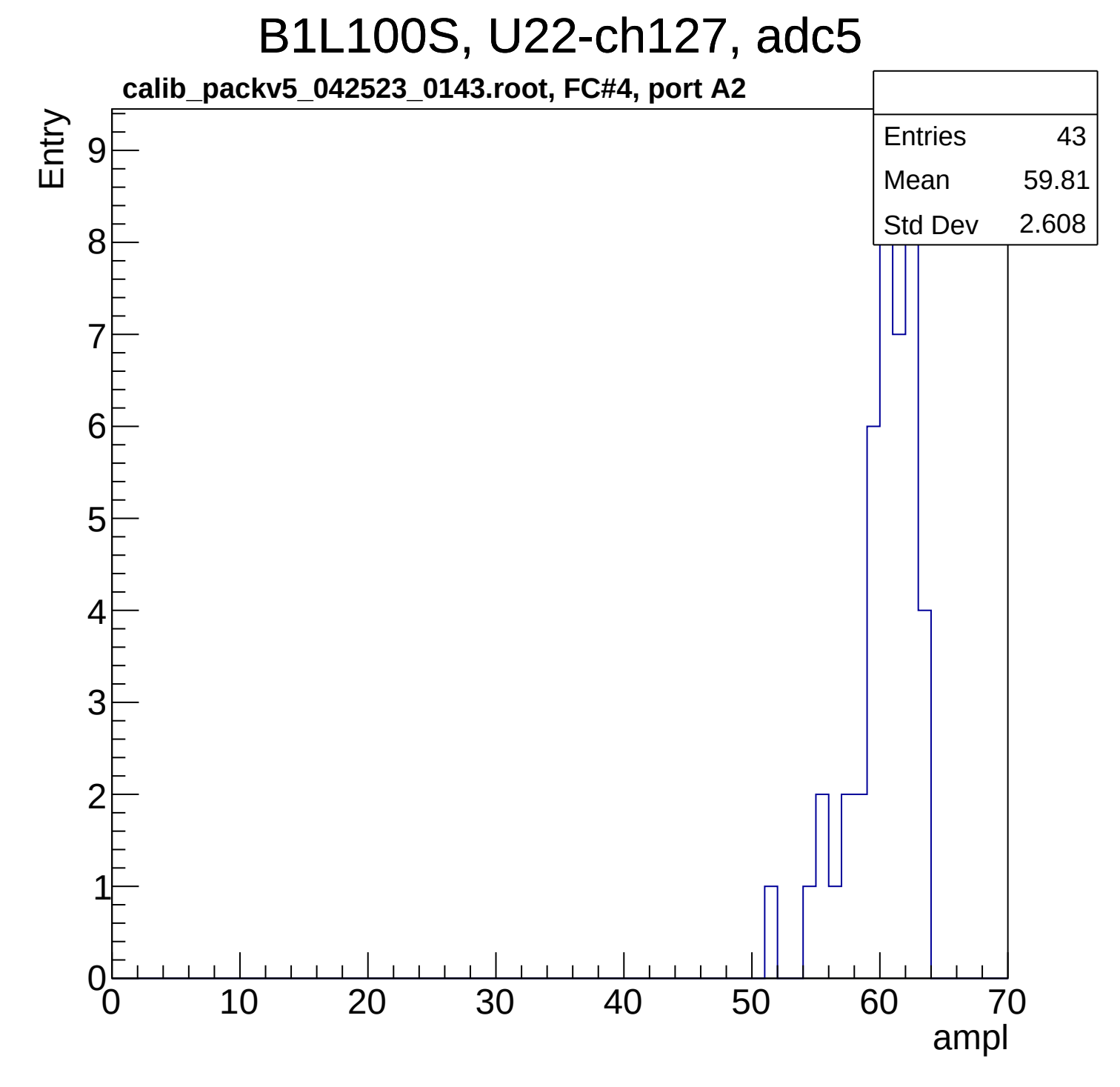
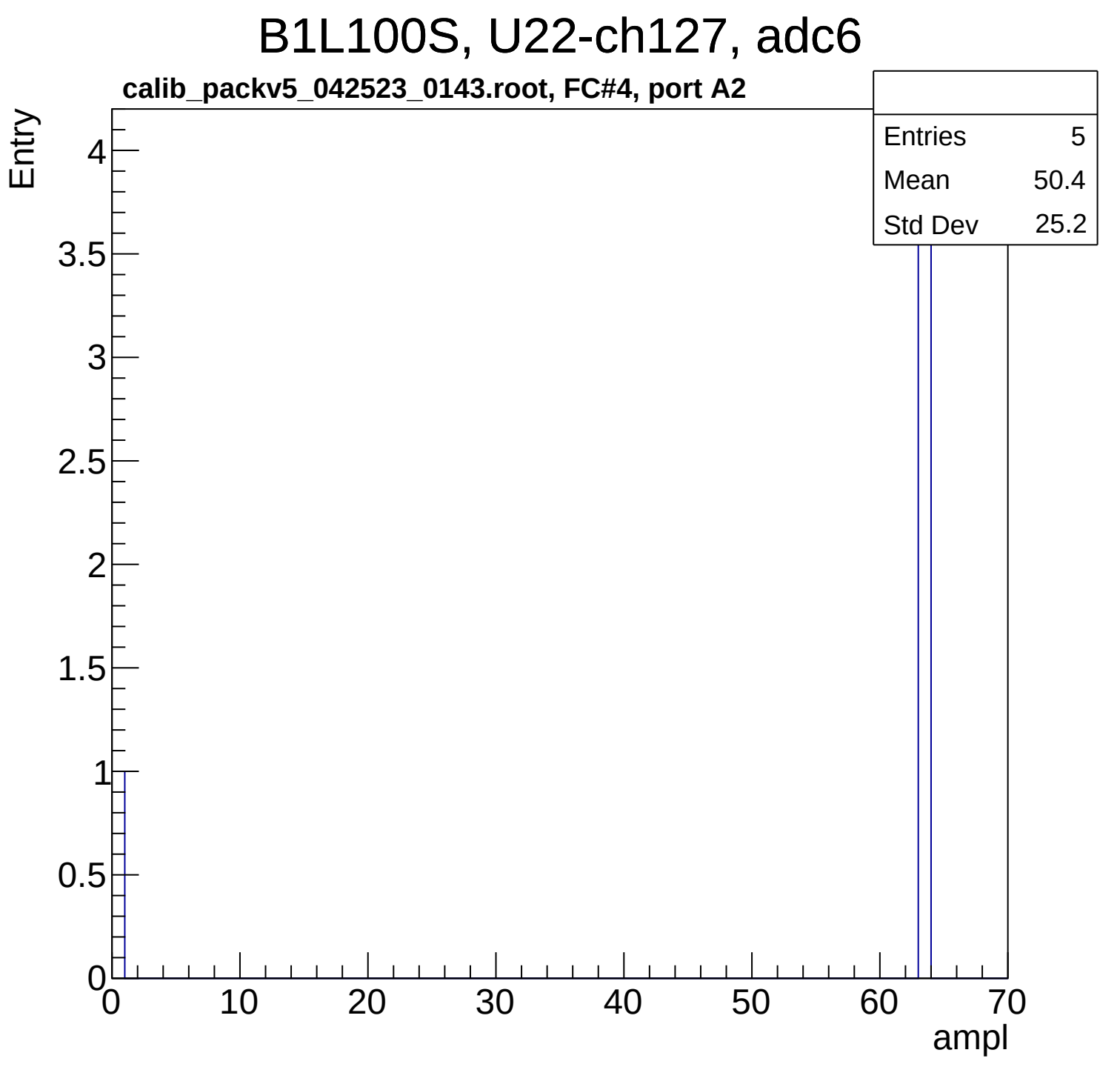
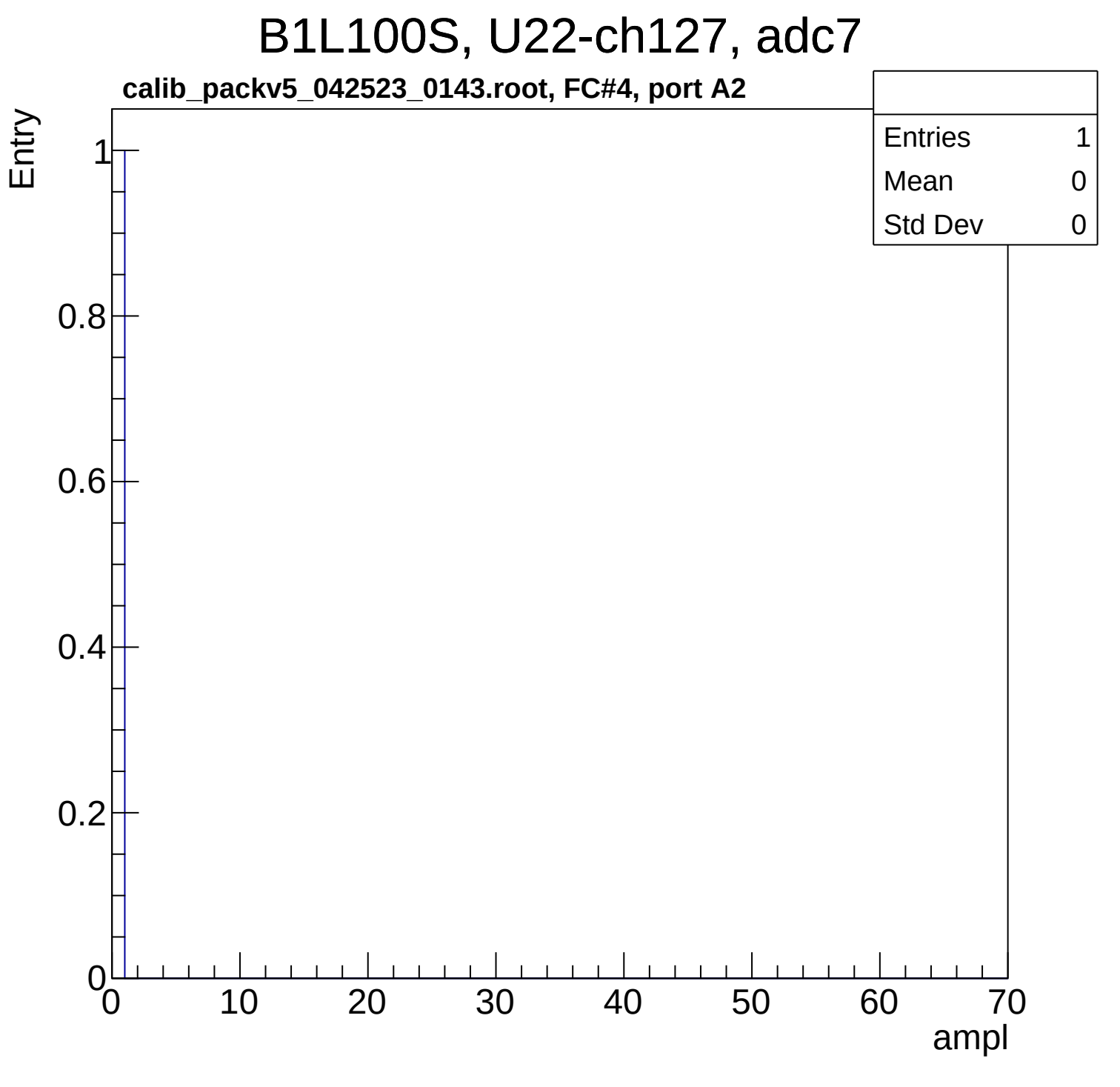
60
70

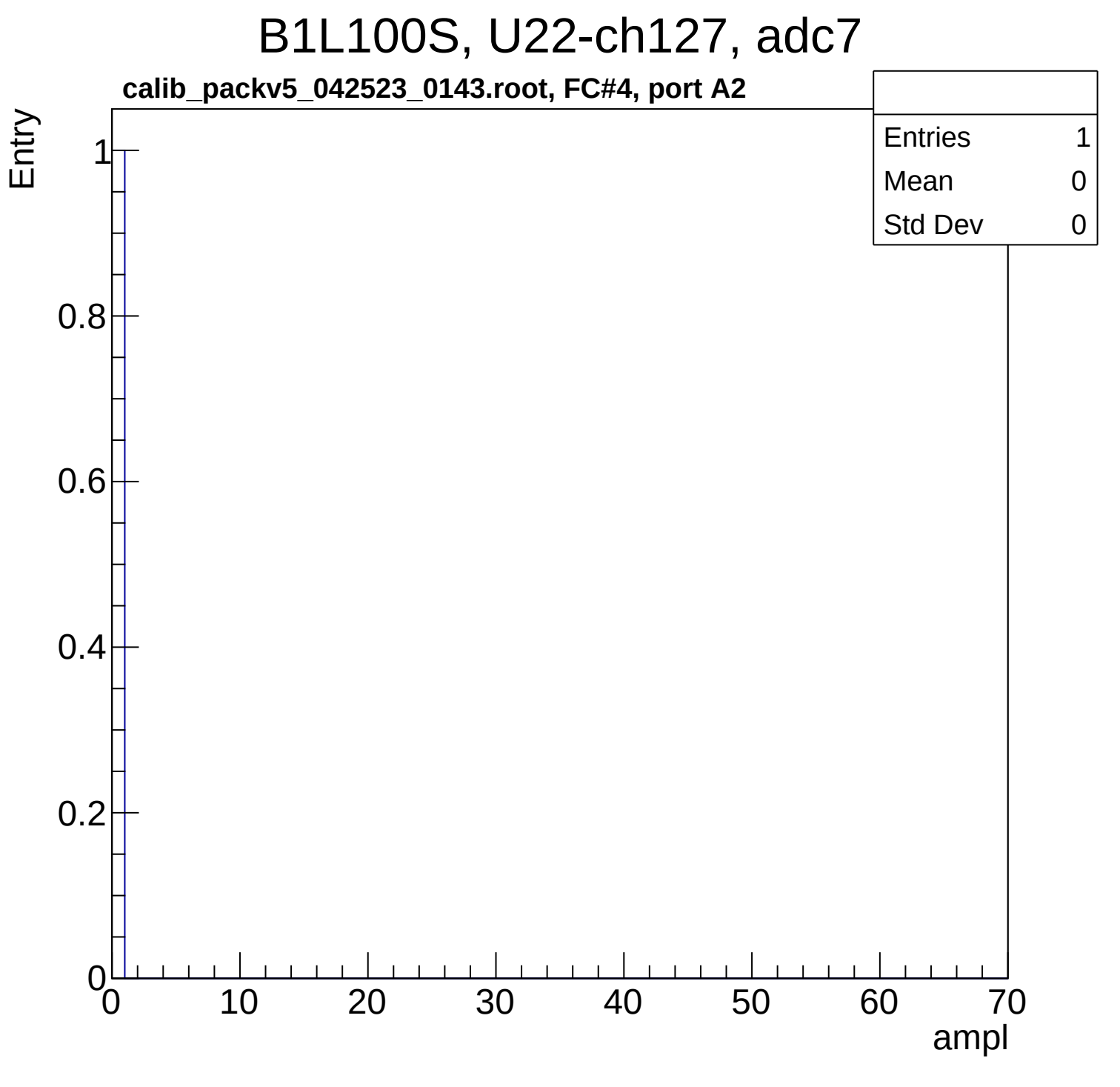

B1L100S, U22-ch127, adc3
calib_packv5_042523_0143.root, FC#4, port A2
Entries 66
Mean 50.95
Std Dev 3.971
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch127, adc4
calib_packv5_042523_0143.root, FC#4, port A2
Entries 46
Mean 56.37
Std Dev 3.039
Entry
ampl
0
1
2
3
4
5
6
7
0
10
20
30
40
50
60
70


B1L100S, U22-ch127, adc5
calib_packv5_042523_0143.root, FC#4, port A2
Entries 43
Mean 59.81
Std Dev 2.608
Entry
ampl
0
1
2
3
4
5
6
7
8
9
0
10
20
30
40
50
60
70


B1L100S, U22-ch127, adc6
calib_packv5_042523_0143.root, FC#4, port A2
Entries 5
Mean 50.4
Std Dev 25.2
Entry
ampl
0
0.5
1
1.5
2
2.5
3
3.5
4
0
10
20
30
40
50
60
70


B1L100S, U22-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl


B1L100S, U22-ch127, adc7
calib_packv5_042523_0143.root, FC#4, port A2
Entries 1
Mean 0
Std Dev 0
Entry
1
0.8
0.6
0.4
0.2
0
0
10
20
30
40
50
60
70
ampl