

B1L000S, U16-ch0

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.88
Std Dev	11.83

Turn on : 25.7932

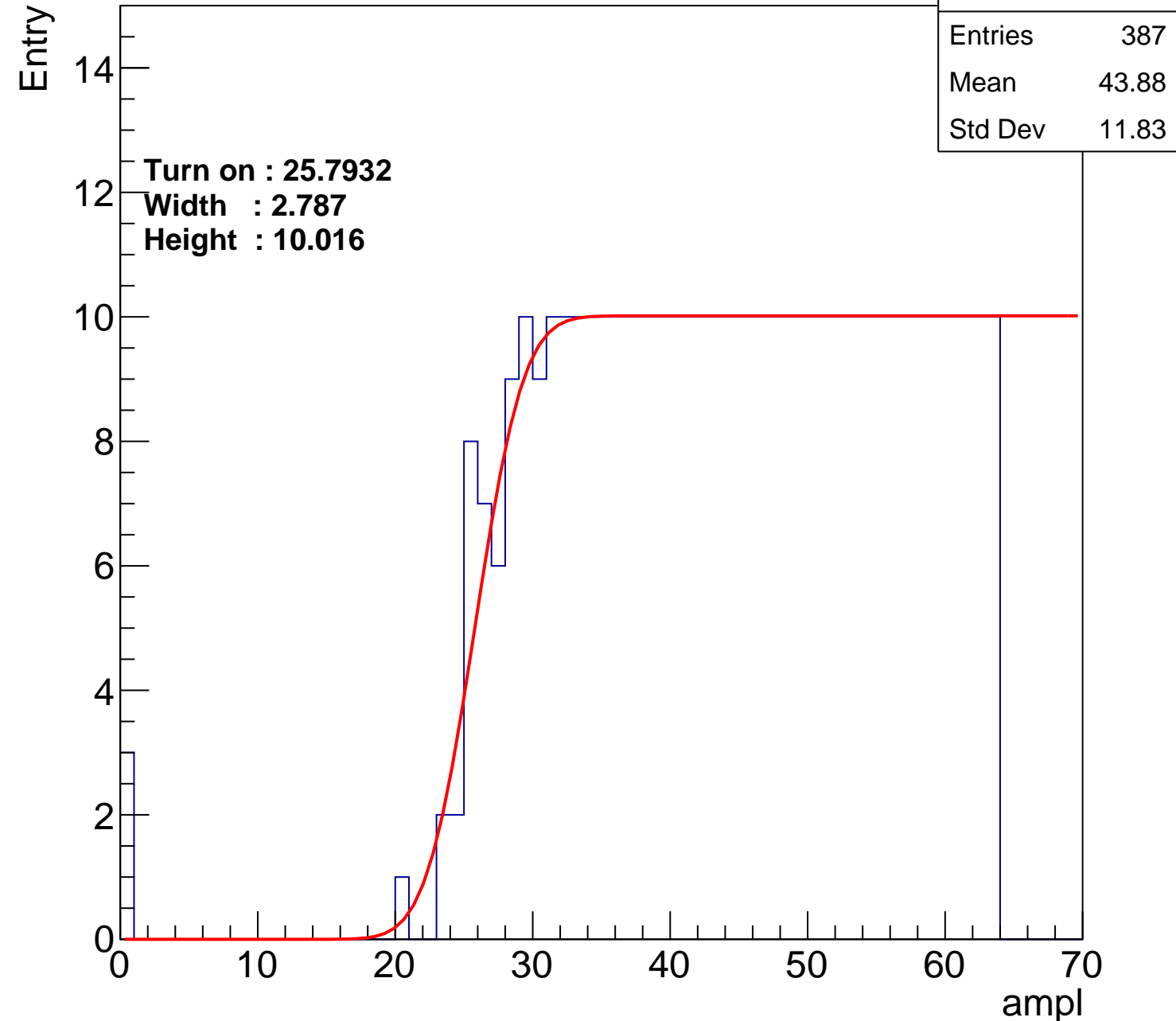
Width : 2.787

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch1

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.19
Std Dev	11.58

Turn on : 26.6182

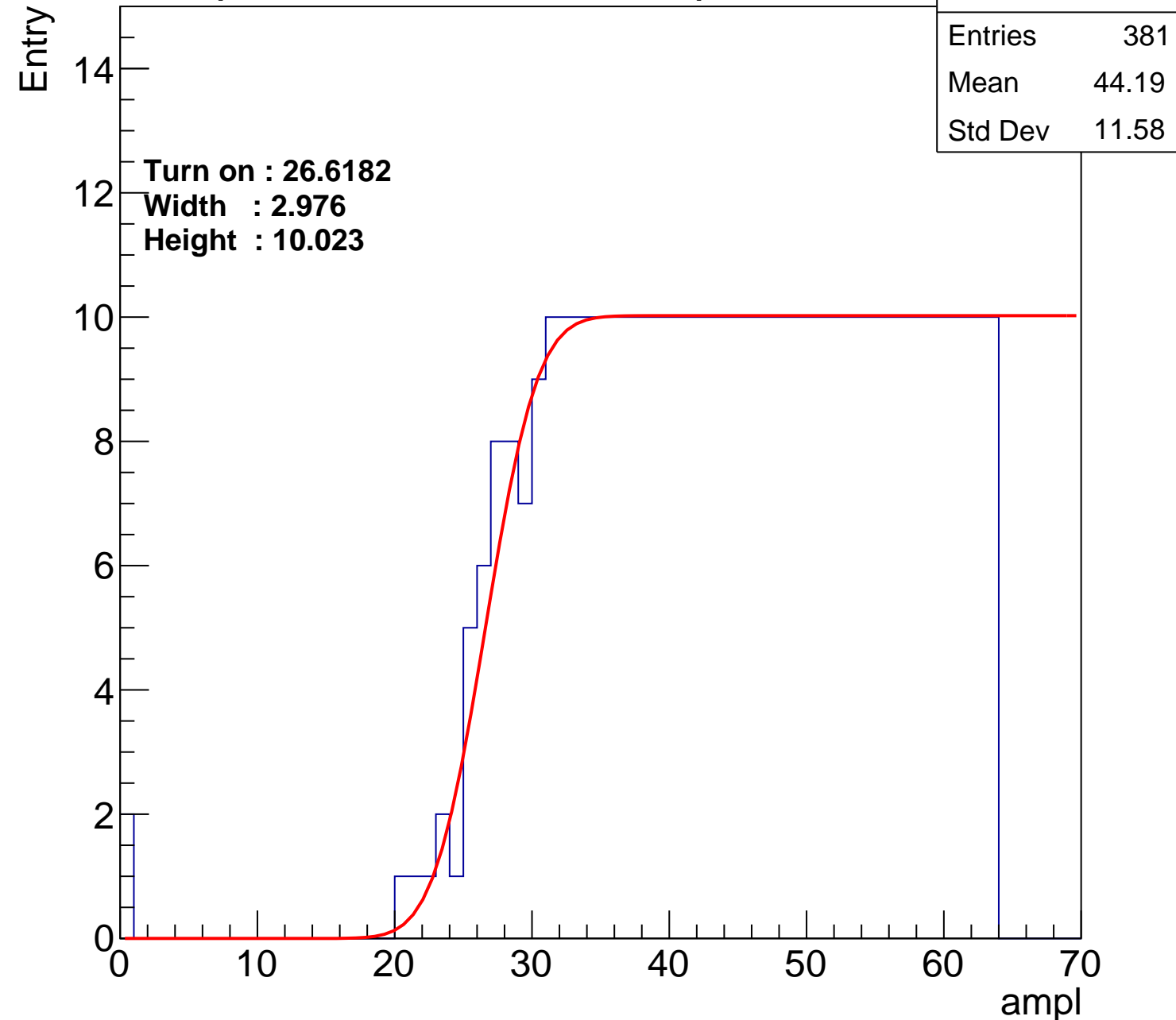
Width : 2.976

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch2

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.74
Std Dev	11.88

Turn on : 25.7138

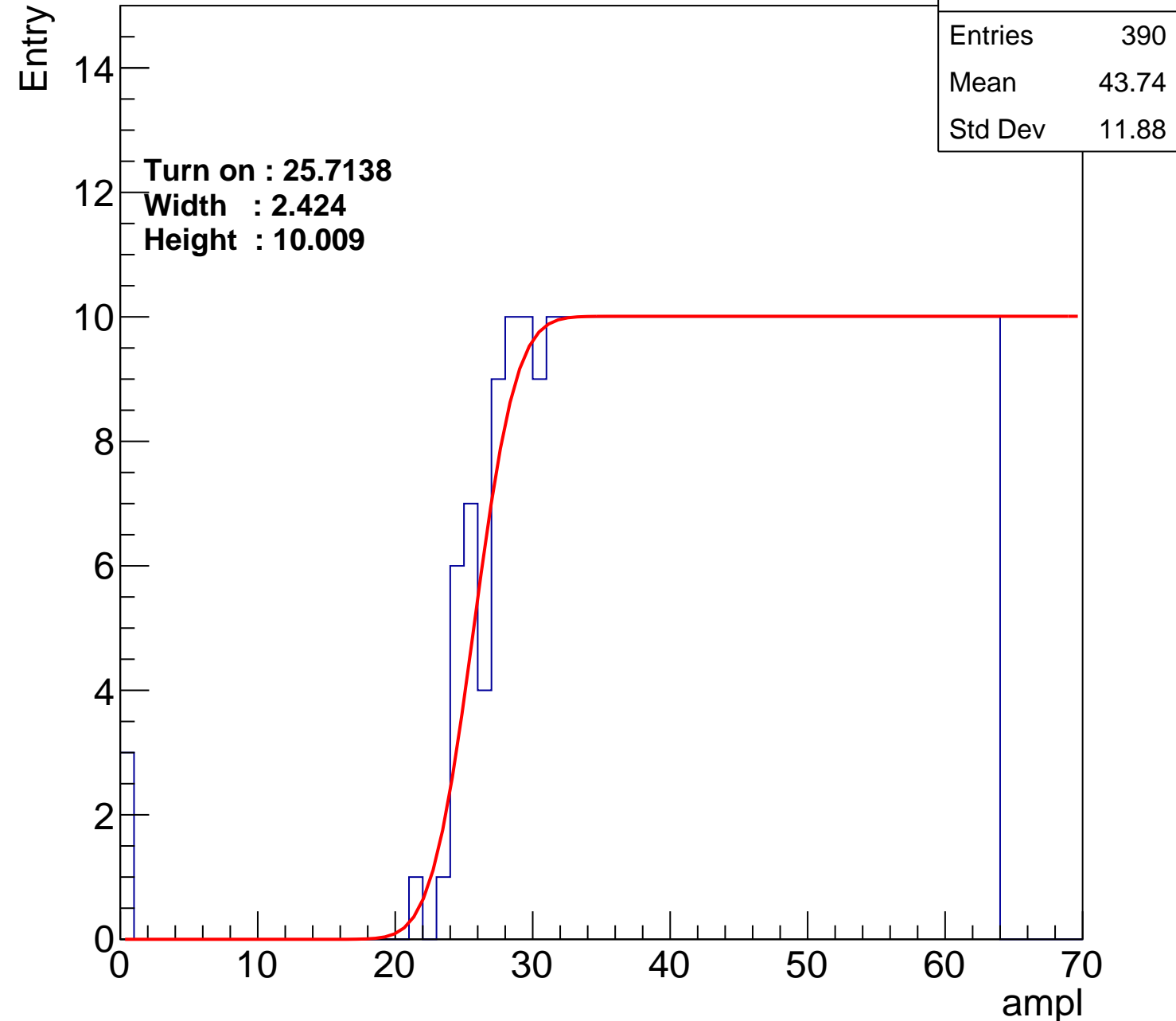
Width : 2.424

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch3

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.15
Std Dev	11.6

Turn on : 26.9273

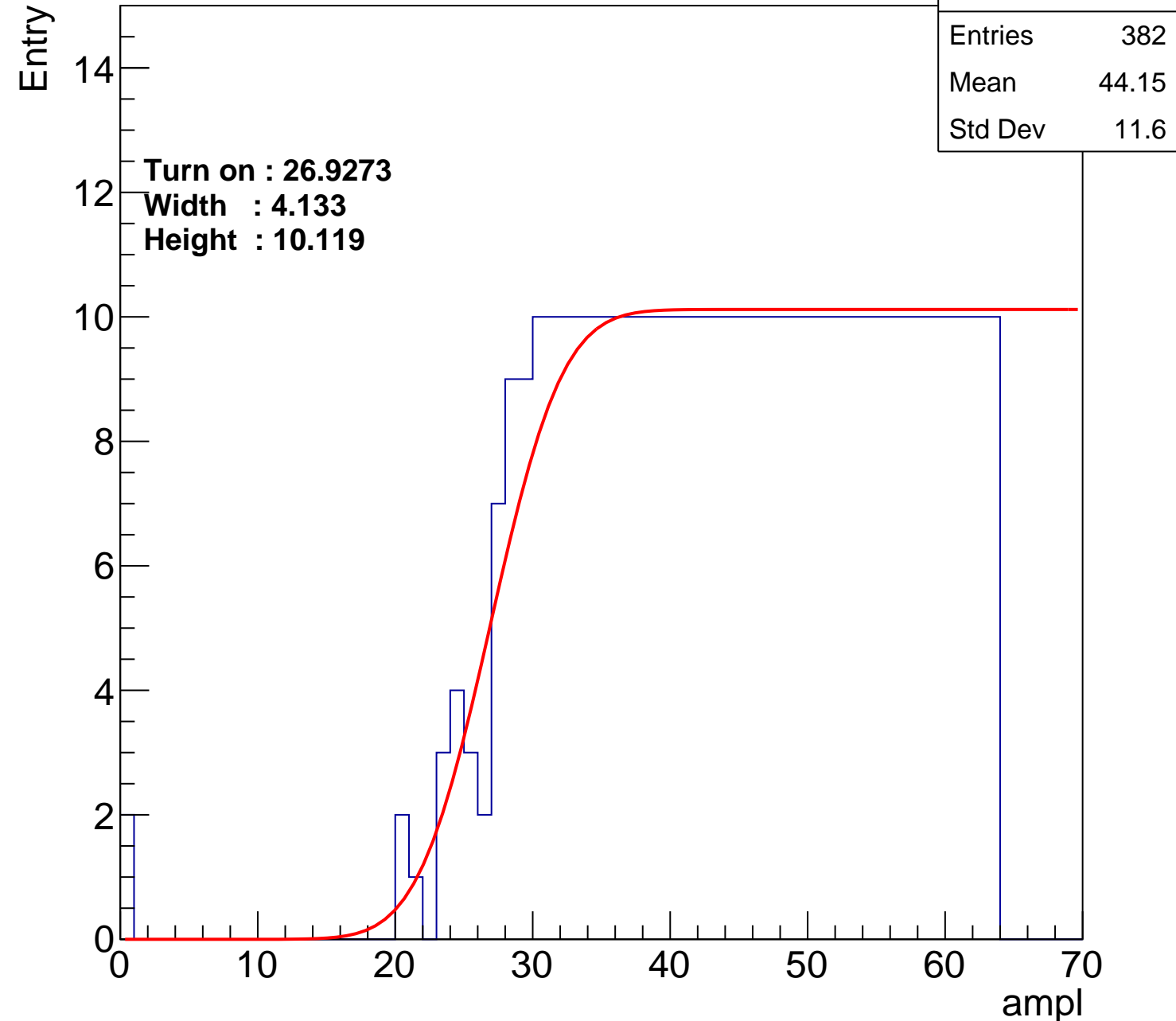
Width : 4.133

Height : 10.119

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch4

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.54
Std Dev	12

Turn on : 24.9849

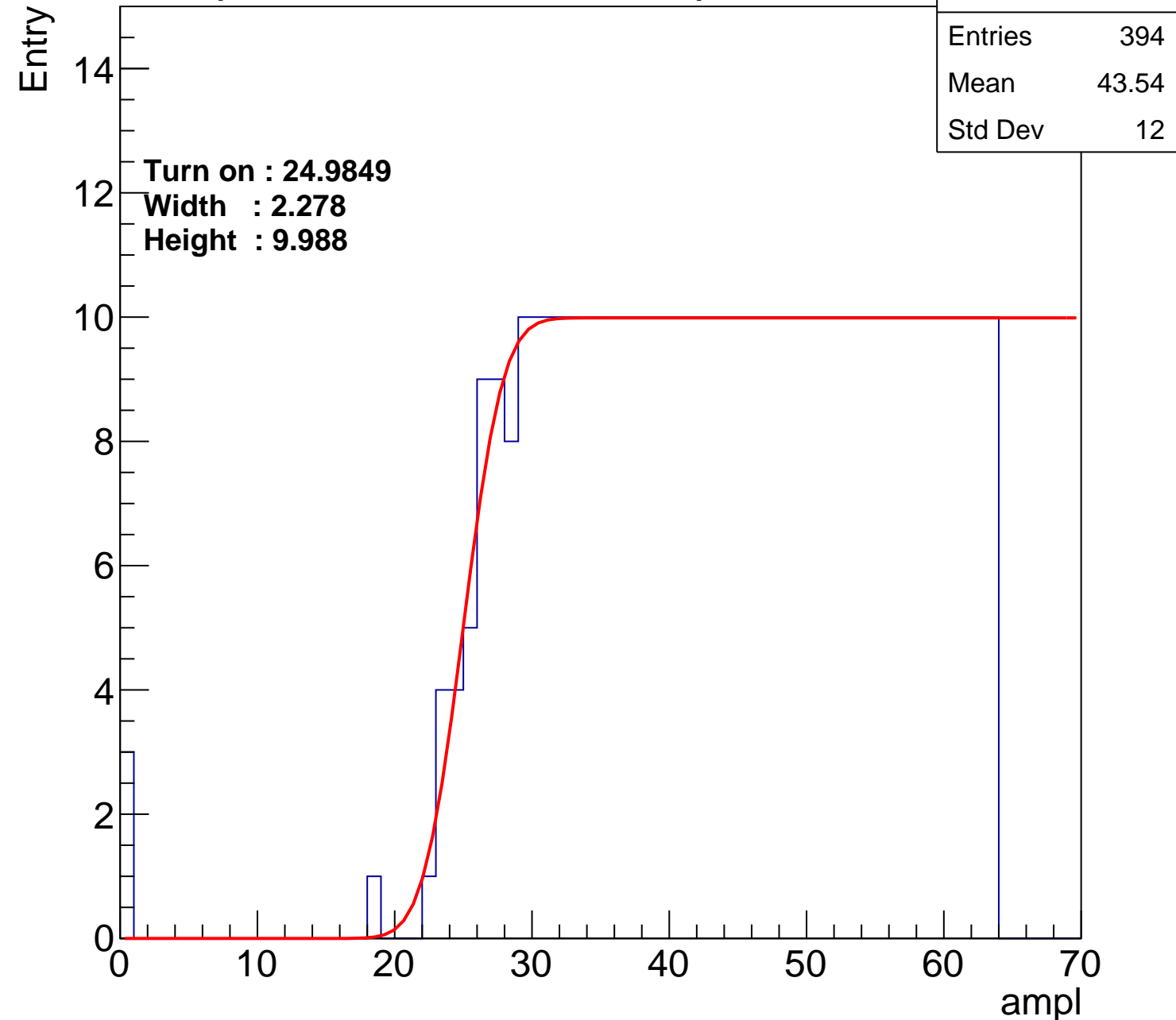
Width : 2.278

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch5

calib_packv5_042523_0143.root, FC#3, port B3

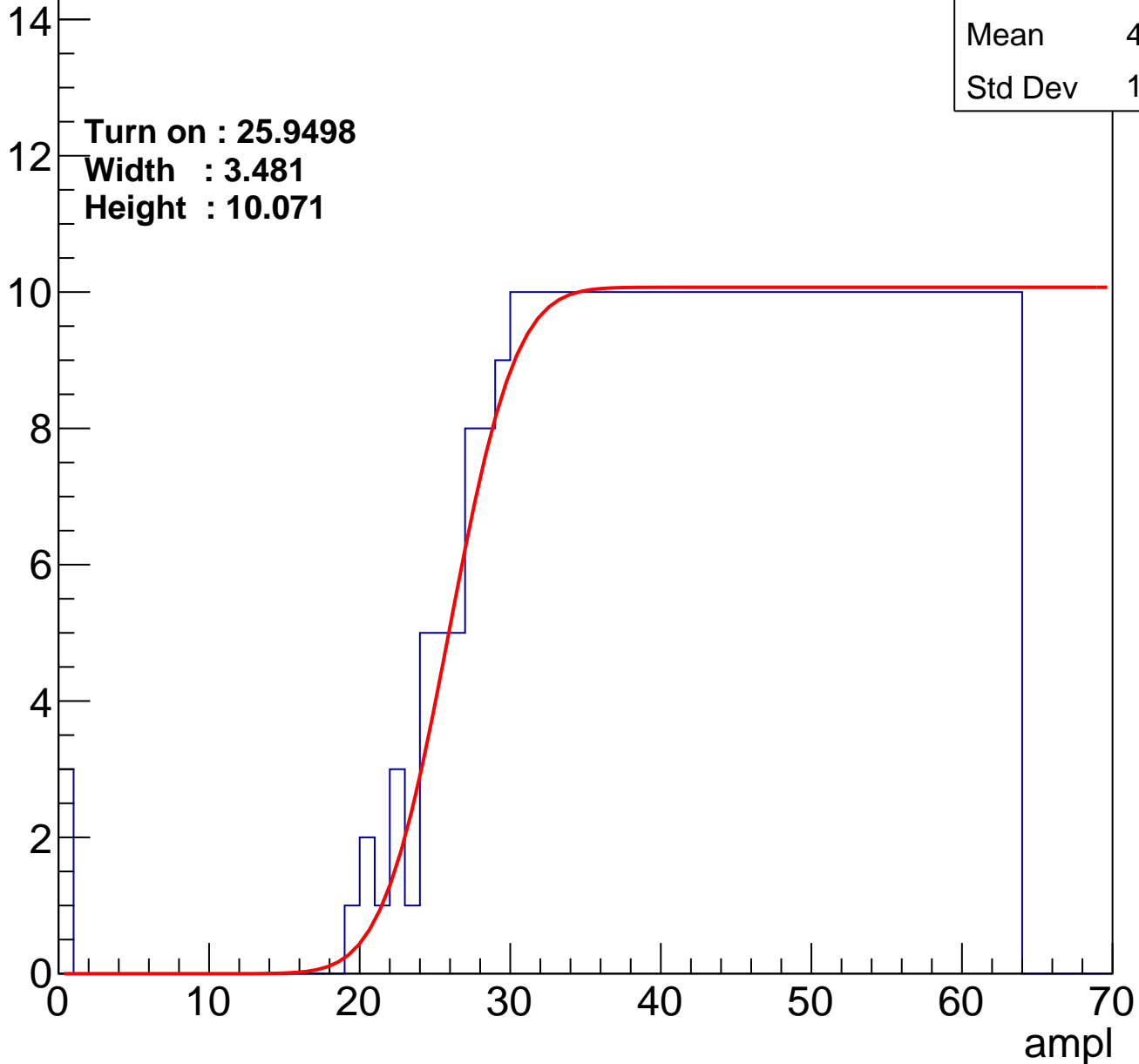
Entries	391
Mean	43.62
Std Dev	12.03

Turn on : 25.9498

Width : 3.481

Height : 10.071

Entry



B1L000S, U16-ch6

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.85
Std Dev	11.71

Turn on : 25.7583

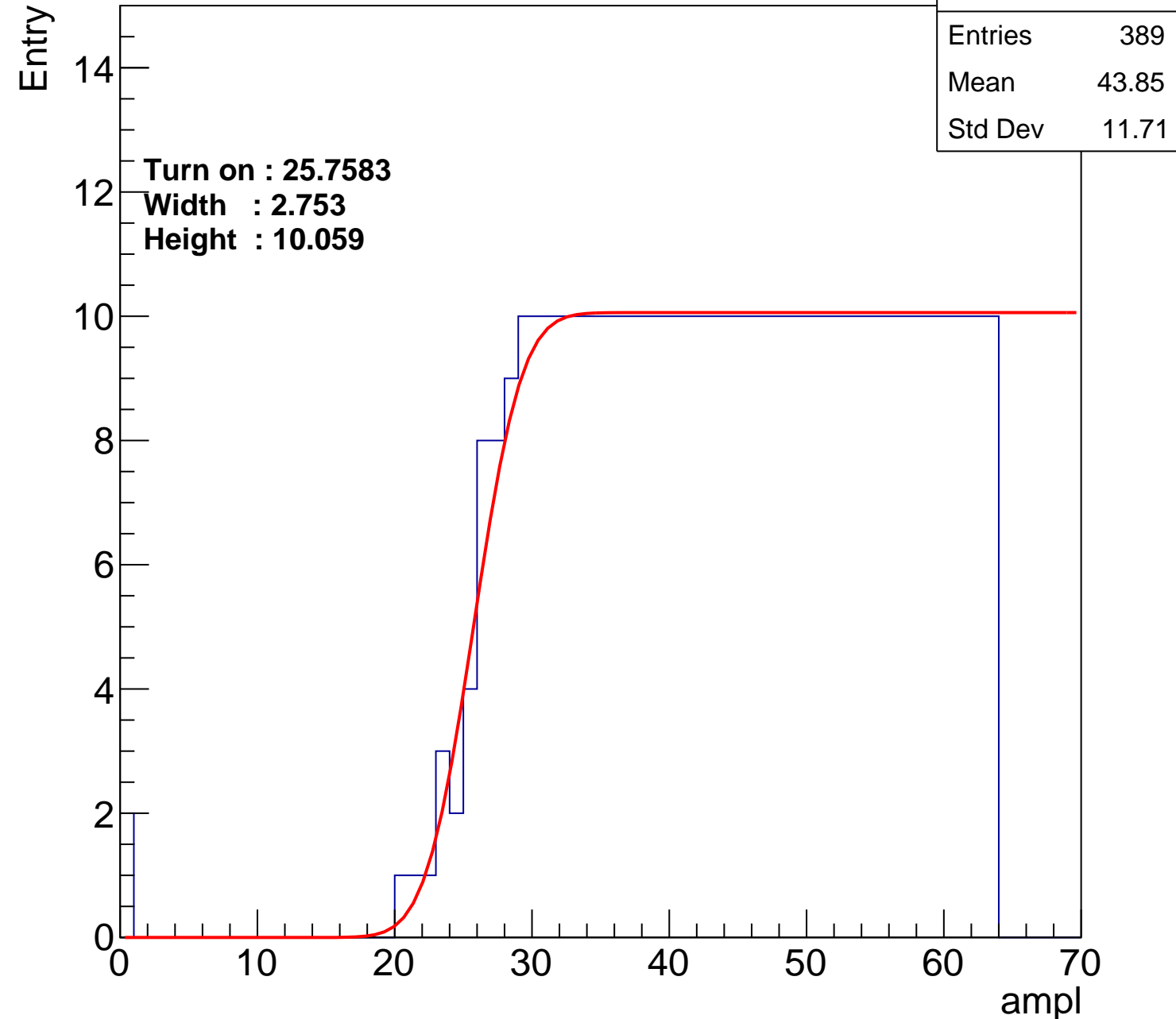
Width : 2.753

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch7

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.46
Std Dev	11.36

Turn on : 26.6324

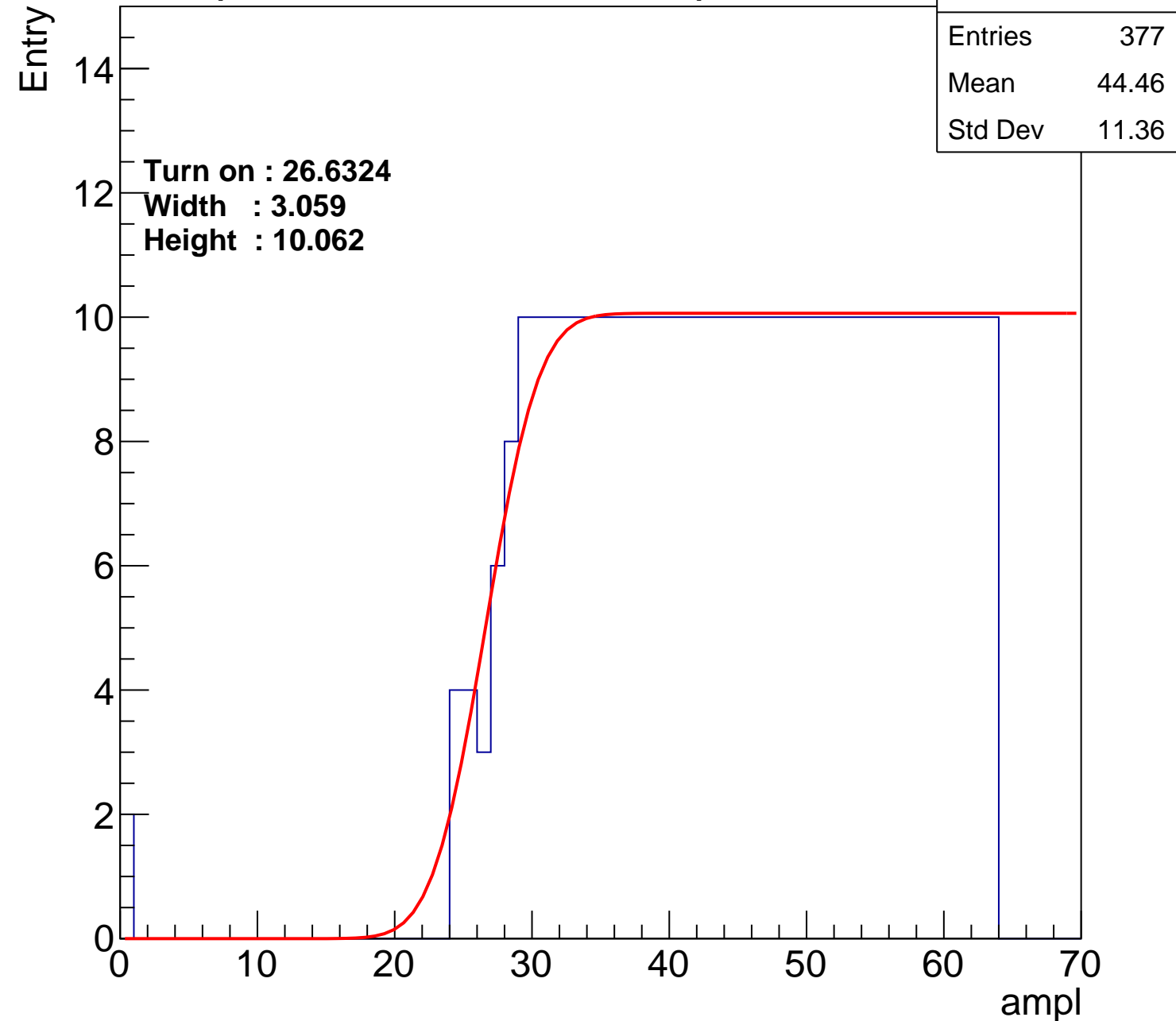
Width : 3.059

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch8

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.21
Std Dev	11.49

Turn on : 25.9641

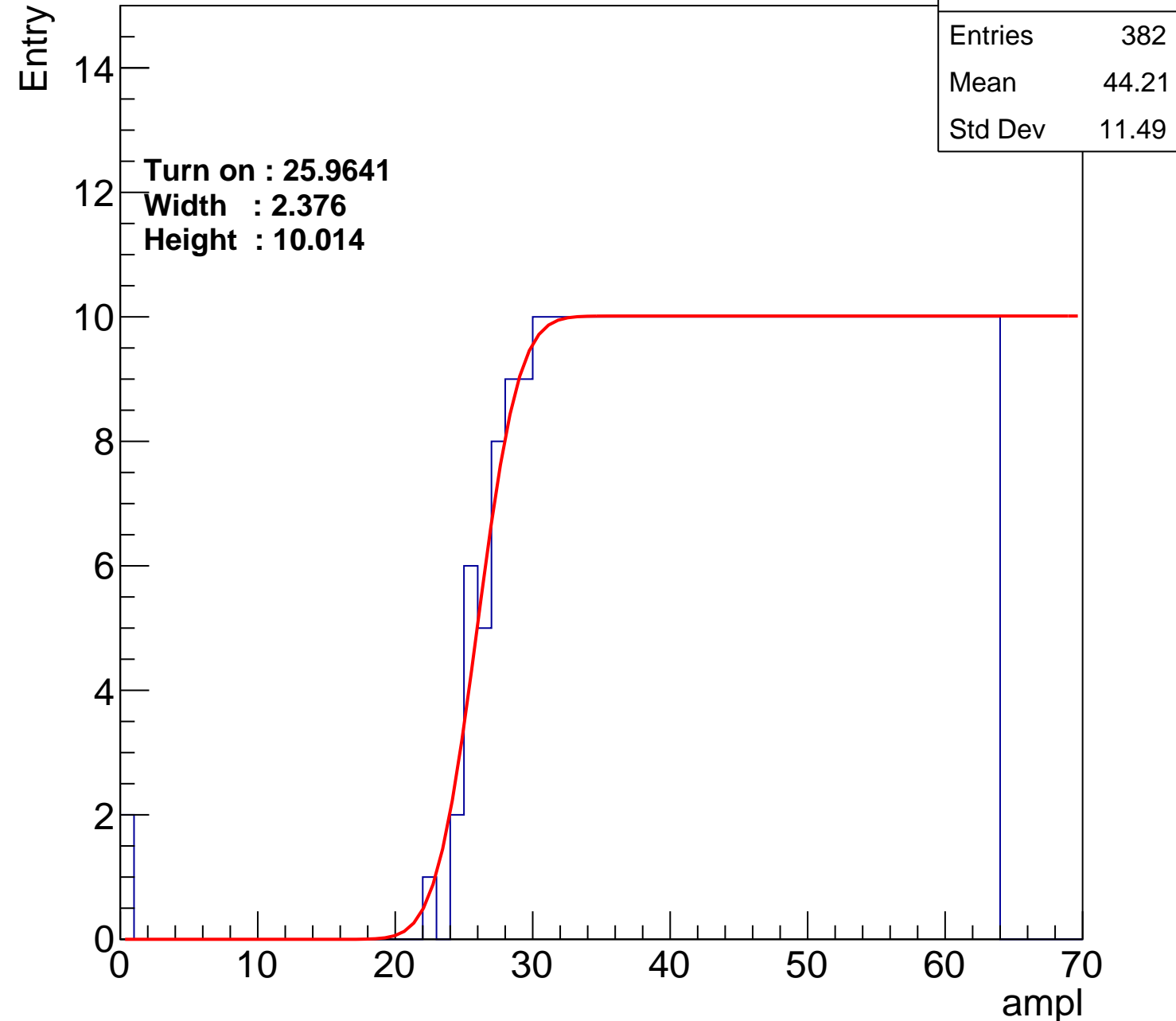
Width : 2.376

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch9

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.59
Std Dev	11.5

Turn on : 27.6362

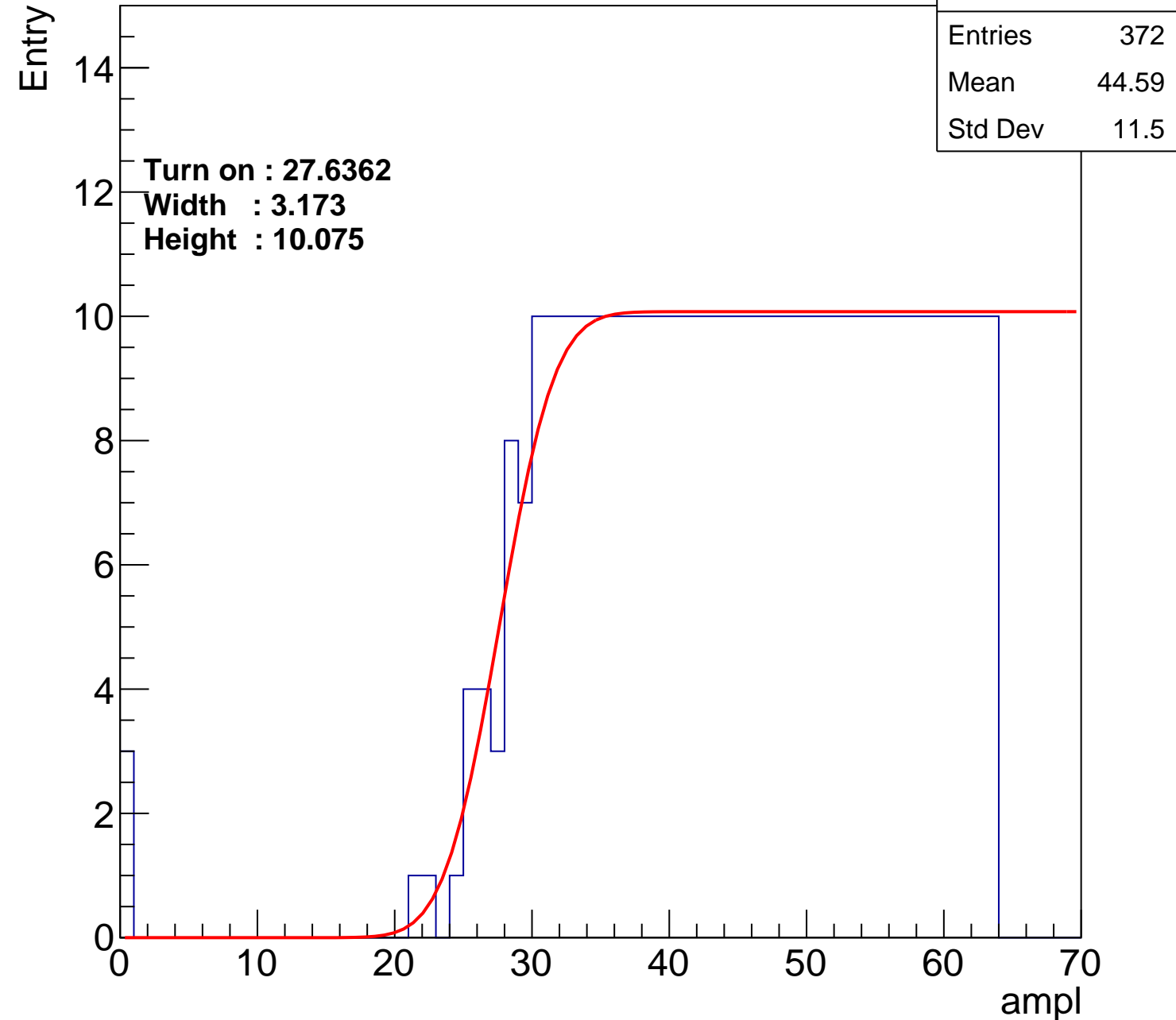
Width : 3.173

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch10

calib_packv5_042523_0143.root, FC#3, port B3

Entries	403
Mean	43.08
Std Dev	12.25

Turn on : 24.5130

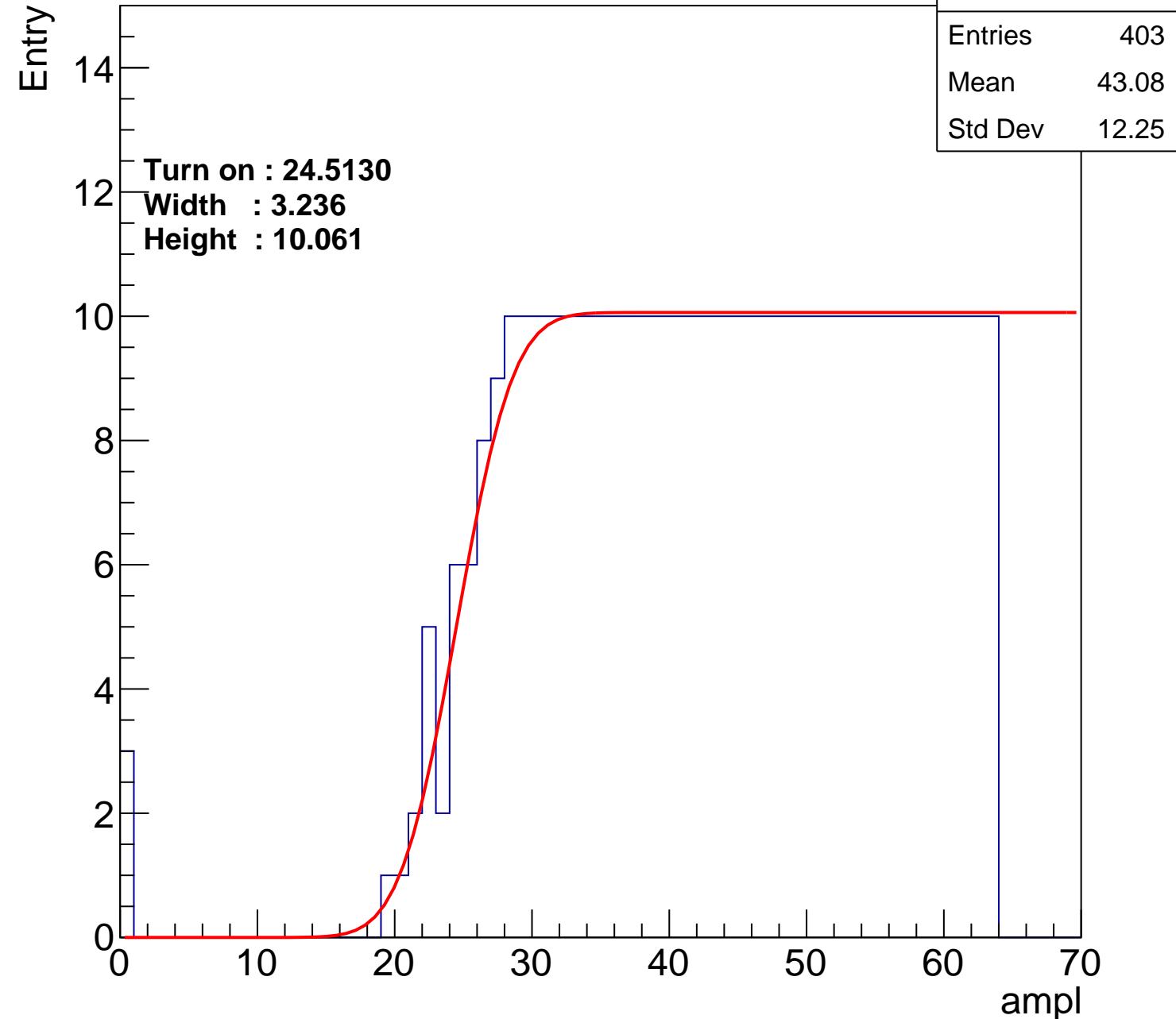
Width : 3.236

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch11

calib_packv5_042523_0143.root, FC#3, port B3

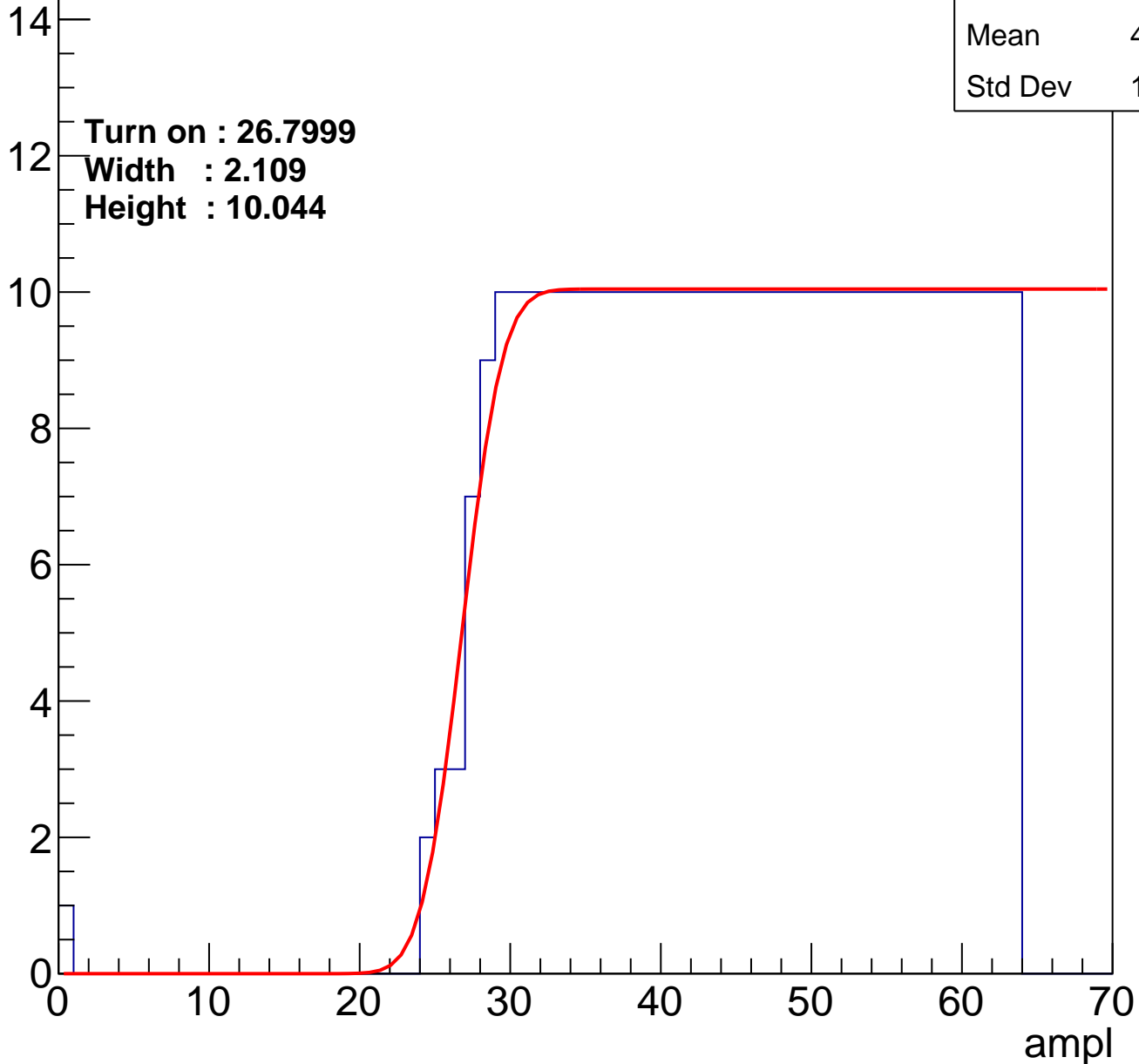
Entries	375
Mean	44.65
Std Dev	11.08

Turn on : 26.7999

Width : 2.109

Height : 10.044

Entry



B1L000S, U16-ch12

calib_packv5_042523_0143.root, FC#3, port B3

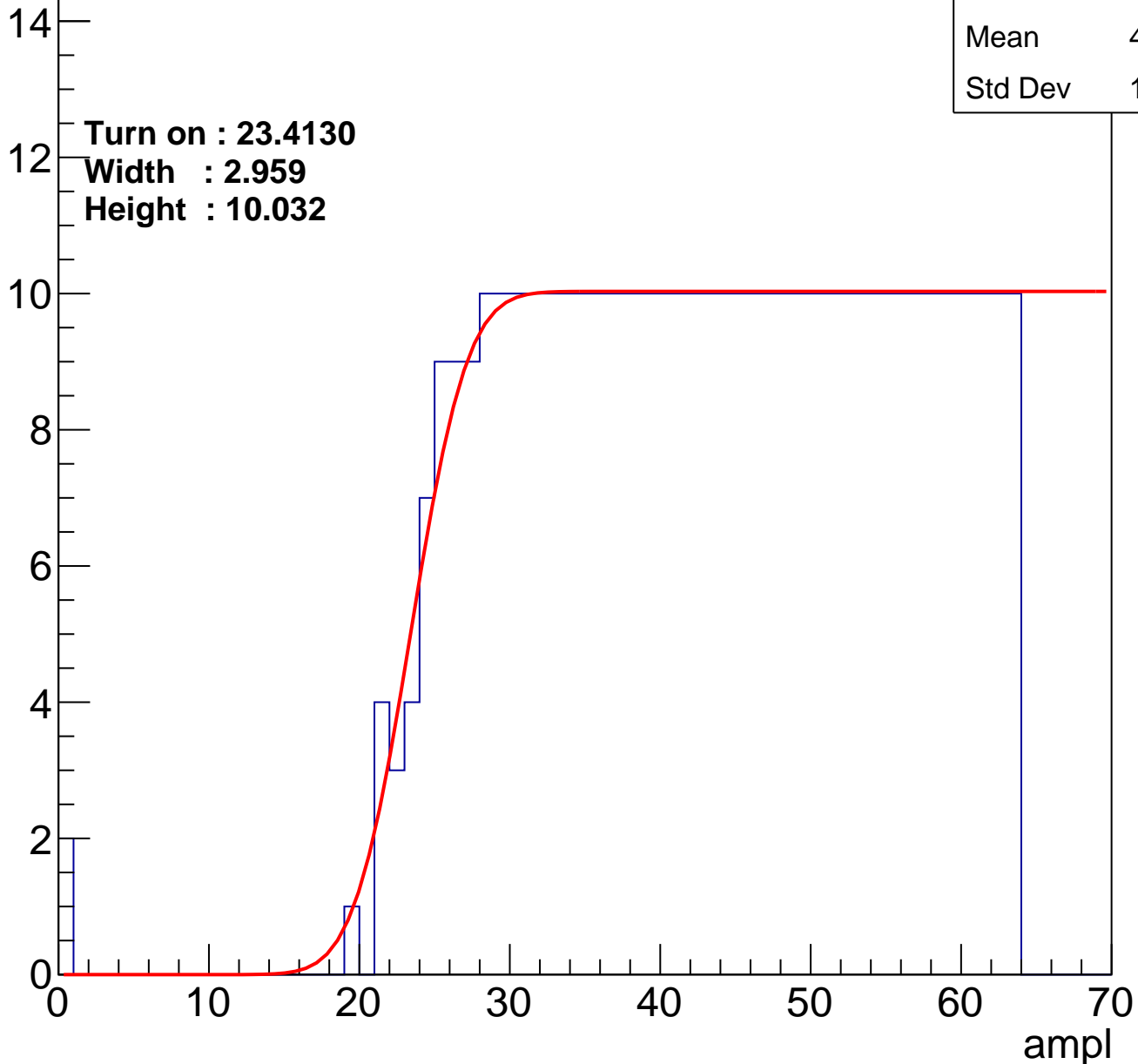
Entries	408
Mean	42.92
Std Dev	12.19

Turn on : 23.4130

Width : 2.959

Height : 10.032

Entry



B1L000S, U16-ch13

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.42
Std Dev	11.24

Turn on : 26.0014

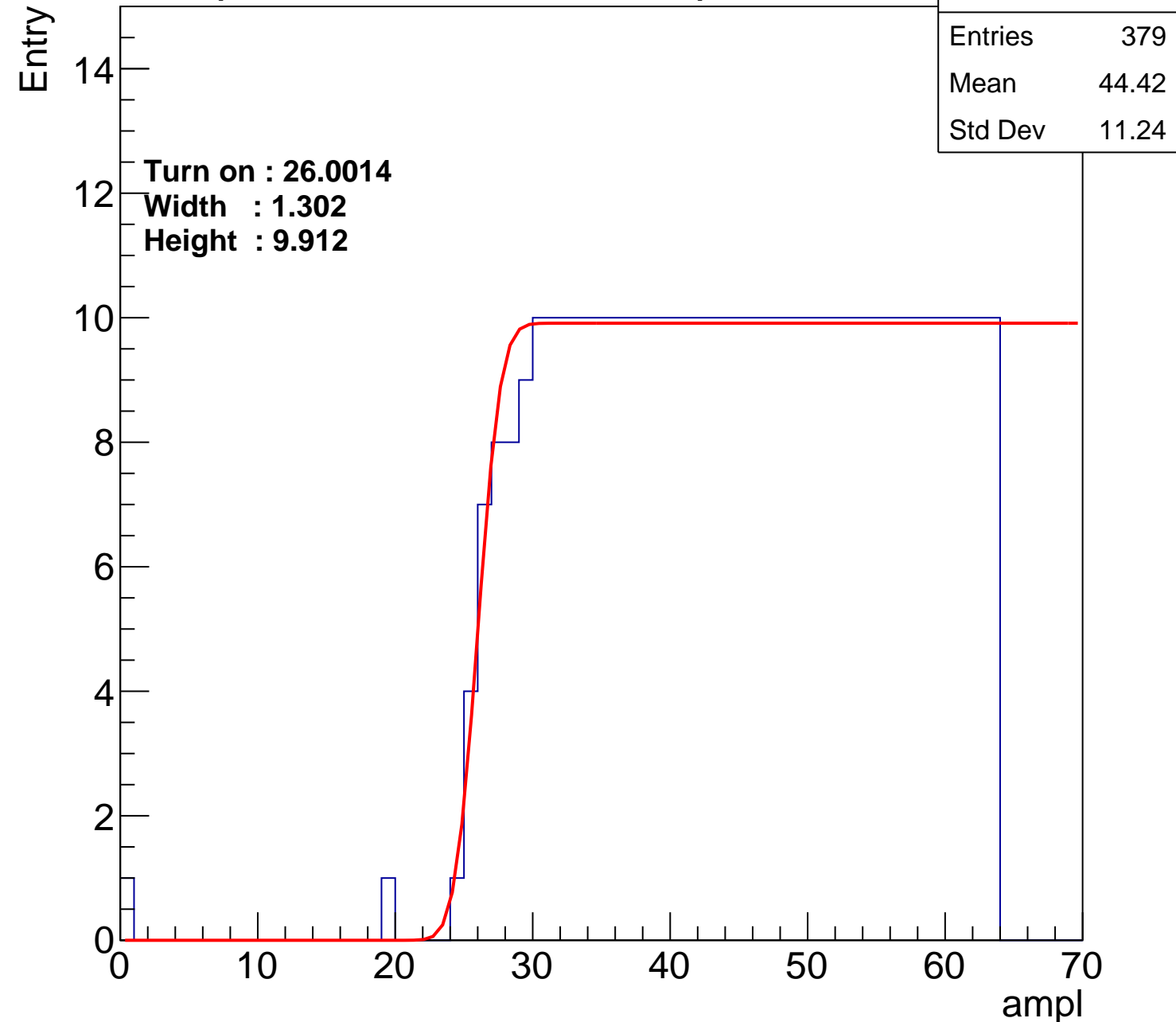
Width : 1.302

Height : 9.912

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch14

calib_packv5_042523_0143.root, FC#3, port B3

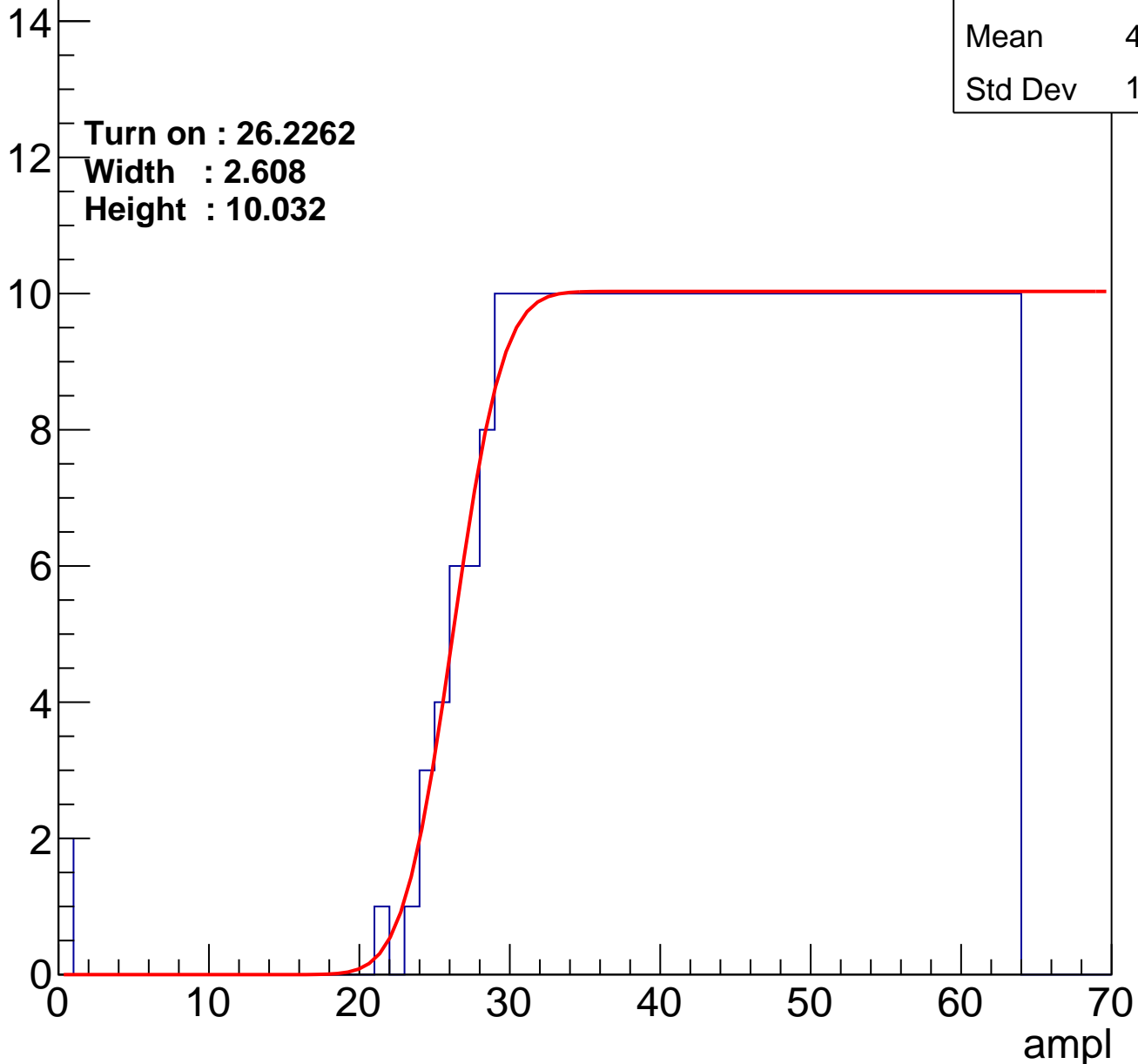
Entries	381
Mean	44.25
Std Dev	11.49

Turn on : 26.2262

Width : 2.608

Height : 10.032

Entry



B1L000S, U16-ch15

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.07
Std Dev	11.73

Turn on : 25.7295

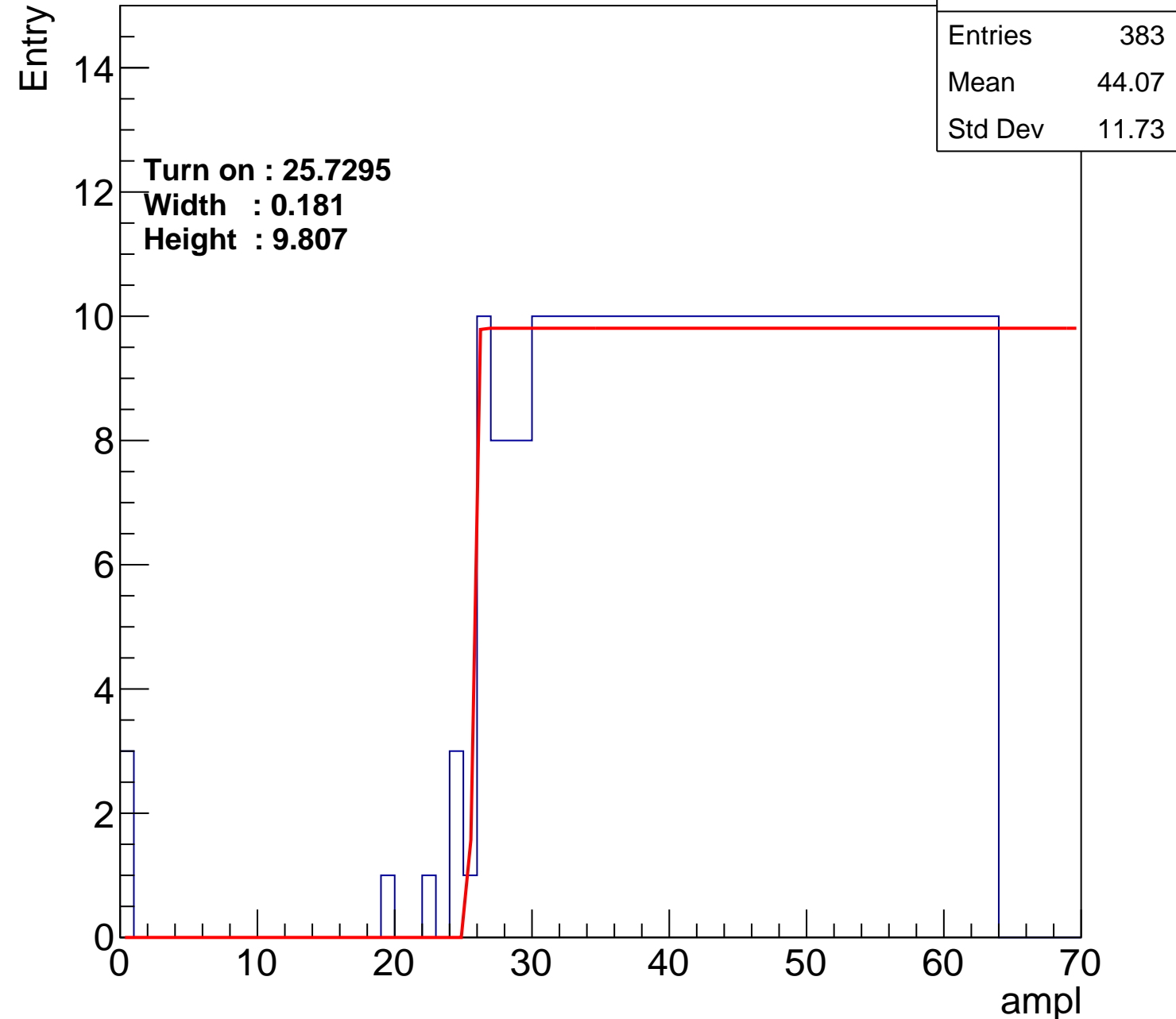
Width : 0.181

Height : 9.807

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch16

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.44
Std Dev	11.39

Turn on : 26.5099

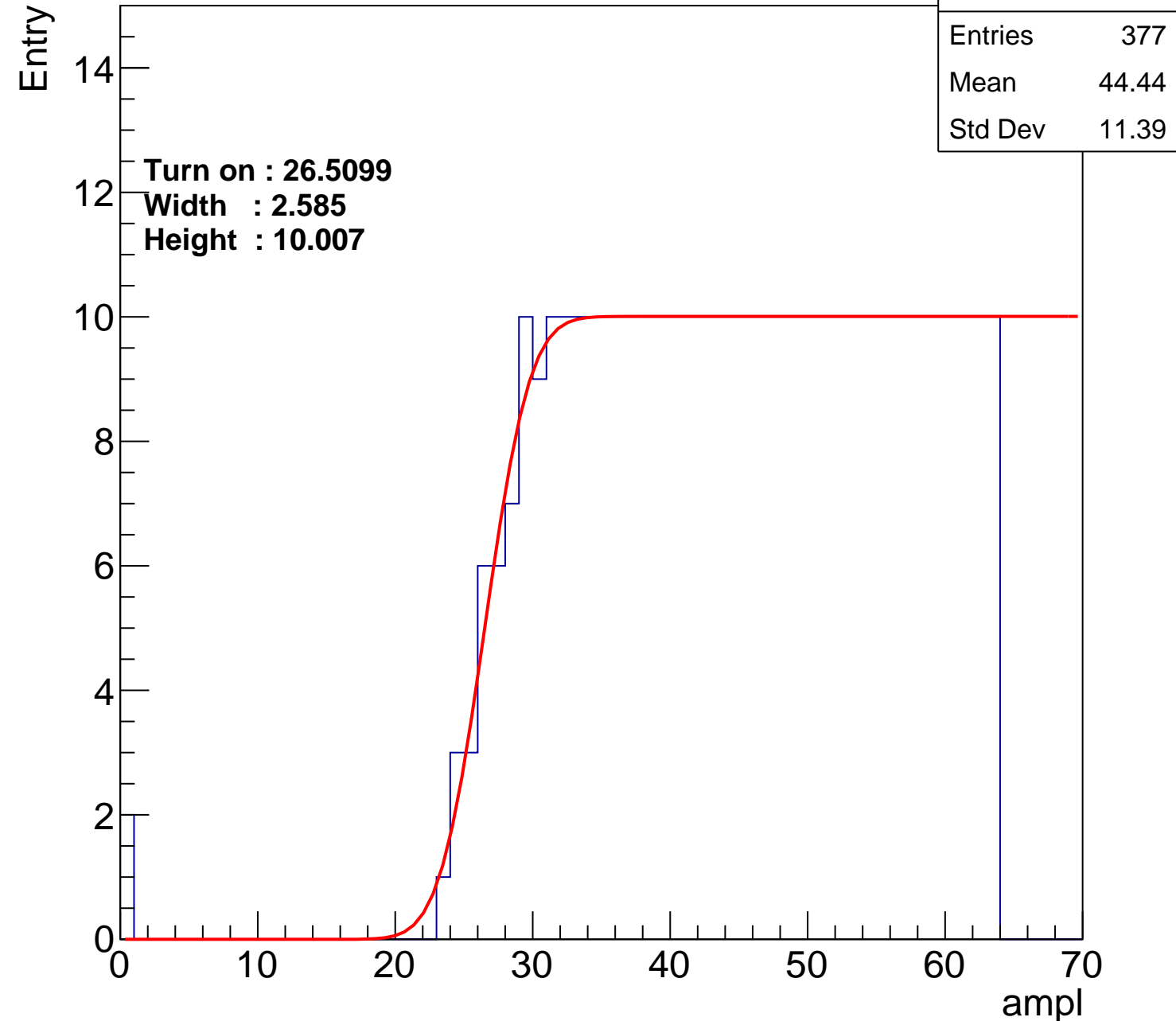
Width : 2.585

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch17

calib_packv5_042523_0143.root, FC#3, port B3

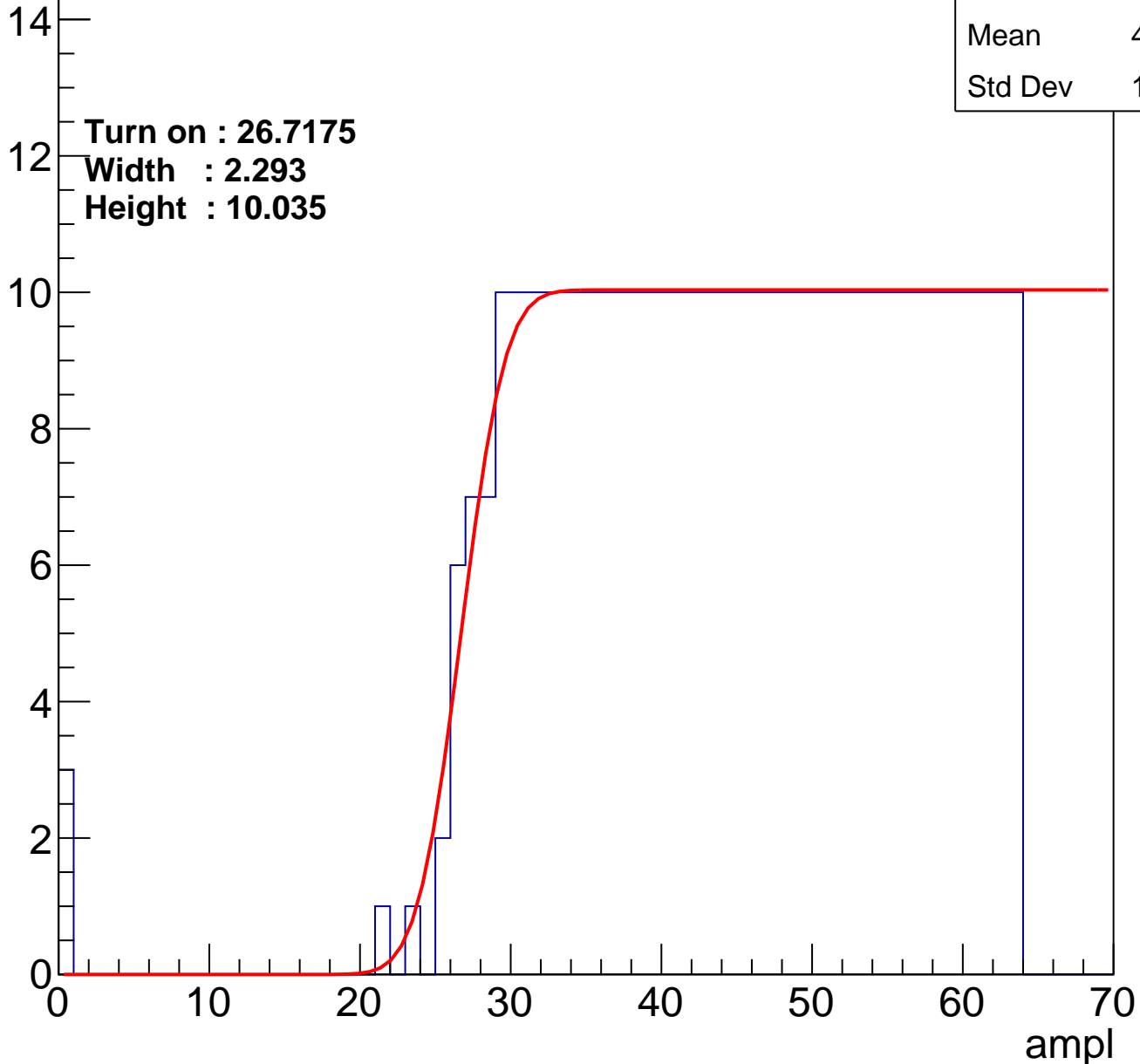
Entries	377
Mean	44.39
Std Dev	11.55

Turn on : 26.7175

Width : 2.293

Height : 10.035

Entry



B1L000S, U16-ch18

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.11
Std Dev	12.41

Turn on : 24.5822

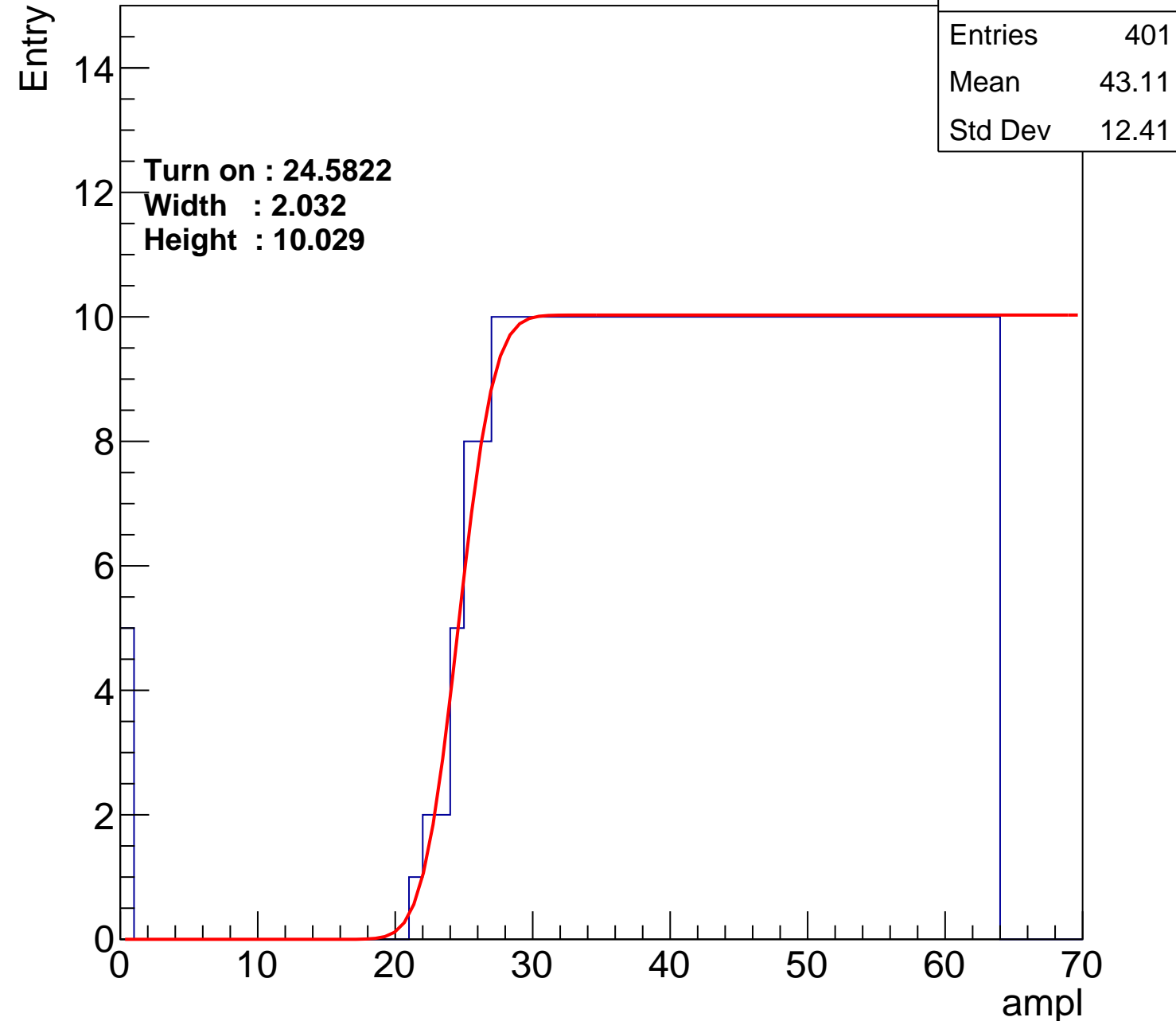
Width : 2.032

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch19

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.28
Std Dev	11.3

Turn on : 26.0926

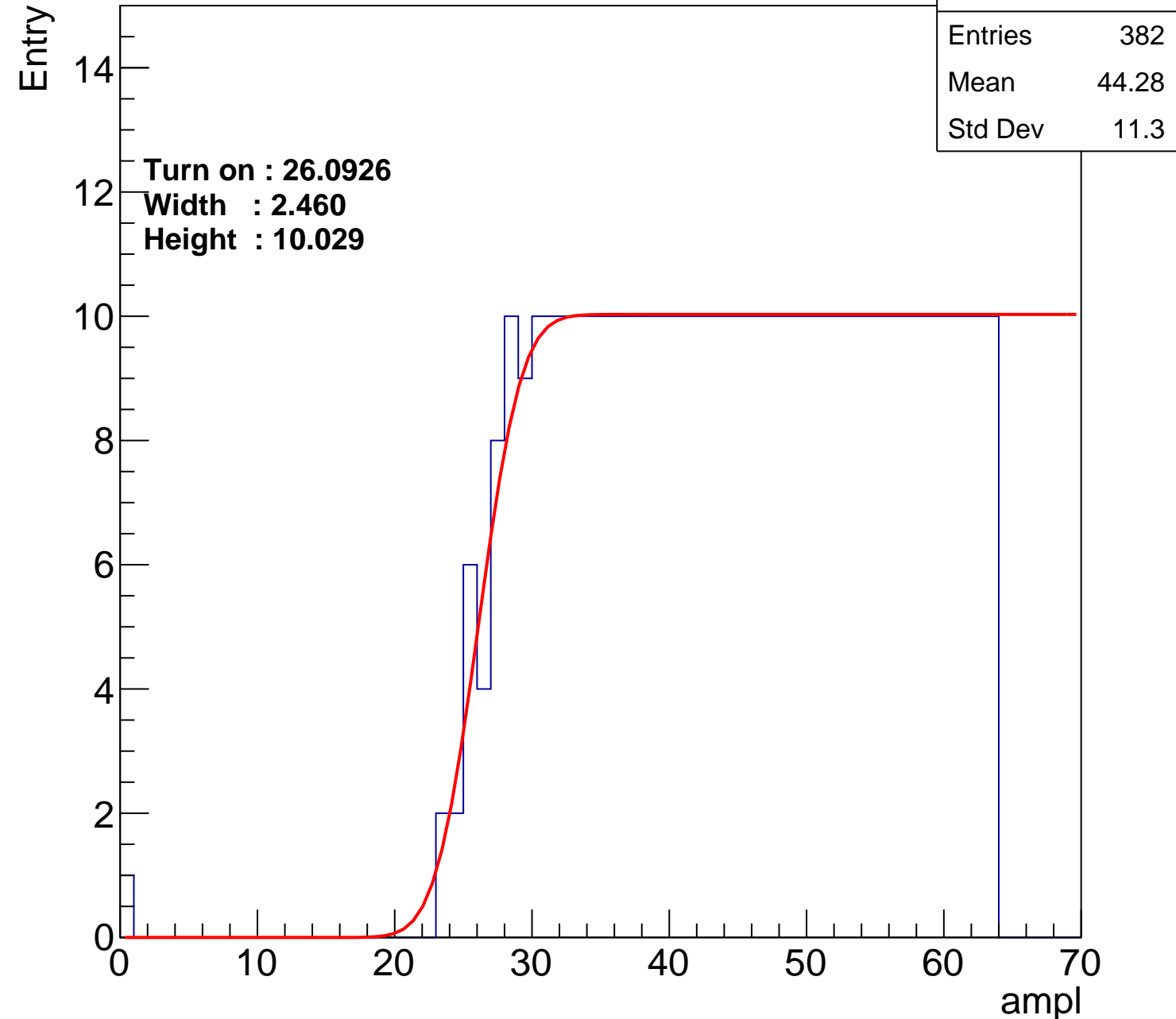
Width : 2.460

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch20

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.45
Std Dev	11.25

Turn on : 26.6393

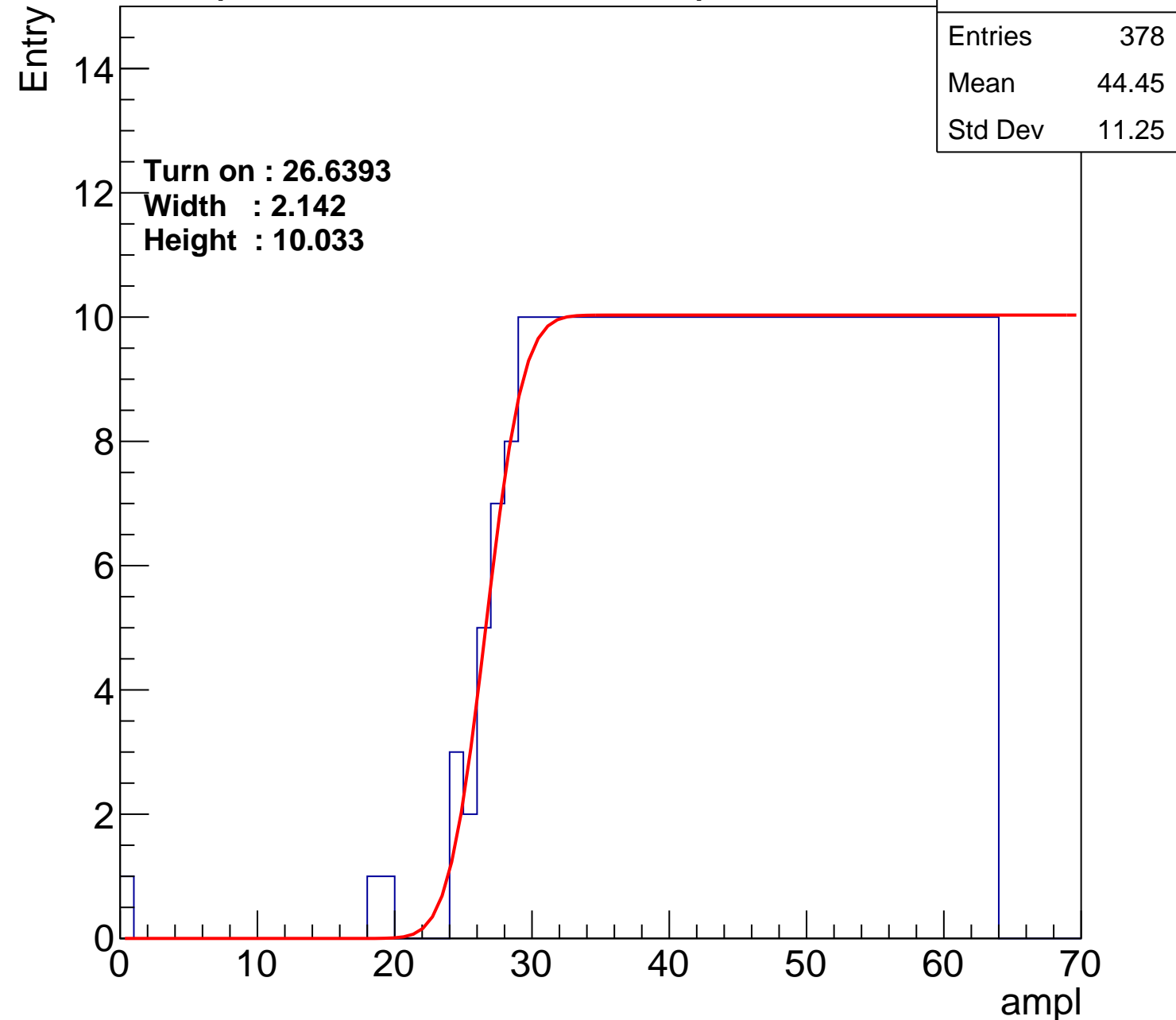
Width : 2.142

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch21

calib_packv5_042523_0143.root, FC#3, port B3

Entries	366
Mean	44.85
Std Dev	11.49

Turn on : 27.6586

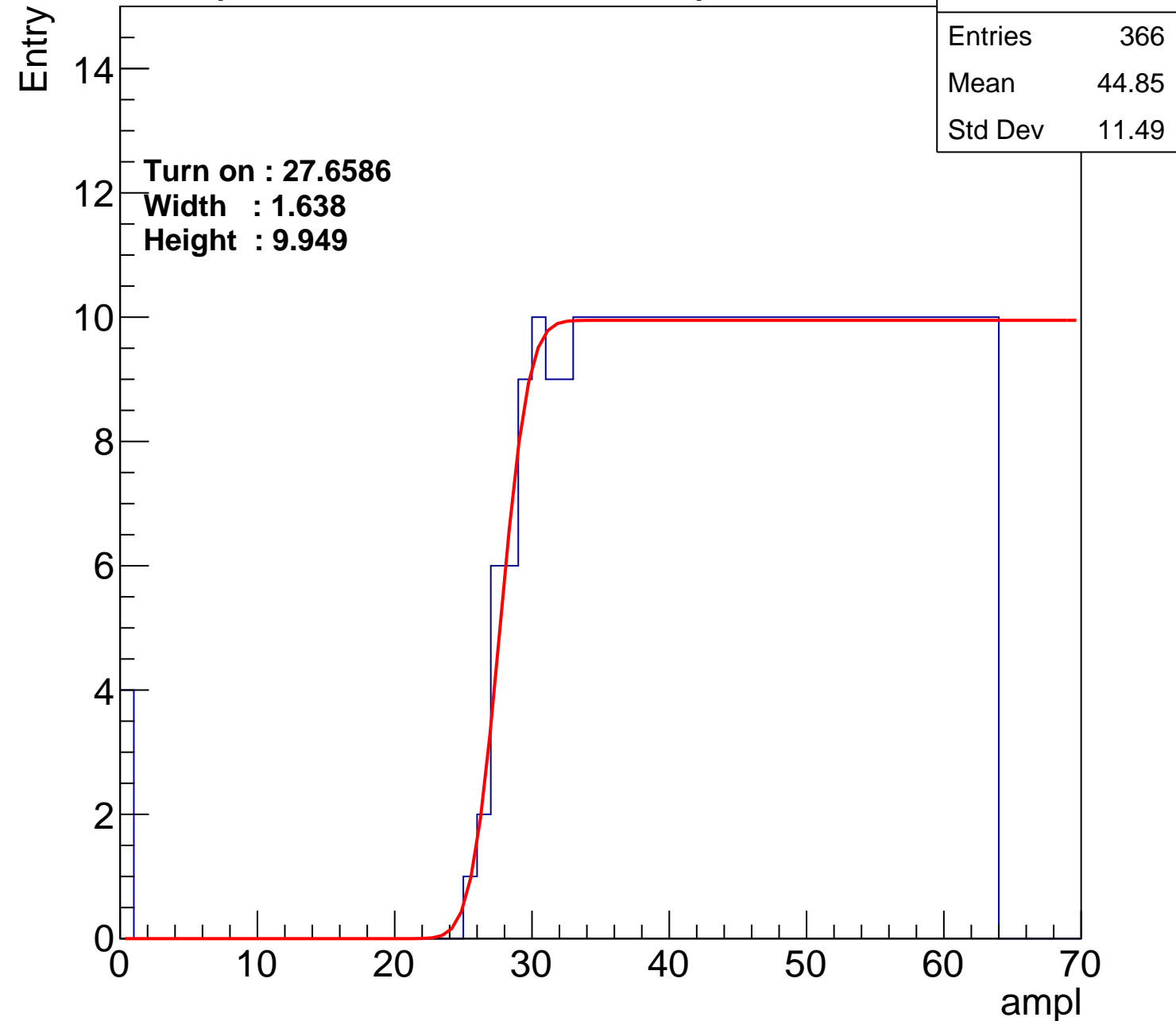
Width : 1.638

Height : 9.949

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch22

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.8
Std Dev	11.72

Turn on : 25.3501

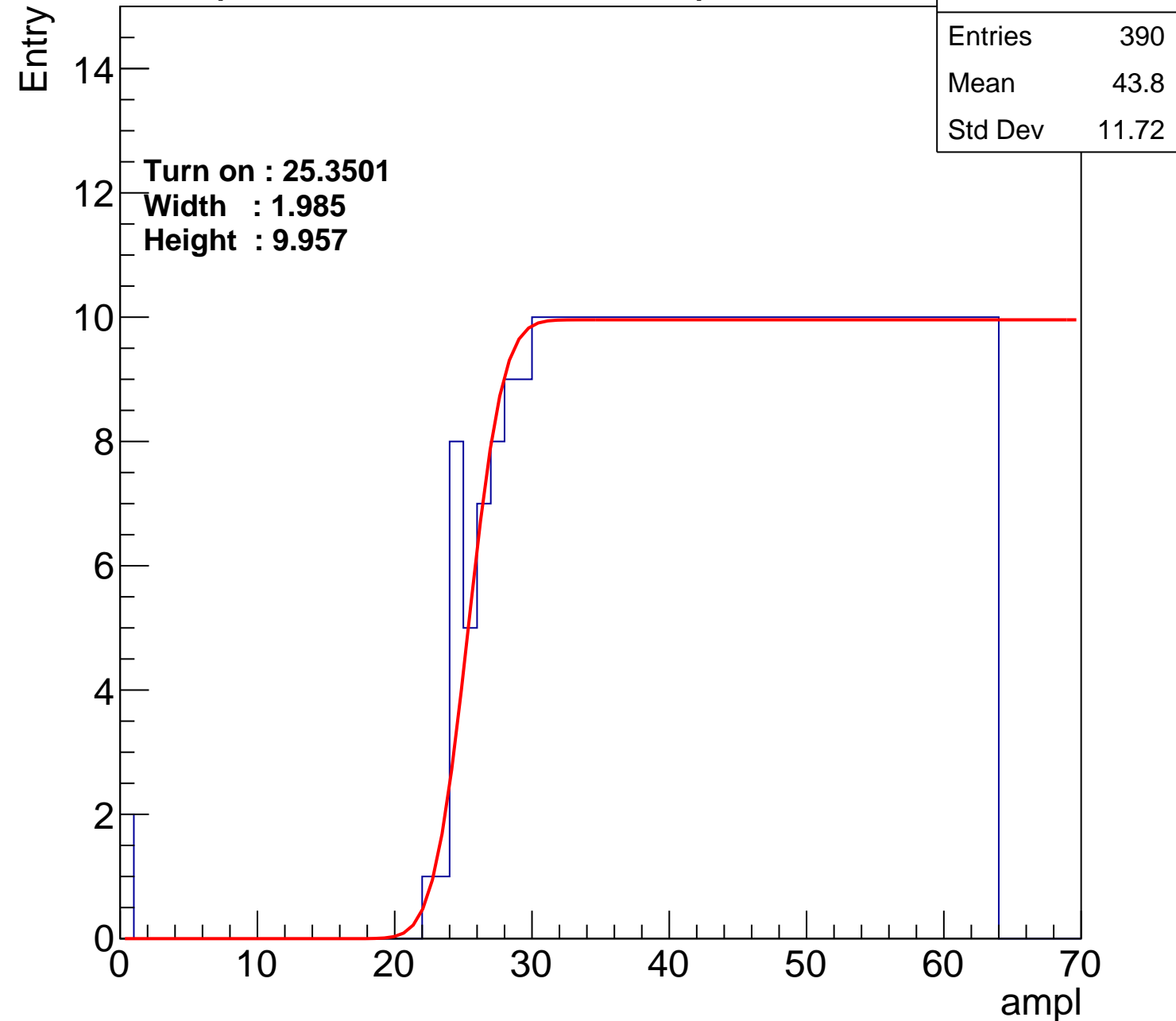
Width : 1.985

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch23

calib_packv5_042523_0143.root, FC#3, port B3

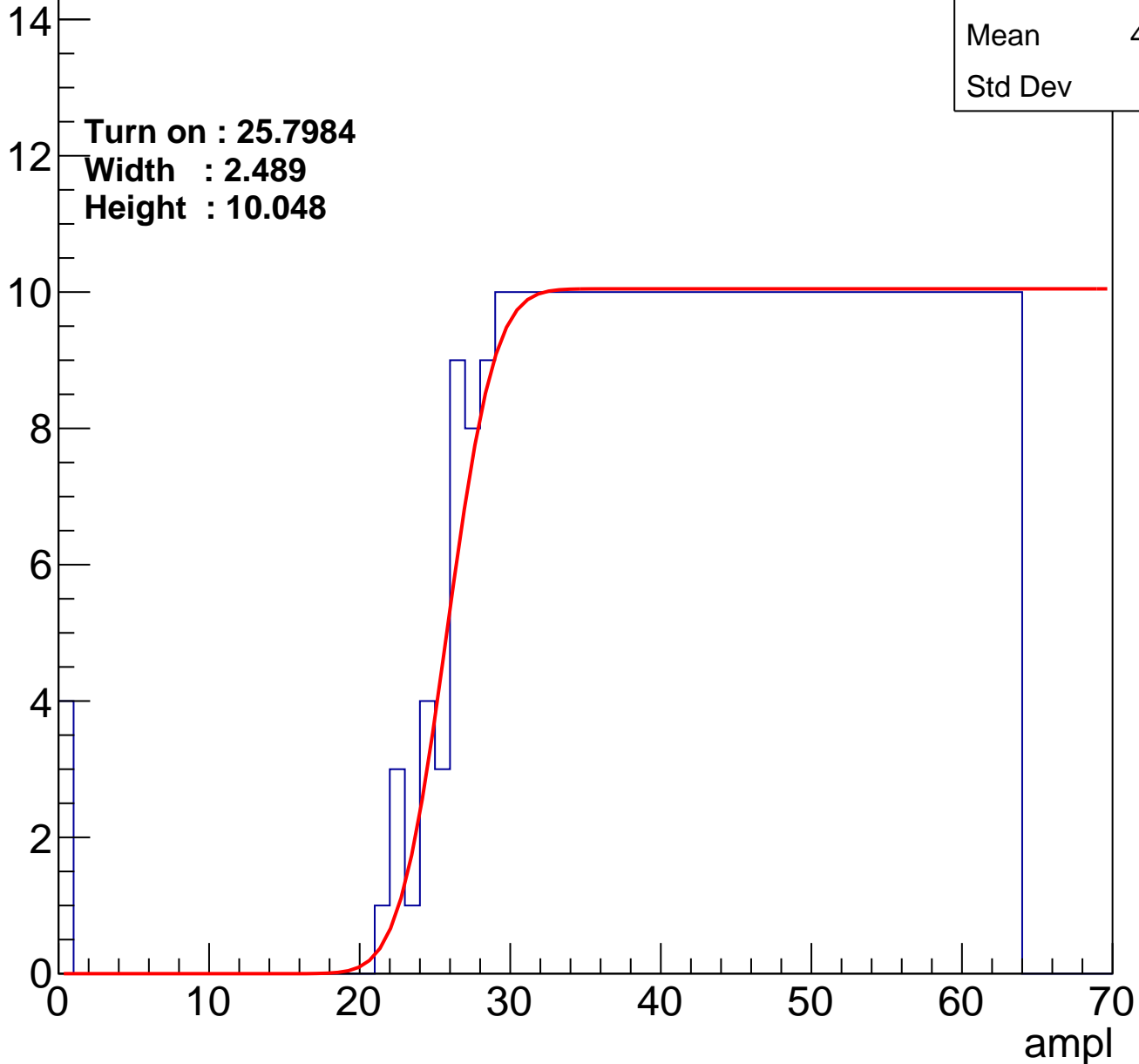
Entries	392
Mean	43.58
Std Dev	12.1

Turn on : 25.7984

Width : 2.489

Height : 10.048

Entry



calib_packv5_042523_0143.root, FC#3, port B3

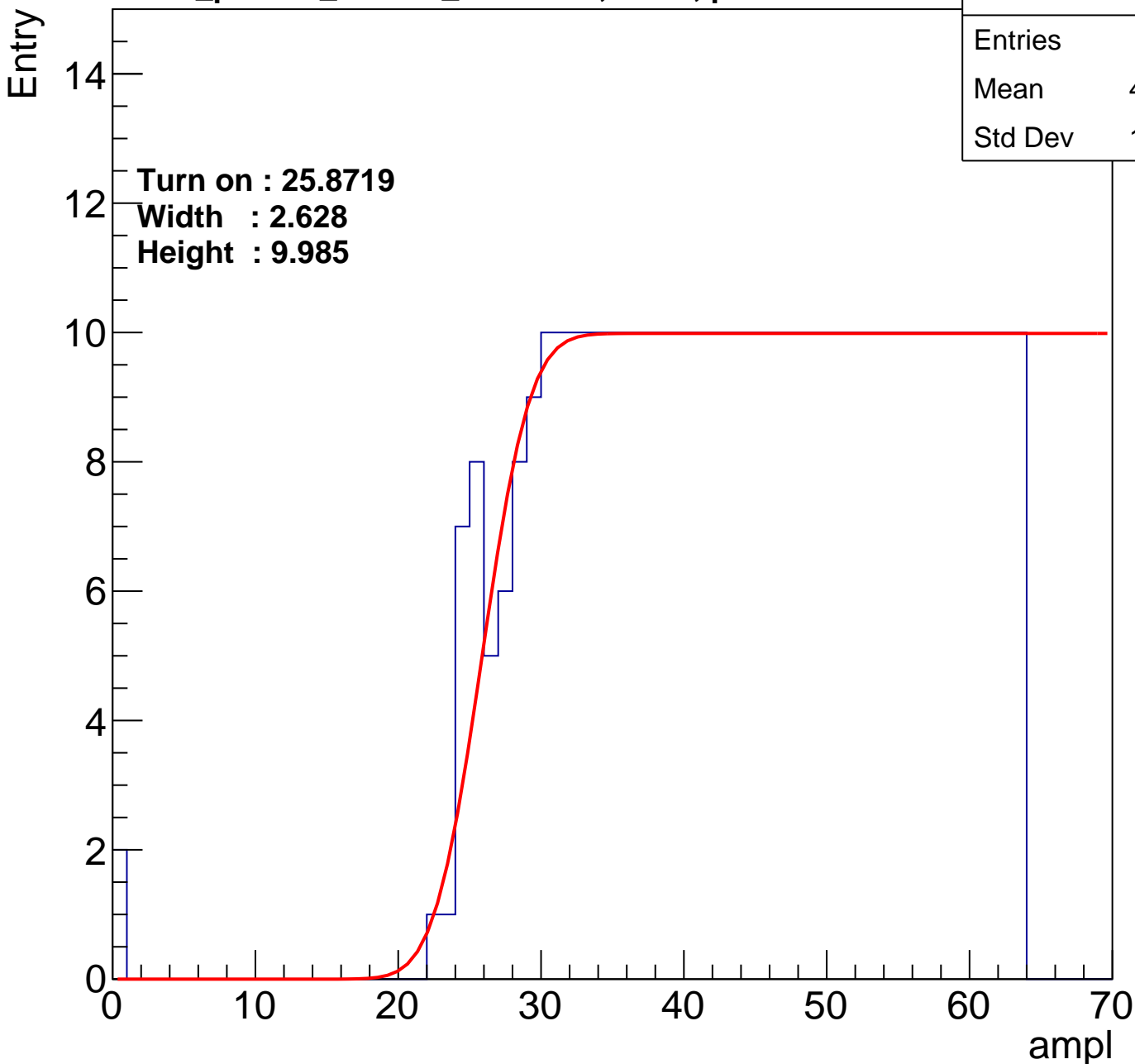
Entries	387
Mean	43.93
Std Dev	11.68

Mean	43.93
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Std Dev	11.68
---------	-------

Width : 2.628

Height : 9.985



B1L000S, U16-ch25

calib_packv5_042523_0143.root, FC#3, port B3

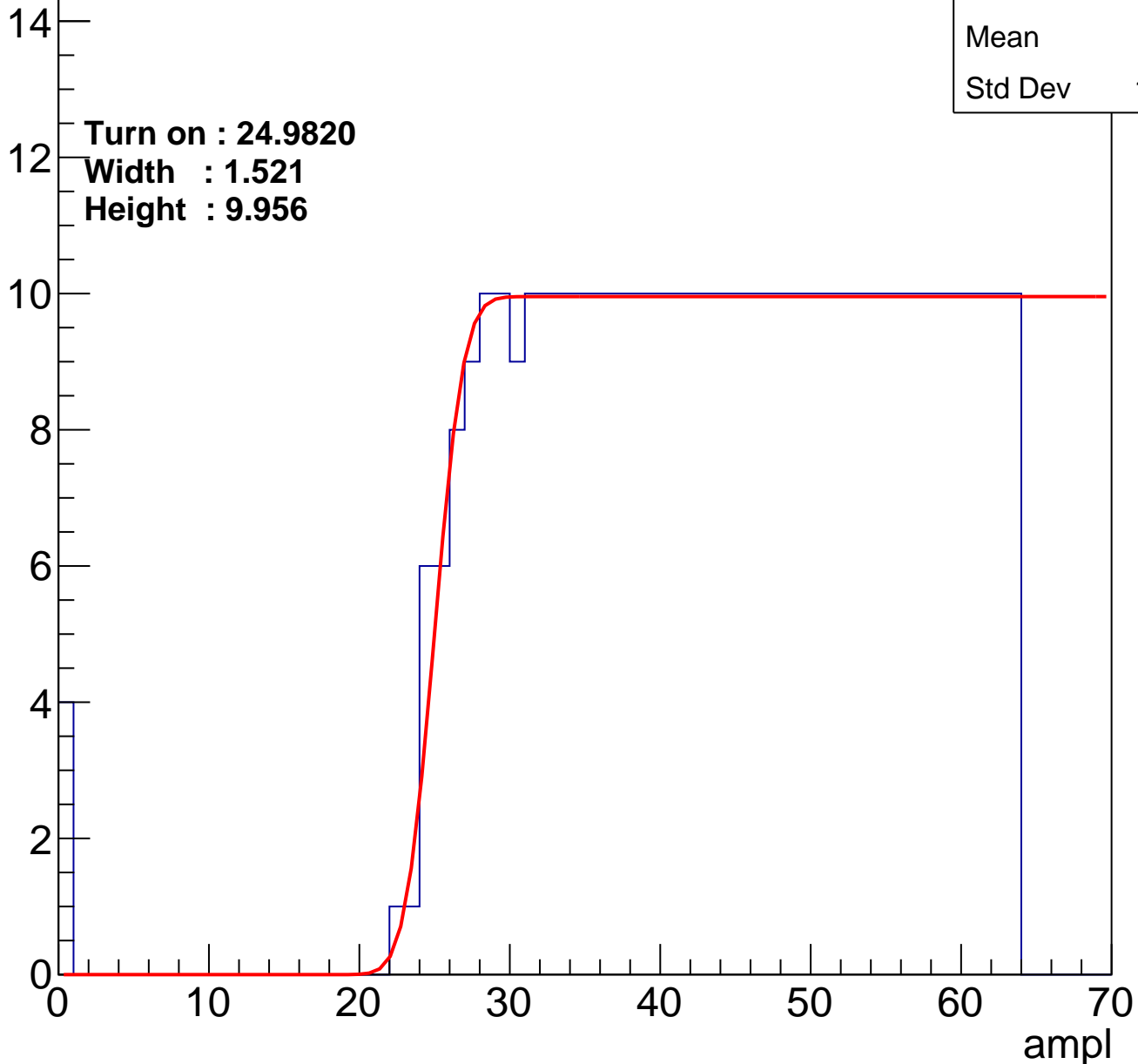
Entries	394
Mean	43.5
Std Dev	12.11

Turn on : 24.9820

Width : 1.521

Height : 9.956

Entry



B1L000S, U16-ch26

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.78
Std Dev	11.76

Turn on : 25.3549

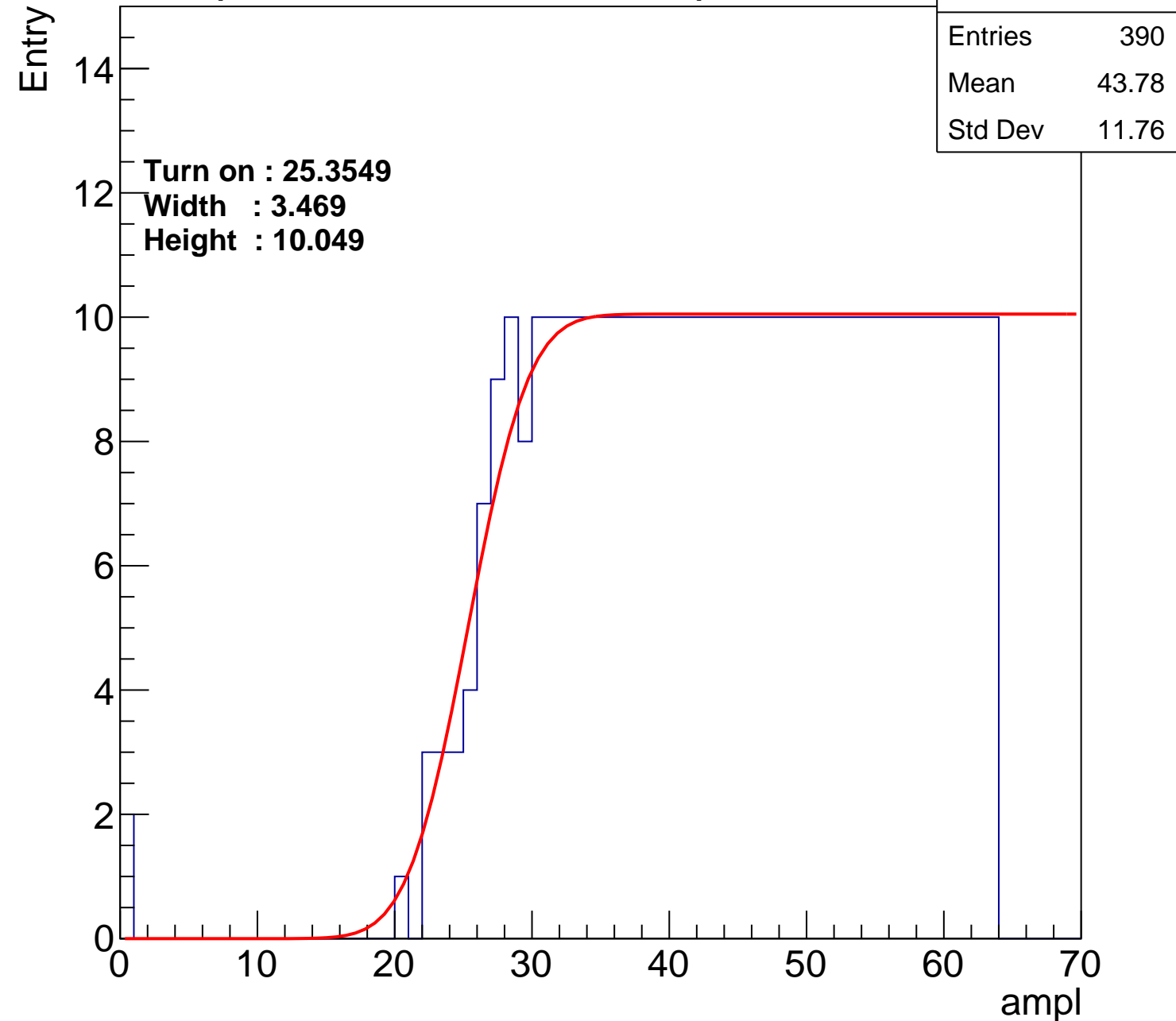
Width : 3.469

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch27

calib_packv5_042523_0143.root, FC#3, port B3

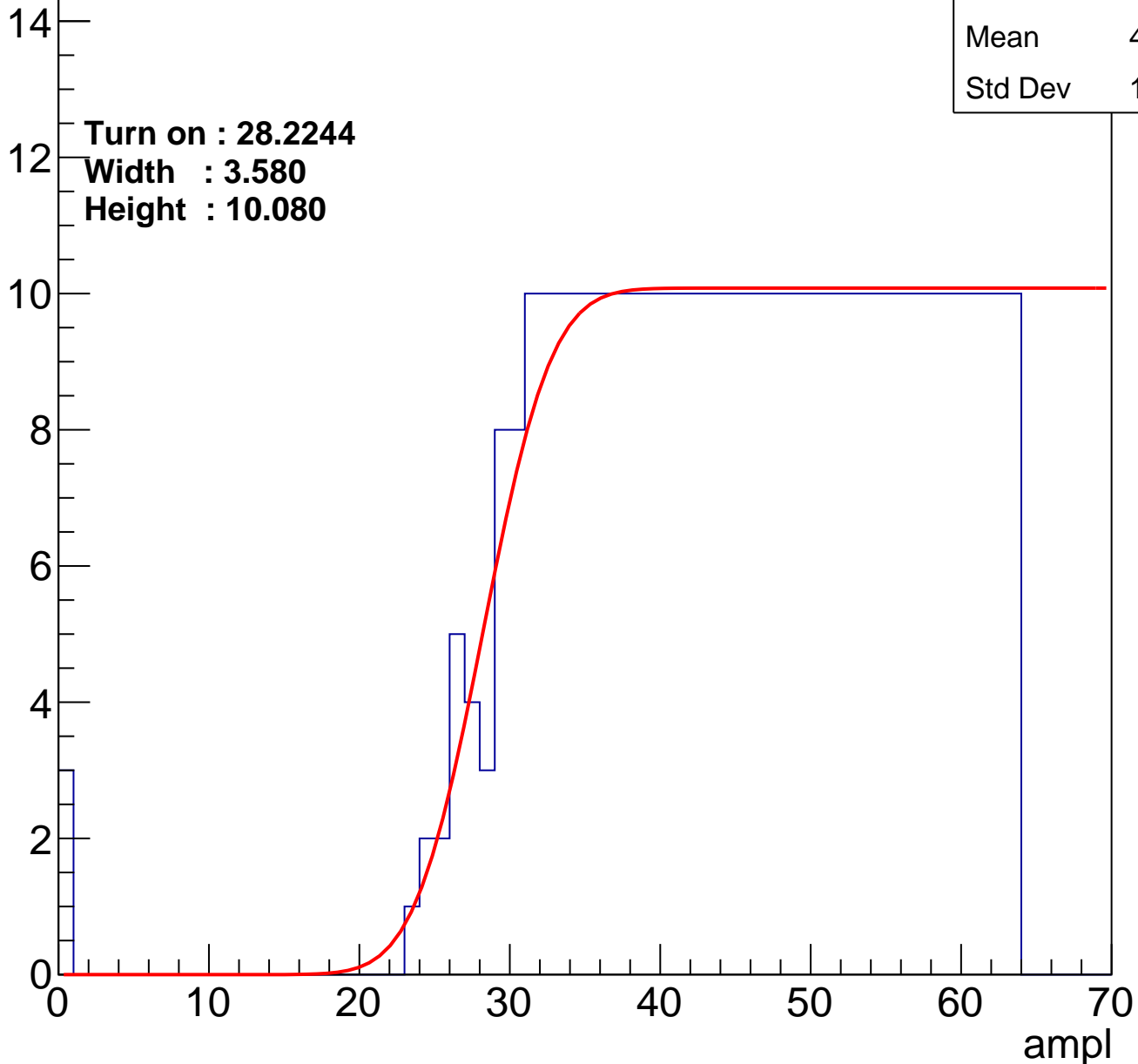
Entries	366
Mean	44.88
Std Dev	11.37

Turn on : 28.2244

Width : 3.580

Height : 10.080

Entry



B1L000S, U16-ch28

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.1
Std Dev	11.56

Turn on : 25.3440

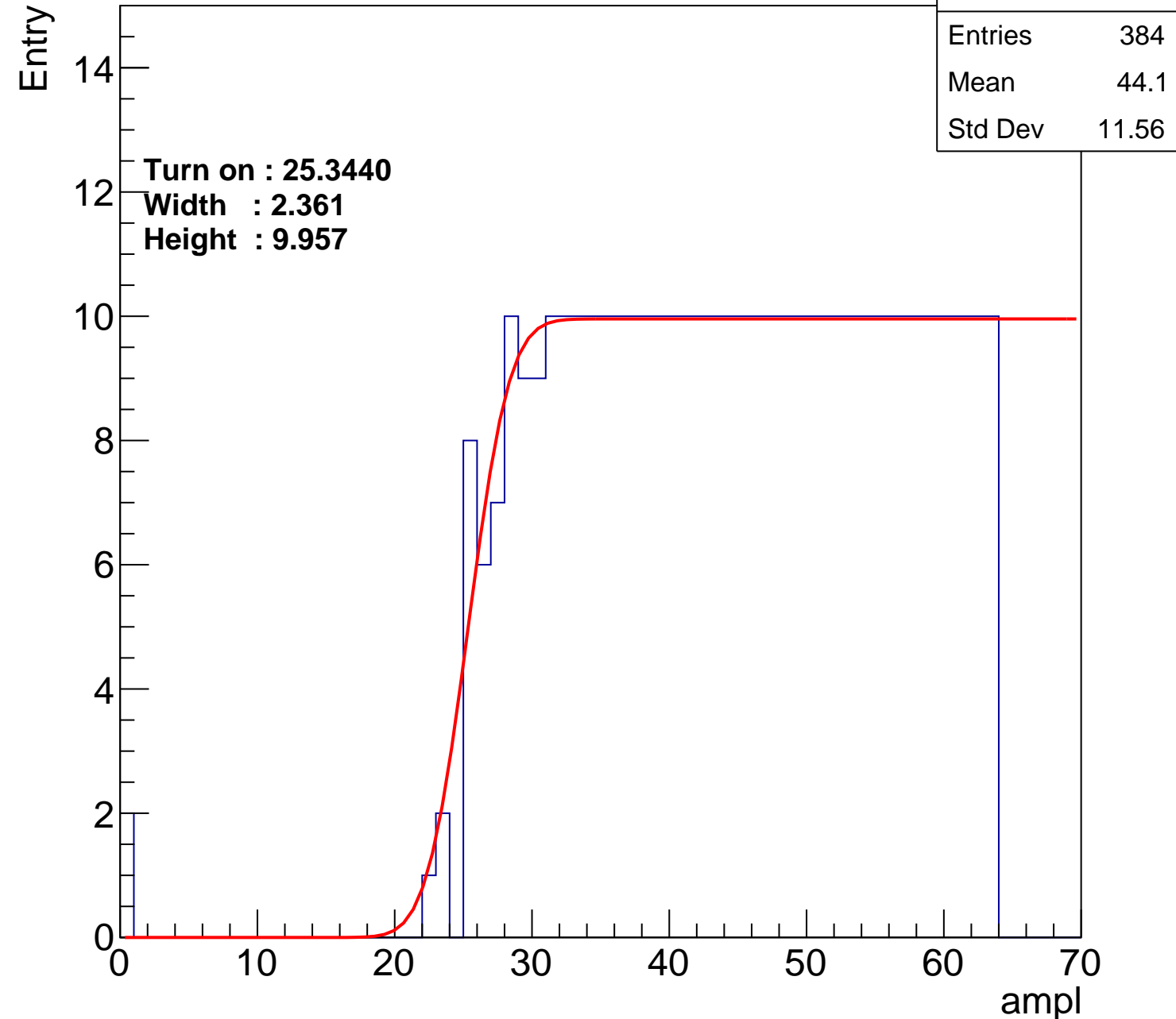
Width : 2.361

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch29

calib_packv5_042523_0143.root, FC#3, port B3

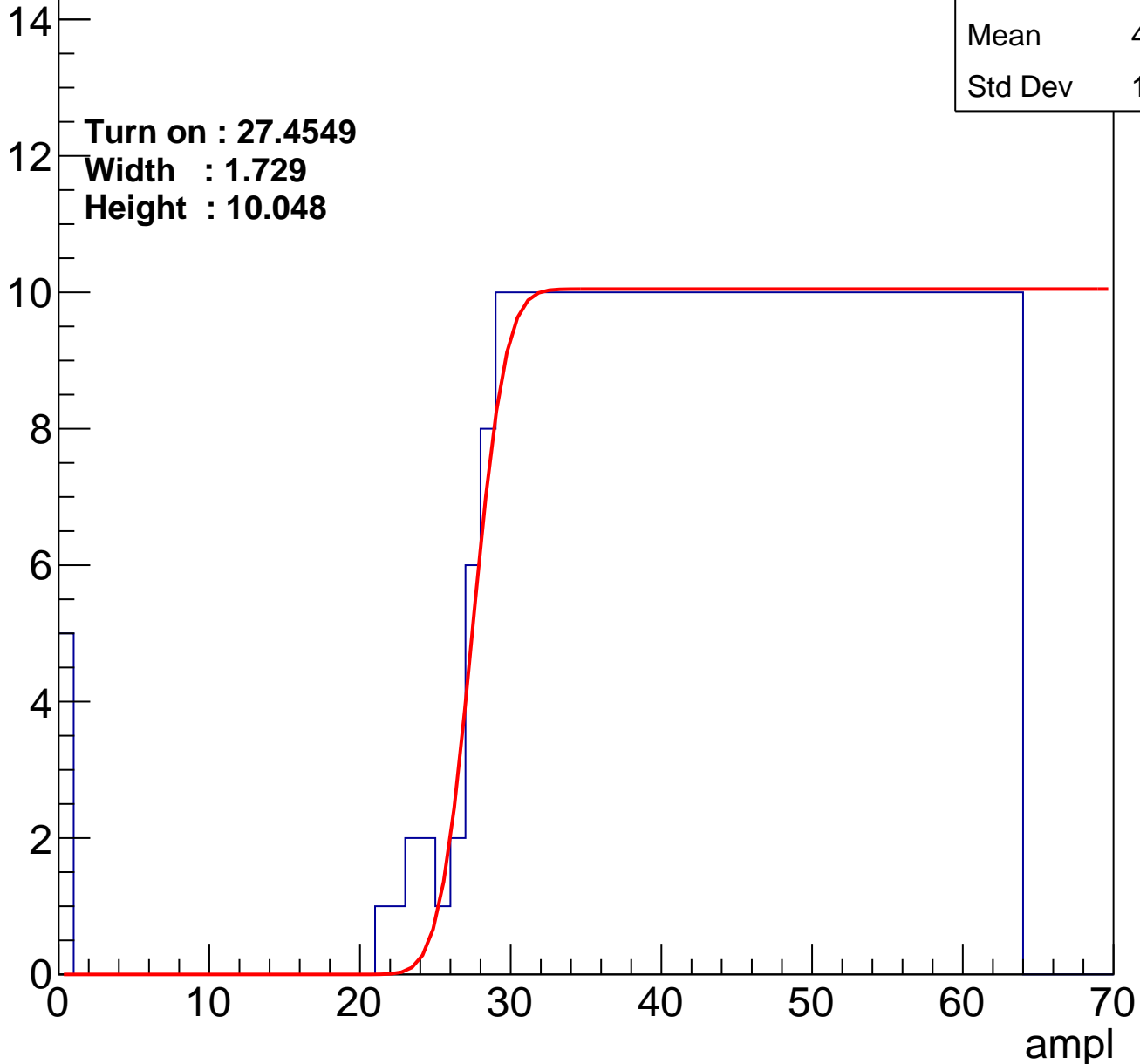
Entries	378
Mean	44.18
Std Dev	11.98

Turn on : 27.4549

Width : 1.729

Height : 10.048

Entry



B1L000S, U16-ch30

calib_packv5_042523_0143.root, FC#3, port B3

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.6169

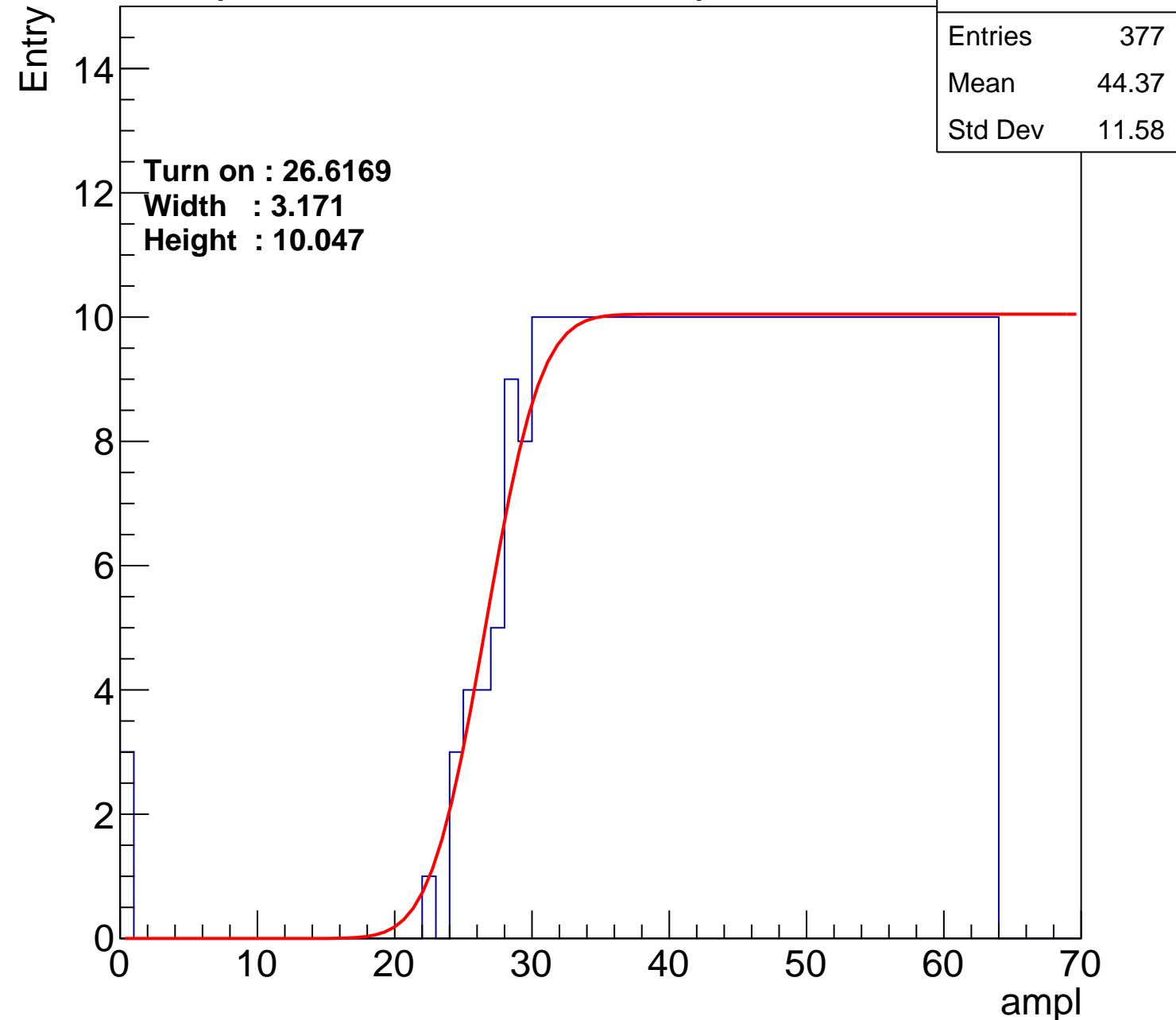
Width : 3.171

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch31

calib_packv5_042523_0143.root, FC#3, port B3

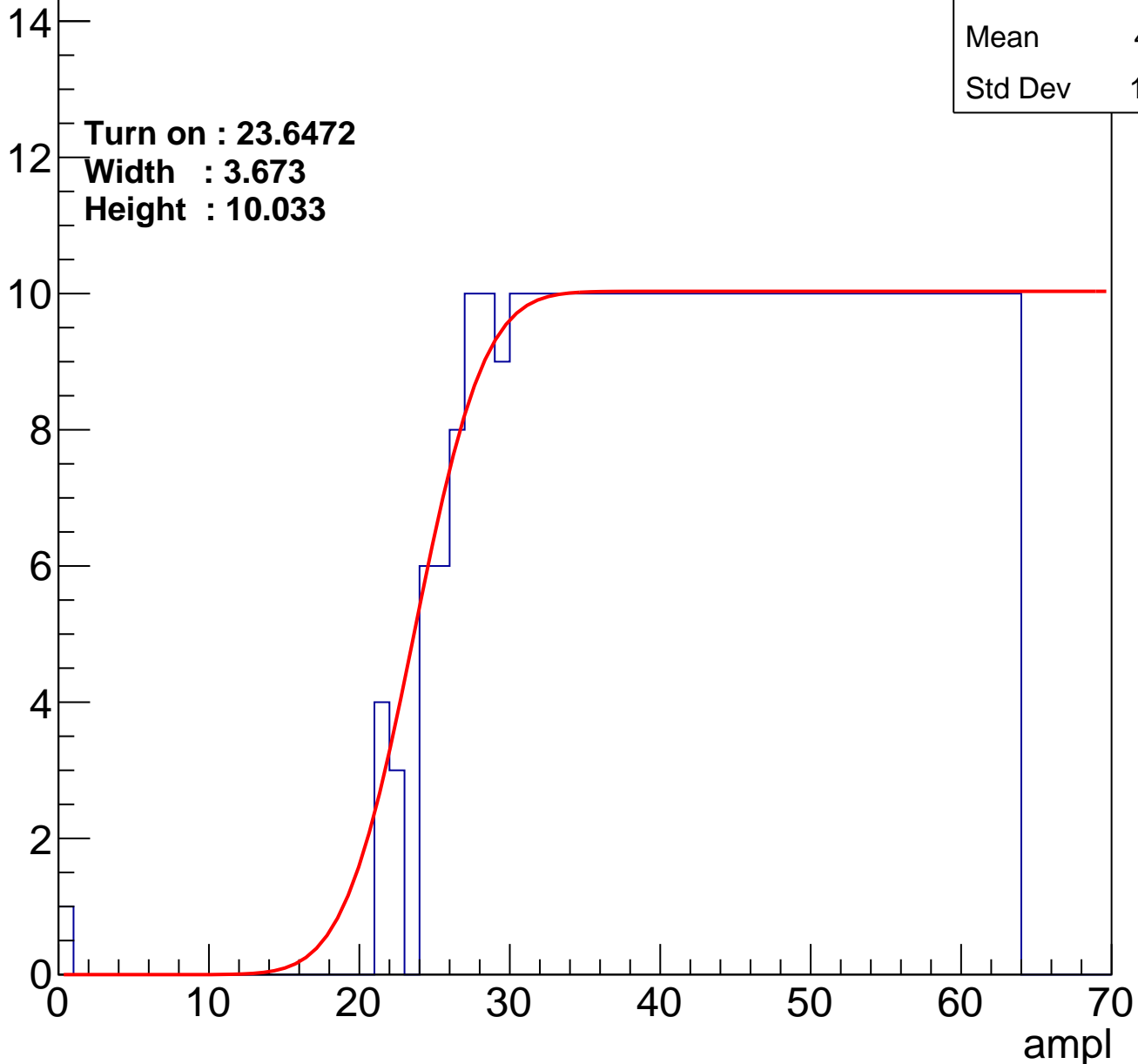
Entries	397
Mean	43.51
Std Dev	11.76

Turn on : 23.6472

Width : 3.673

Height : 10.033

Entry



B1L000S, U16-ch32

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.45
Std Dev	11.24

Turn on : 26.6480

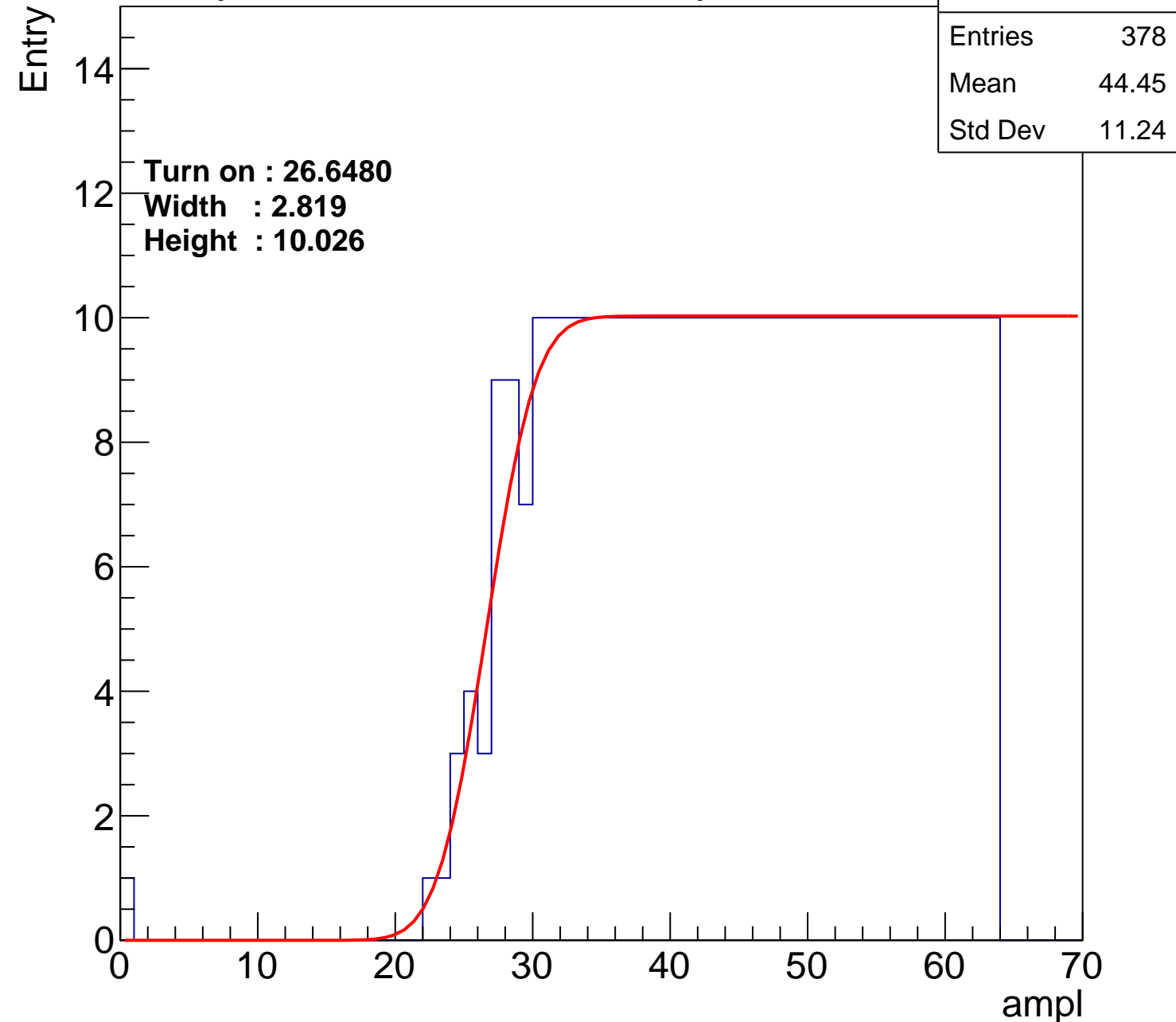
Width : 2.819

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch33

calib_packv5_042523_0143.root, FC#3, port B3

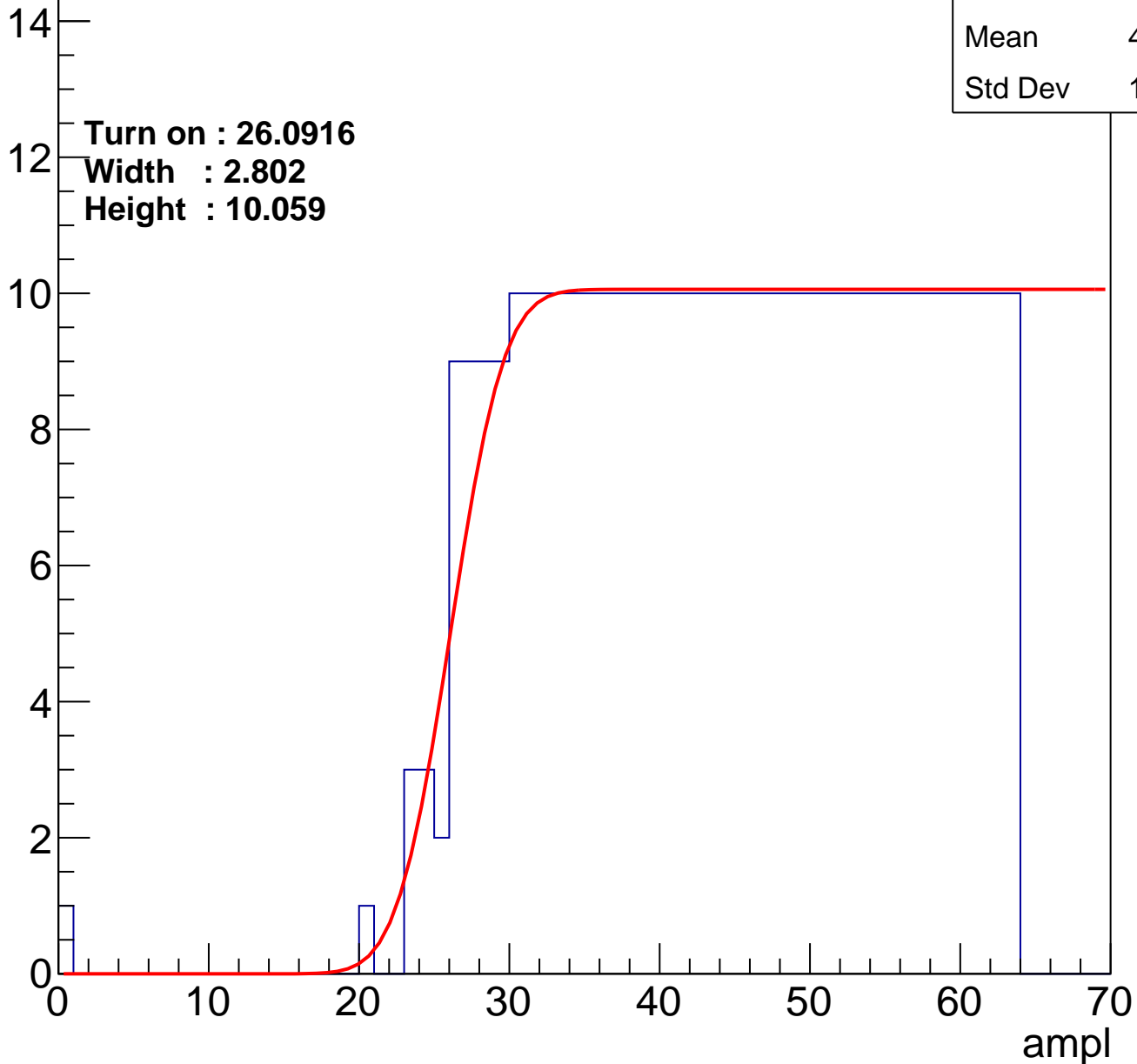
Entries	386
Mean	44.07
Std Dev	11.43

Turn on : 26.0916

Width : 2.802

Height : 10.059

Entry



B1L000S, U16-ch34

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.76
Std Dev	11.85

Turn on : 25.4720

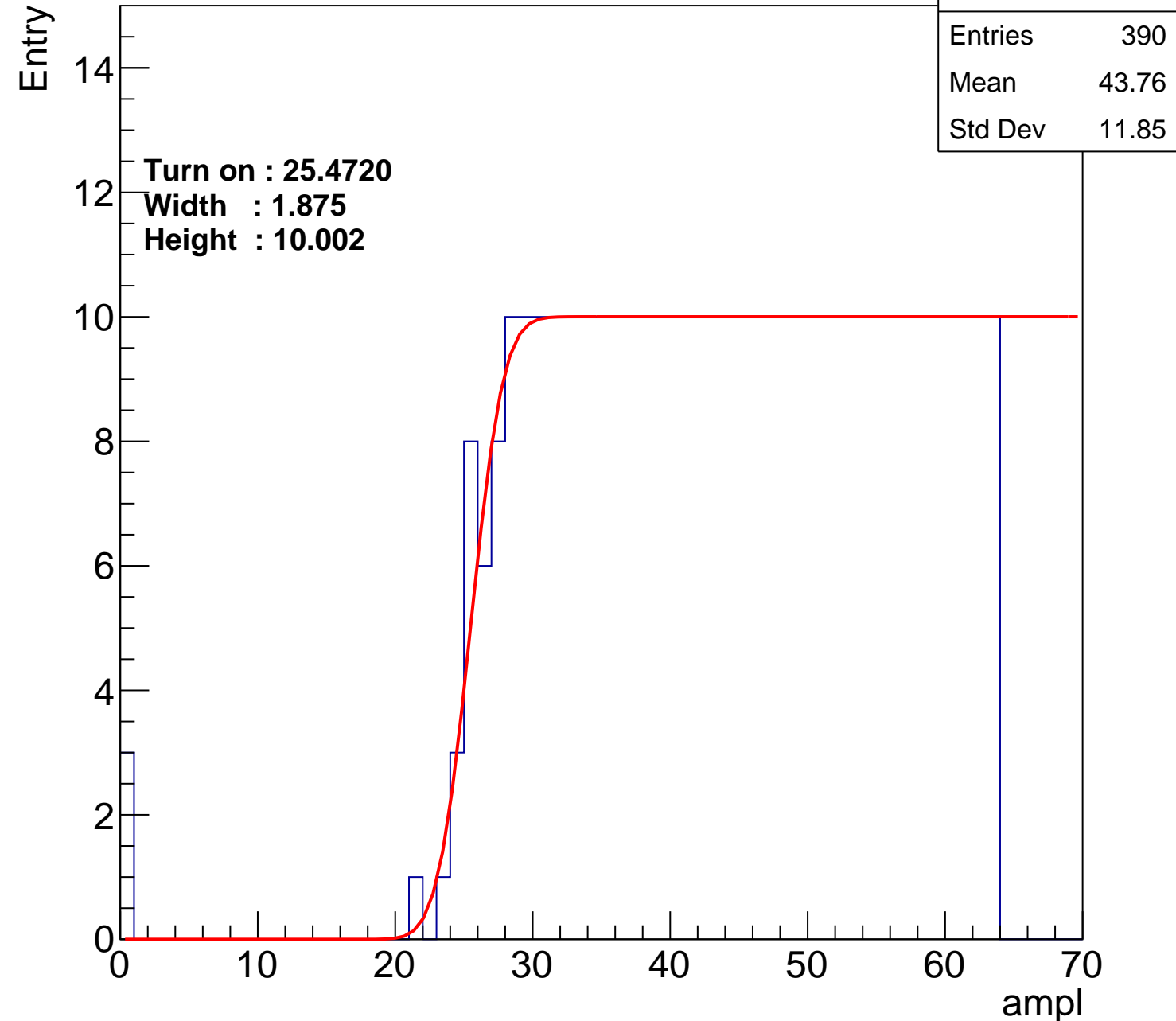
Width : 1.875

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch35

calib_packv5_042523_0143.root, FC#3, port B3

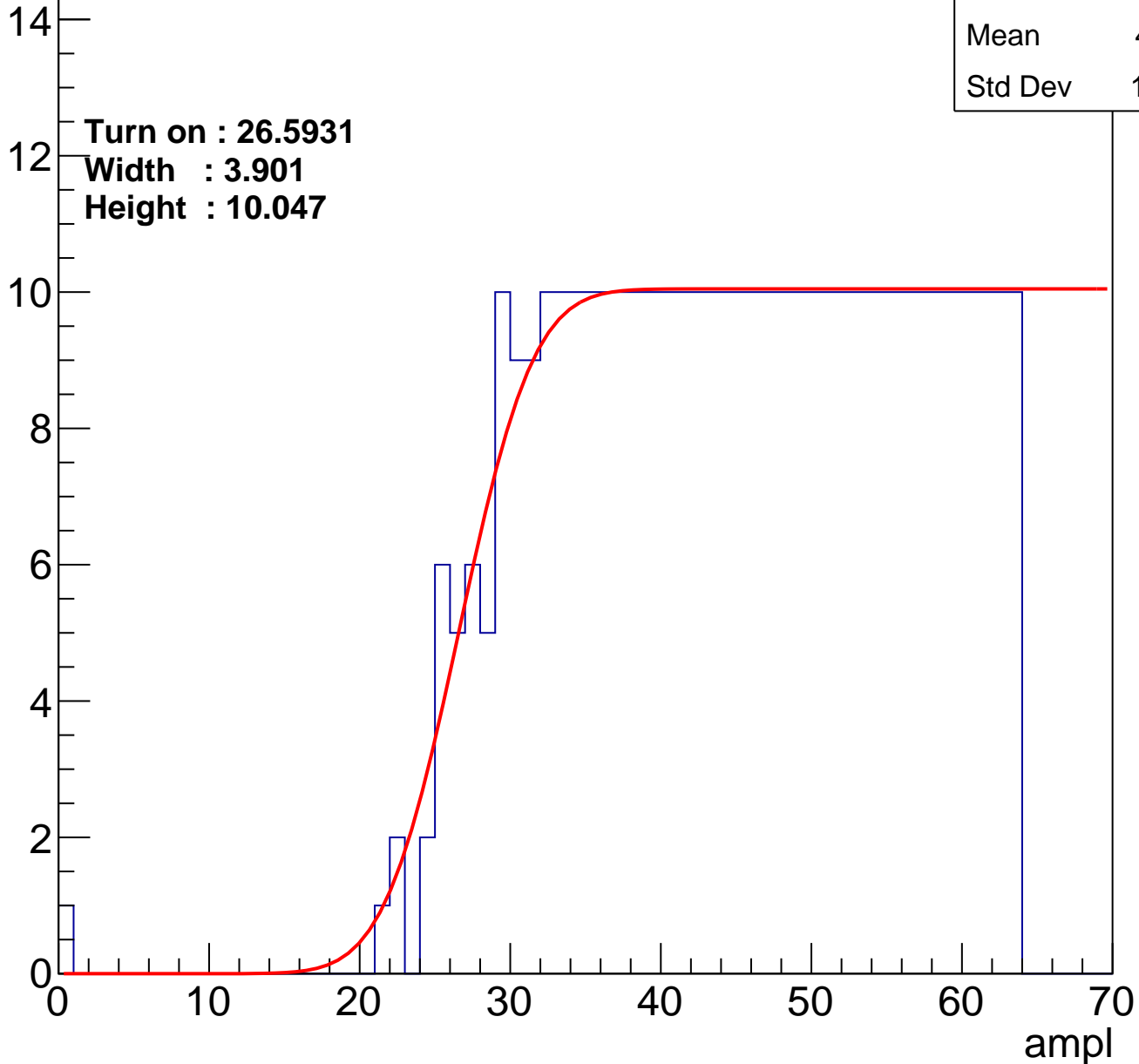
Entries	376
Mean	44.51
Std Dev	11.26

Turn on : 26.5931

Width : 3.901

Height : 10.047

Entry



B1L000S, U16-ch36

calib_packv5_042523_0143.root, FC#3, port B3

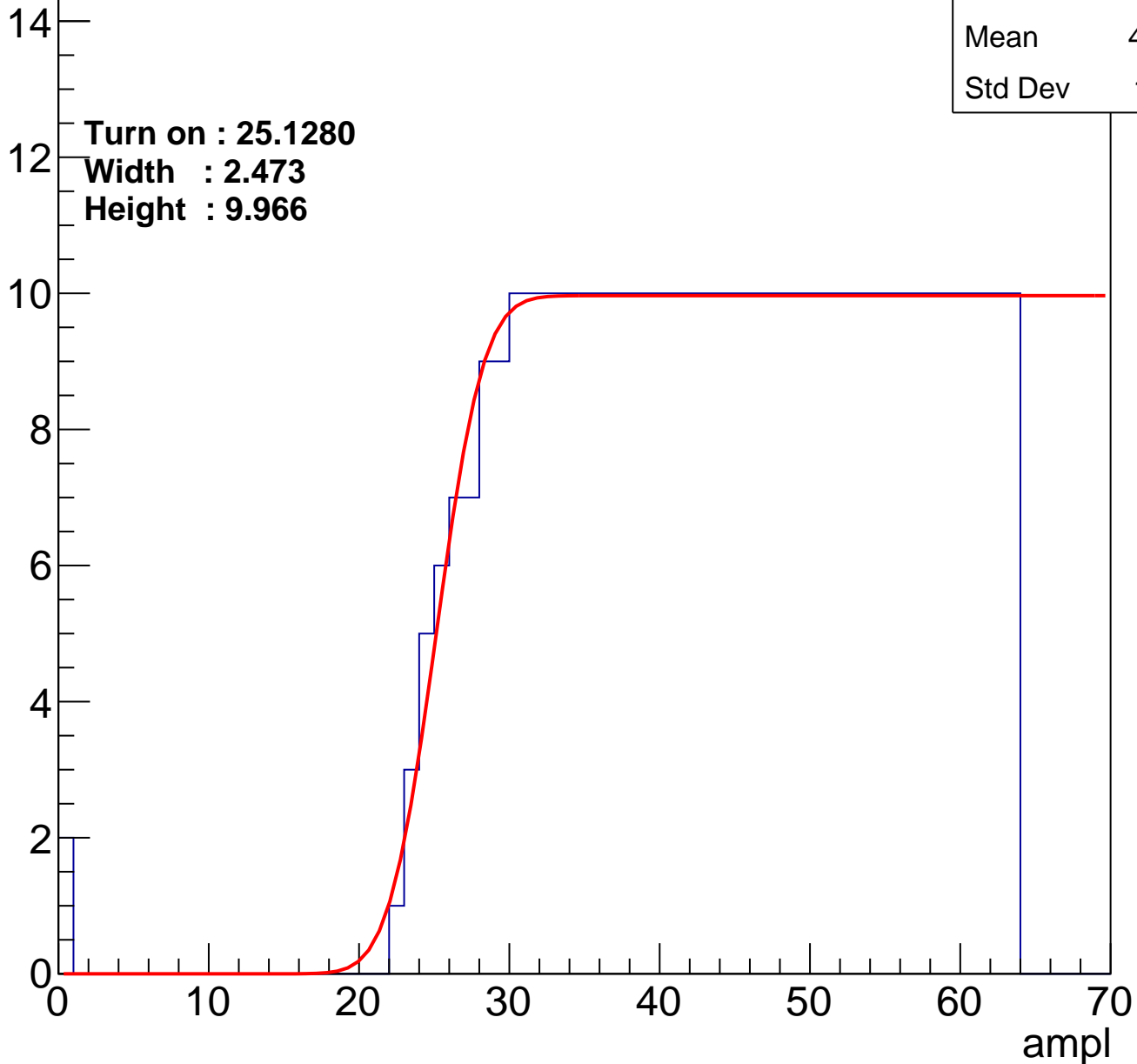
Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.1280

Width : 2.473

Height : 9.966

Entry



B1L000S, U16-ch37

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.2759

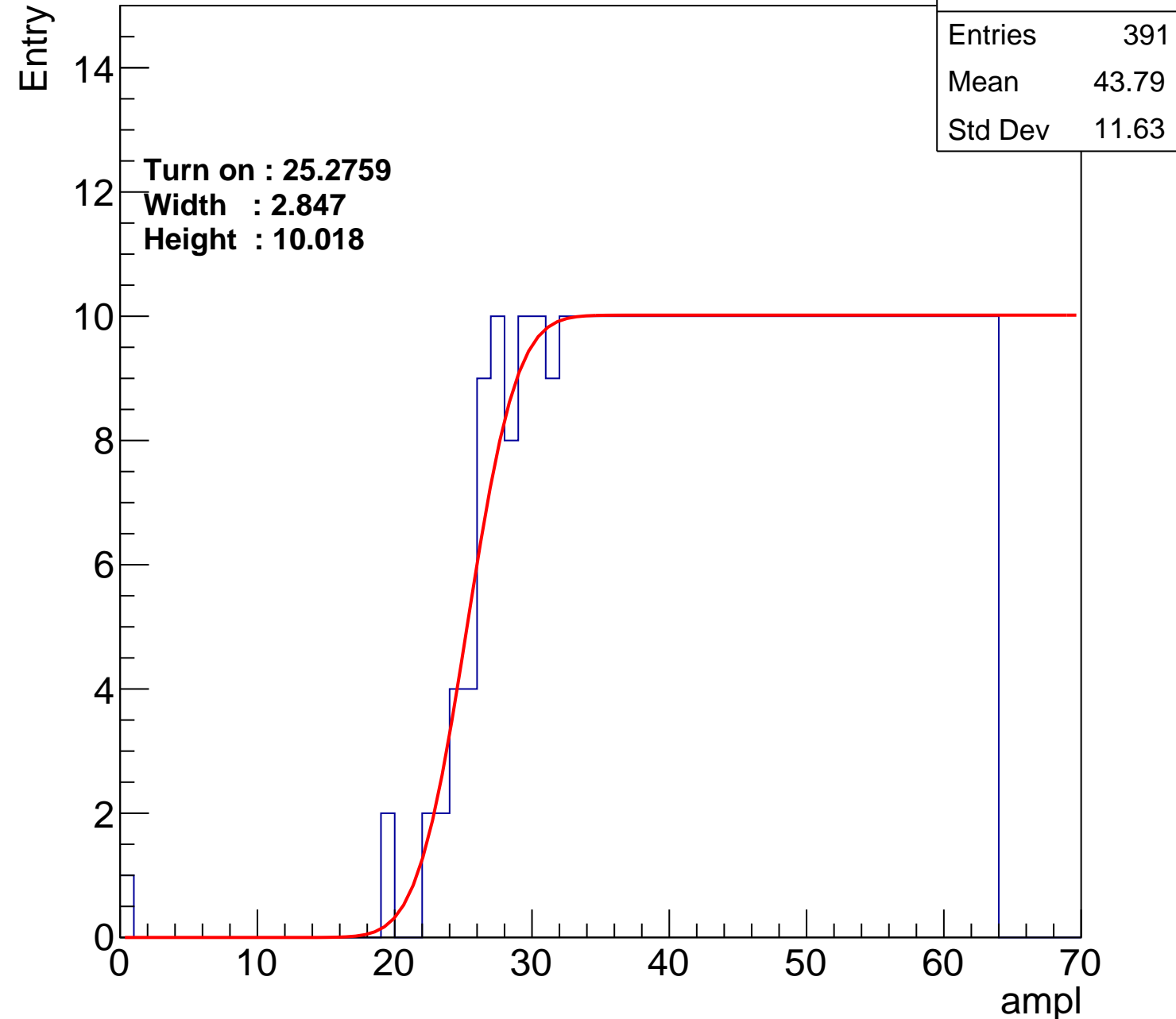
Width : 2.847

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch38

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.11
Std Dev	11.68

Turn on : 25.8548

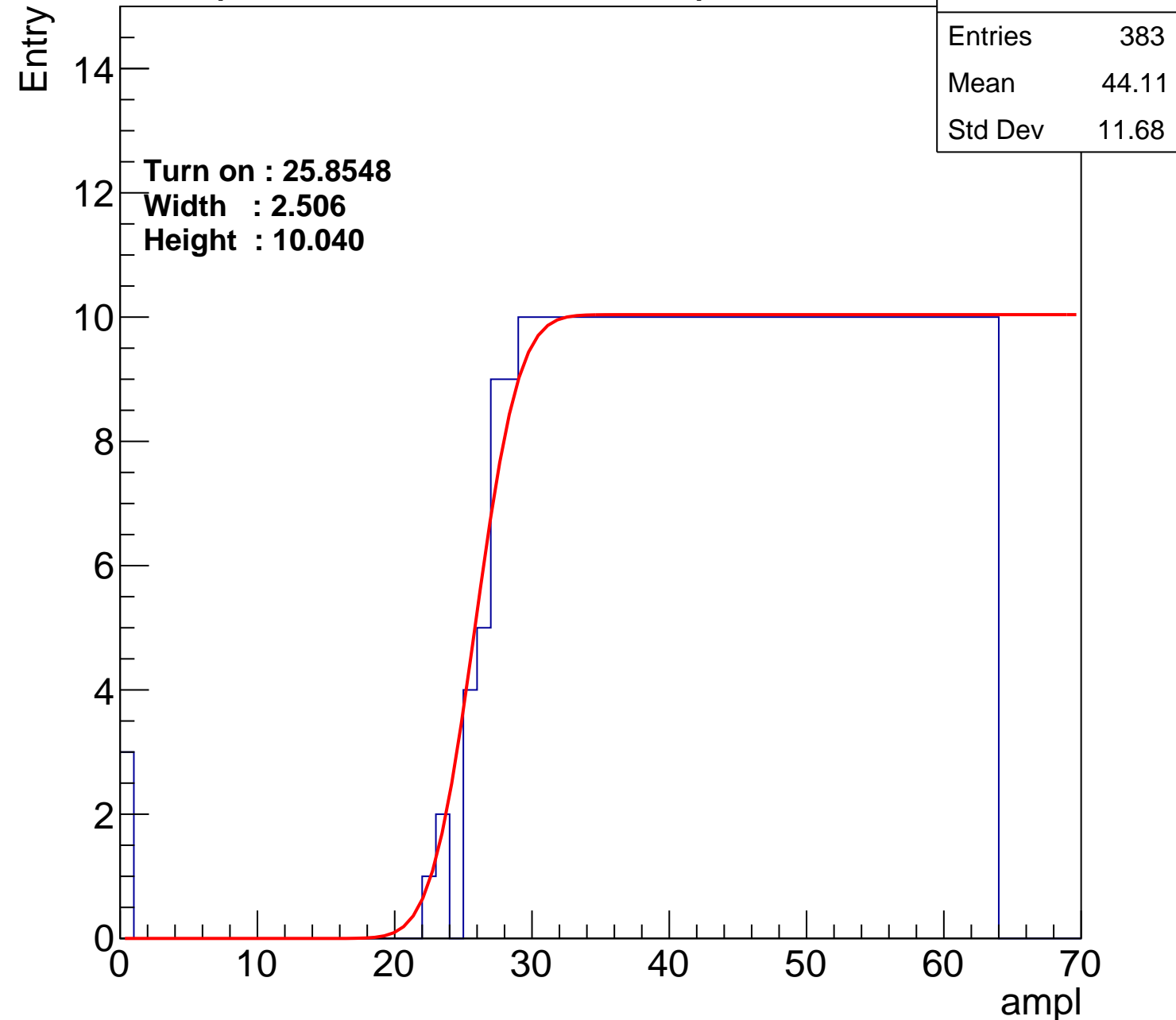
Width : 2.506

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch39

calib_packv5_042523_0143.root, FC#3, port B3

Entries	364
Mean	45.16
Std Dev	10.83

Turn on : 26.5826

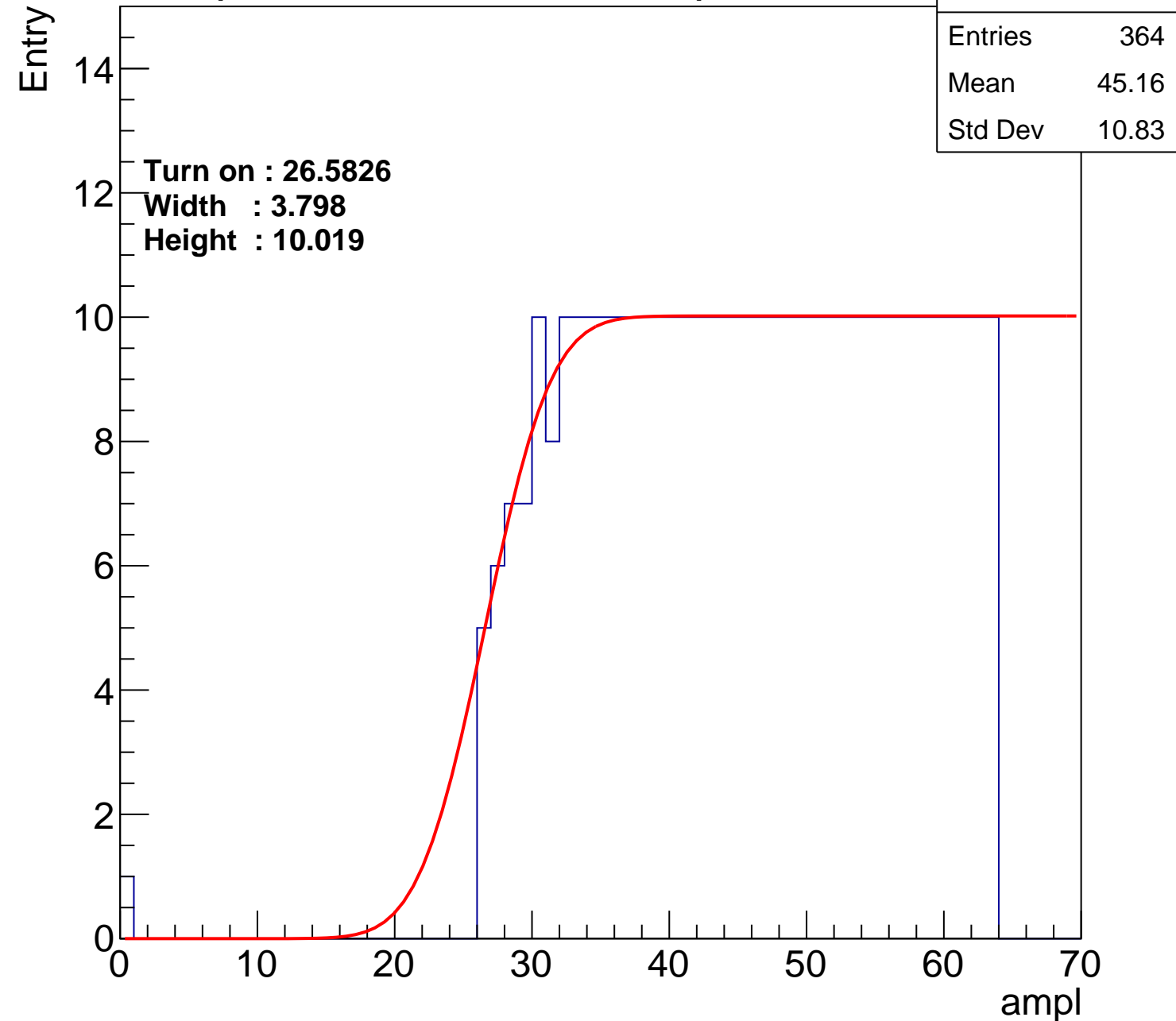
Width : 3.798

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch40

calib_packv5_042523_0143.root, FC#3, port B3

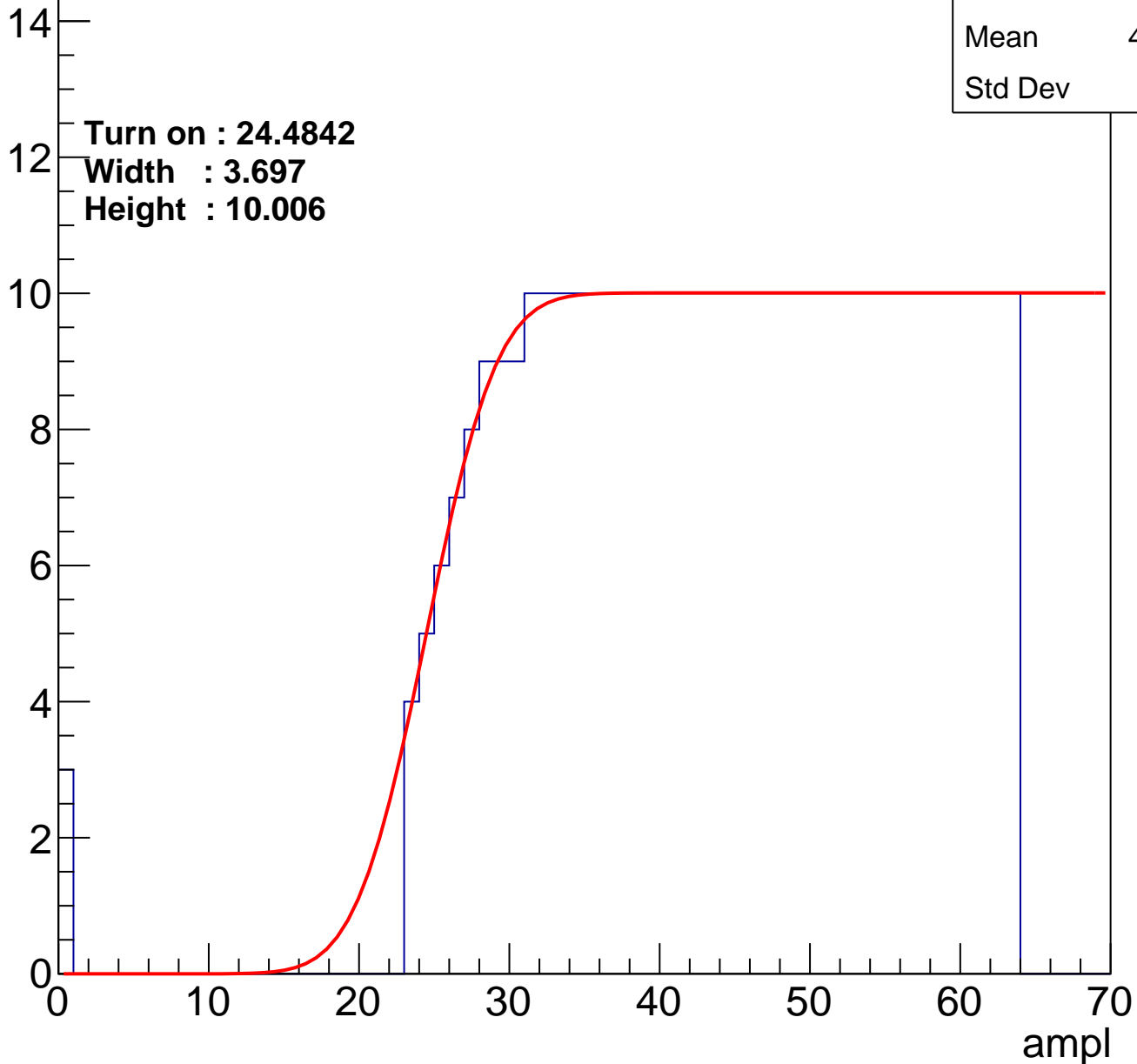
Entries	390
Mean	43.73
Std Dev	11.9

Turn on : 24.4842

Width : 3.697

Height : 10.006

Entry



B1L000S, U16-ch41

calib_packv5_042523_0143.root, FC#3, port B3

Entries	381
Mean	44.27
Std Dev	11.44

Turn on : 25.9650

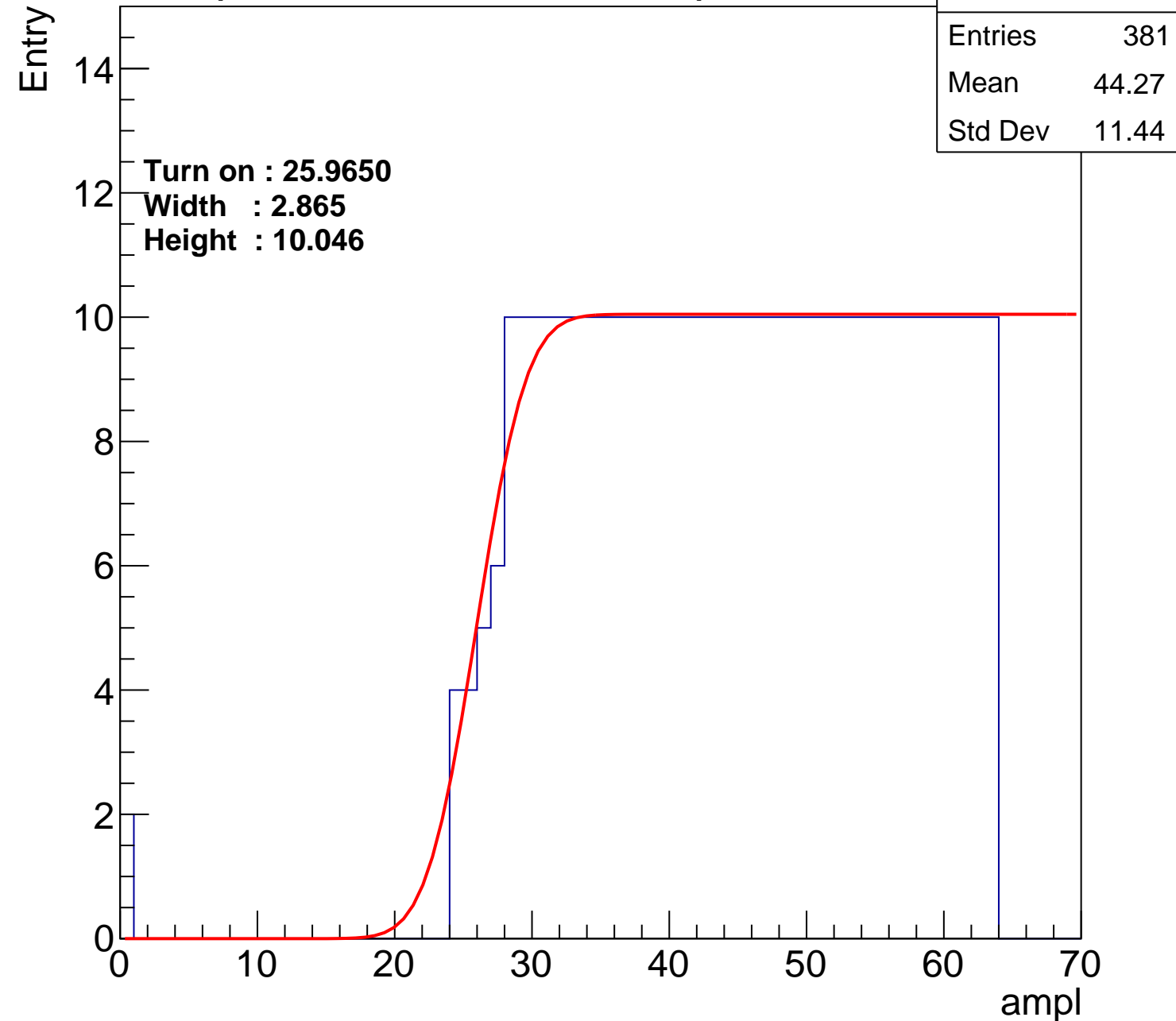
Width : 2.865

Height : 10.046

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch42

calib_packv5_042523_0143.root, FC#3, port B3

Entries	388
Mean	43.85
Std Dev	11.76

Turn on : 25.9493

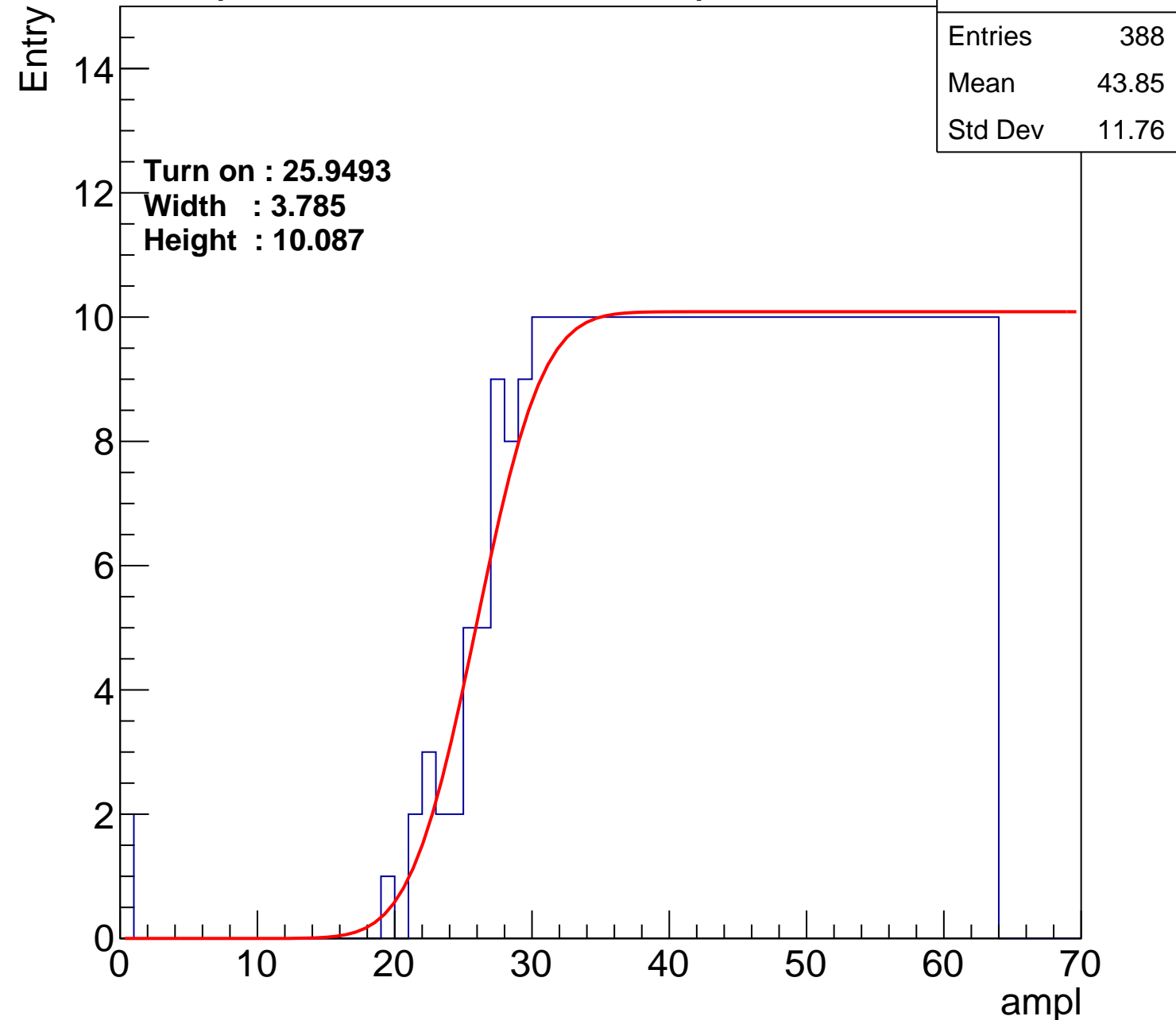
Width : 3.785

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch43

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.12
Std Dev	11.71

Turn on : 26.3086

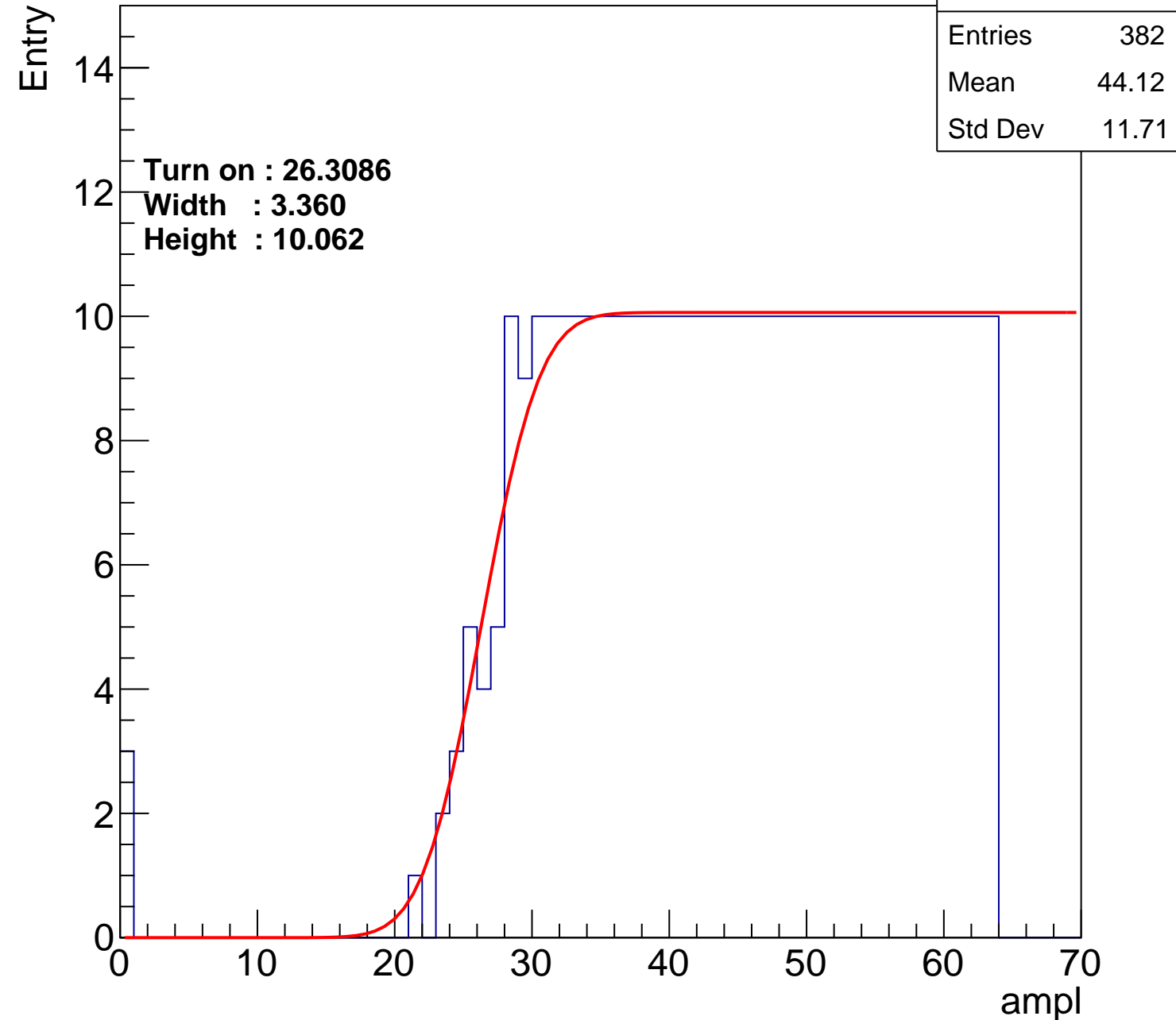
Width : 3.360

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch44

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.53
Std Dev	11.53

Turn on : 27.3095

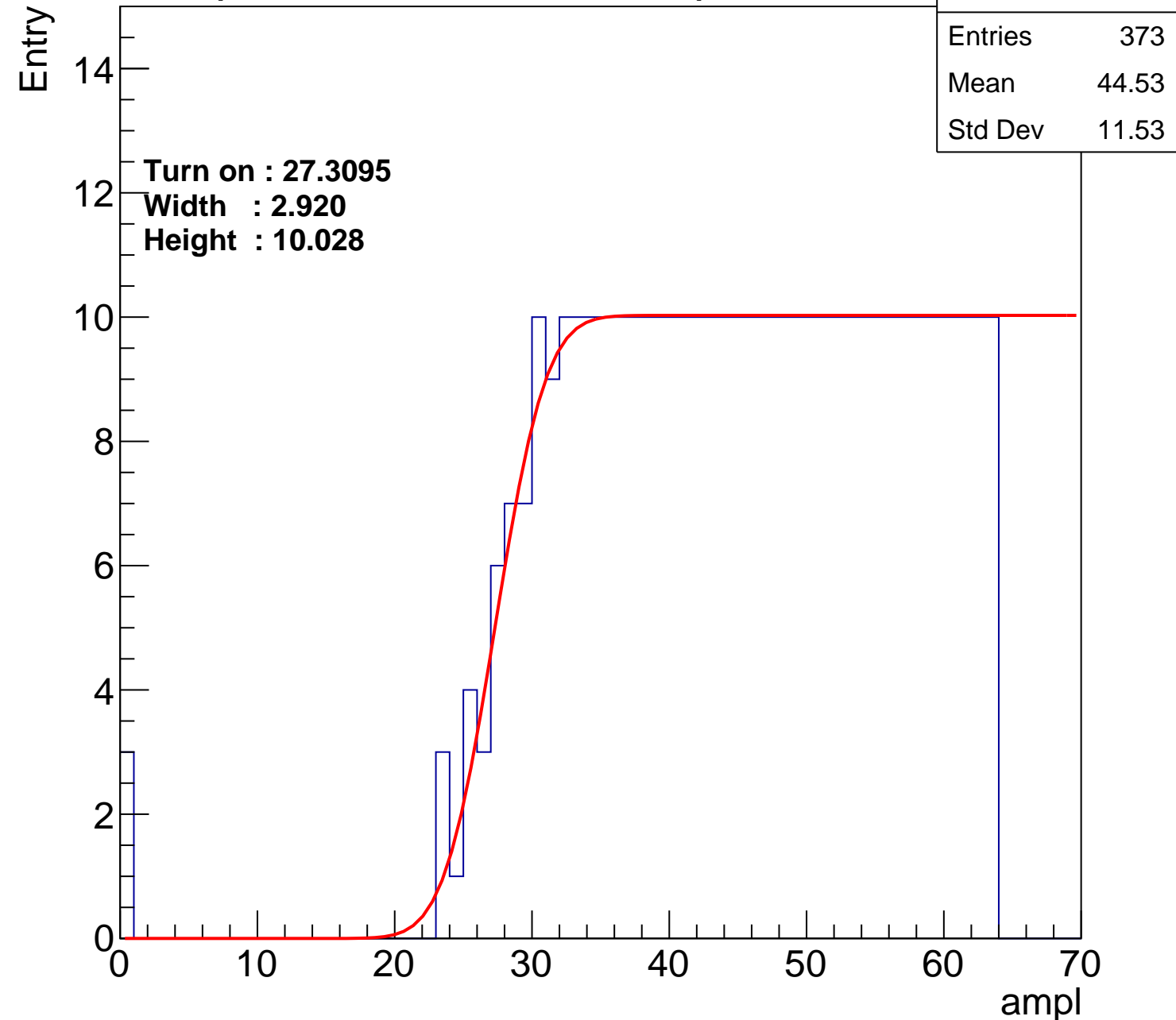
Width : 2.920

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch45

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 26.5447

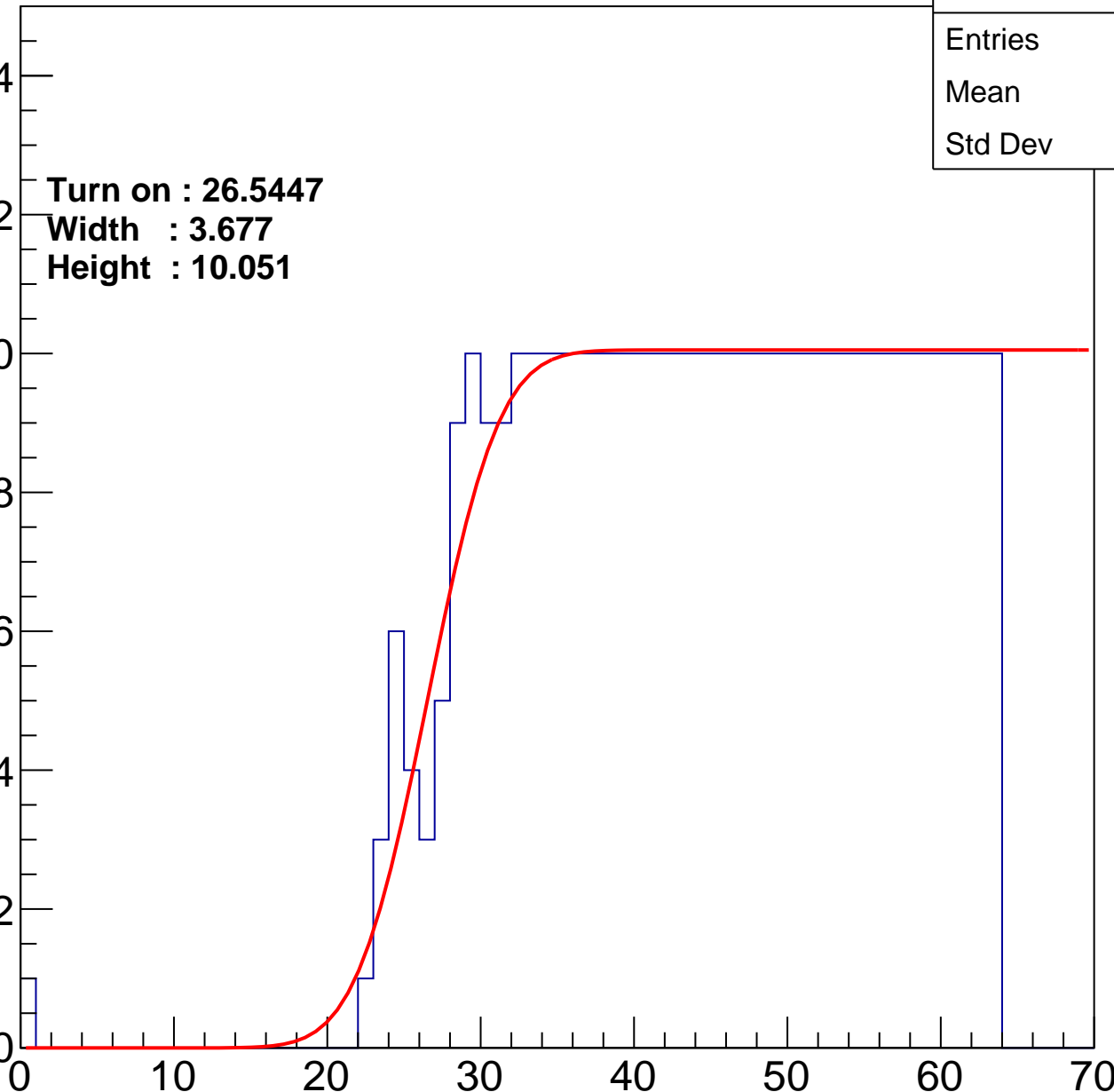
Width : 3.677

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch46

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.15
Std Dev	11.53

Turn on : 25.6039

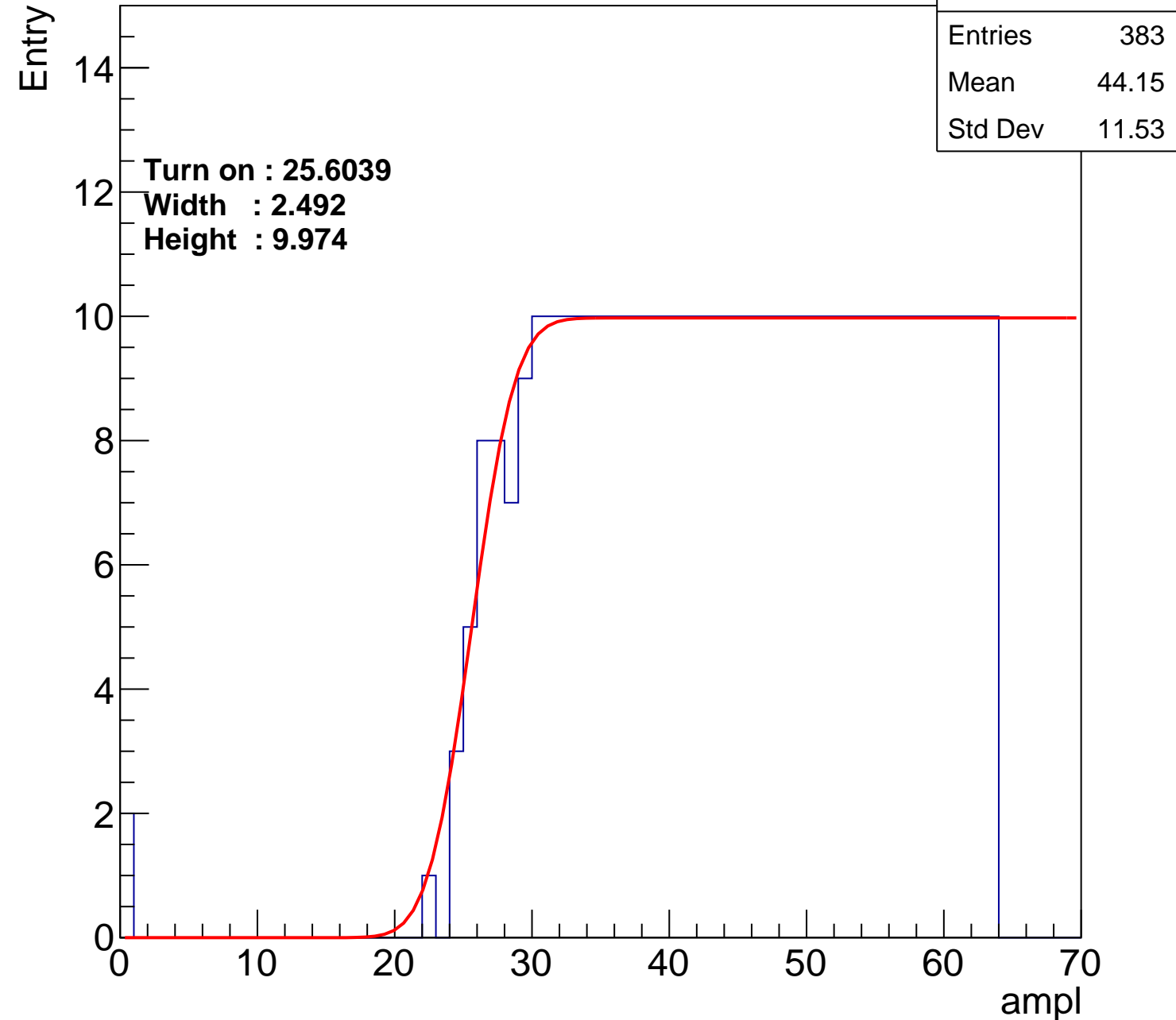
Width : 2.492

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch47

calib_packv5_042523_0143.root, FC#3, port B3

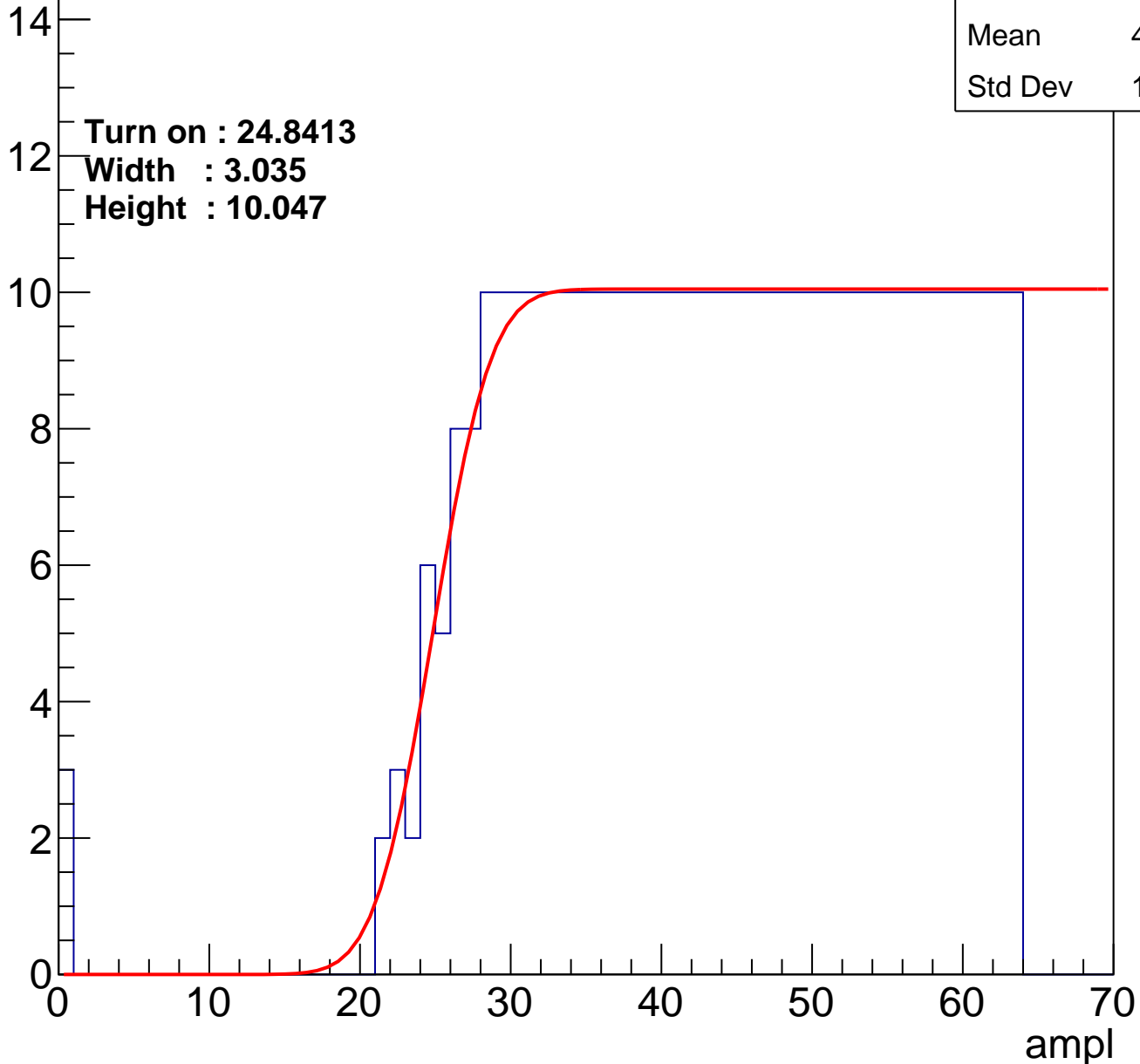
Entries	397
Mean	43.39
Std Dev	12.07

Turn on : 24.8413

Width : 3.035

Height : 10.047

Entry



B1L000S, U16-ch48

calib_packv5_042523_0143.root, FC#3, port B3

Entries	400
Mean	42.99
Std Dev	12.76

Turn on : 24.6975

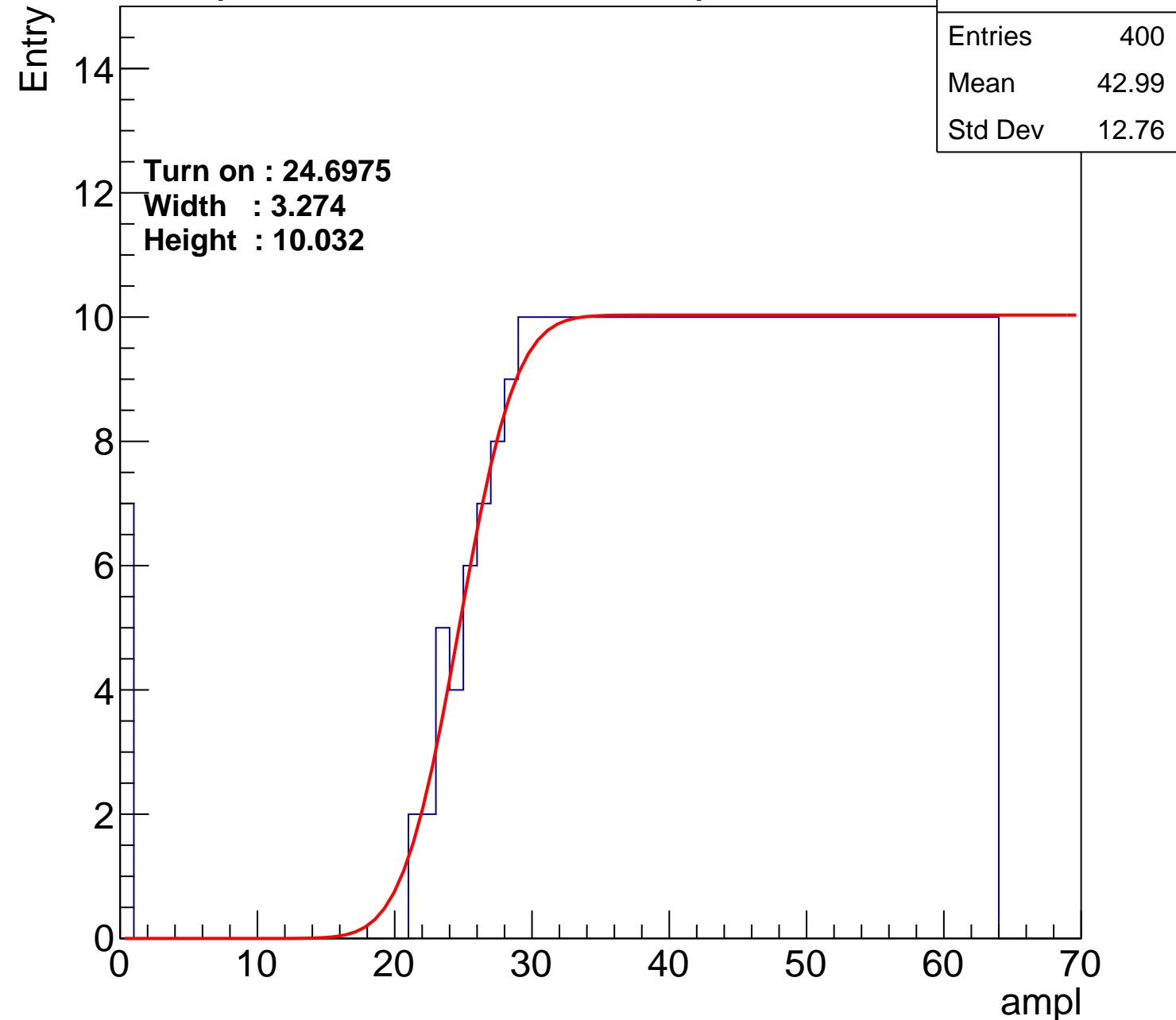
Width : 3.274

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch49

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.71
Std Dev	12.06

Turn on : 26.2252

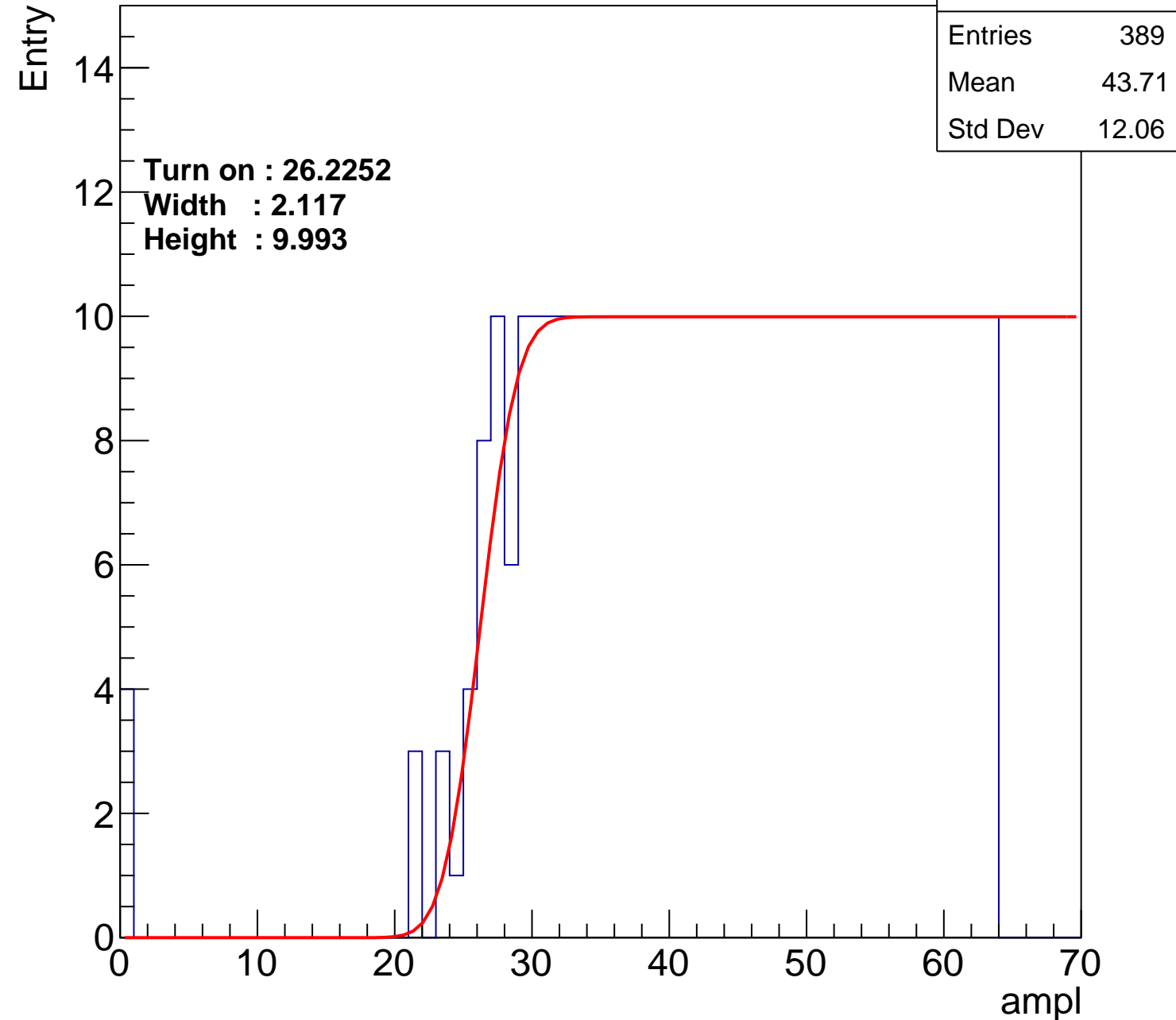
Width : 2.117

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch50

calib_packv5_042523_0143.root, FC#3, port B3

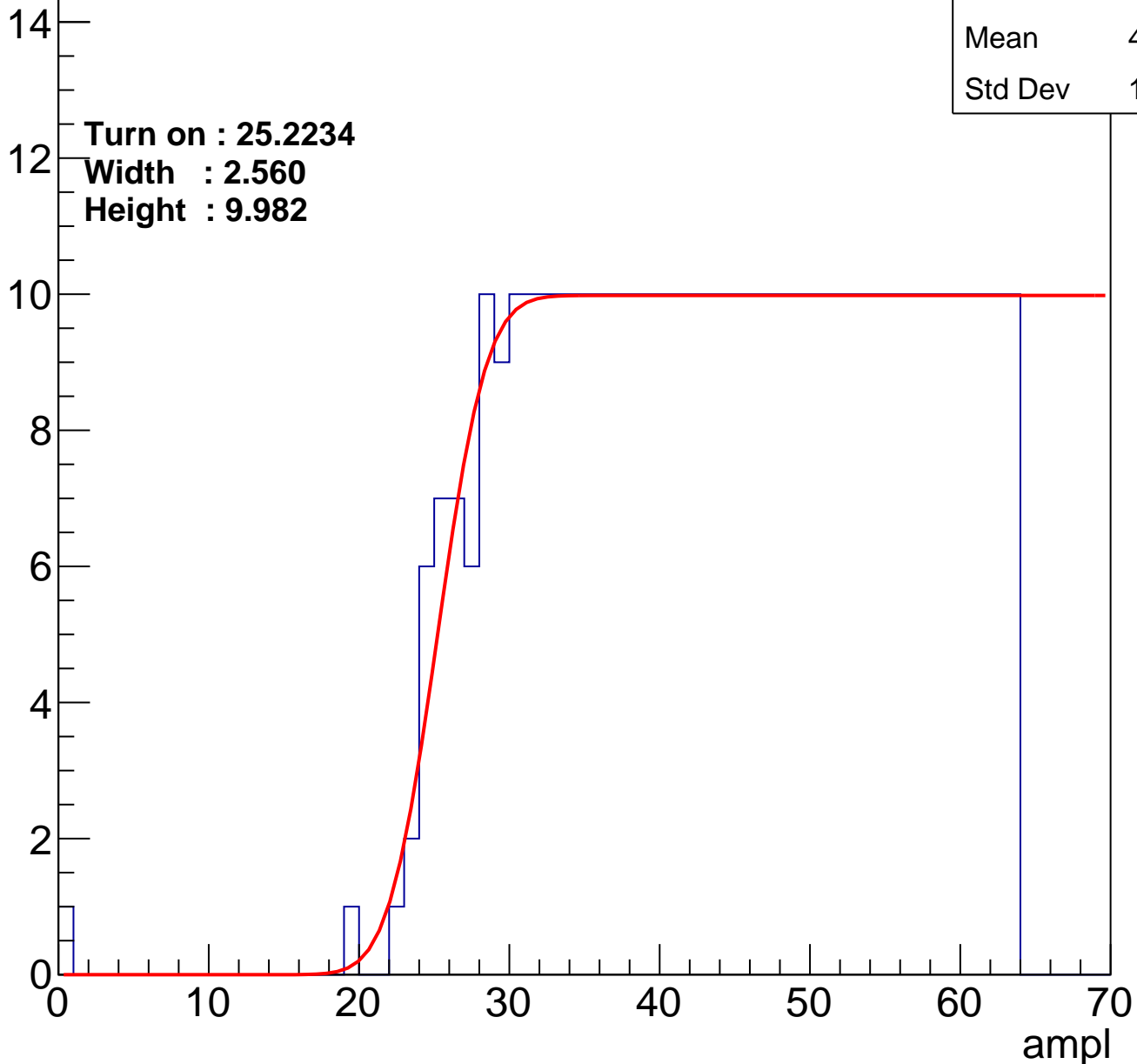
Entries	390
Mean	43.85
Std Dev	11.58

Turn on : 25.2234

Width : 2.560

Height : 9.982

Entry



B1L000S, U16-ch51

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.11
Std Dev	11.61

Turn on : 26.1212

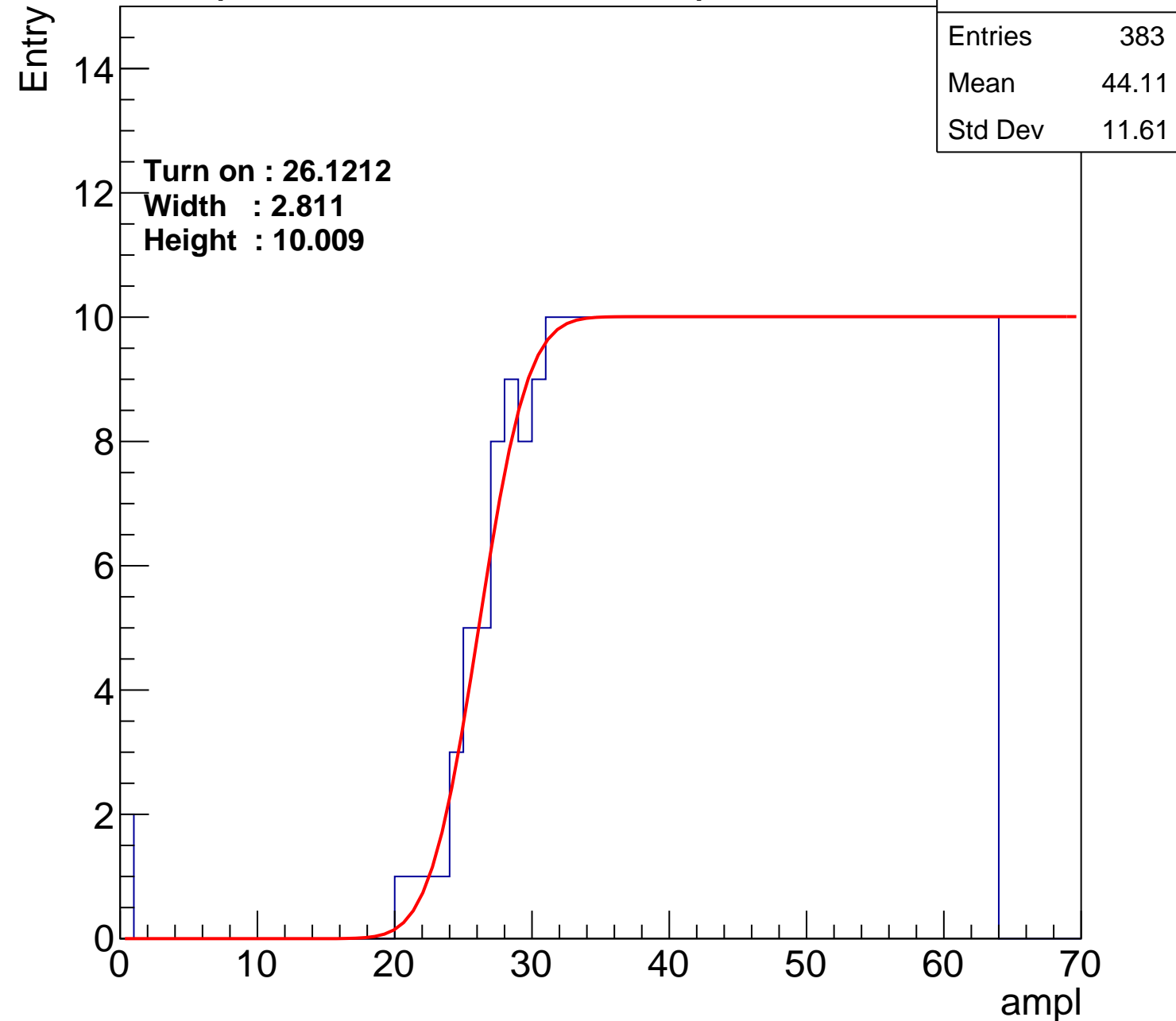
Width : 2.811

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch52

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.72
Std Dev	11.93

Turn on : 25.0907

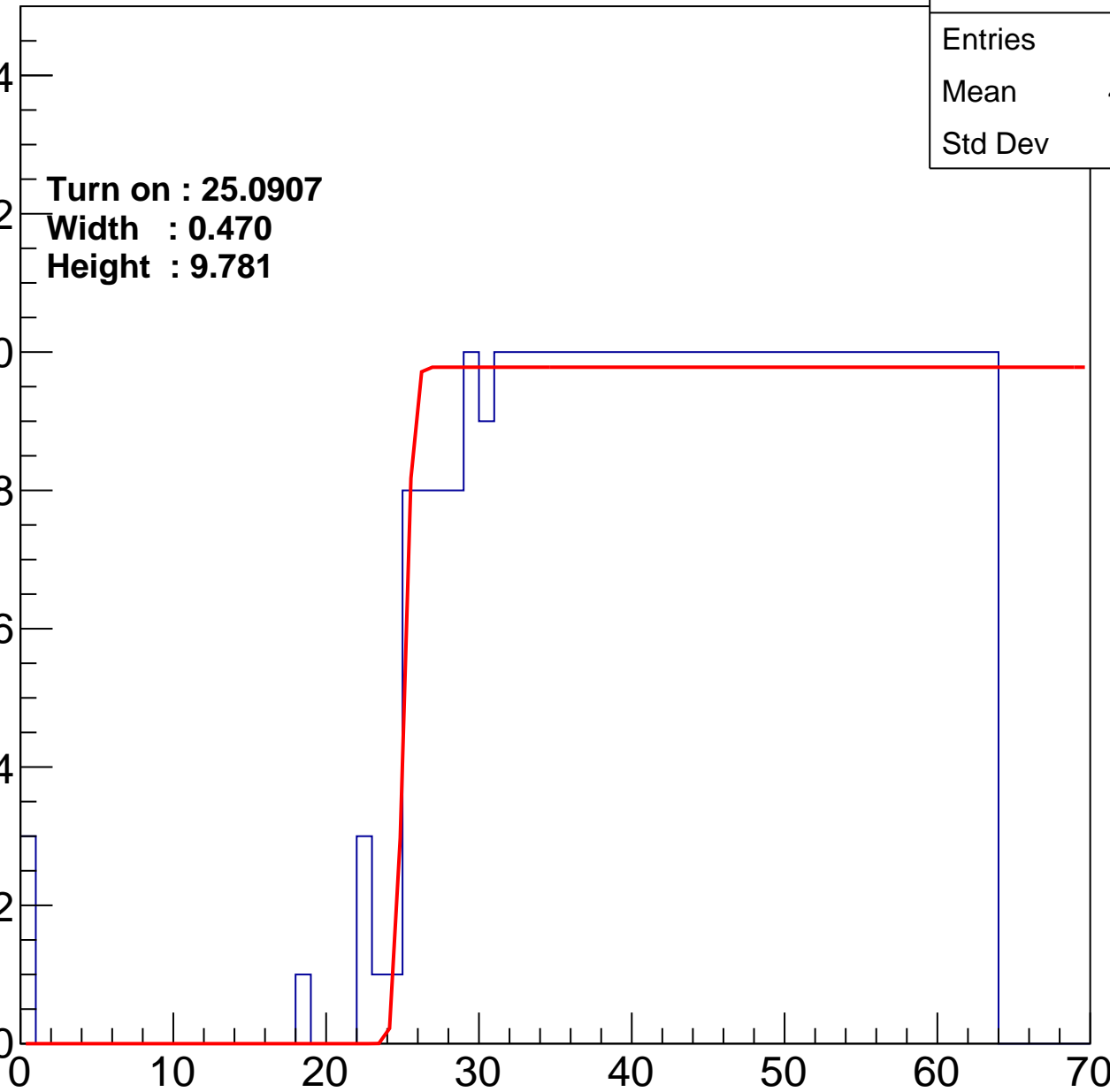
Width : 0.470

Height : 9.781

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

calib_packv5_042523_0143.root, FC#3, port B3

Turn on : 26.0176
Width : 2.331
Height : 10.012



B1L000S, U16-ch54

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.83
Std Dev	11.96

Turn on : 25.9350

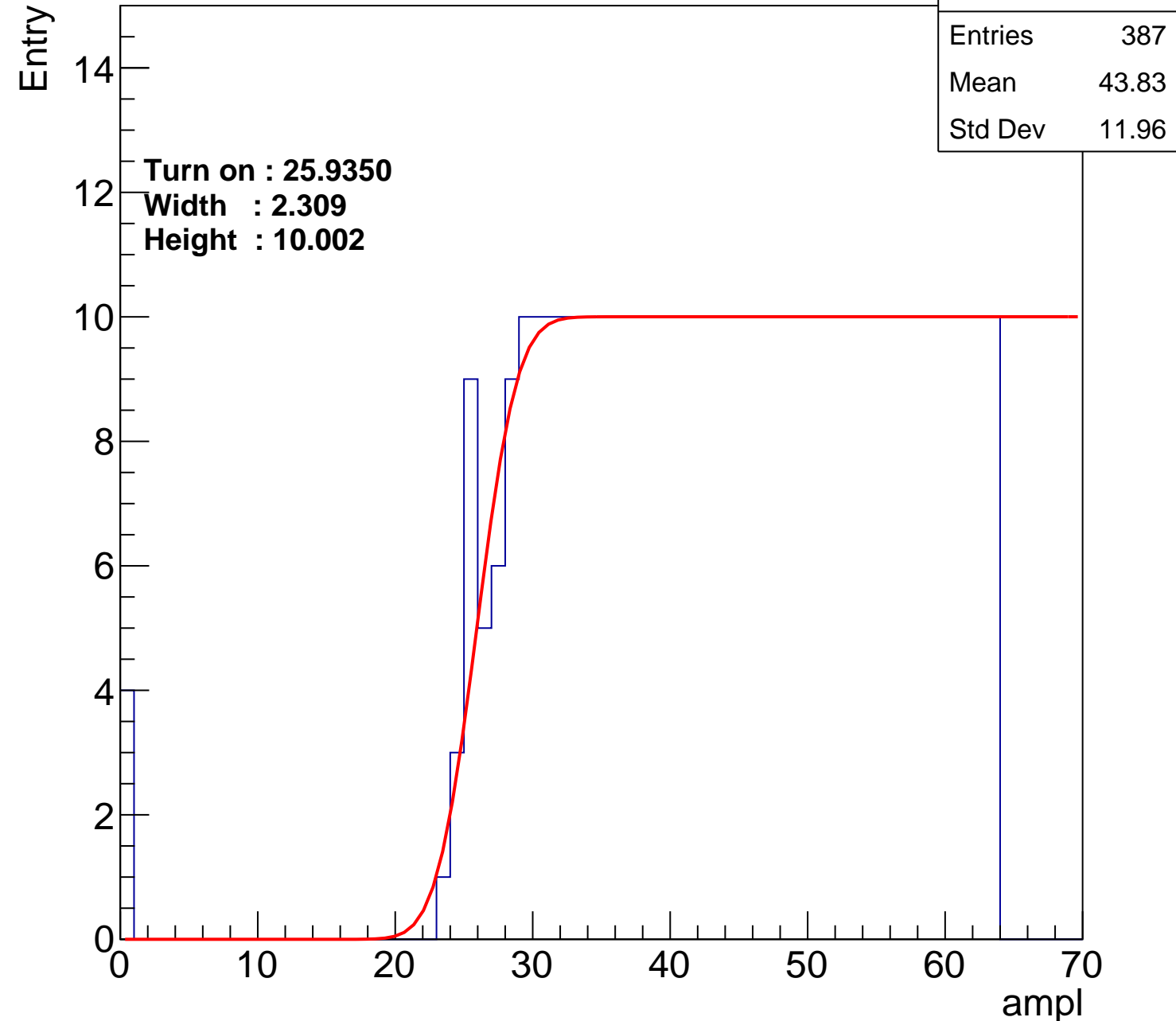
Width : 2.309

Height : 10.002

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch55

calib_packv5_042523_0143.root, FC#3, port B3

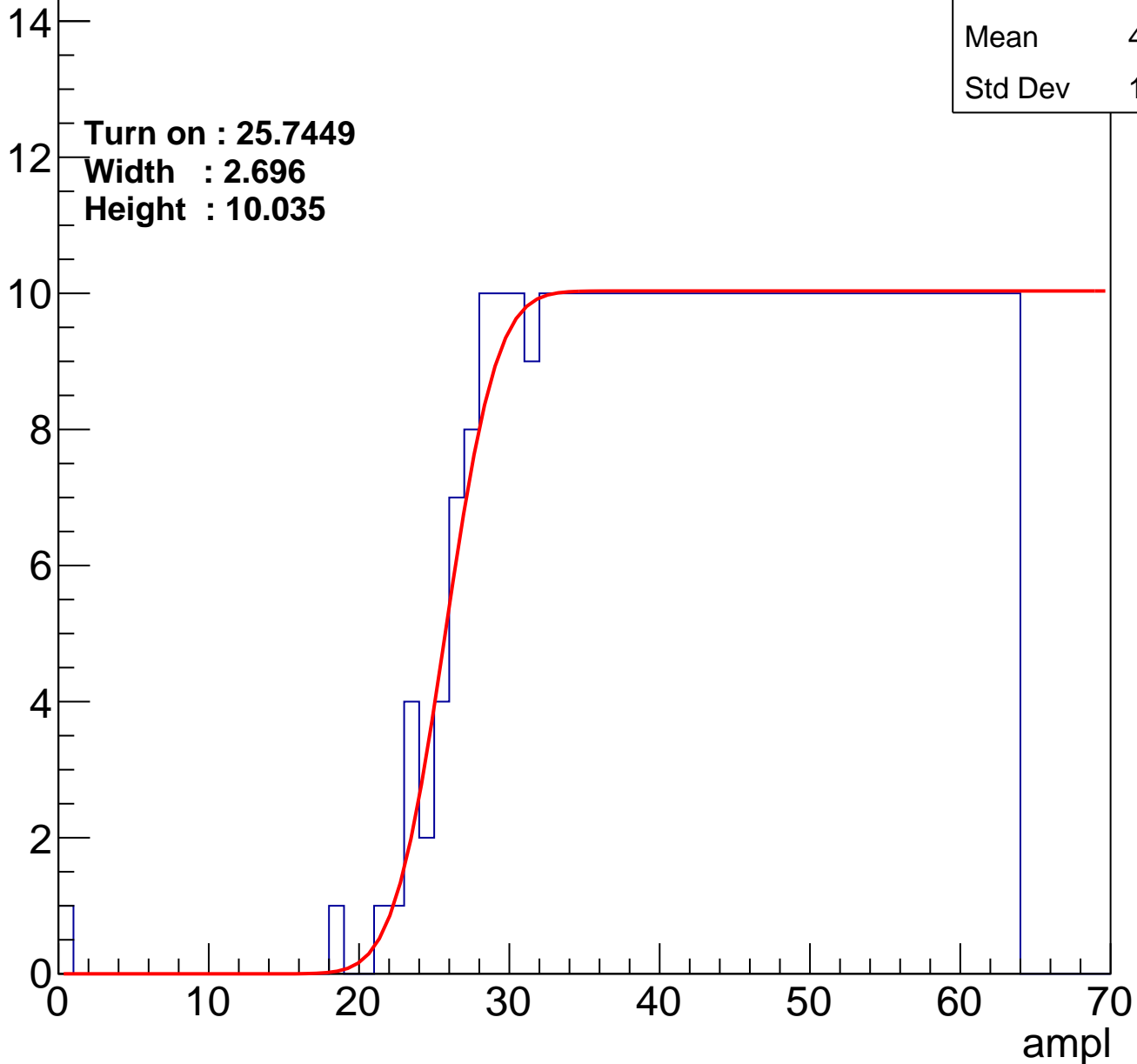
Entries	388
Mean	43.94
Std Dev	11.54

Turn on : 25.7449

Width : 2.696

Height : 10.035

Entry



B1L000S, U16-ch56

calib_packv5_042523_0143.root, FC#3, port B3

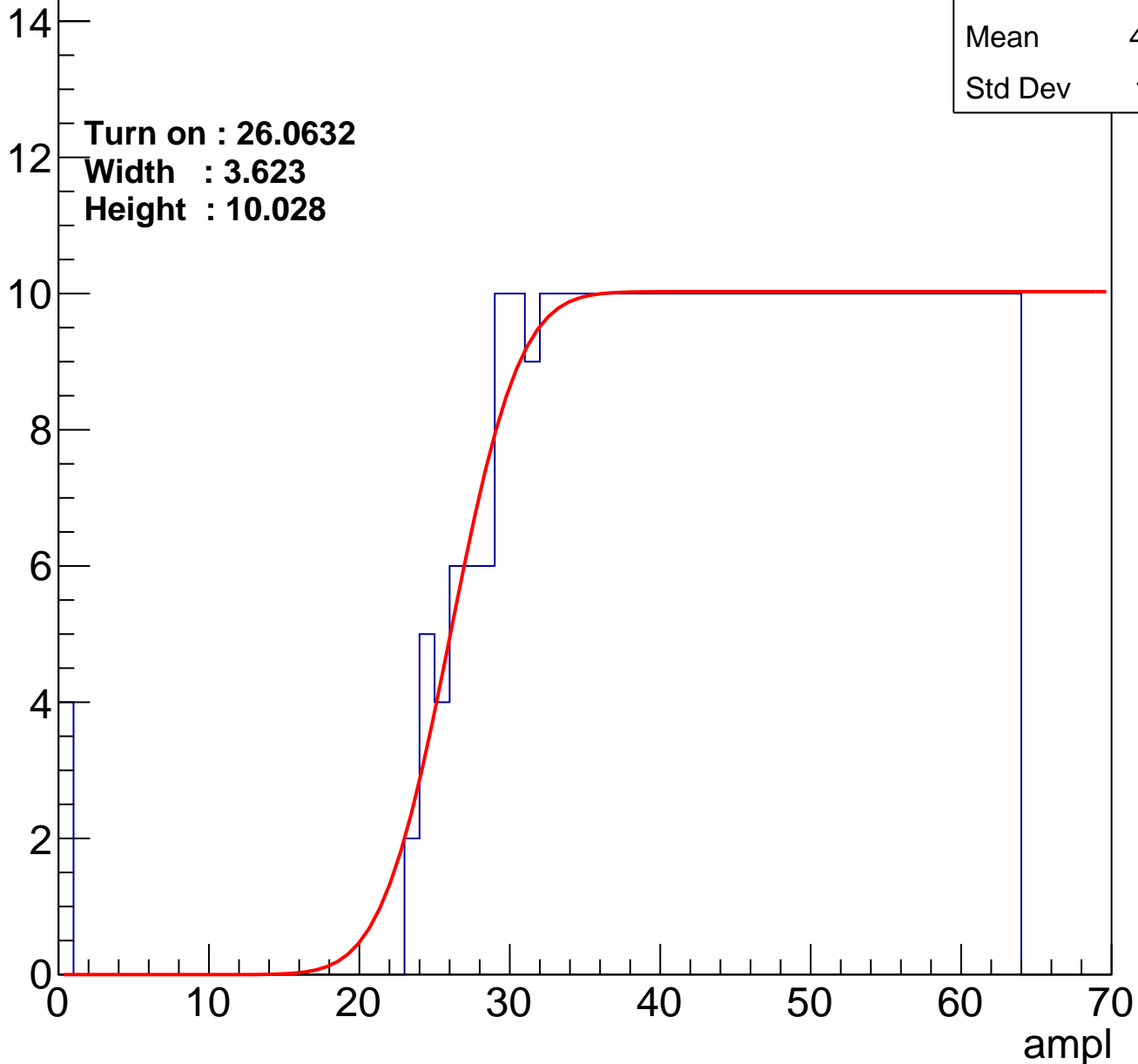
Entries	382
Mean	44.03
Std Dev	11.91

Turn on : 26.0632

Width : 3.623

Height : 10.028

Entry



B1L000S, U16-ch57

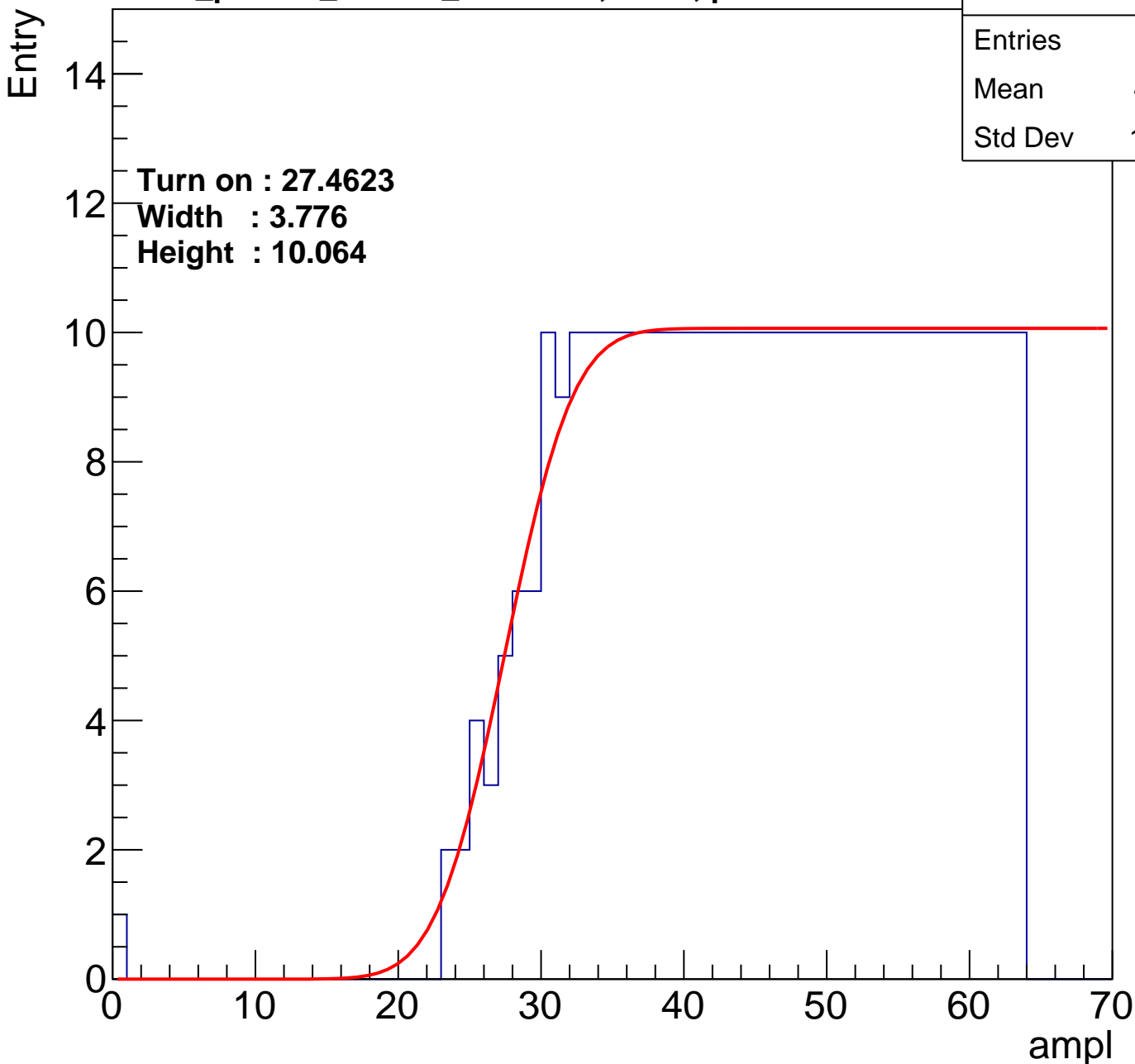
calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.4623

Width : 3.776

Height : 10.064



B1L000S, U16-ch58

calib_packv5_042523_0143.root, FC#3, port B3

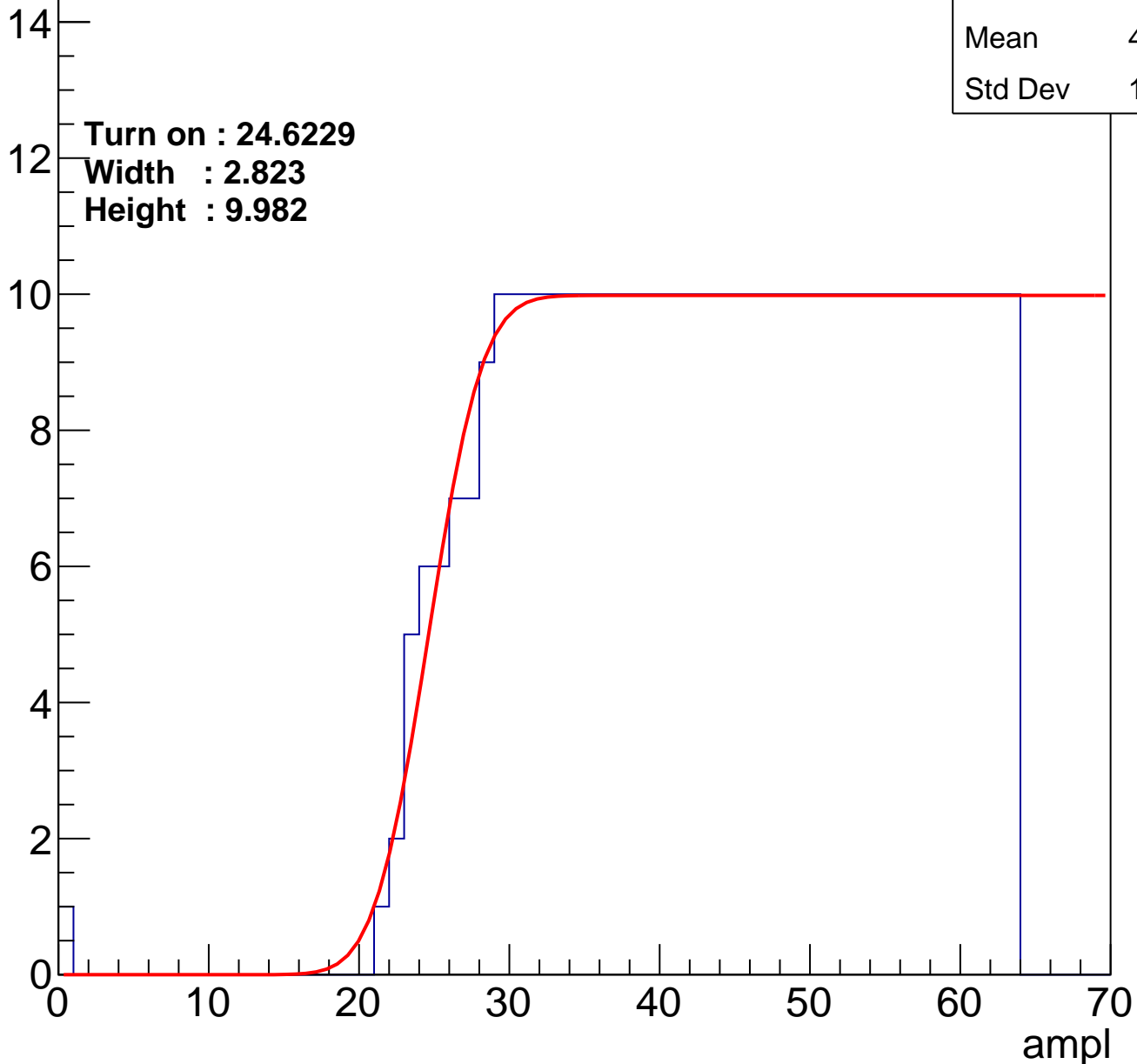
Entries	394
Mean	43.65
Std Dev	11.69

Turn on : 24.6229

Width : 2.823

Height : 9.982

Entry



B1L000S, U16-ch59

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 26.4165

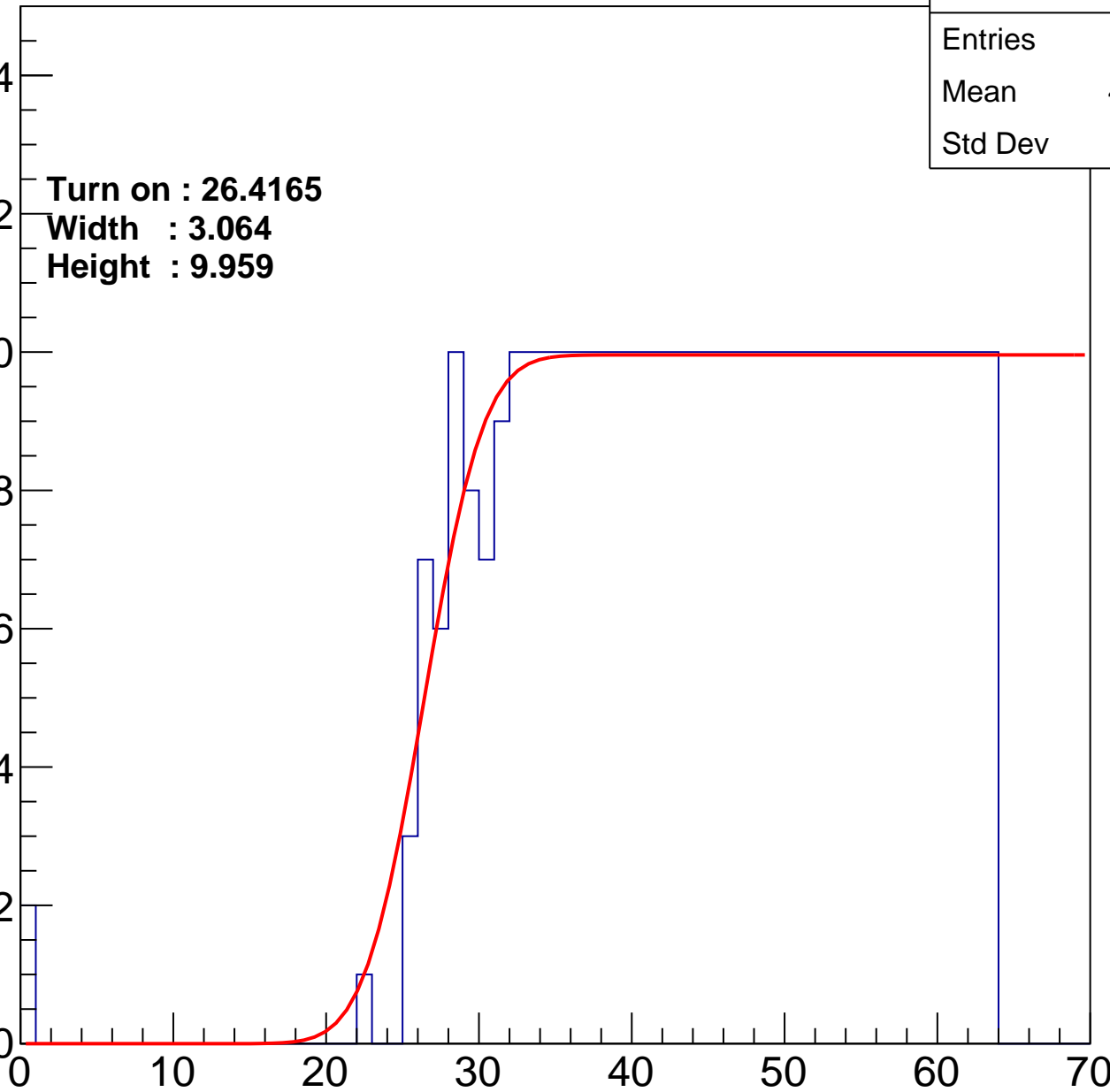
Width : 3.064

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch60

calib_packv5_042523_0143.root, FC#3, port B3

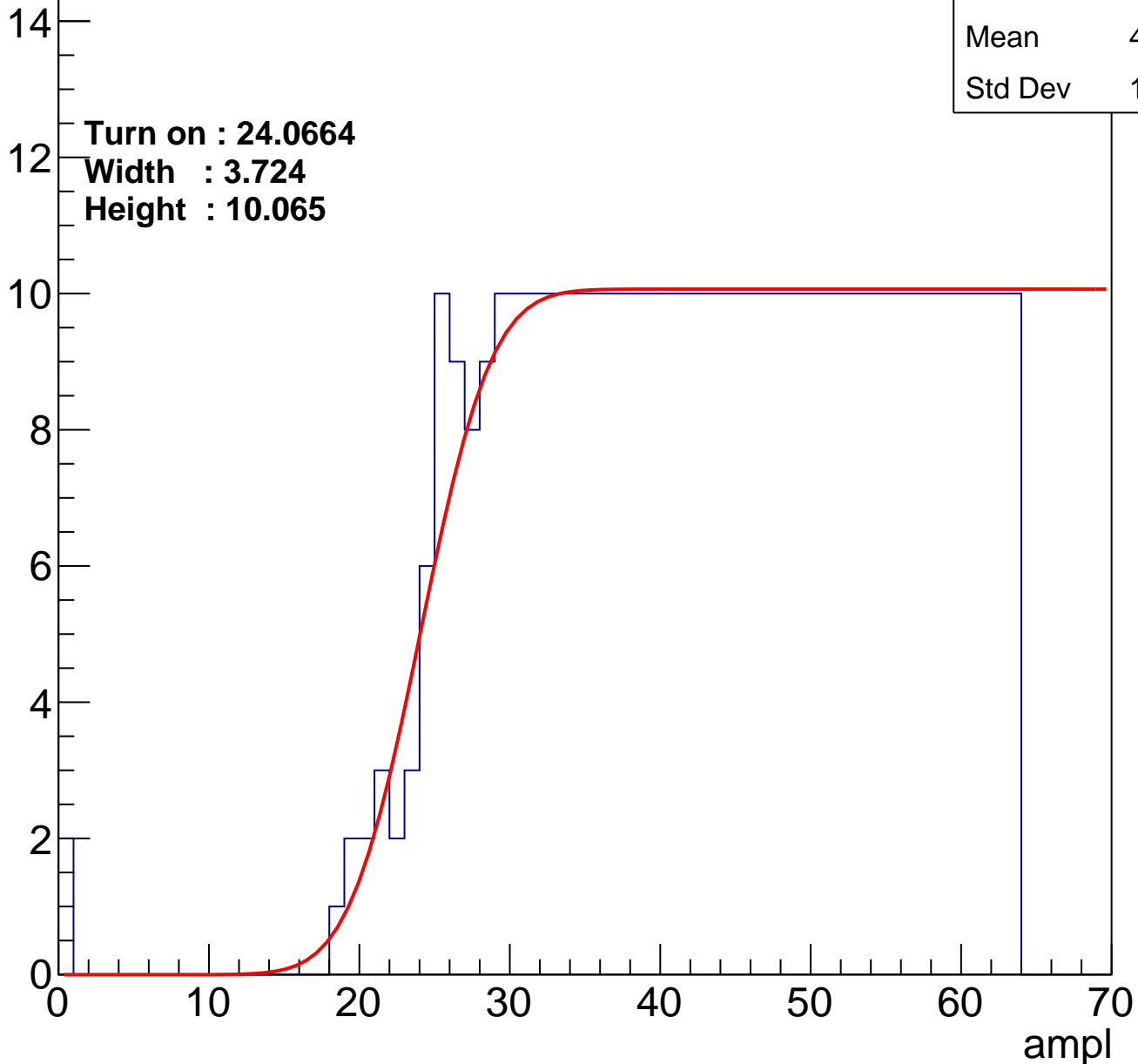
Entries	407
Mean	42.92
Std Dev	12.24

Turn on : 24.0664

Width : 3.724

Height : 10.065

Entry



B1L000S, U16-ch61

calib_packv5_042523_0143.root, FC#3, port B3

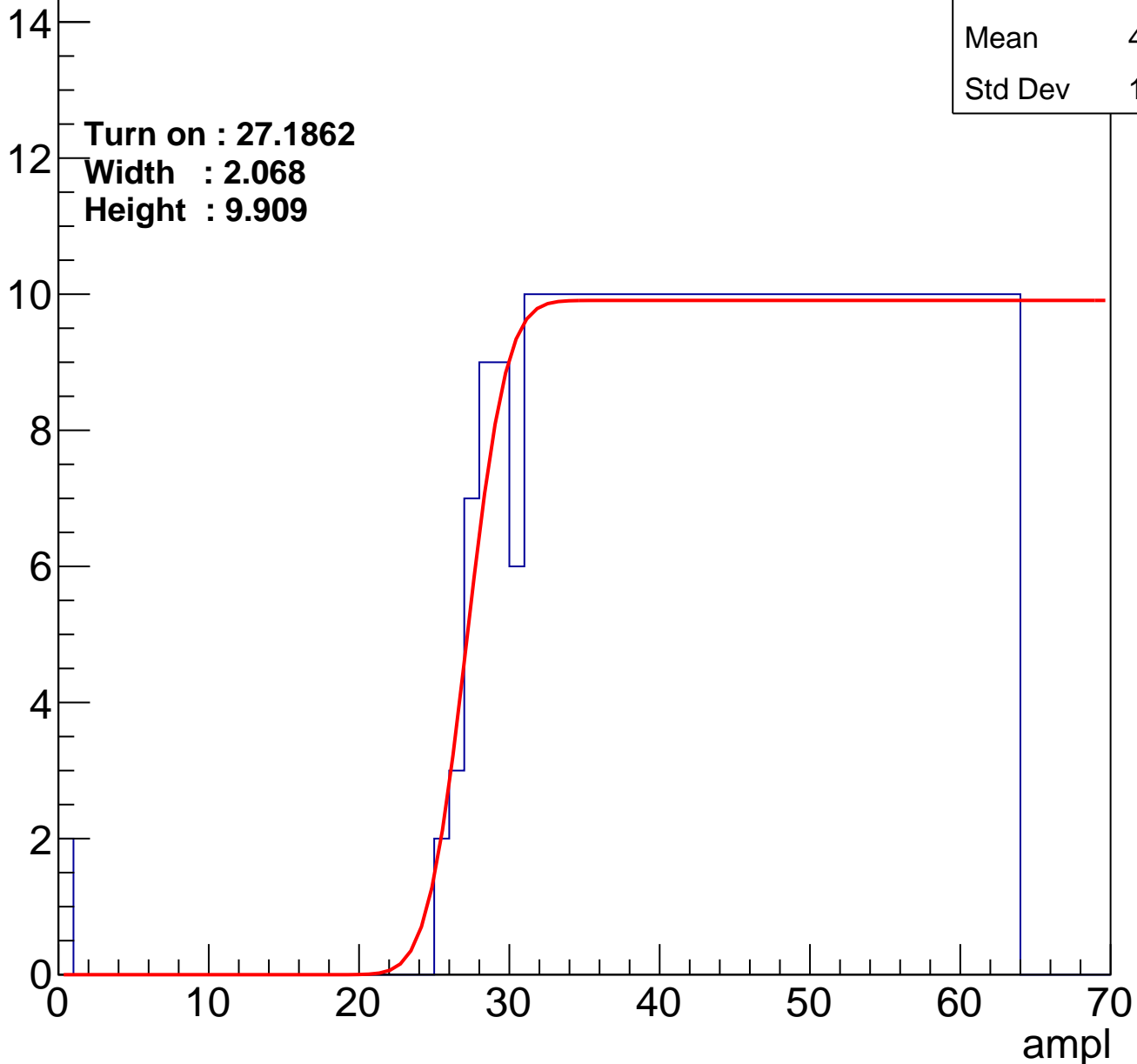
Entries	368
Mean	44.89
Std Dev	11.14

Turn on : 27.1862

Width : 2.068

Height : 9.909

Entry



B1L000S, U16-ch62

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.66
Std Dev	11.96

Turn on : 25.5530

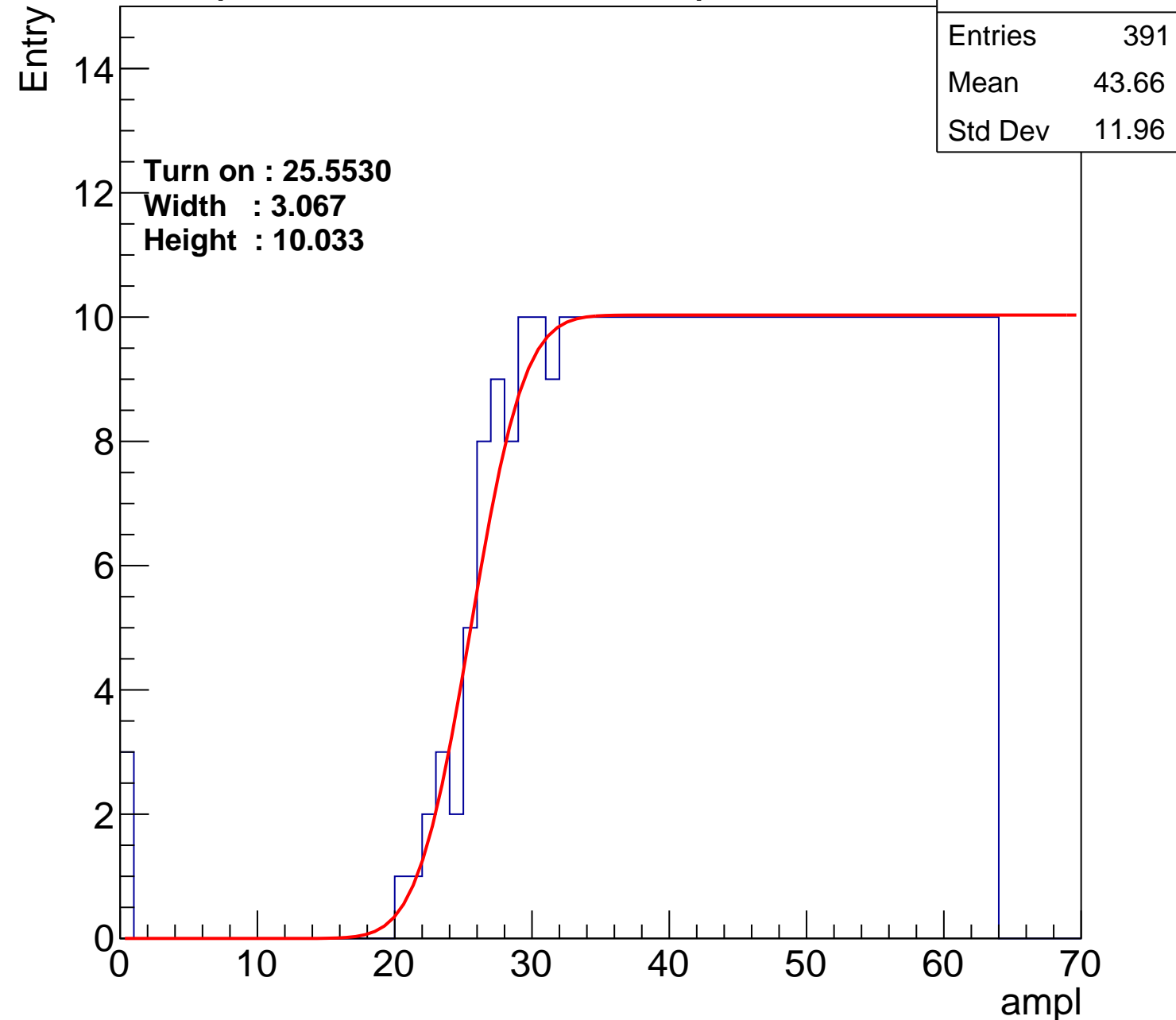
Width : 3.067

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch63

calib_packv5_042523_0143.root, FC#3, port B3

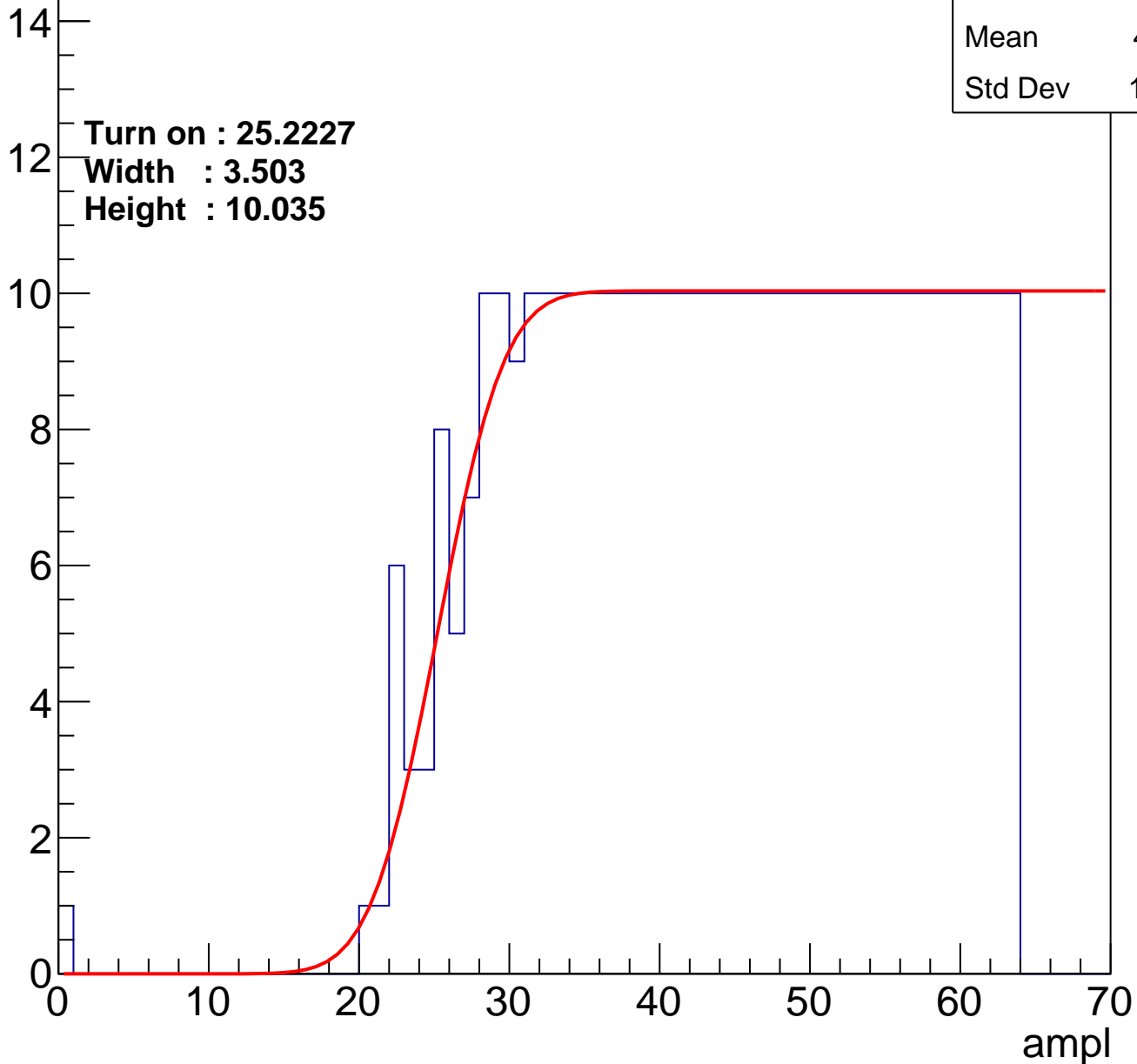
Entries	394
Mean	43.61
Std Dev	11.75

Turn on : 25.2227

Width : 3.503

Height : 10.035

Entry



B1L000S, U16-ch64

calib_packv5_042523_0143.root, FC#3, port B3

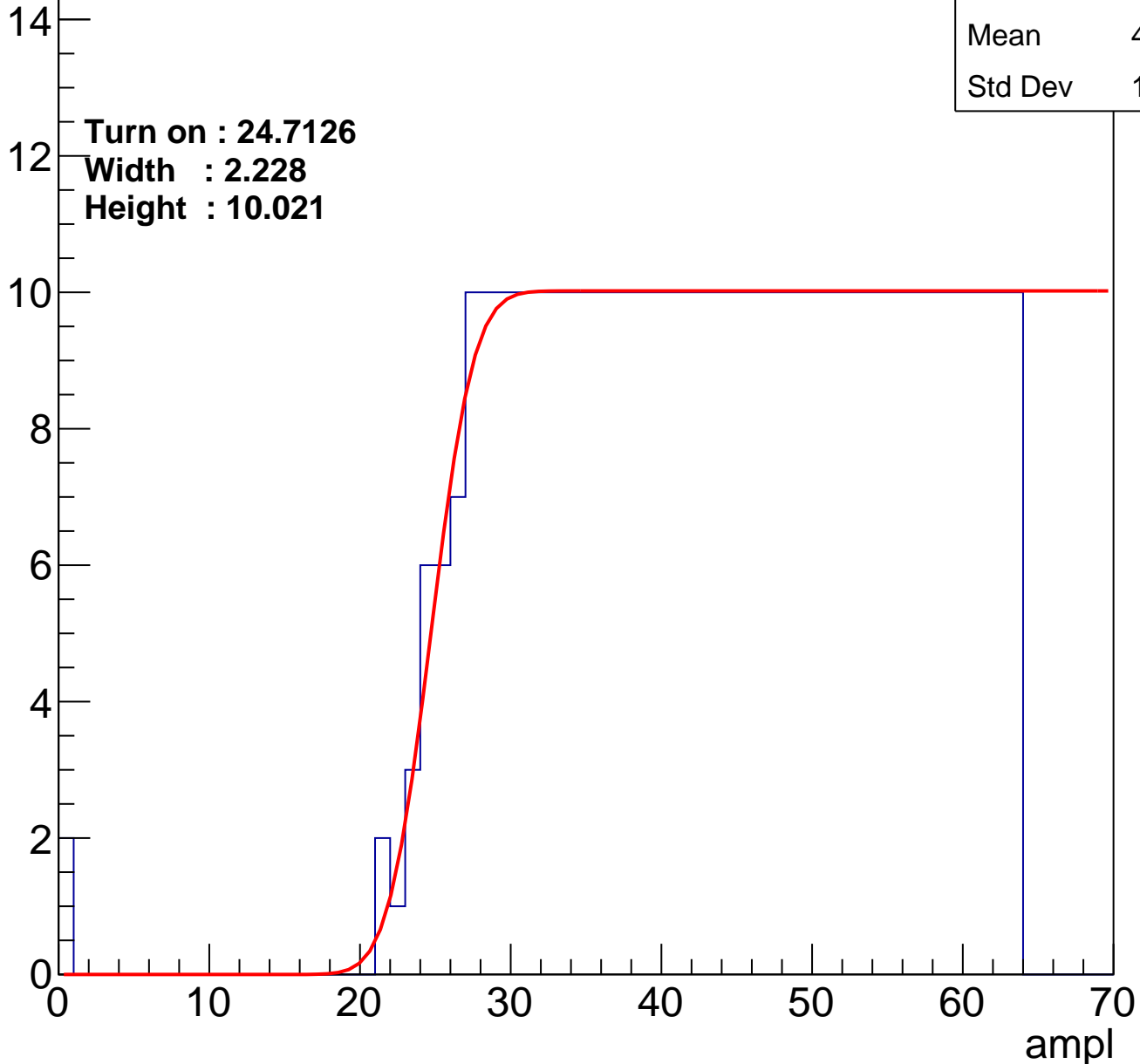
Entries	397
Mean	43.47
Std Dev	11.88

Turn on : 24.7126

Width : 2.228

Height : 10.021

Entry



B1L000S, U16-ch65

calib_packv5_042523_0143.root, FC#3, port B3

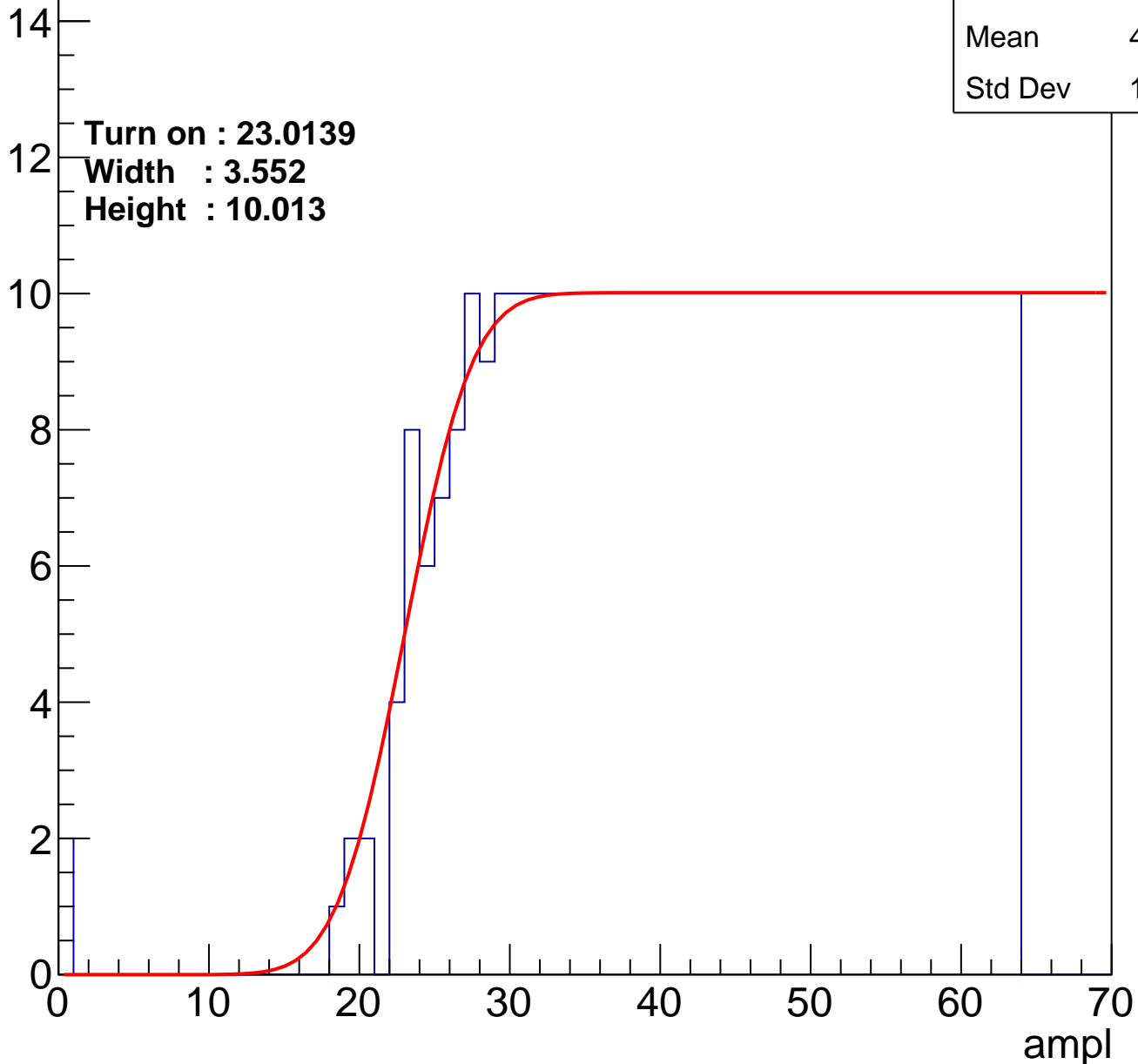
Entries	409
Mean	42.83
Std Dev	12.28

Turn on : 23.0139

Width : 3.552

Height : 10.013

Entry



B1L000S, U16-ch66

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	43.98
Std Dev	12.06

Turn on : 26.0006

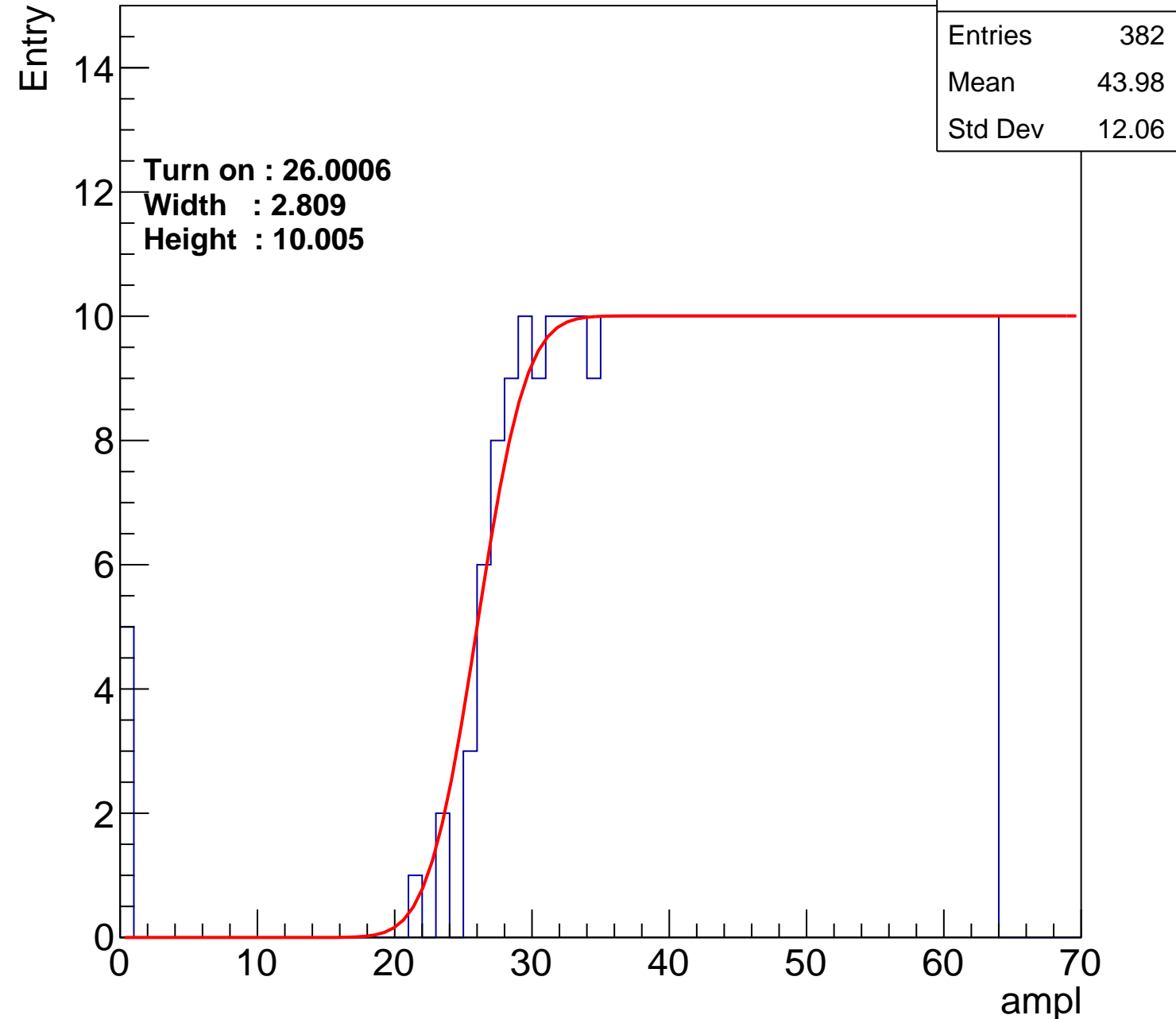
Width : 2.809

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch67

calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.73
Std Dev	11.1

Turn on : 27.0732

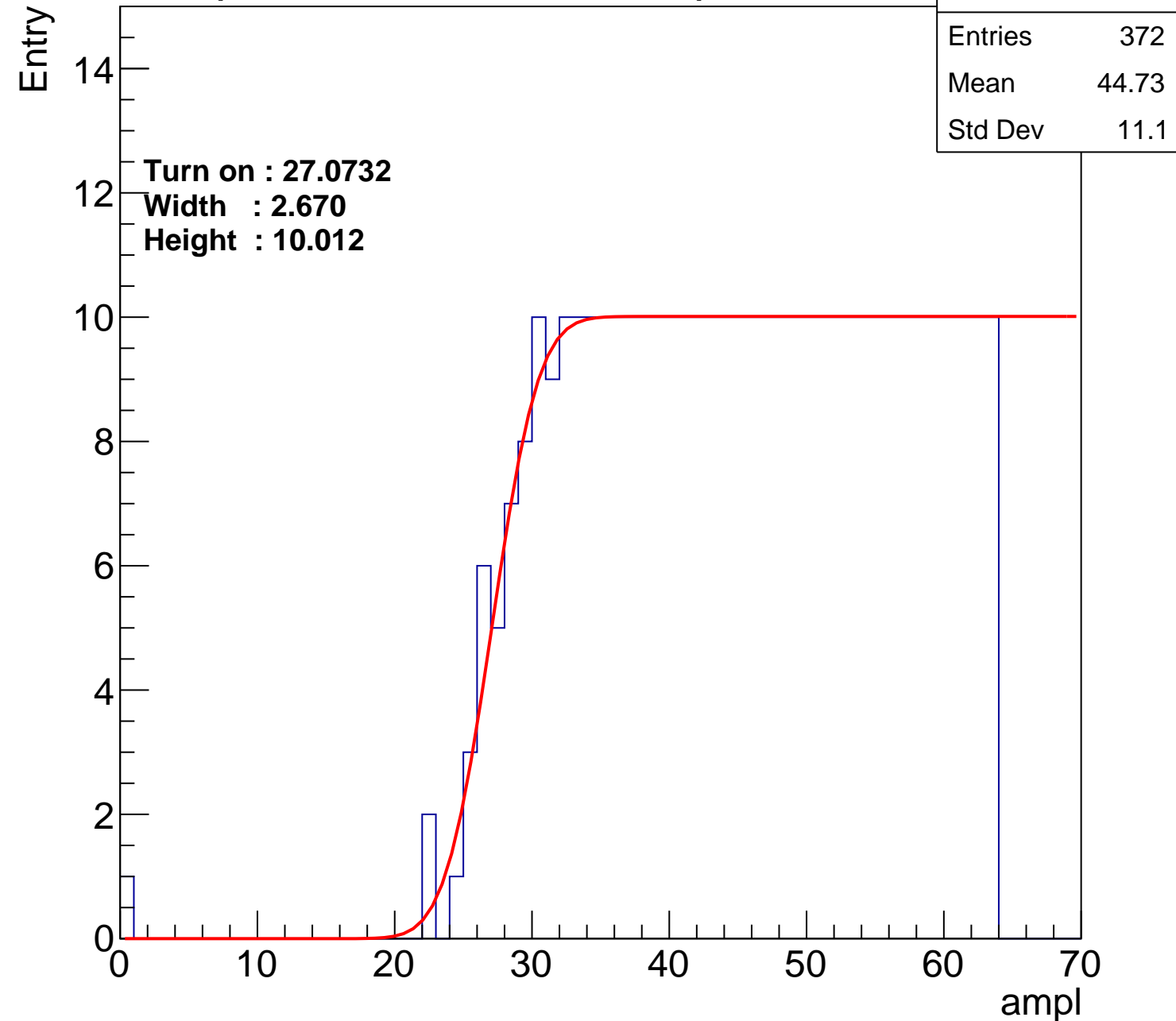
Width : 2.670

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch68

calib_packv5_042523_0143.root, FC#3, port B3

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.7411

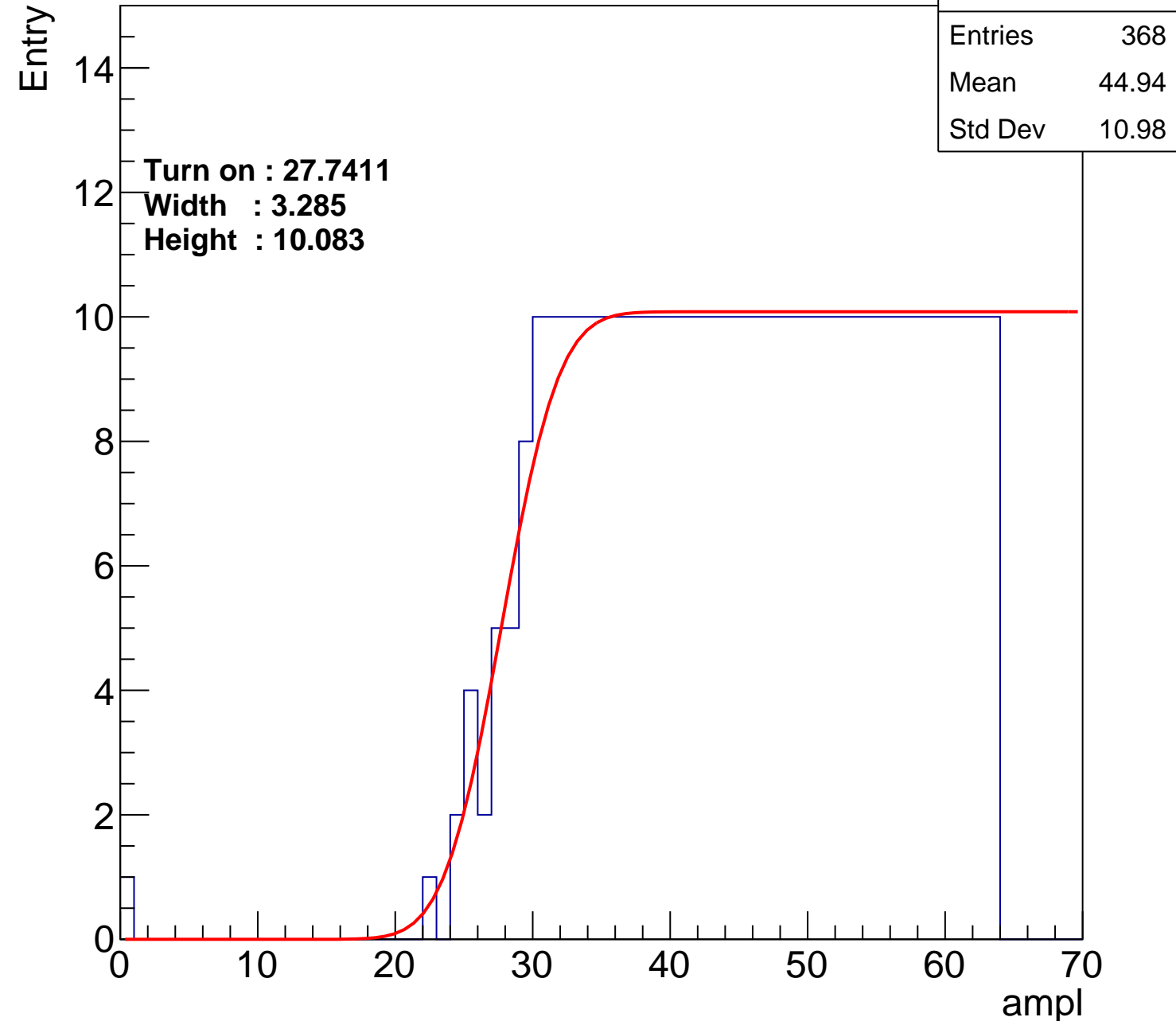
Width : 3.285

Height : 10.083

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch69

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.41
Std Dev	12.33

Turn on : 25.3167

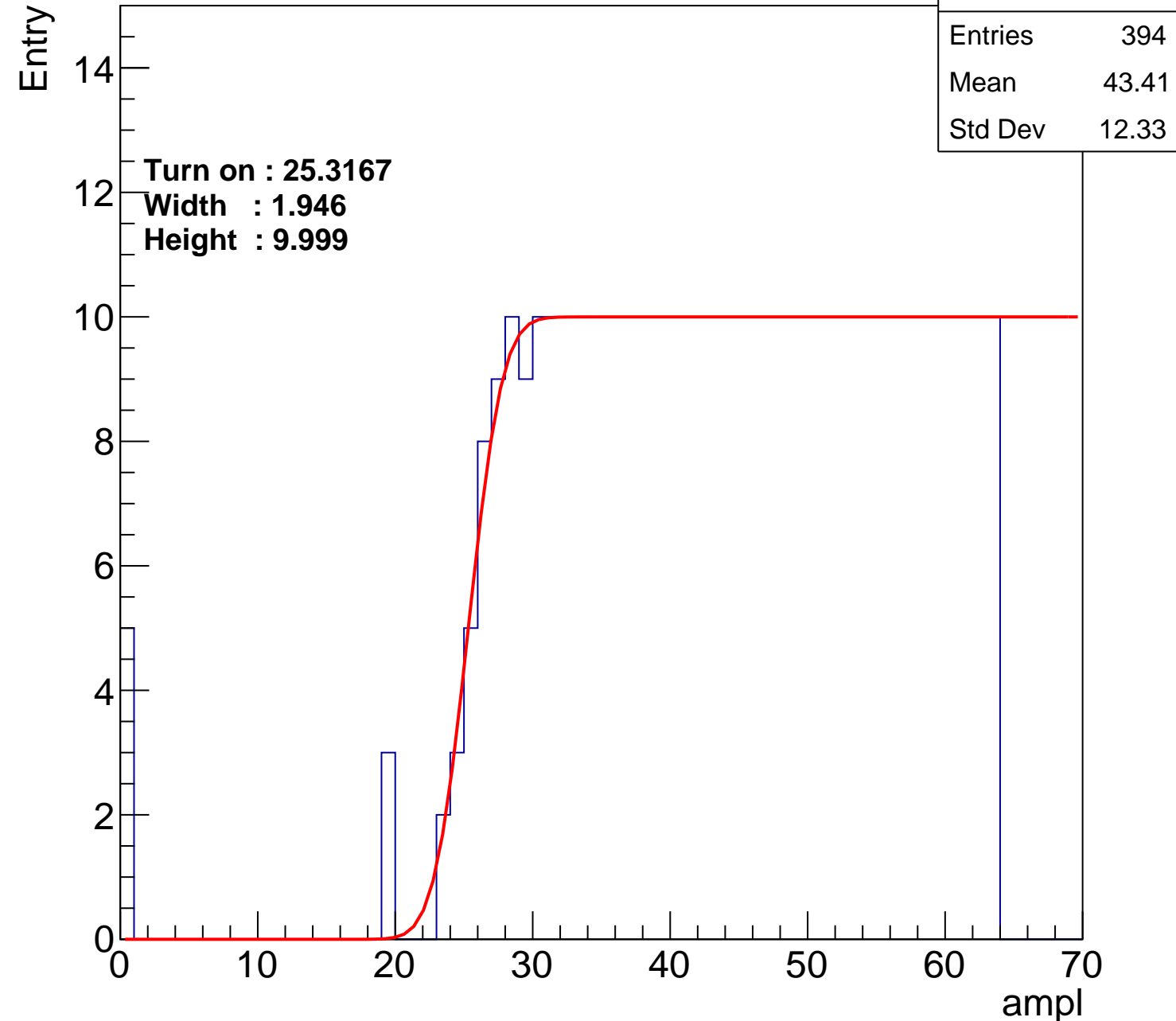
Width : 1.946

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch70

calib_packv5_042523_0143.root, FC#3, port B3

Entries	389
Mean	43.81
Std Dev	11.82

Turn on : 25.2790

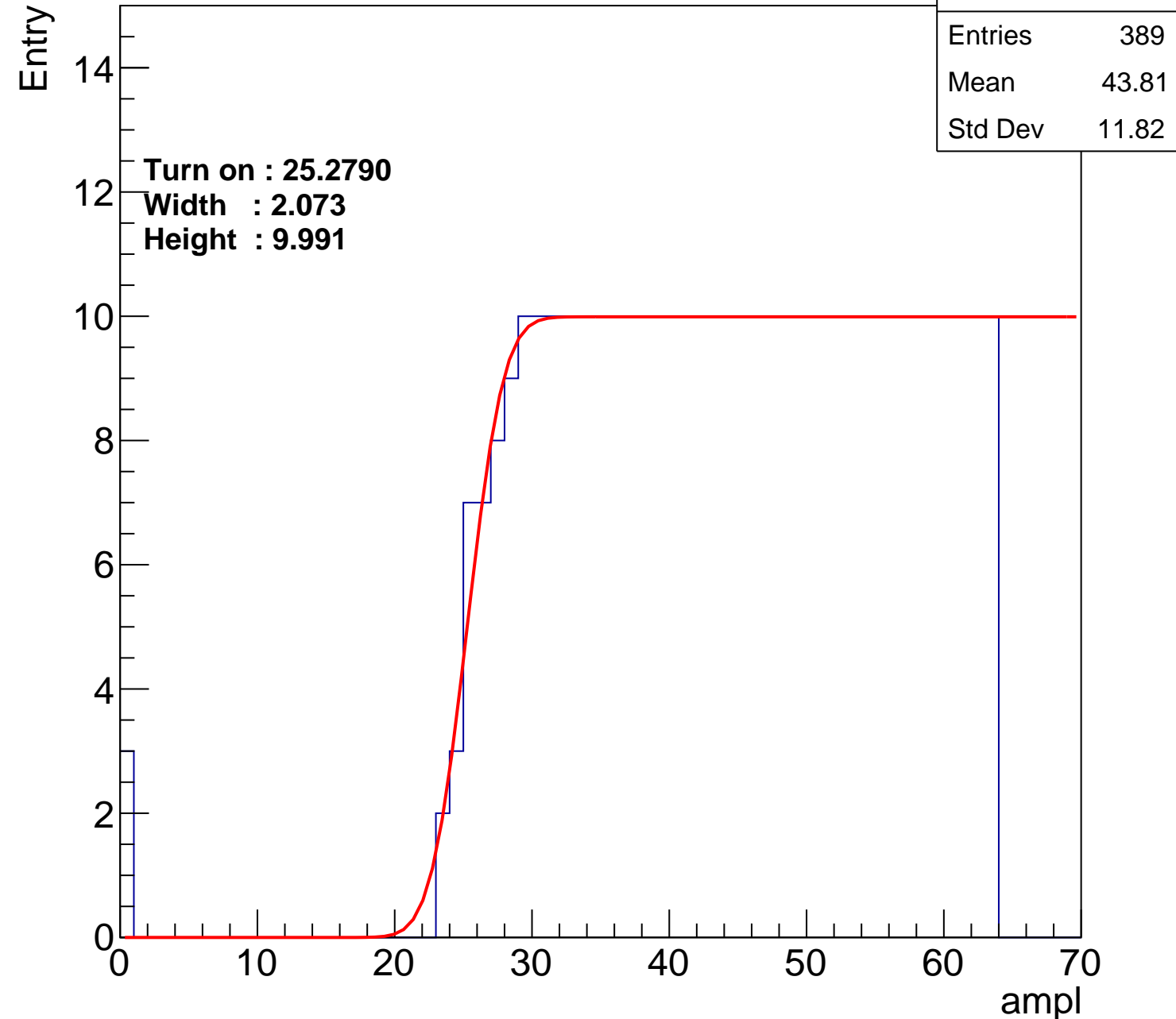
Width : 2.073

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch71

calib_packv5_042523_0143.root, FC#3, port B3

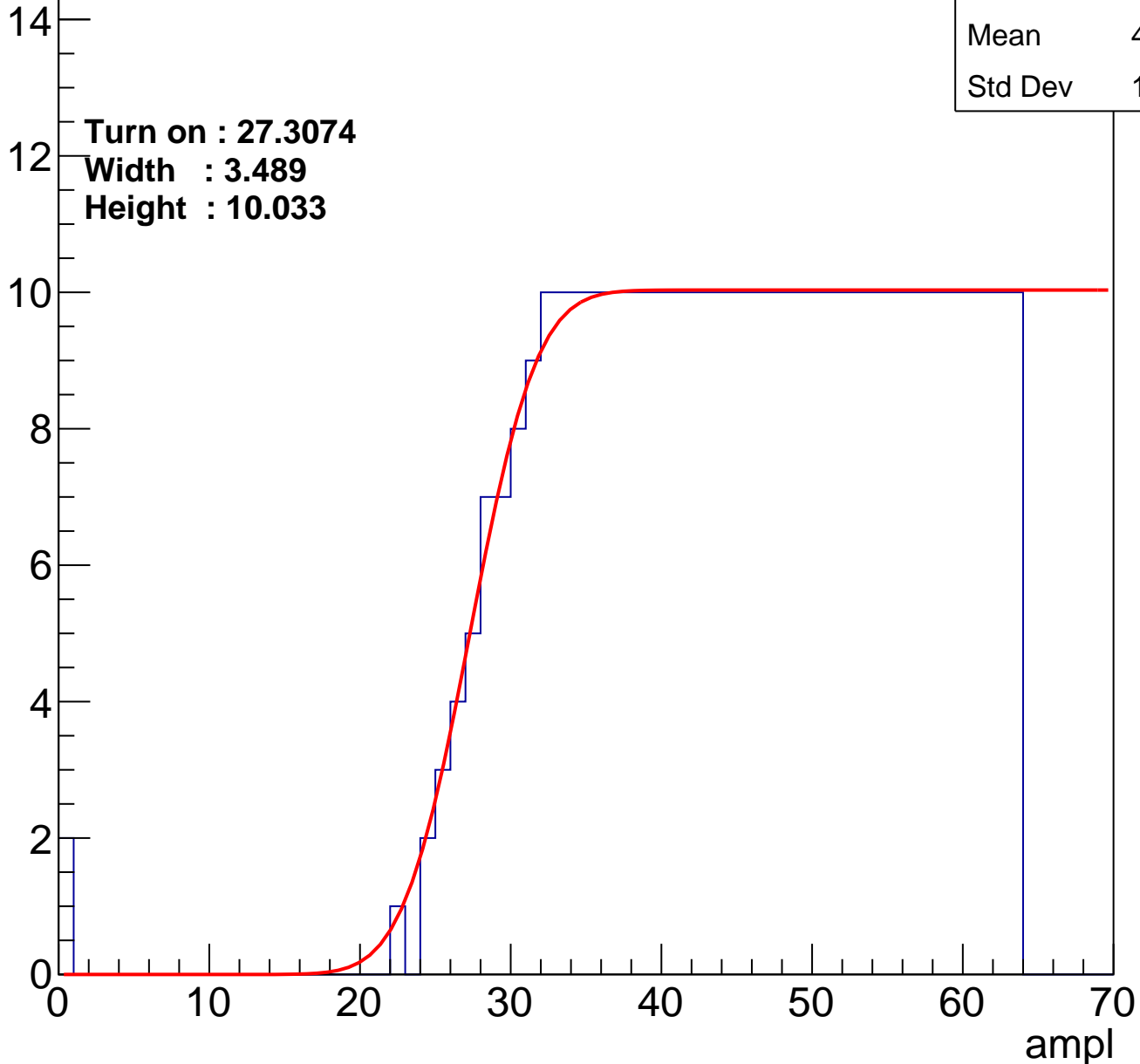
Entries	368
Mean	44.84
Std Dev	11.23

Turn on : 27.3074

Width : 3.489

Height : 10.033

Entry



B1L000S, U16-ch72

calib_packv5_042523_0143.root, FC#3, port B3

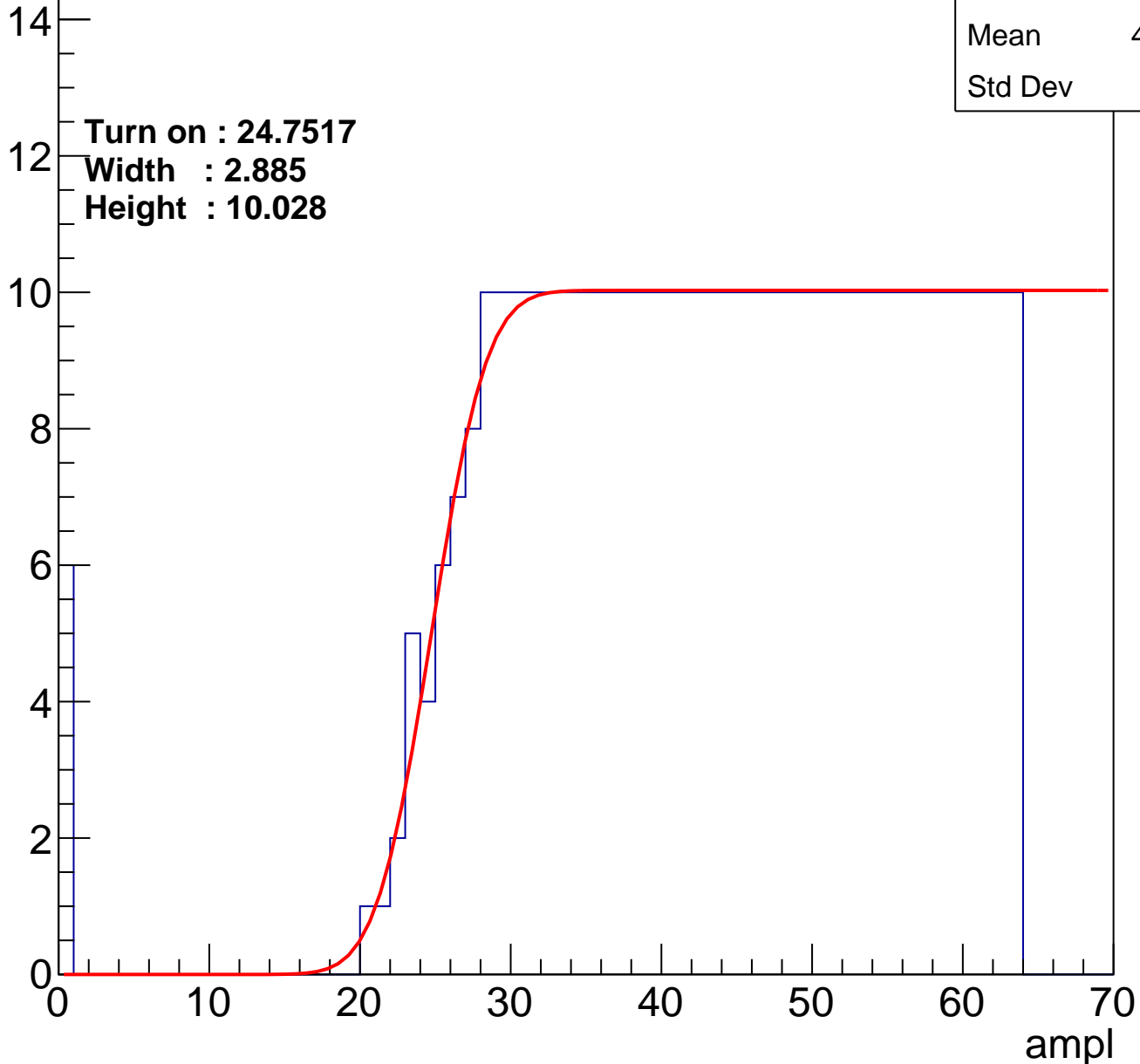
Entries	400
Mean	43.06
Std Dev	12.6

Turn on : 24.7517

Width : 2.885

Height : 10.028

Entry



B1L000S, U16-ch73

calib_packv5_042523_0143.root, FC#3, port B3

Entries	398
Mean	43.14
Std Dev	12.51

Turn on : 24.6470

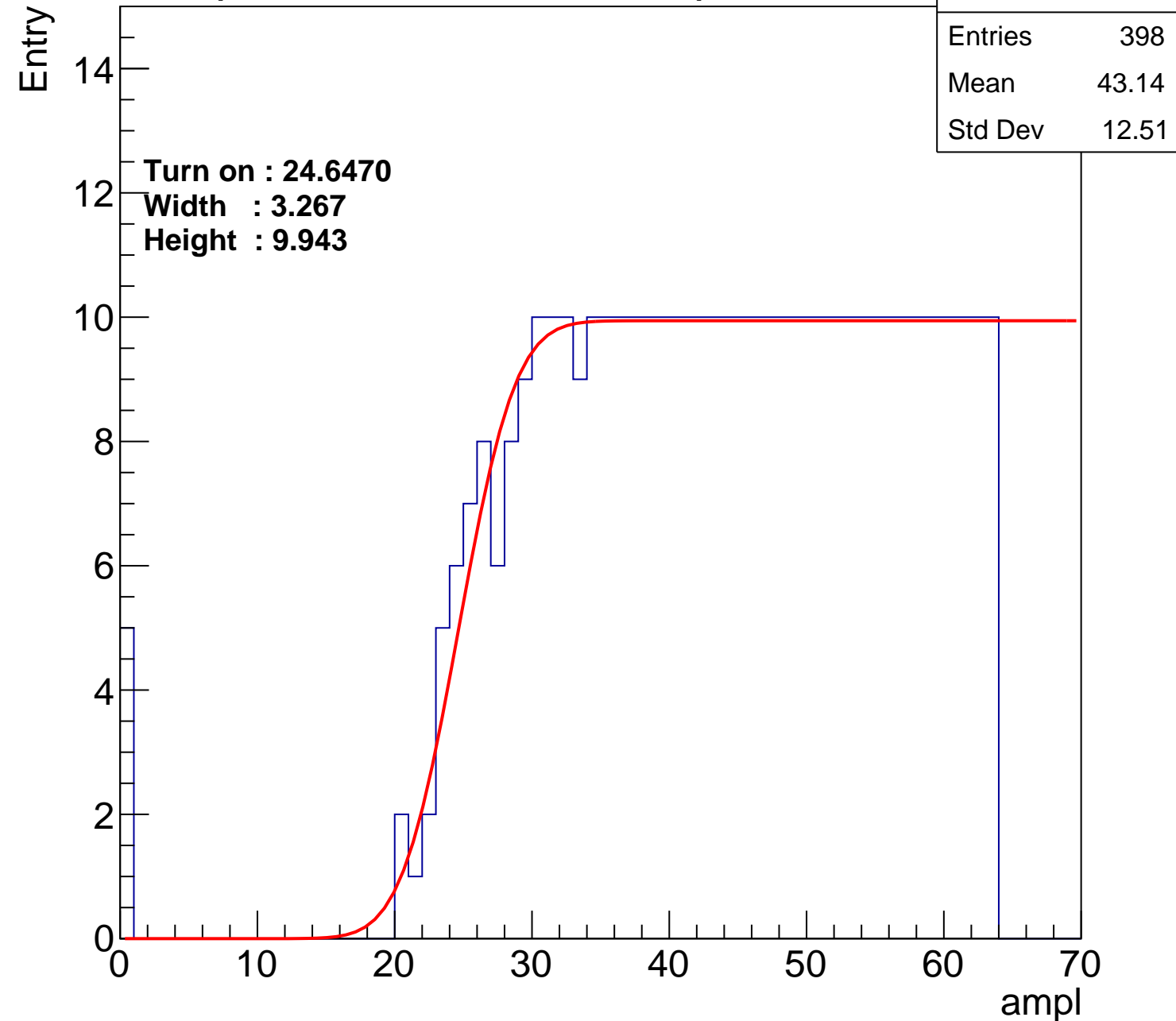
Width : 3.267

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch74

calib_packv5_042523_0143.root, FC#3, port B3

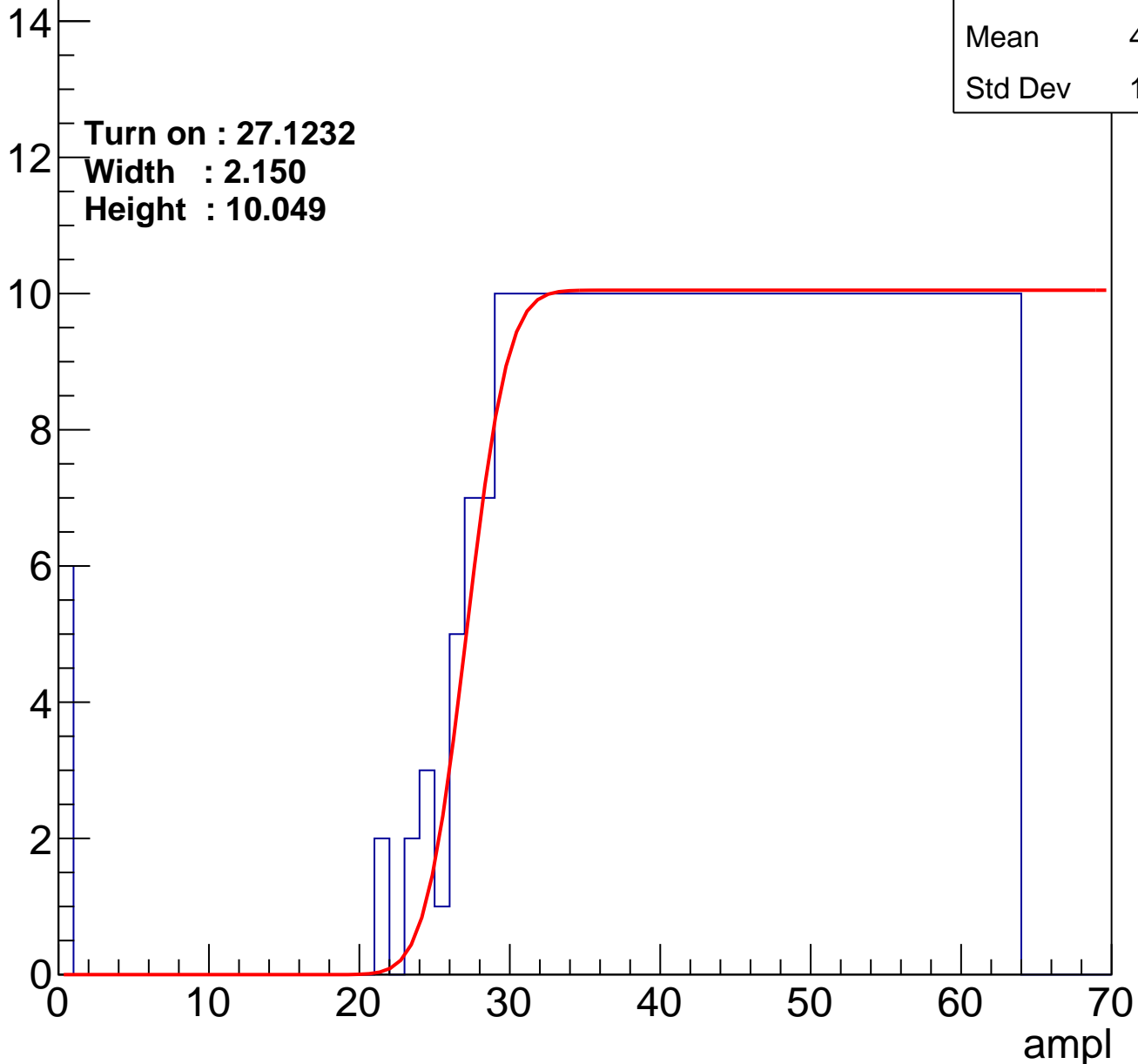
Entries	383
Mean	43.86
Std Dev	12.27

Turn on : 27.1232

Width : 2.150

Height : 10.049

Entry



B1L000S, U16-ch75

calib_packv5_042523_0143.root, FC#3, port B3

Entries	376
Mean	44.49
Std Dev	11.36

Turn on : 26.5808

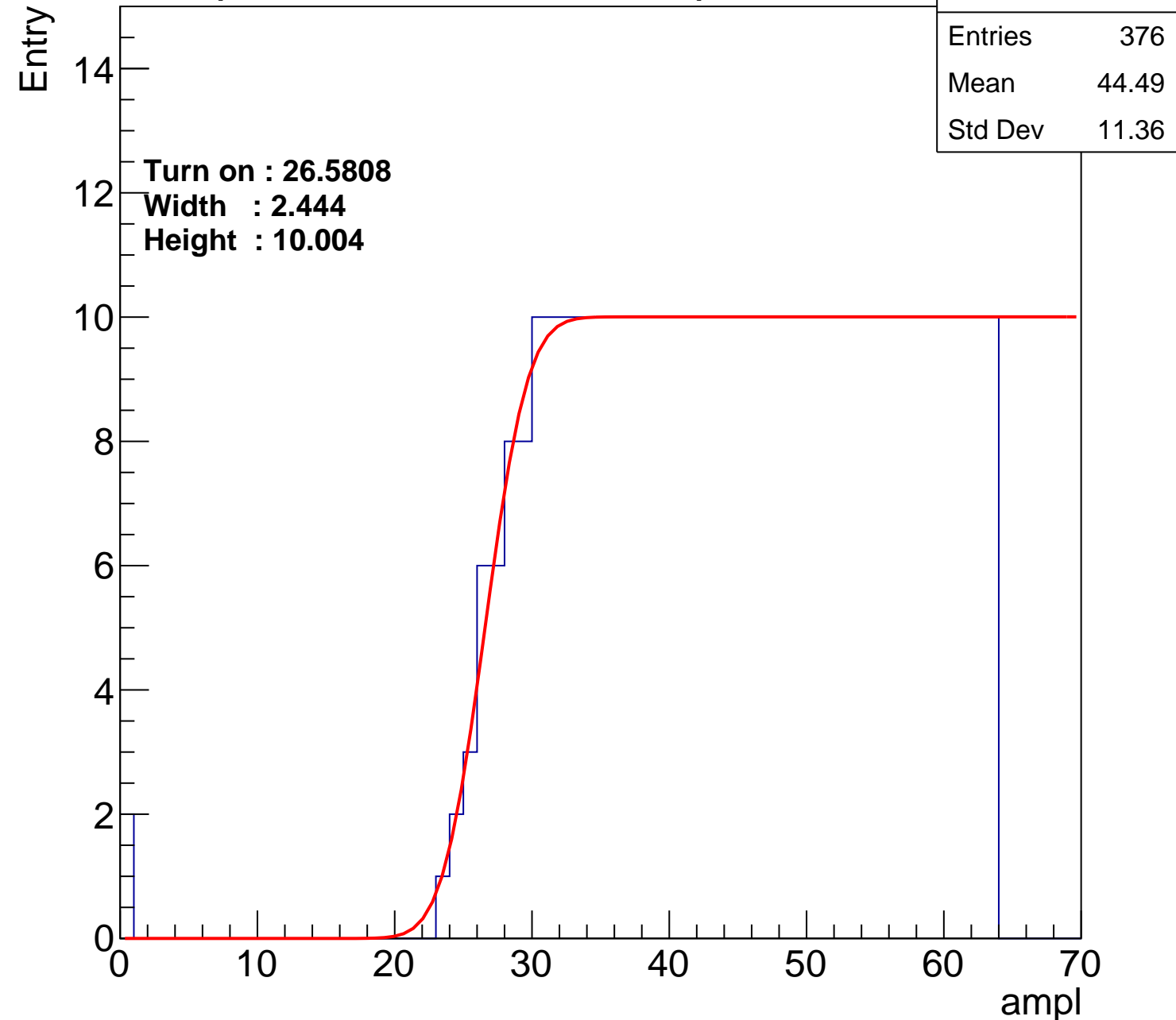
Width : 2.444

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch76

calib_packv5_042523_0143.root, FC#3, port B3

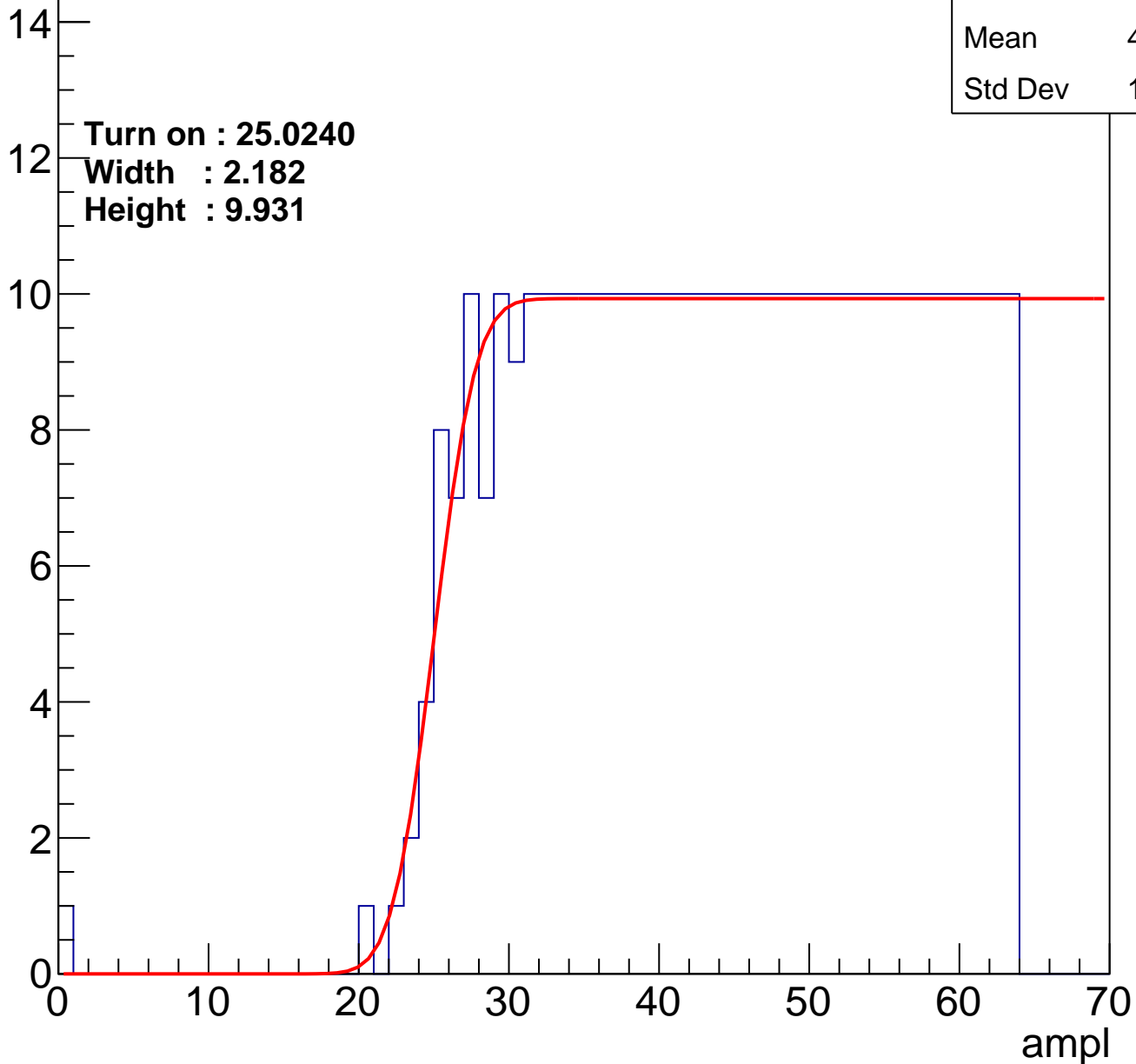
Entries	390
Mean	43.85
Std Dev	11.57

Turn on : 25.0240

Width : 2.182

Height : 9.931

Entry



B1L000S, U16-ch77

calib_packv5_042523_0143.root, FC#3, port B3

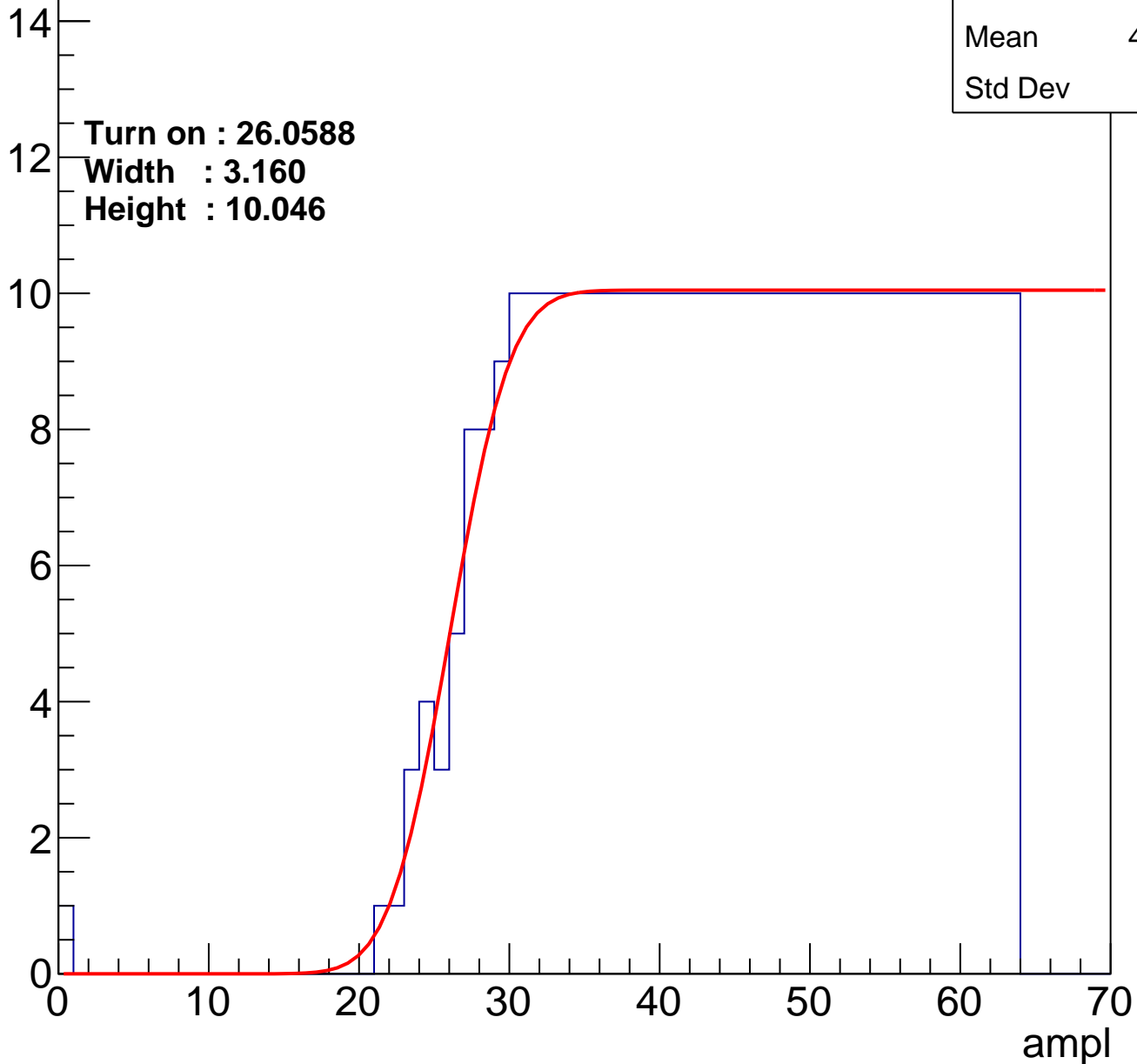
Entries	383
Mean	44.19
Std Dev	11.4

Turn on : 26.0588

Width : 3.160

Height : 10.046

Entry



B1L000S, U16-ch78

calib_packv5_042523_0143.root, FC#3, port B3

Entries	378
Mean	44.33
Std Dev	11.52

Turn on : 26.6343

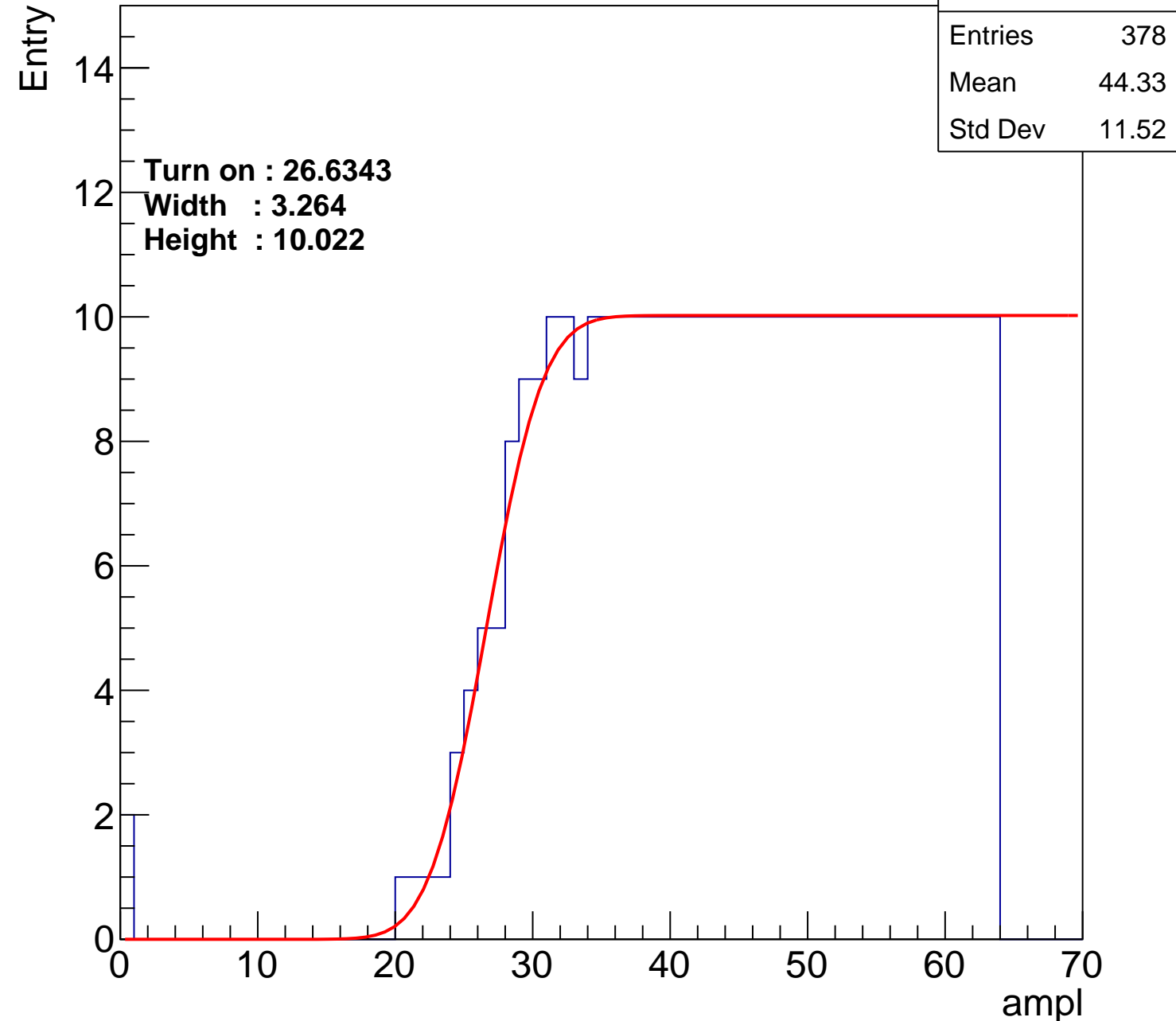
Width : 3.264

Height : 10.022

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch79

calib_packv5_042523_0143.root, FC#3, port B3

Entries	406
Mean	42.93
Std Dev	12.34

Turn on : 23.9383

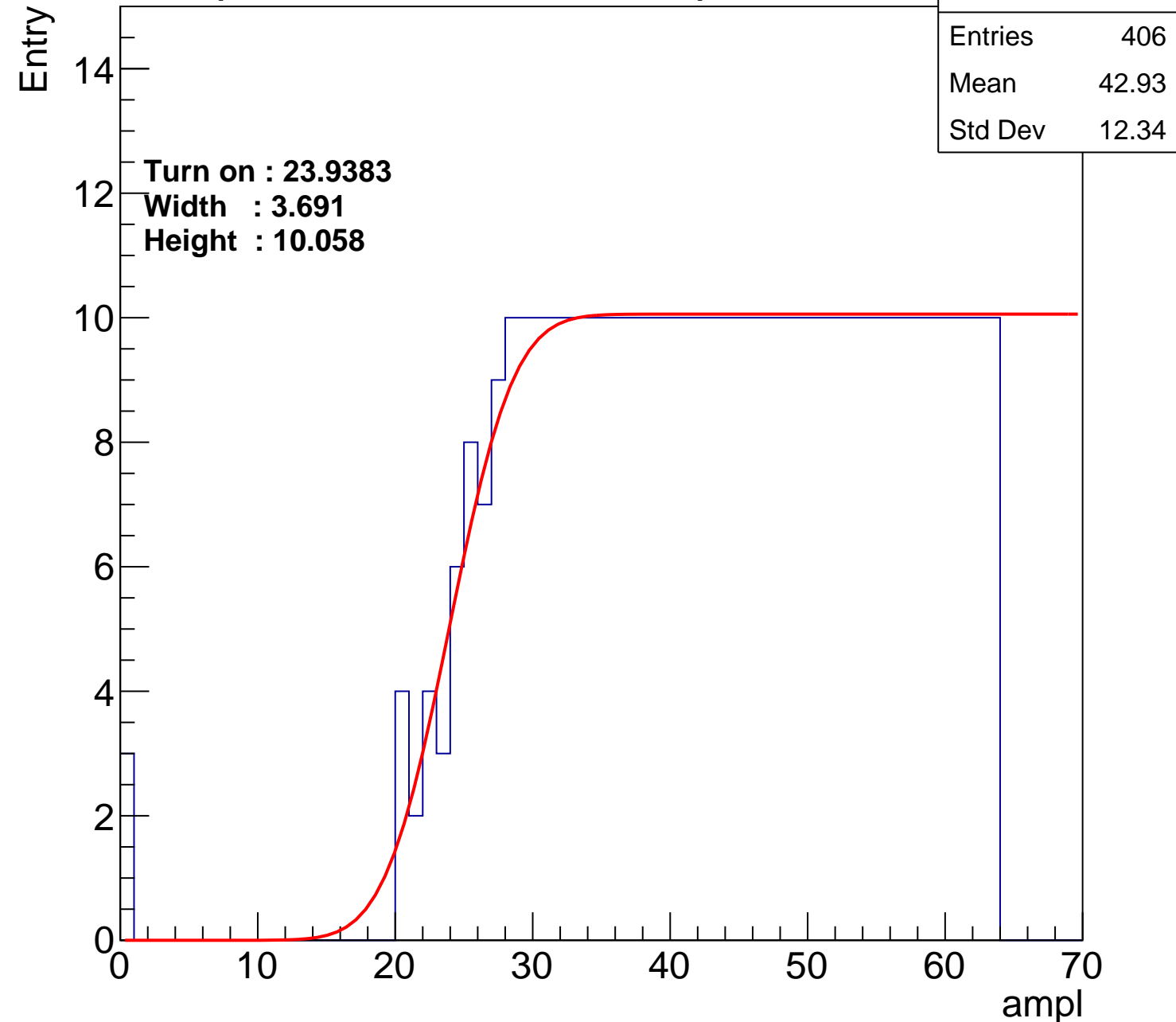
Width : 3.691

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#3, port B3

Entries	372
Mean	44.74
Std Dev	11.09

Mean	44.74
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Std Dev	11.09
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Width : 2.880

Height : 9.981



B1L000S, U16-ch81

calib_packv5_042523_0143.root, FC#3, port B3

Entries	365
Mean	44.75
Std Dev	11.77

Turn on : 27.6603

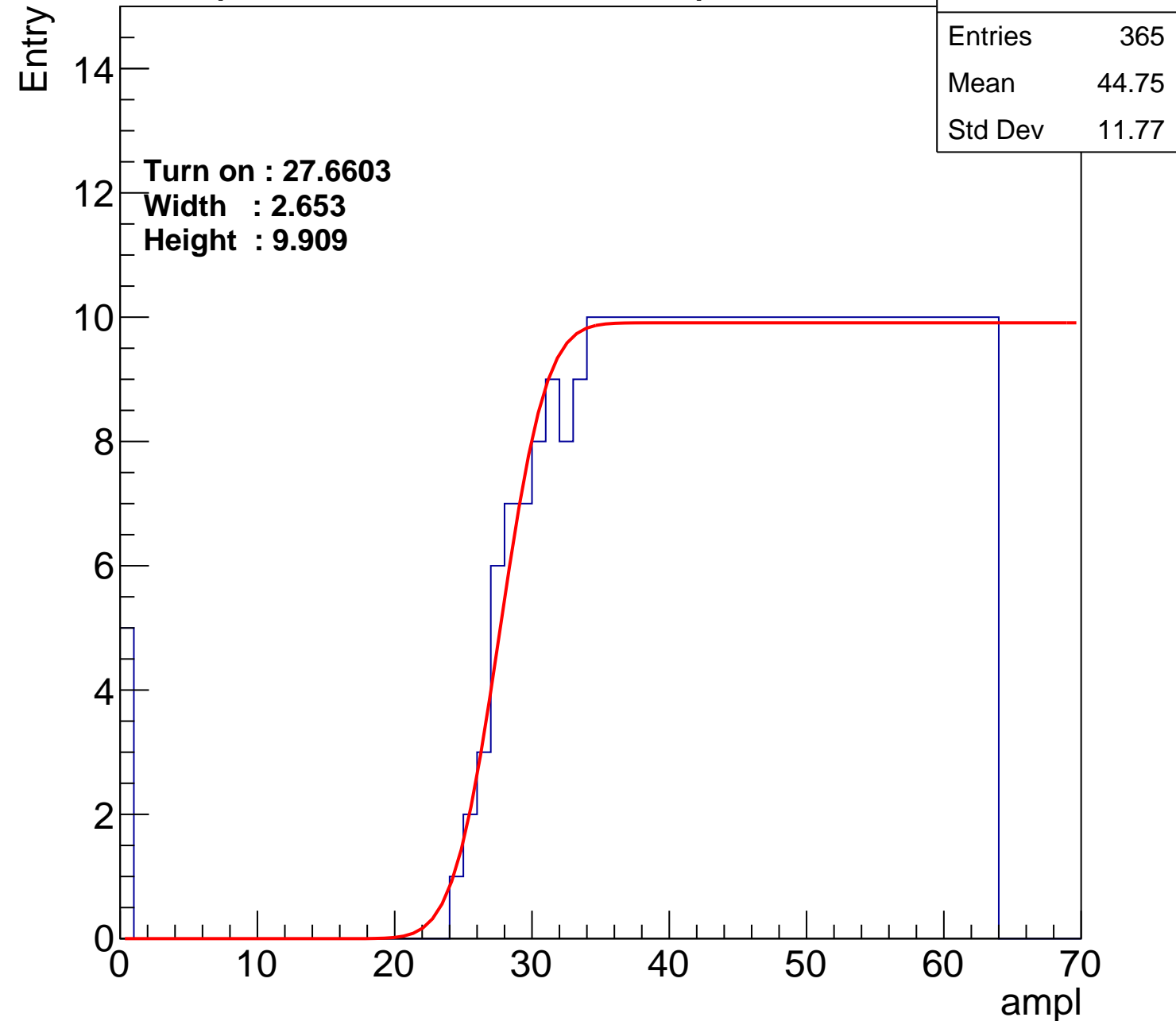
Width : 2.653

Height : 9.909

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch82

calib_packv5_042523_0143.root, FC#3, port B3

Entries	385
Mean	44.03
Std Dev	11.62

Turn on : 25.6783

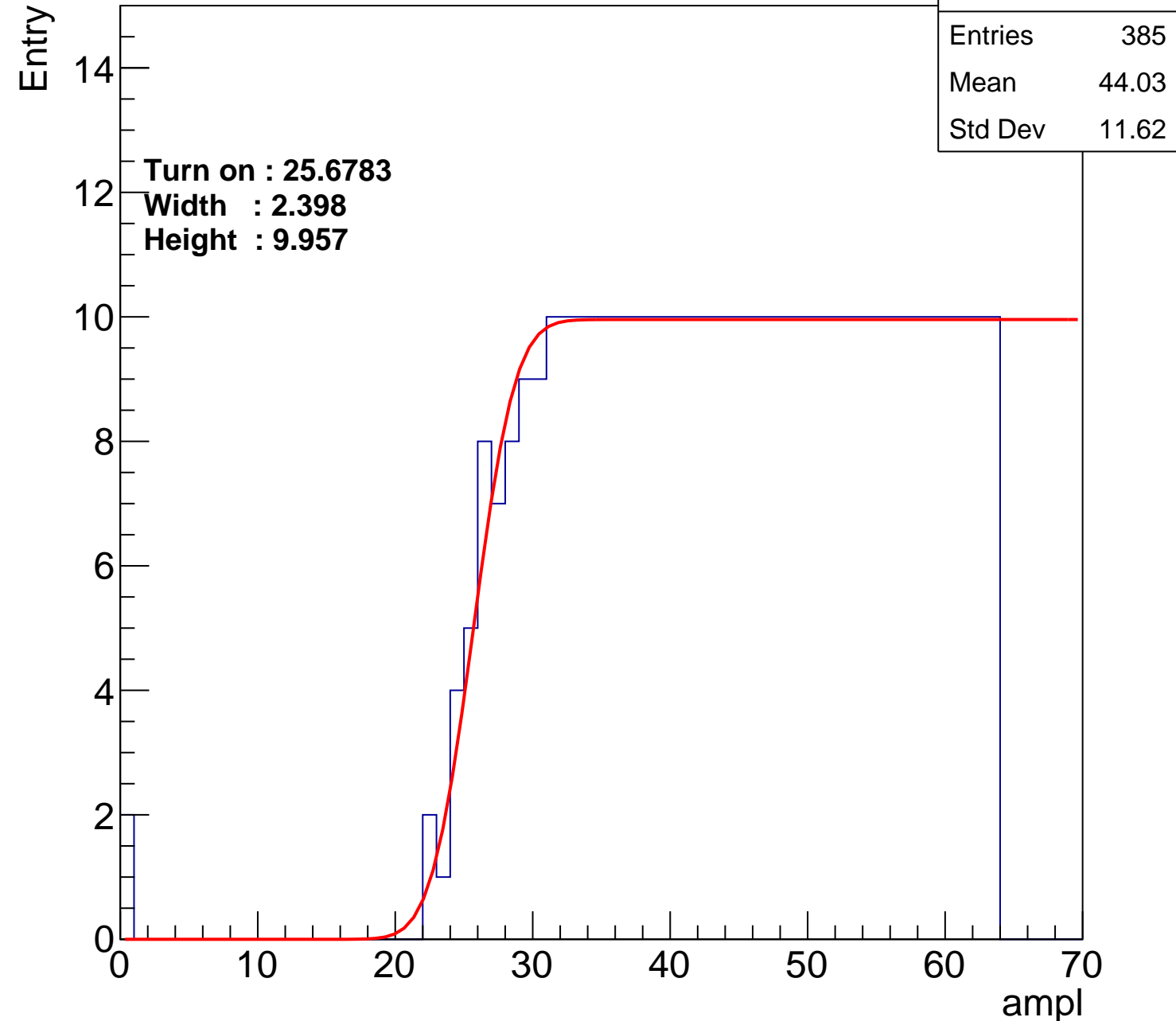
Width : 2.398

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch83

calib_packv5_042523_0143.root, FC#3, port B3

Entries	393
Mean	43.38
Std Dev	12.47

Turn on : 25.0015

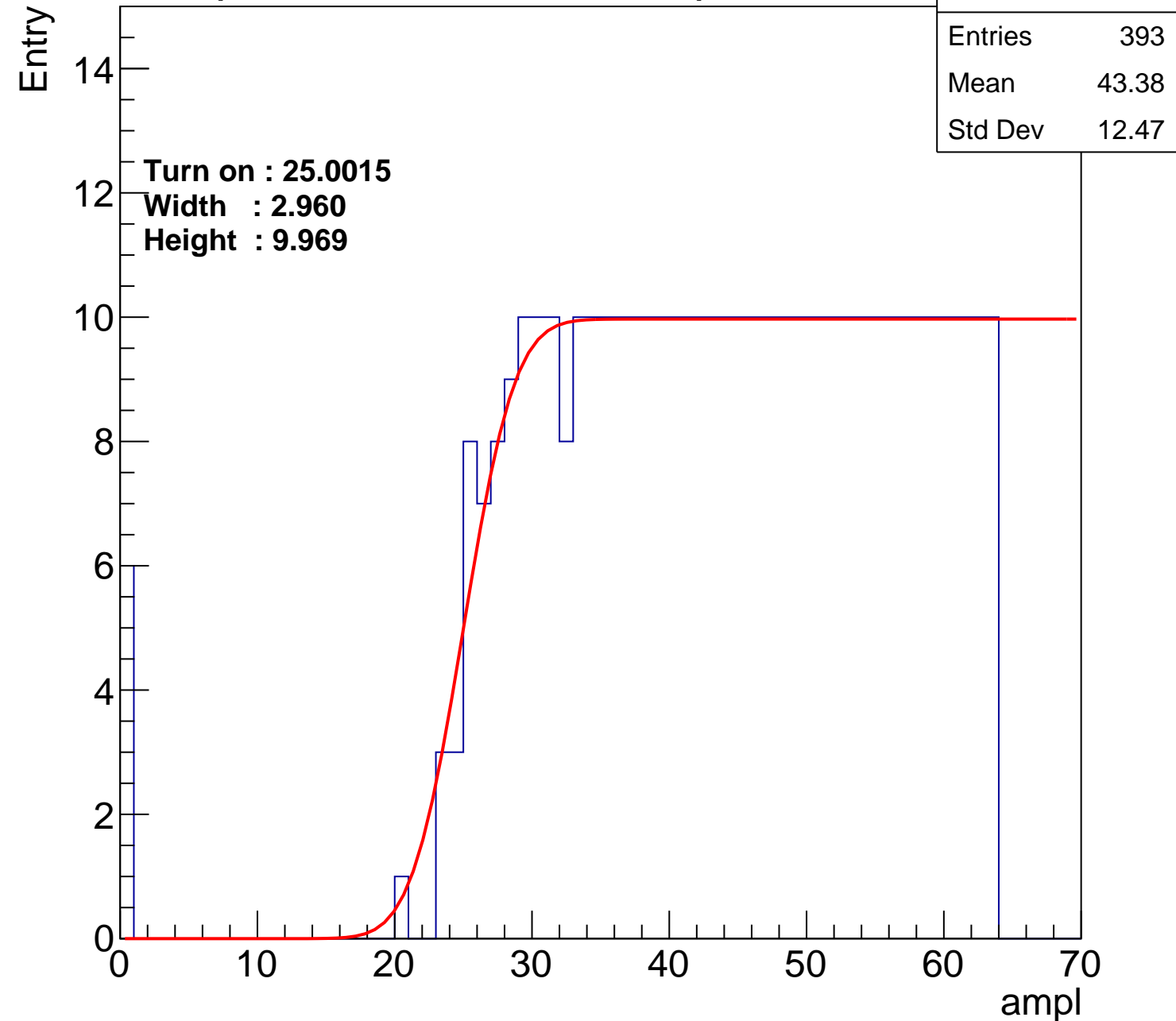
Width : 2.960

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch84

calib_packv5_042523_0143.root, FC#3, port B3

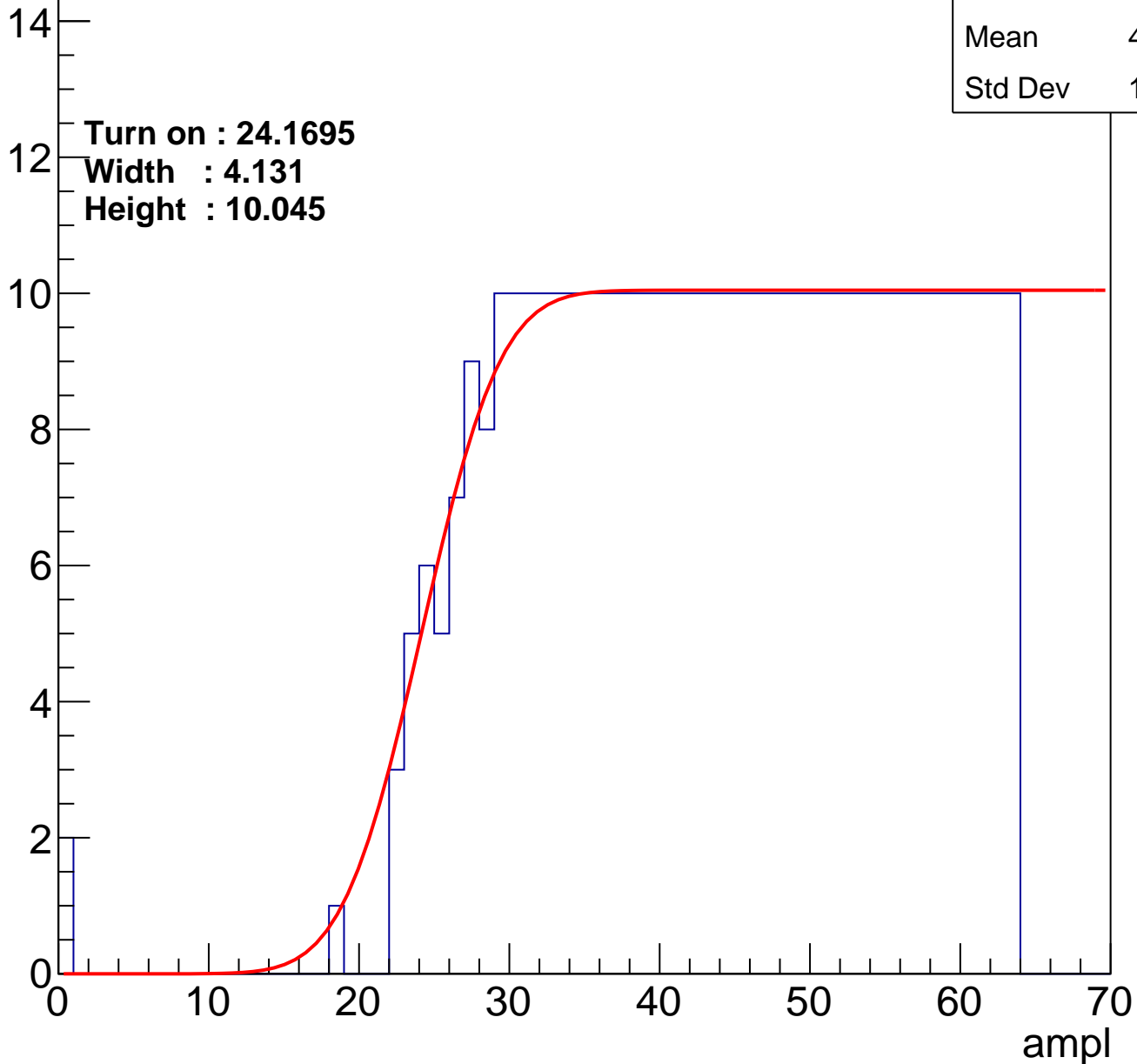
Entries	396
Mean	43.48
Std Dev	11.93

Turn on : 24.1695

Width : 4.131

Height : 10.045

Entry



B1L000S, U16-ch85

calib_packv5_042523_0143.root, FC#3, port B3

Entries	394
Mean	43.46
Std Dev	12.19

Turn on : 25.6253

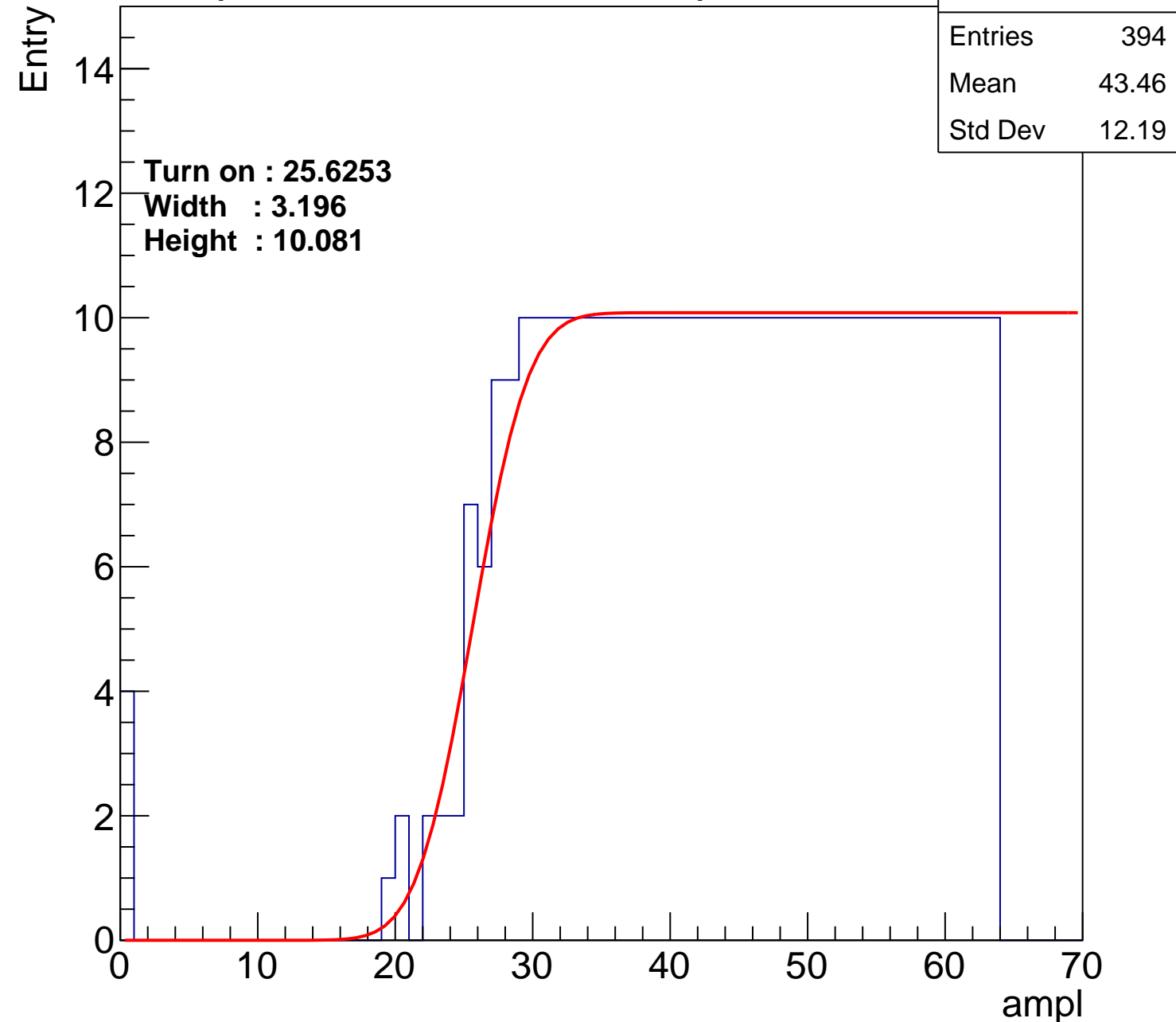
Width : 3.196

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch86

calib_packv5_042523_0143.root, FC#3, port B3

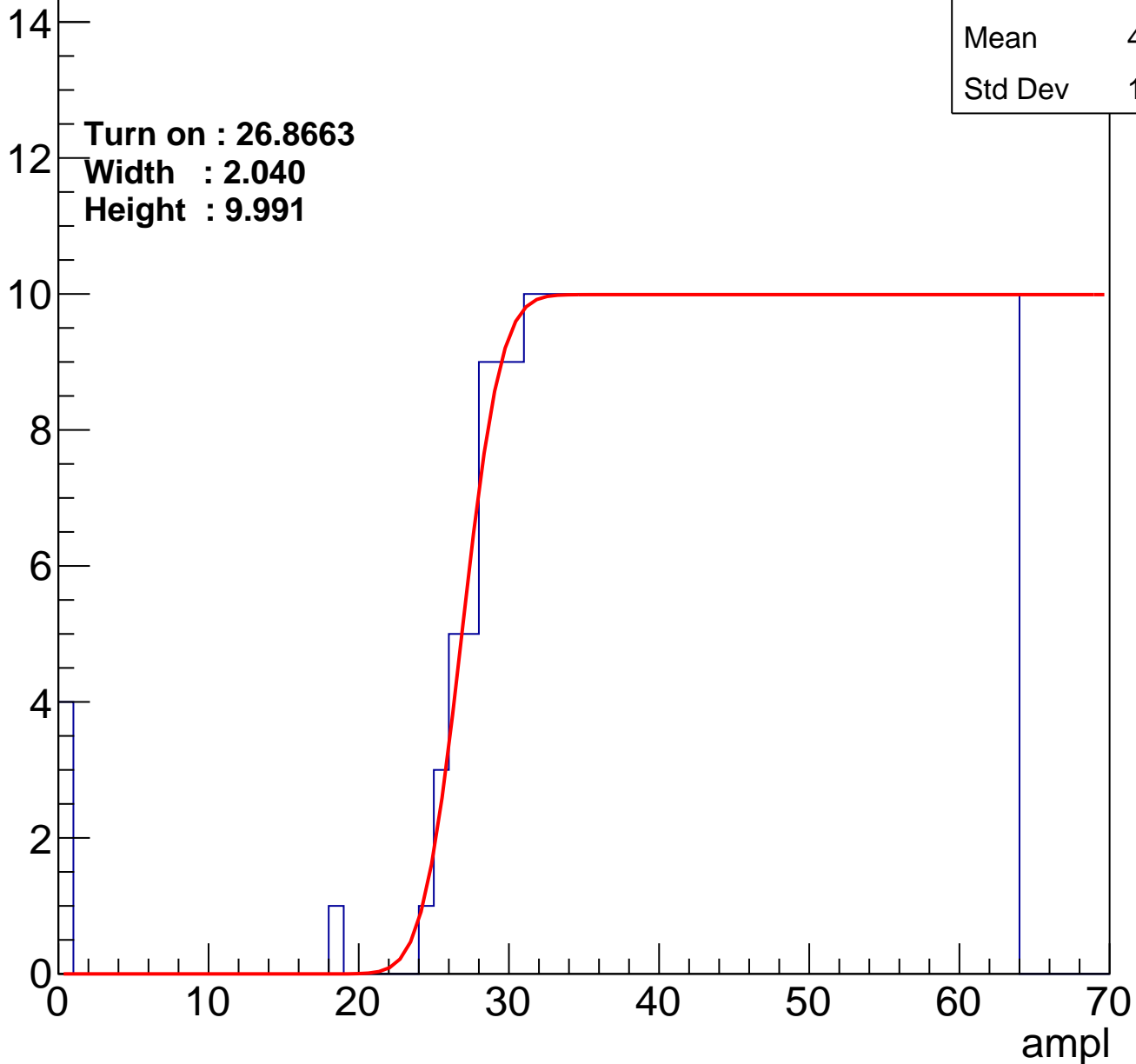
Entries	376
Mean	44.35
Std Dev	11.75

Turn on : 26.8663

Width : 2.040

Height : 9.991

Entry



B1L000S, U16-ch87

calib_packv5_042523_0143.root, FC#3, port B3

Entries	399
Mean	43.35
Std Dev	11.96

Turn on : 24.1172

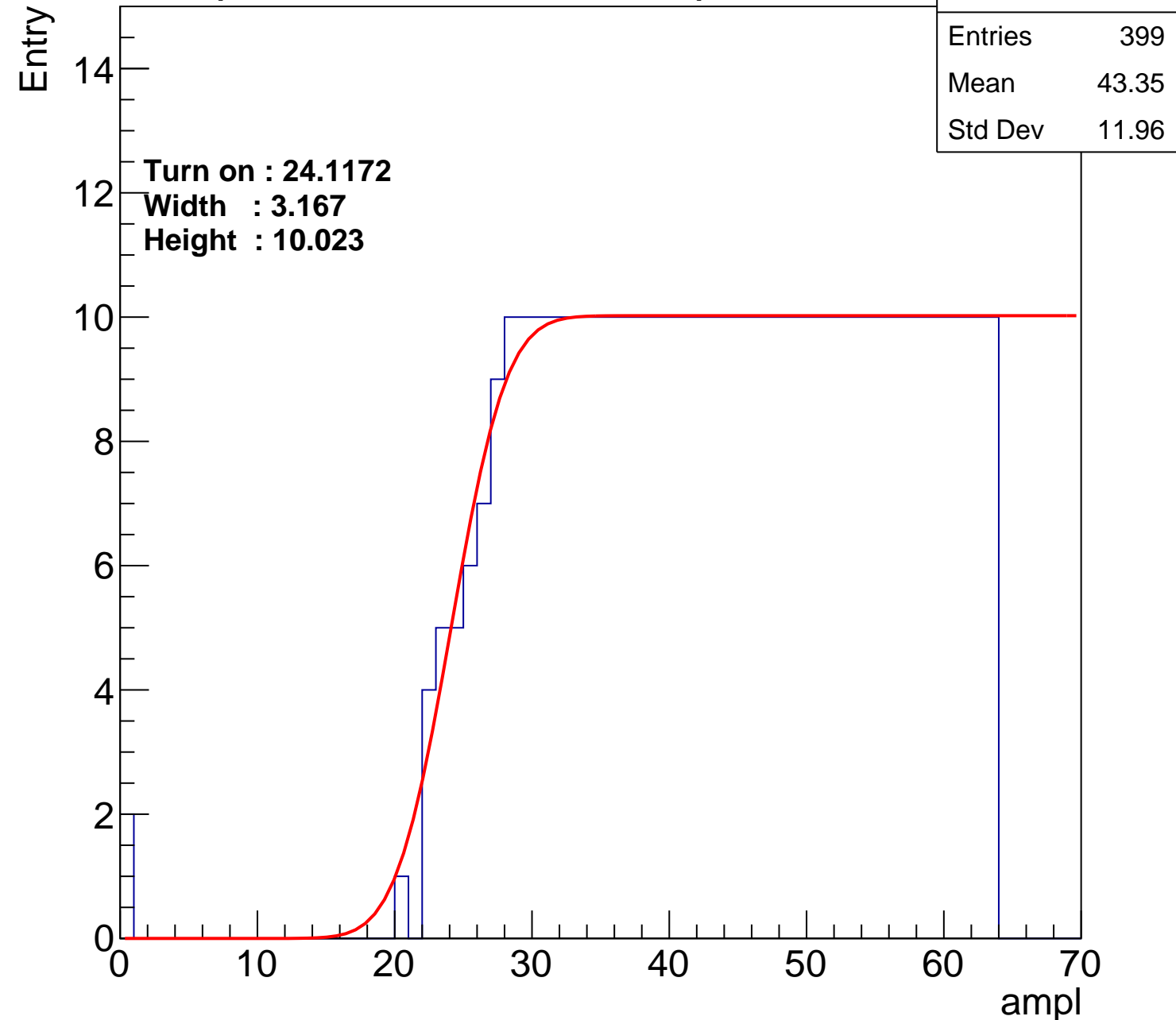
Width : 3.167

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch88

calib_packv5_042523_0143.root, FC#3, port B3

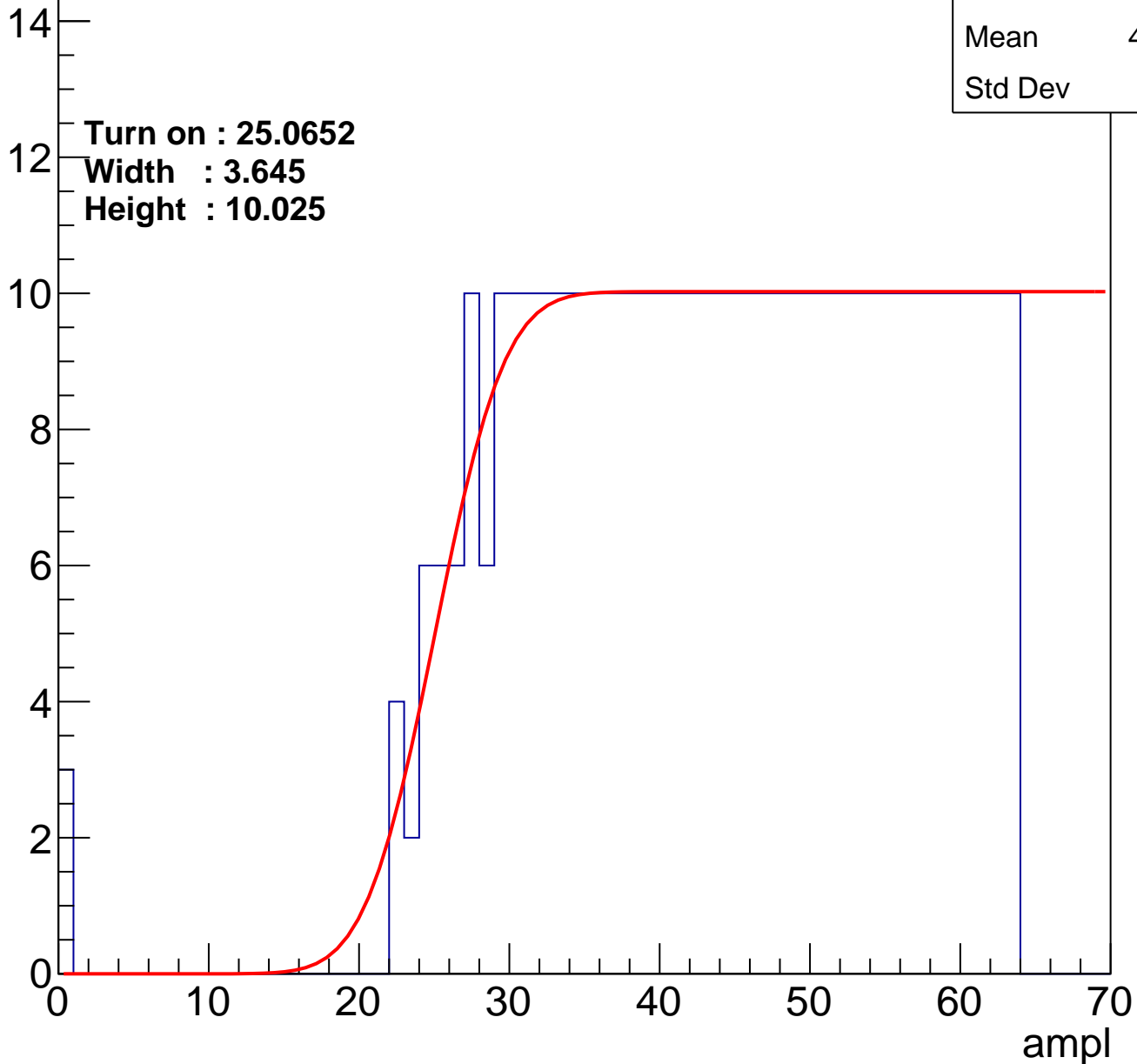
Entries	393
Mean	43.57
Std Dev	12

Turn on : 25.0652

Width : 3.645

Height : 10.025

Entry



B1L000S, U16-ch89

calib_packv5_042523_0143.root, FC#3, port B3

Entries	390
Mean	43.69
Std Dev	11.98

Turn on : 25.6204

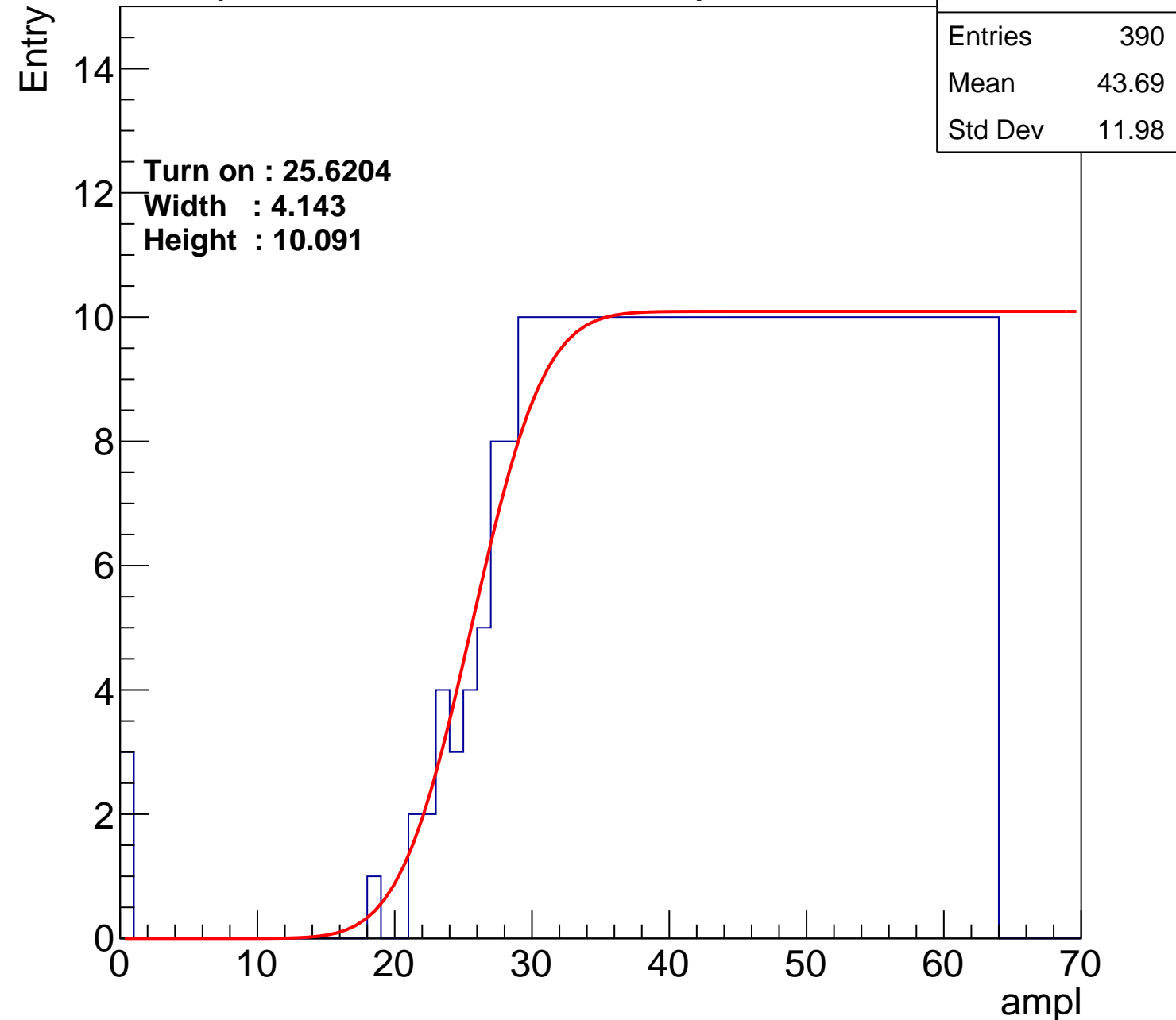
Width : 4.143

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch90

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.26
Std Dev	11.64

Turn on : 26.3220

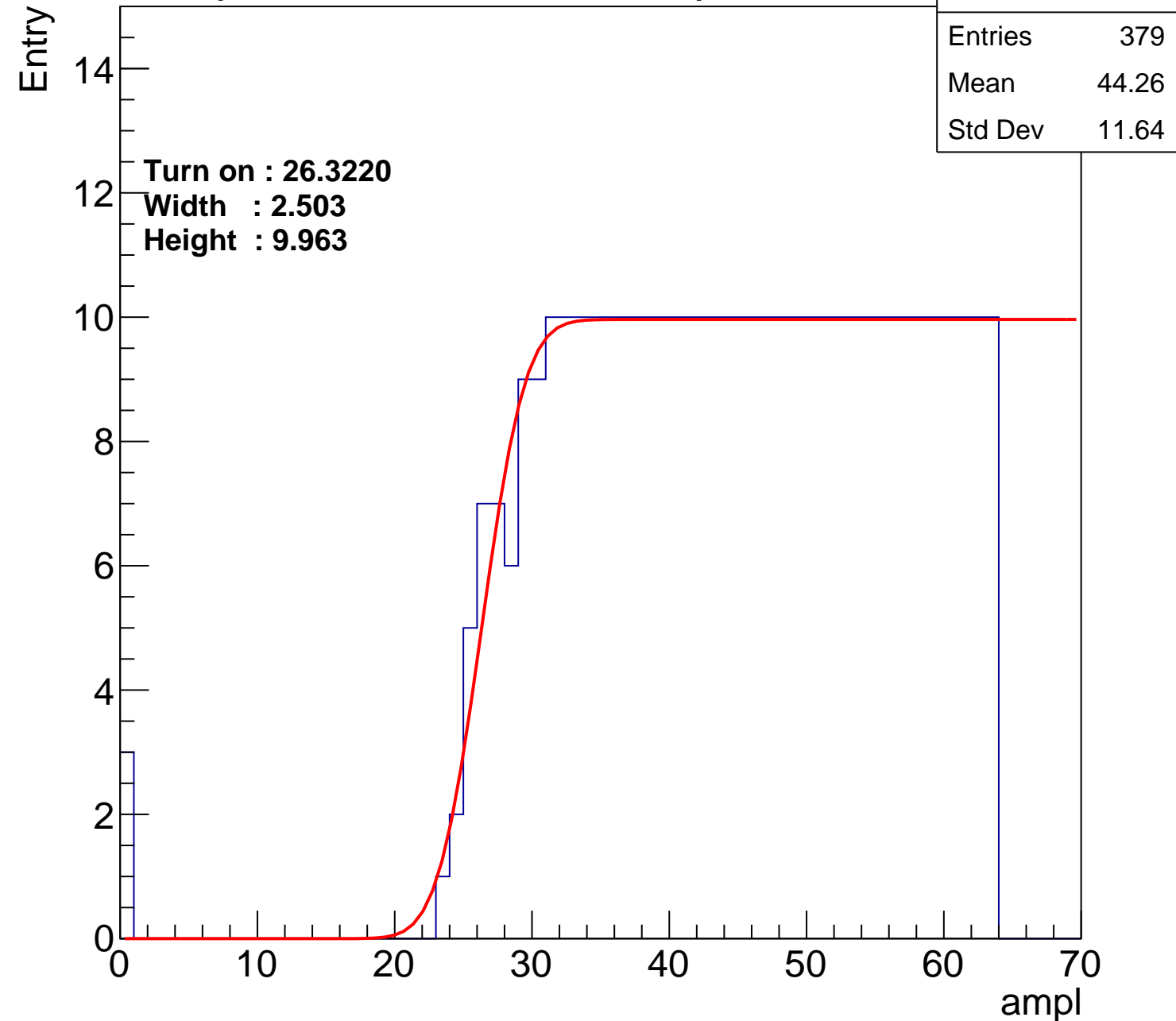
Width : 2.503

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch91

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.04
Std Dev	11.73

Turn on : 25.9935

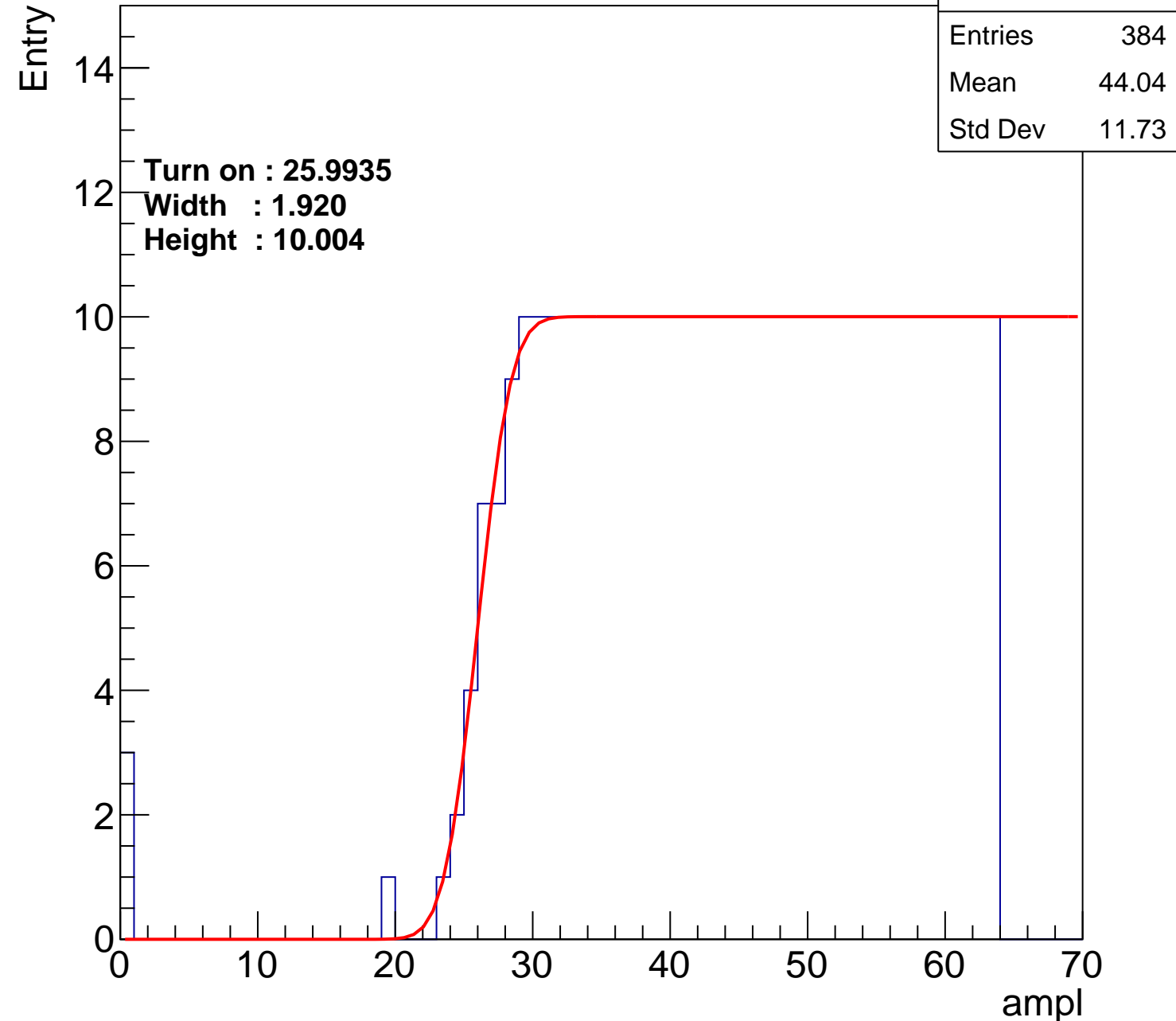
Width : 1.920

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch92

calib_packv5_042523_0143.root, FC#3, port B3

Entries	401
Mean	43.03
Std Dev	12.59

Turn on : 24.6761

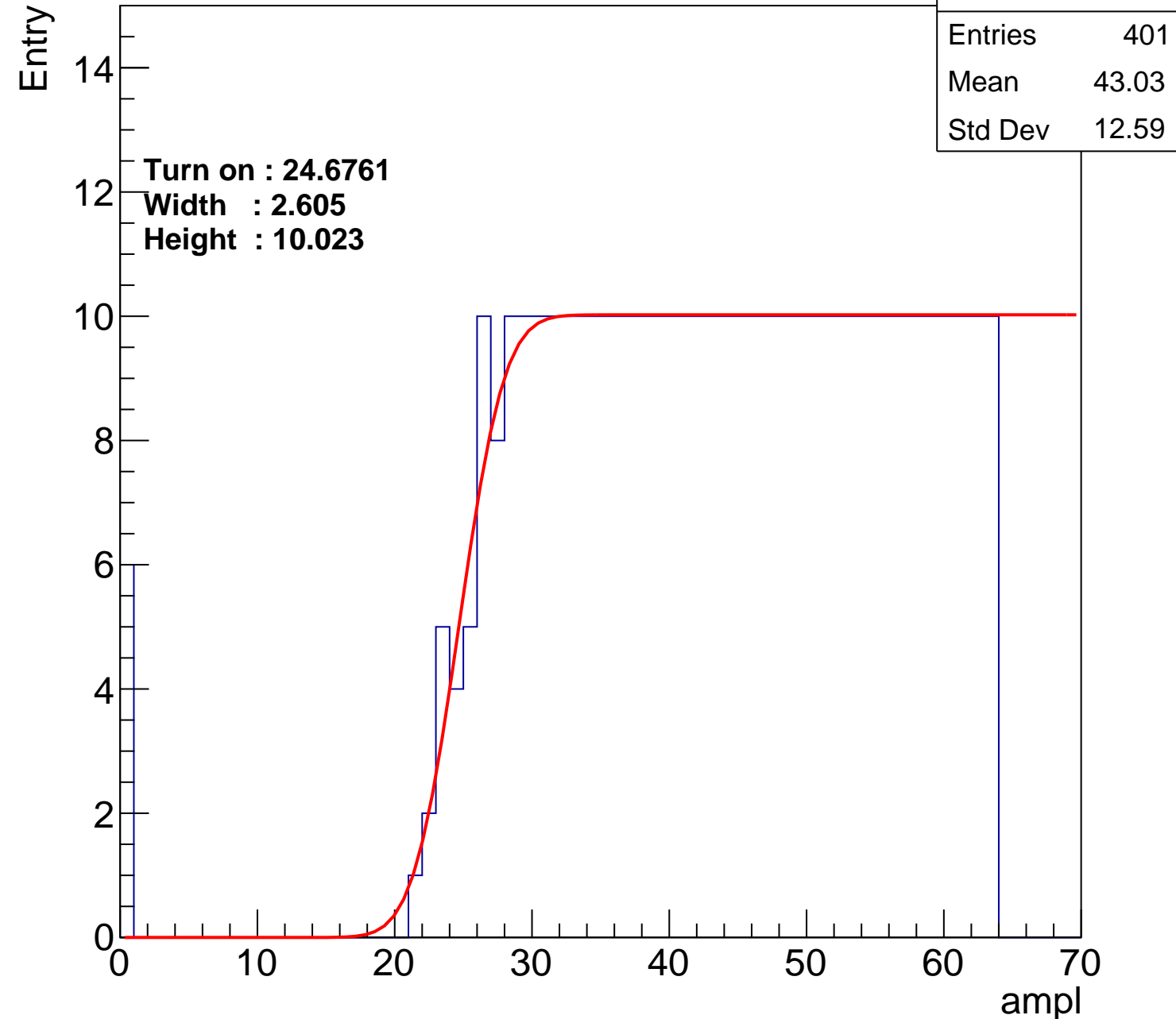
Width : 2.605

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch93

calib_packv5_042523_0143.root, FC#3, port B3

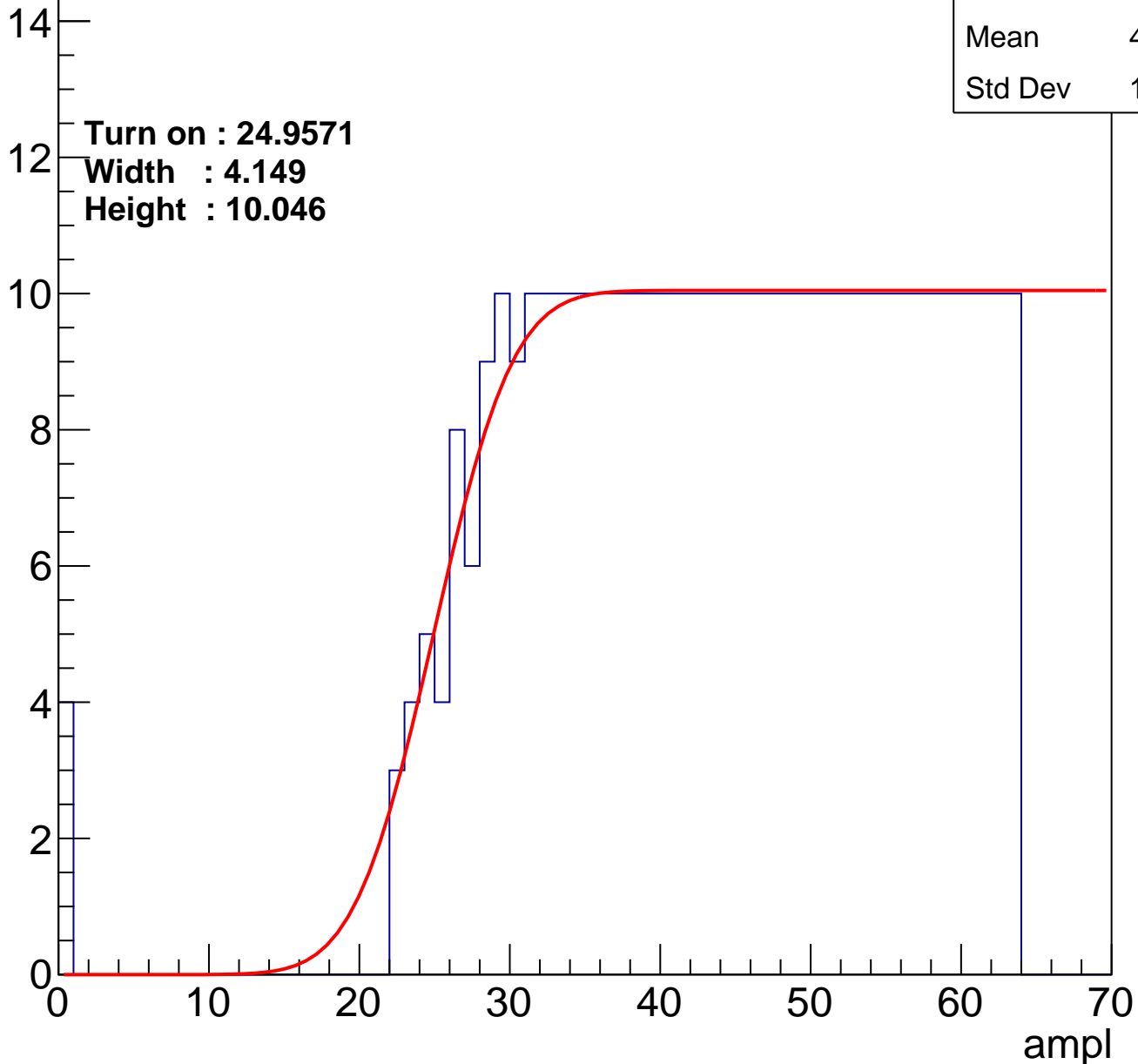
Entries	392
Mean	43.55
Std Dev	12.15

Turn on : 24.9571

Width : 4.149

Height : 10.046

Entry



B1L000S, U16-ch94

calib_packv5_042523_0143.root, FC#3, port B3

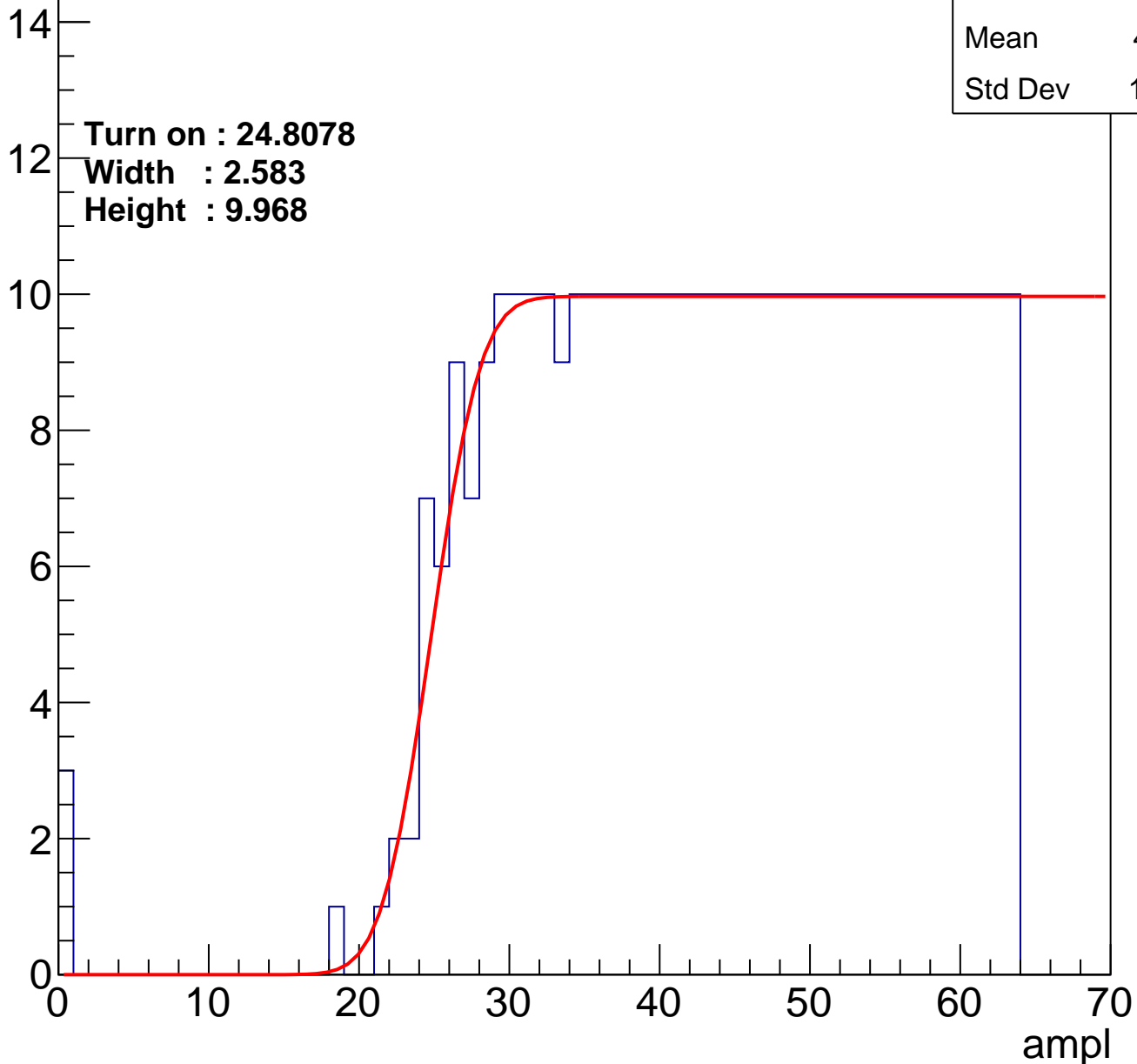
Entries	396
Mean	43.41
Std Dev	12.09

Turn on : 24.8078

Width : 2.583

Height : 9.968

Entry



B1L000S, U16-ch95

calib_packv5_042523_0143.root, FC#3, port B3

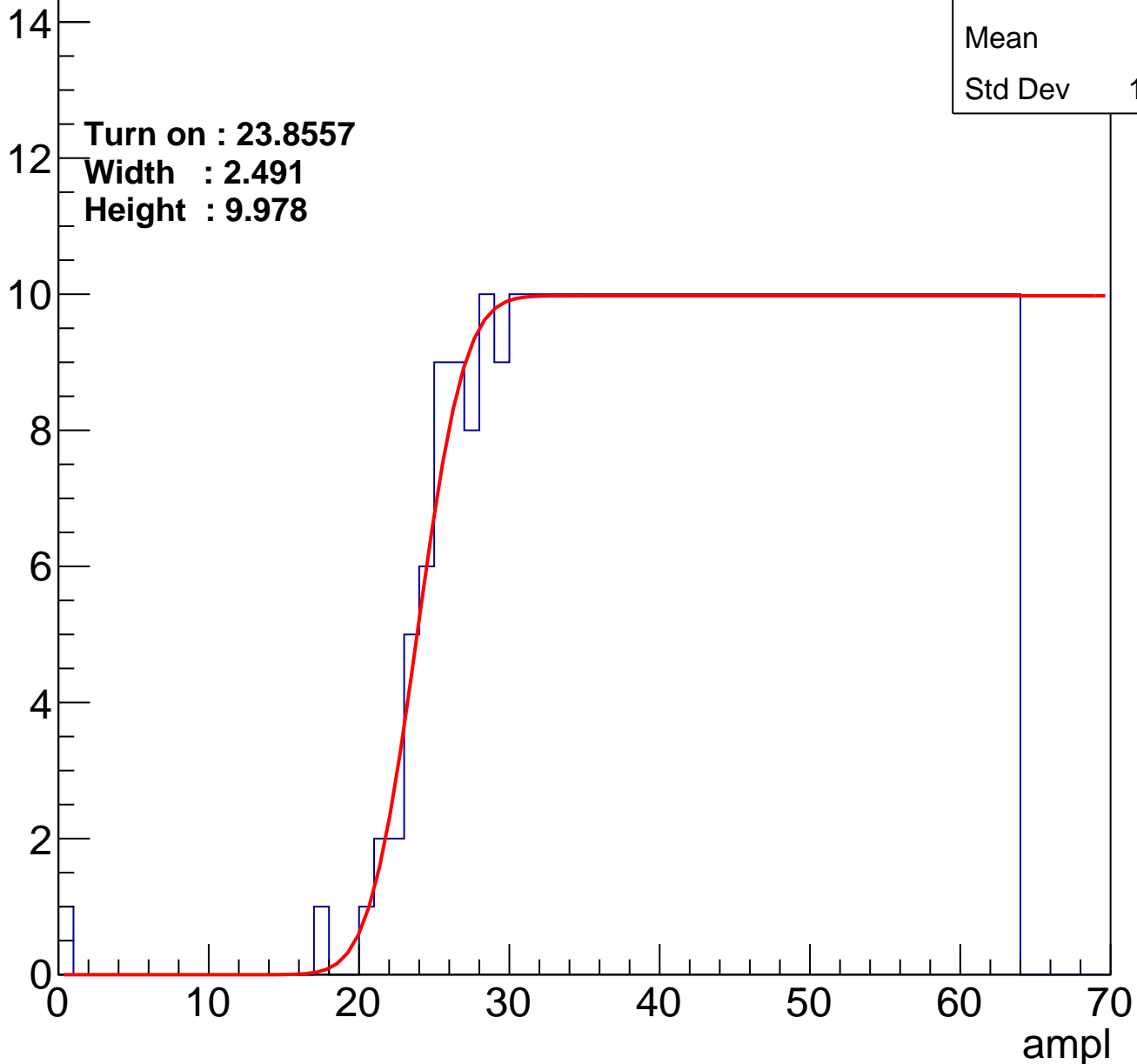
Entries	403
Mean	43.2
Std Dev	11.95

Turn on : 23.8557

Width : 2.491

Height : 9.978

Entry



B1L000S, U16-ch96

calib_packv5_042523_0143.root, FC#3, port B3

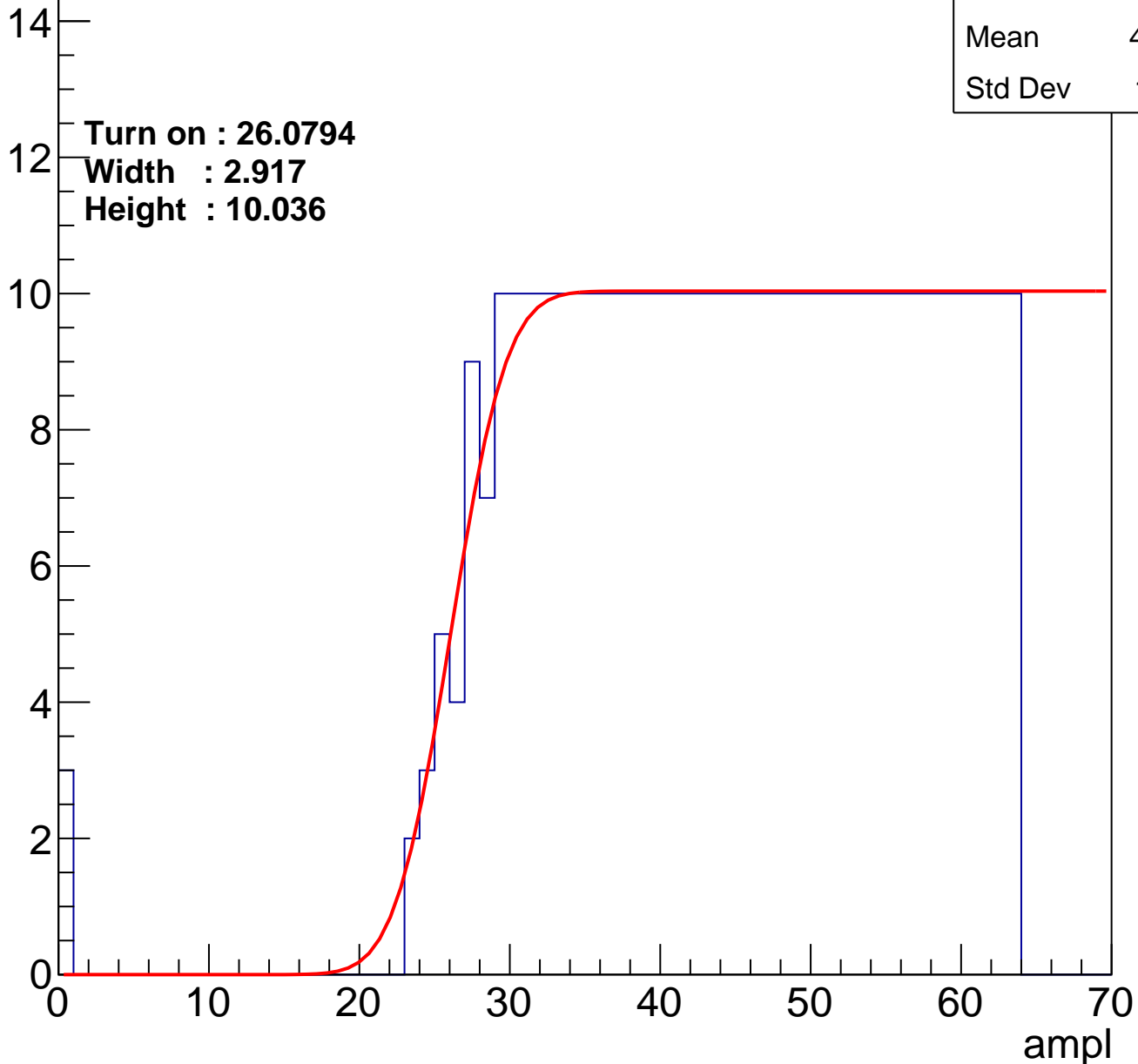
Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 26.0794

Width : 2.917

Height : 10.036

Entry



B1L000S, U16-ch97

calib_packv5_042523_0143.root, FC#3, port B3

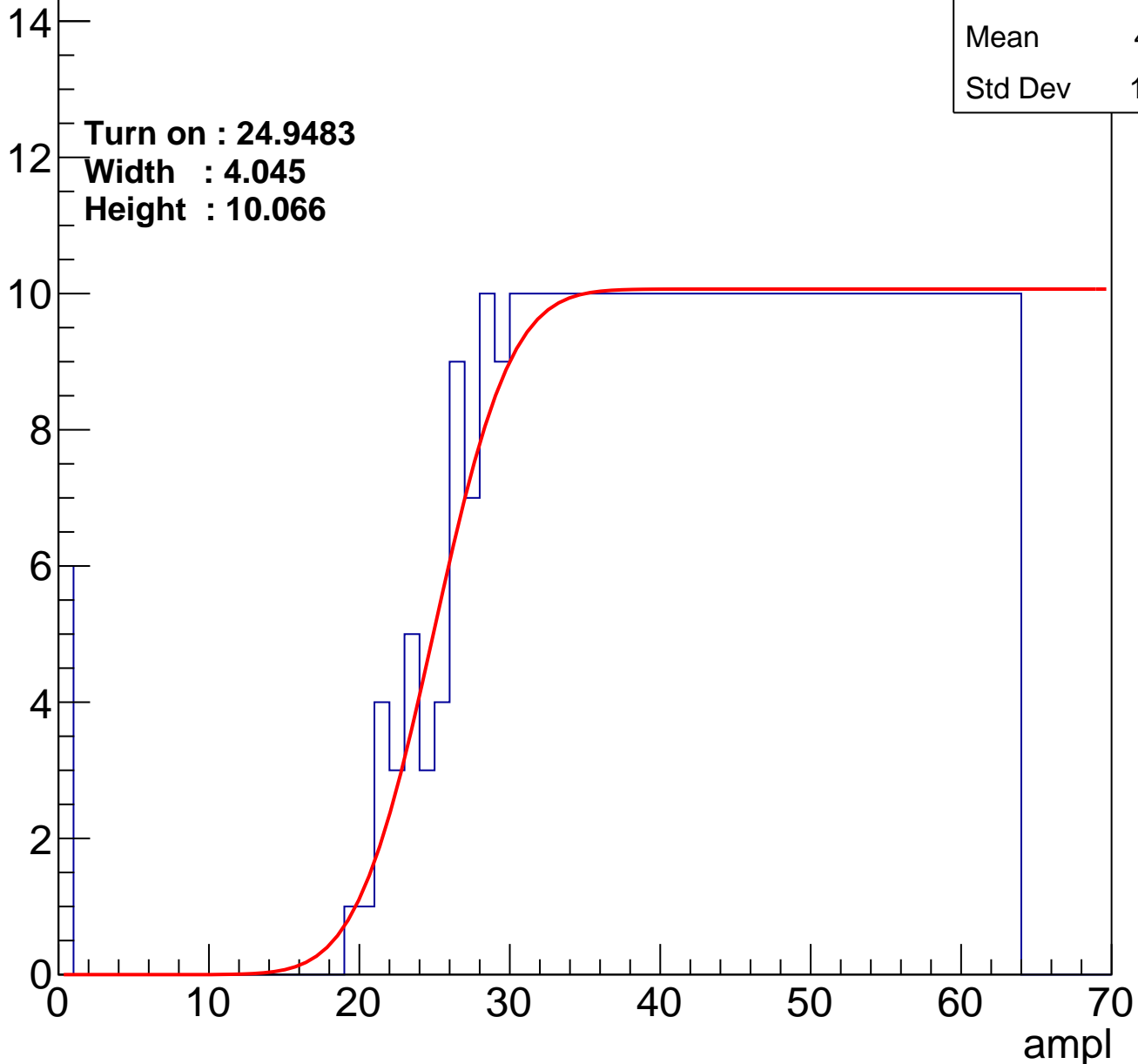
Entries	402
Mean	42.91
Std Dev	12.73

Turn on : 24.9483

Width : 4.045

Height : 10.066

Entry



B1L000S, U16-ch98

calib_packv5_042523_0143.root, FC#3, port B3

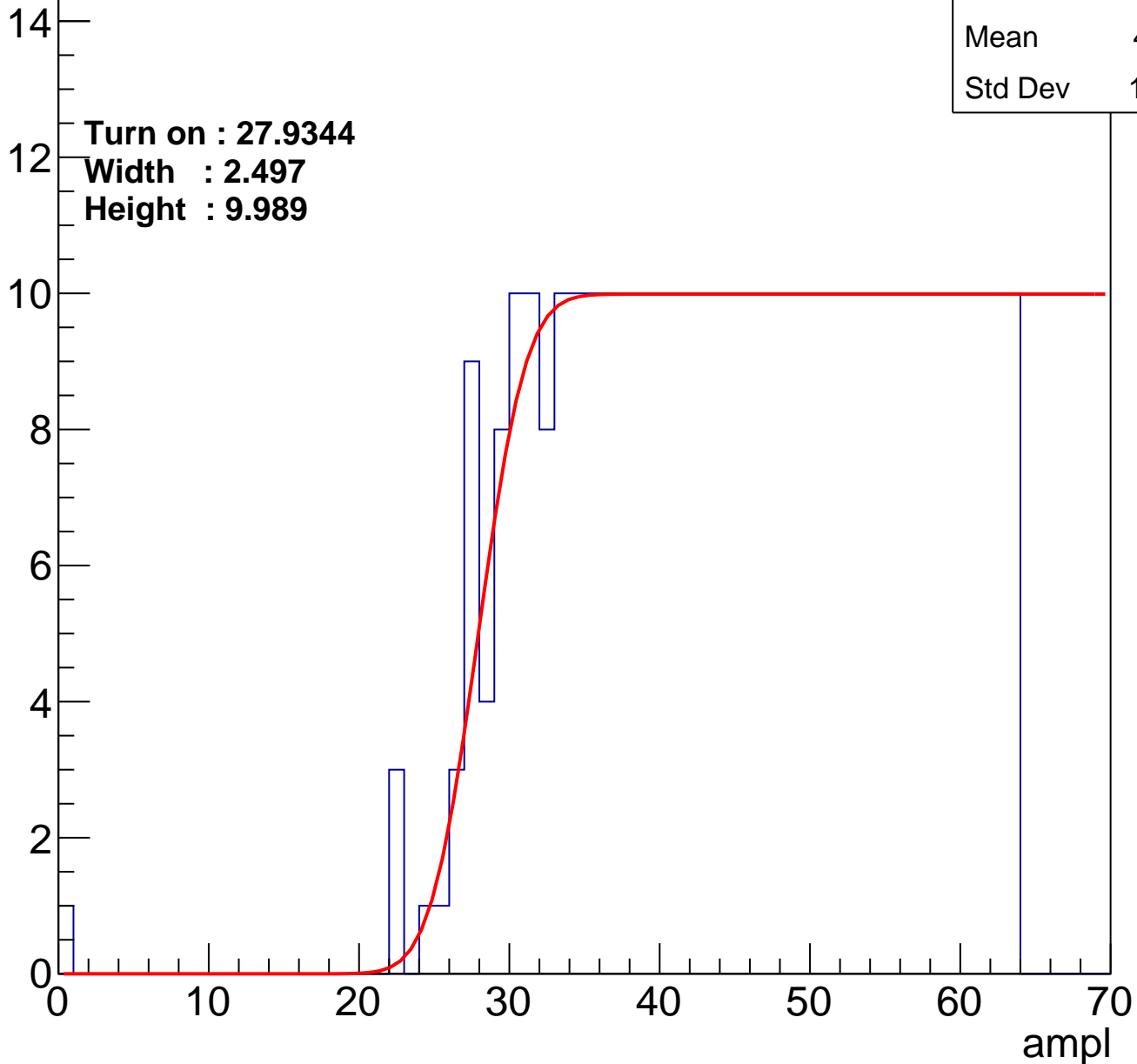
Entries	368
Mean	44.91
Std Dev	11.03

Turn on : 27.9344

Width : 2.497

Height : 9.989

Entry



B1L000S, U16-ch99

calib_packv5_042523_0143.root, FC#3, port B3

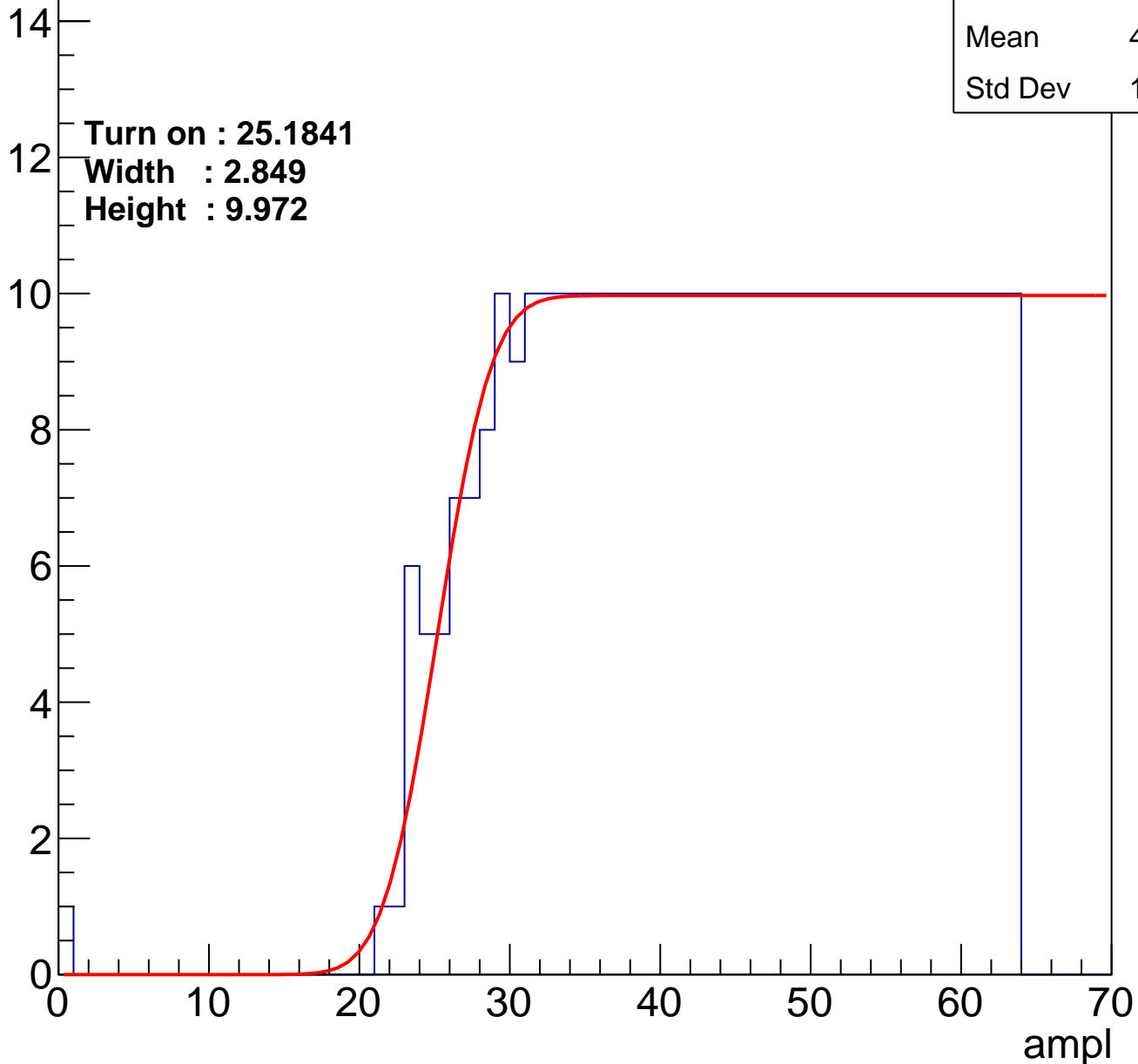
Entries	390
Mean	43.82
Std Dev	11.62

Turn on : 25.1841

Width : 2.849

Height : 9.972

Entry



B1L000S, U16-ch100

calib_packv5_042523_0143.root, FC#3, port B3

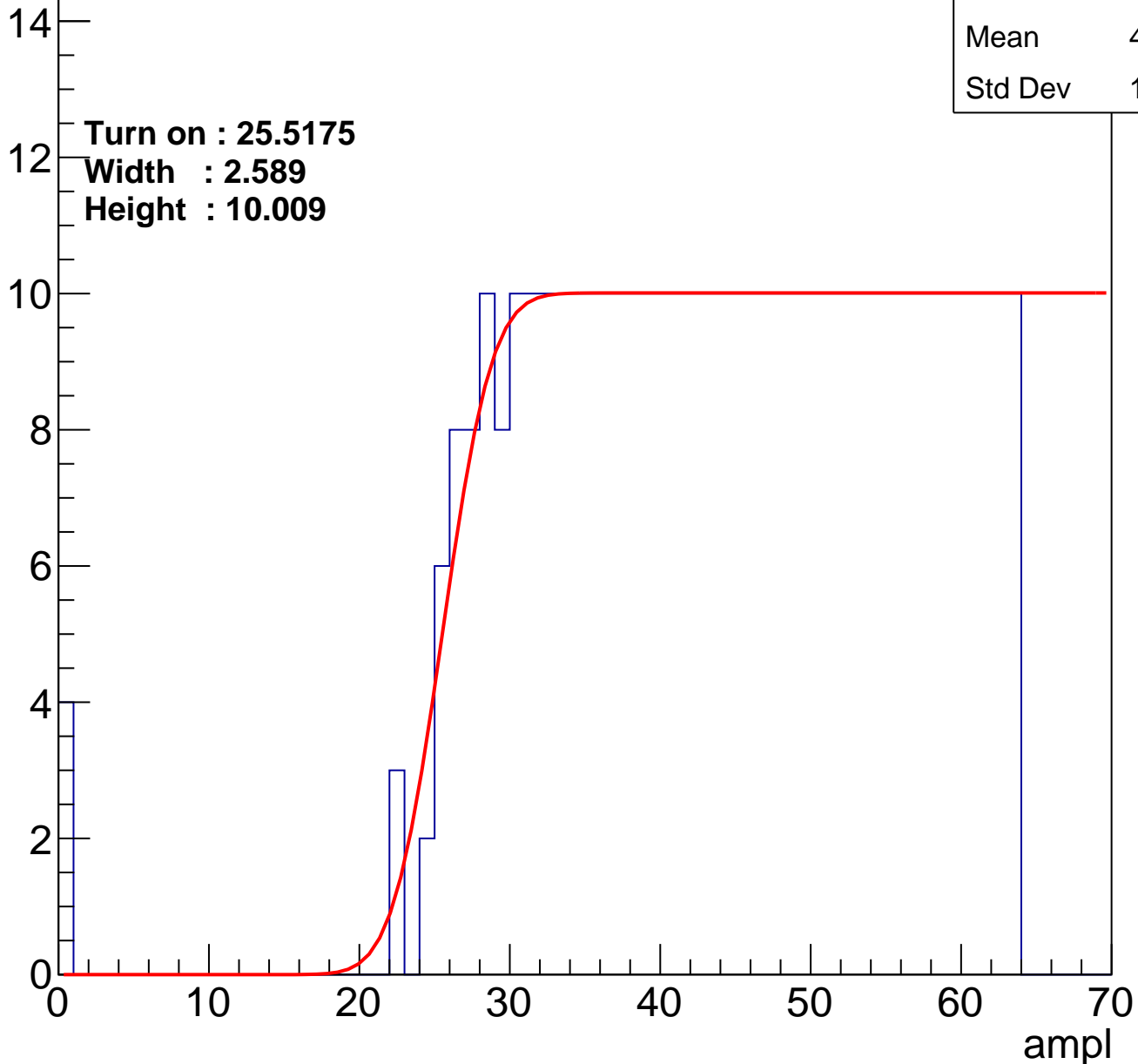
Entries	389
Mean	43.73
Std Dev	12.02

Turn on : 25.5175

Width : 2.589

Height : 10.009

Entry



B1L000S, U16-ch101

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.17
Std Dev	11.73

Turn on : 26.7168

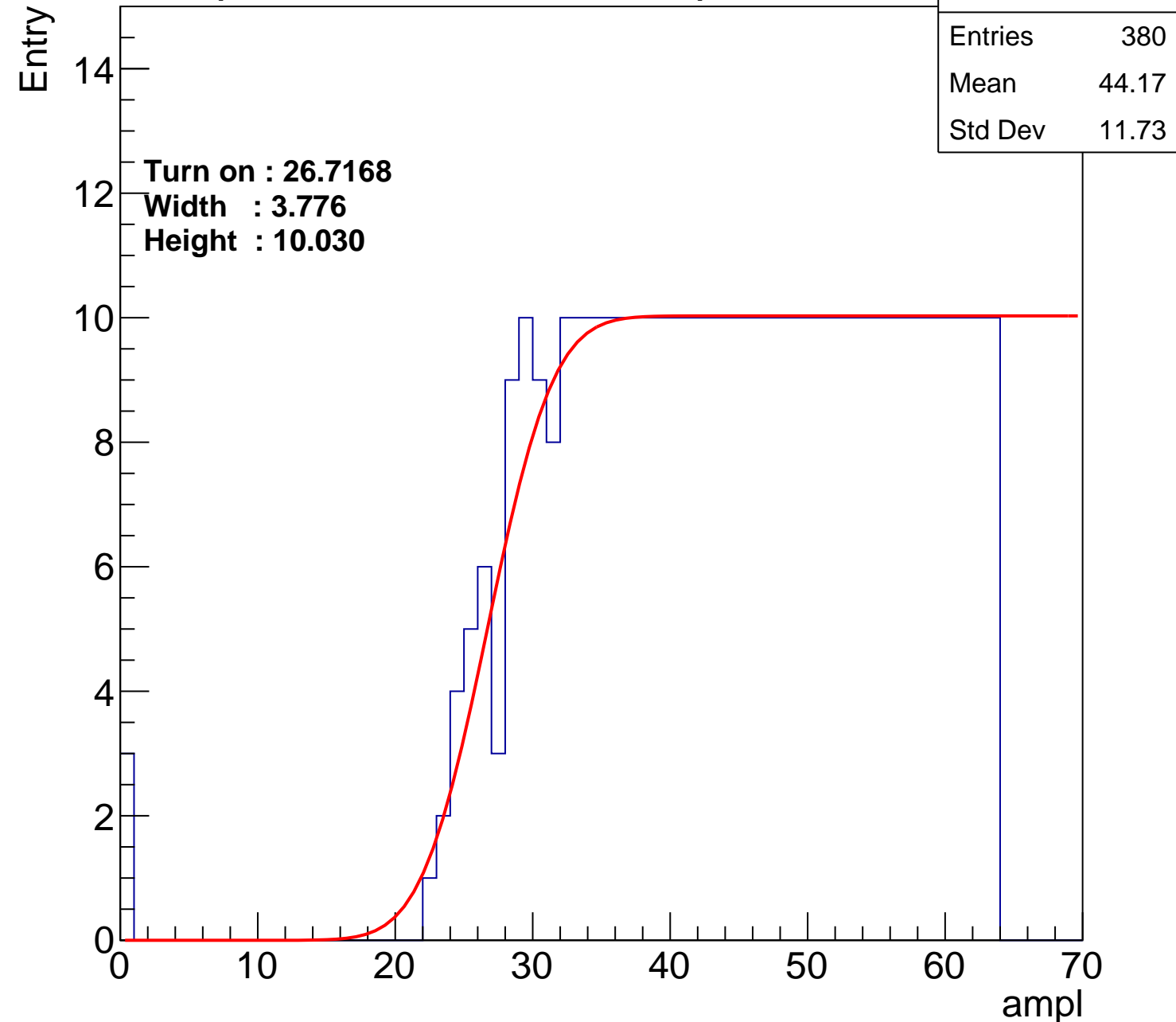
Width : 3.776

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch102

calib_packv5_042523_0143.root, FC#3, port B3

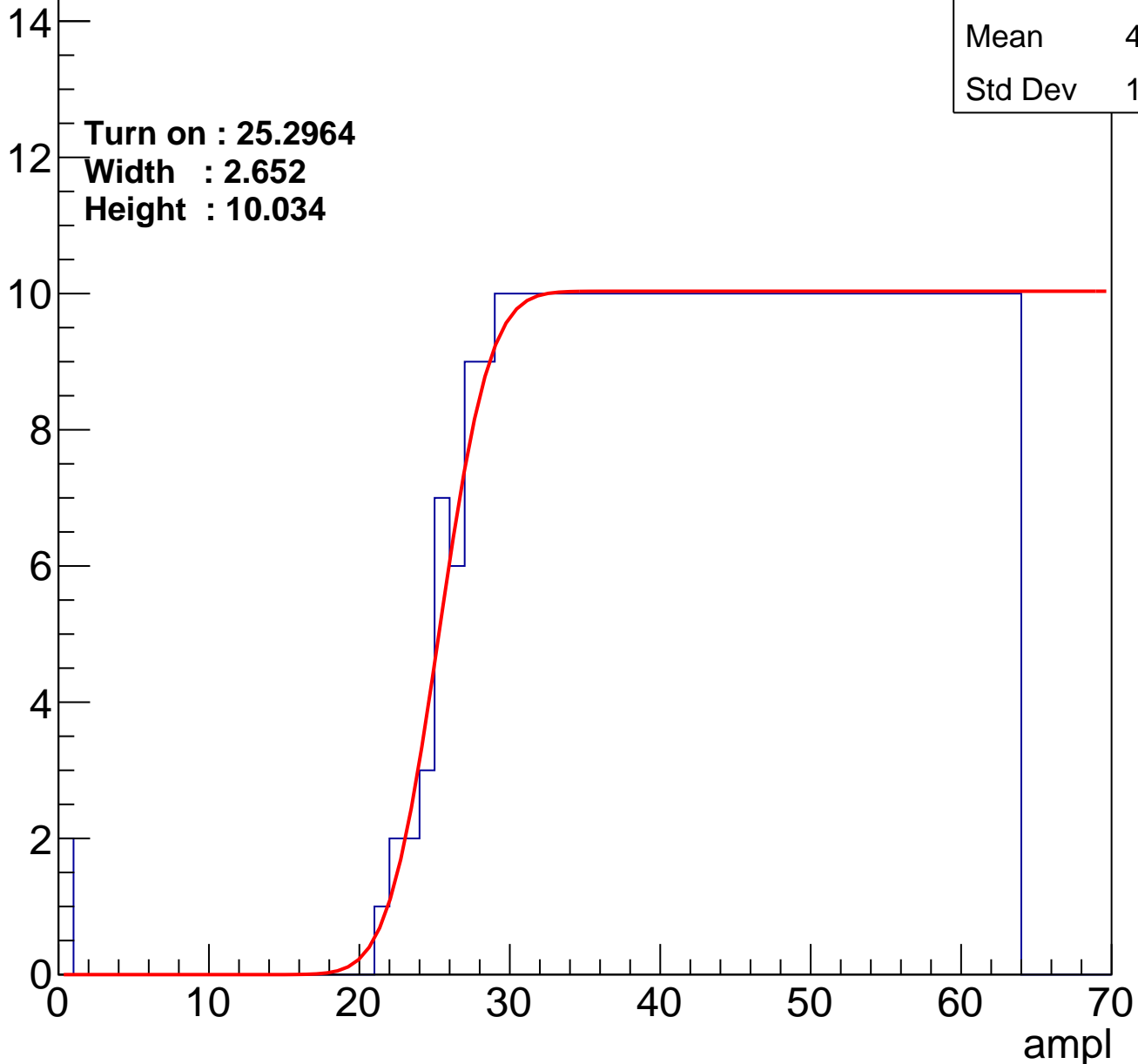
Entries	391
Mean	43.76
Std Dev	11.74

Turn on : 25.2964

Width : 2.652

Height : 10.034

Entry



B1L000S, U16-ch103

calib_packv5_042523_0143.root, FC#3, port B3

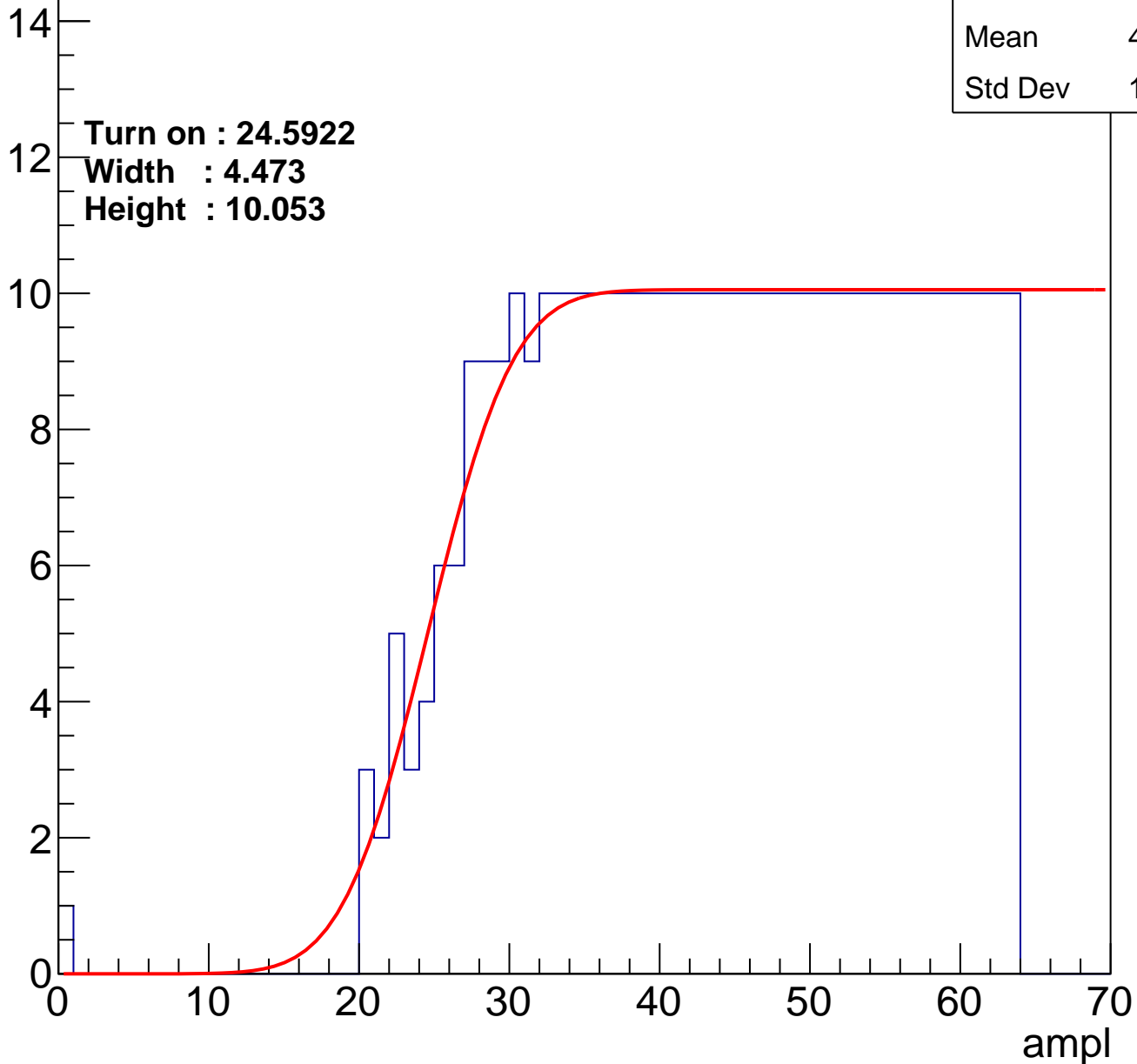
Entries	396
Mean	43.48
Std Dev	11.86

Turn on : 24.5922

Width : 4.473

Height : 10.053

Entry



B1L000S, U16-ch104

calib_packv5_042523_0143.root, FC#3, port B3

Entries	392
Mean	43.67
Std Dev	11.84

Turn on : 24.9687

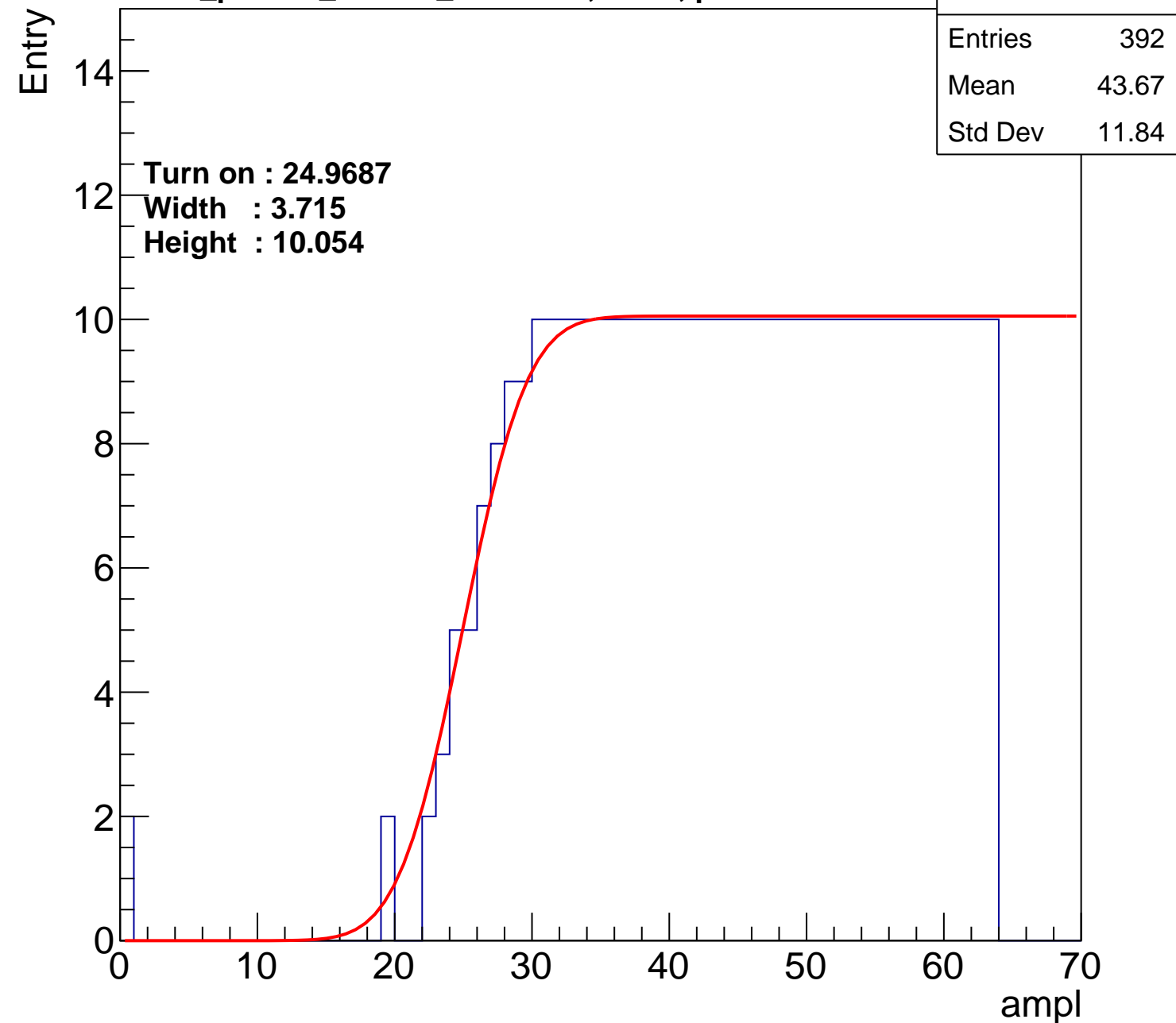
Width : 3.715

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch105

calib_packv5_042523_0143.root, FC#3, port B3

Entries	375
Mean	44.61
Std Dev	11.15

Turn on : 27.0127

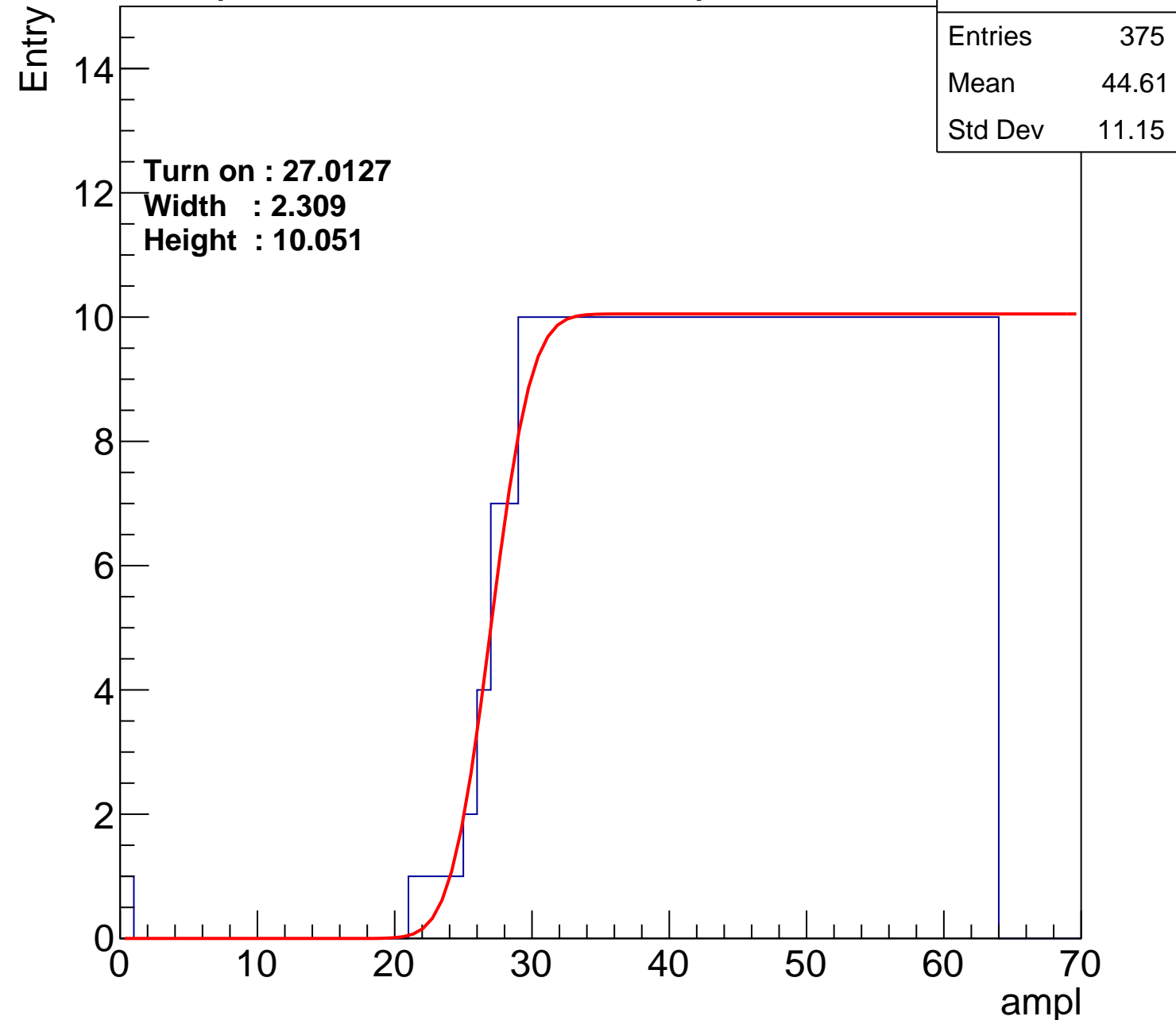
Width : 2.309

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch106

calib_packv5_042523_0143.root, FC#3, port B3

Entries	362
Mean	45.24
Std Dev	10.81

Turn on : 28.1094

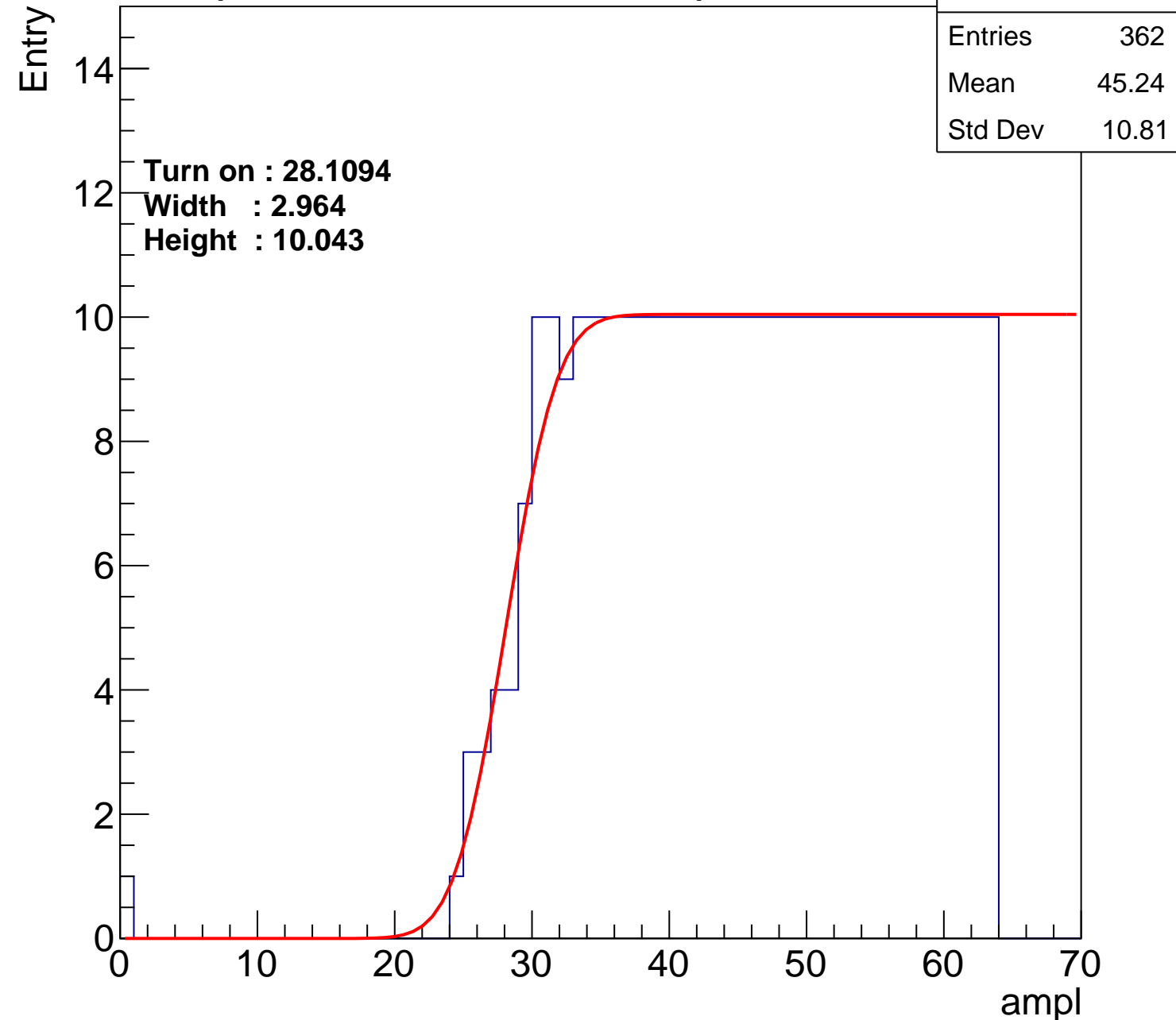
Width : 2.964

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch107

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.89
Std Dev	11.81

Turn on : 25.4971

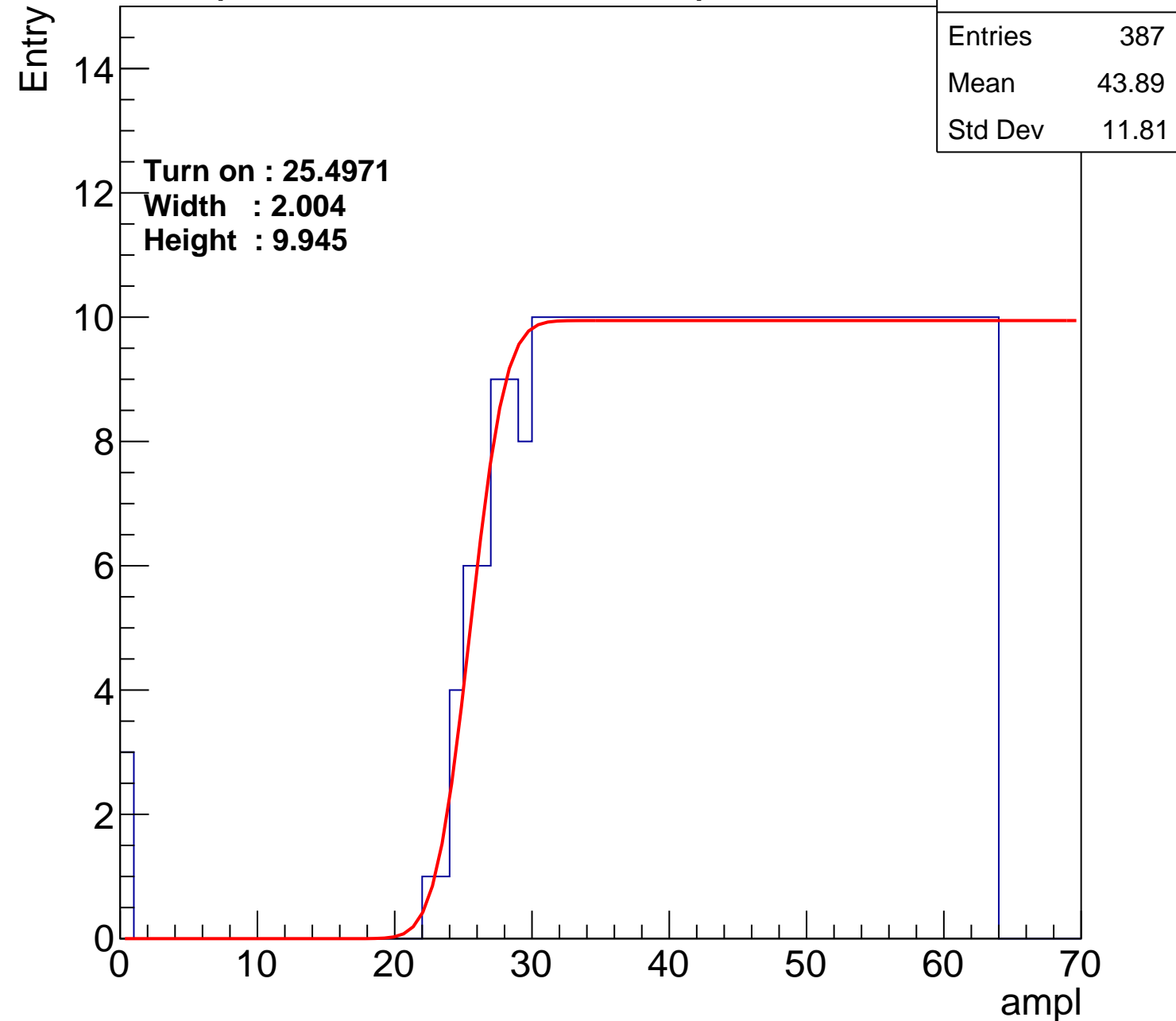
Width : 2.004

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch108

calib_packv5_042523_0143.root, FC#3, port B3

Entries	382
Mean	44.11
Std Dev	11.73

Turn on : 26.0084

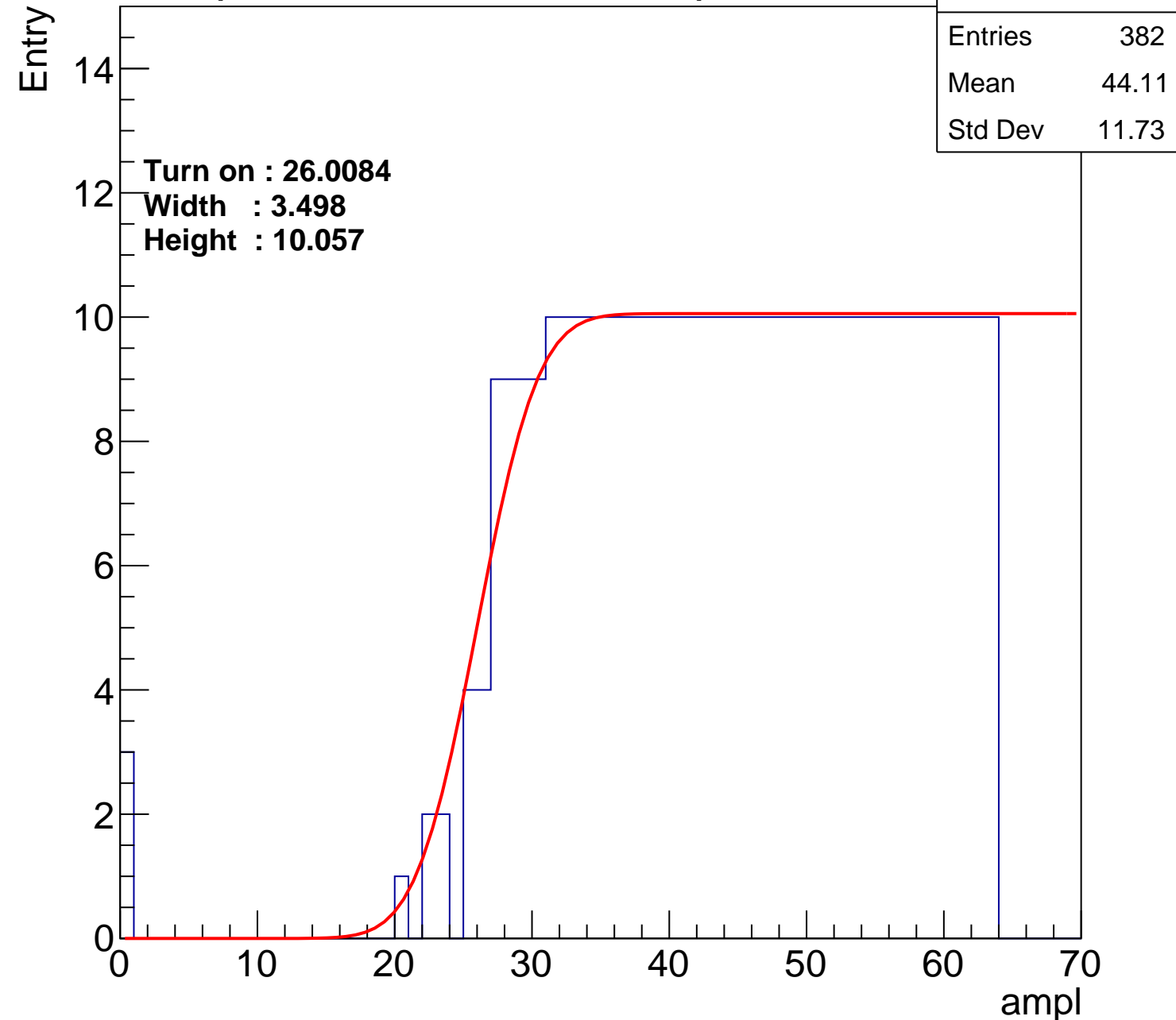
Width : 3.498

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch109

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.33
Std Dev	11.45

Turn on : 26.2080

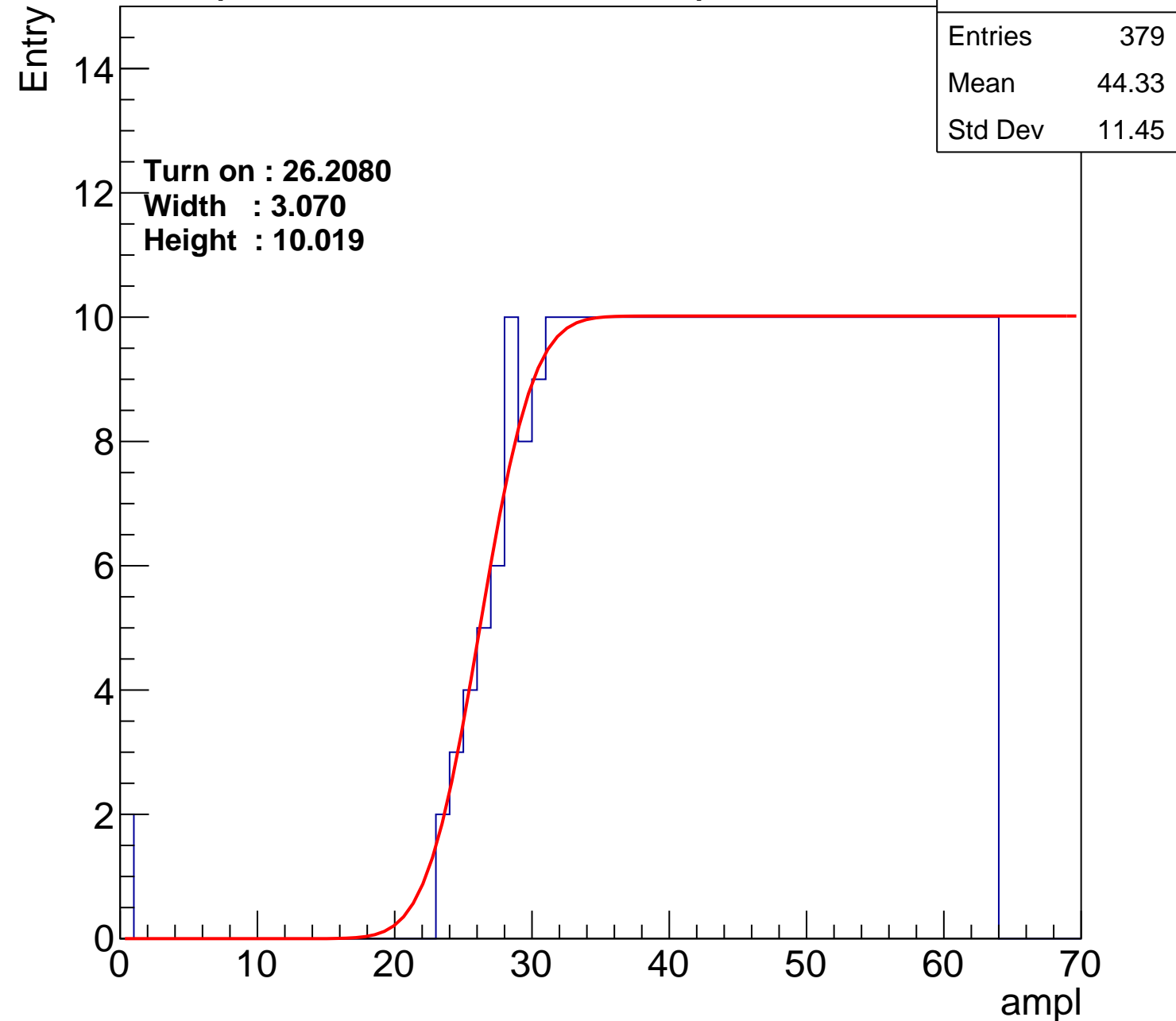
Width : 3.070

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch110

calib_packv5_042523_0143.root, FC#3, port B3

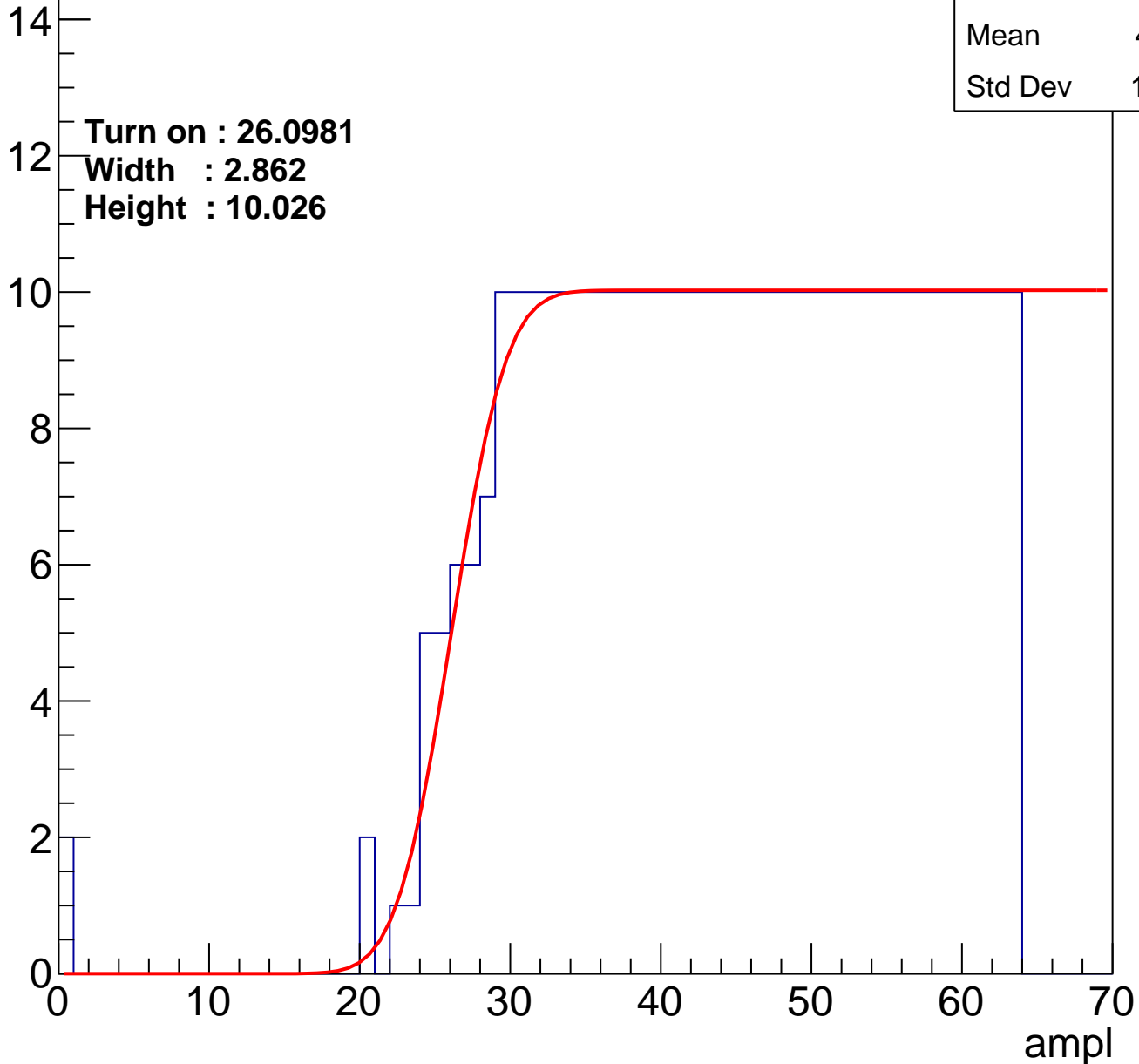
Entries	385
Mean	44.01
Std Dev	11.66

Turn on : 26.0981

Width : 2.862

Height : 10.026

Entry



B1L000S, U16-ch111

calib_packv5_042523_0143.root, FC#3, port B3

Entries	391
Mean	43.7
Std Dev	11.9

Turn on : 25.1629

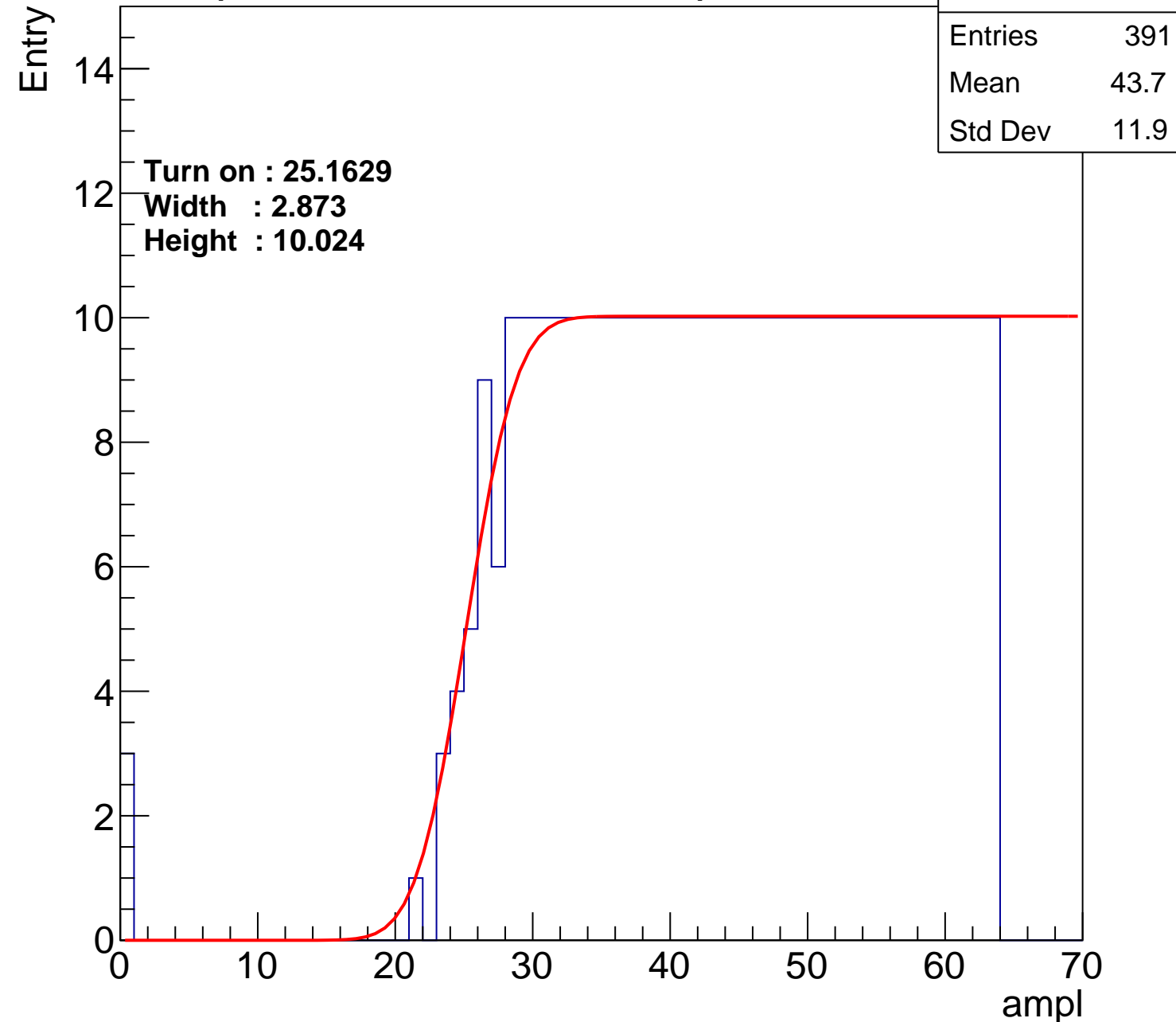
Width : 2.873

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch112

calib_packv5_042523_0143.root, FC#3, port B3

Entries	373
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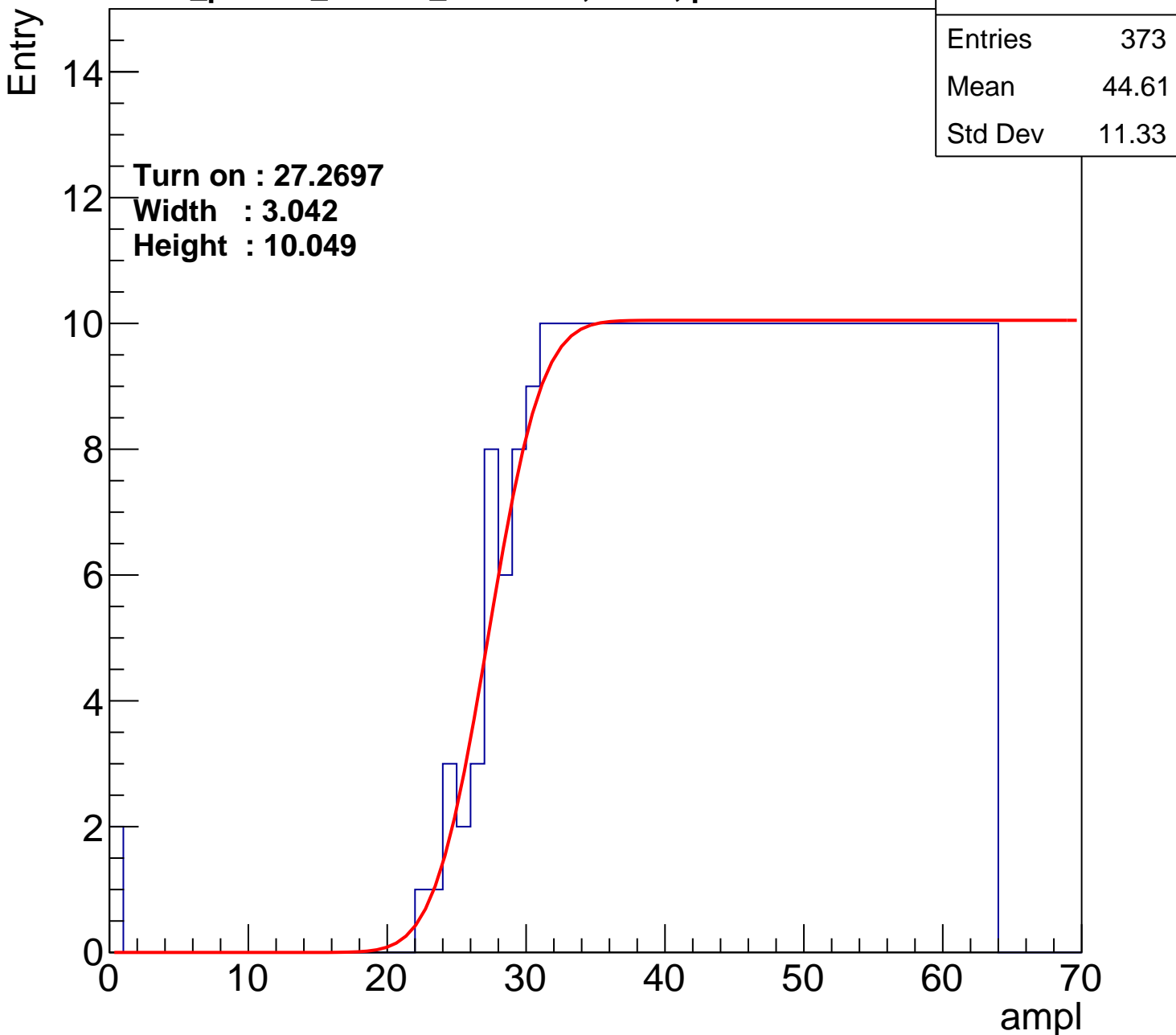
Mean	44.61
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Std Dev	11.33
---------	-------

Turn on : 27.2697

Width : 3.042

Height : 10.049



B1L000S, U16-ch113

calib_packv5_042523_0143.root, FC#3, port B3

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.8285

Width : 2.745

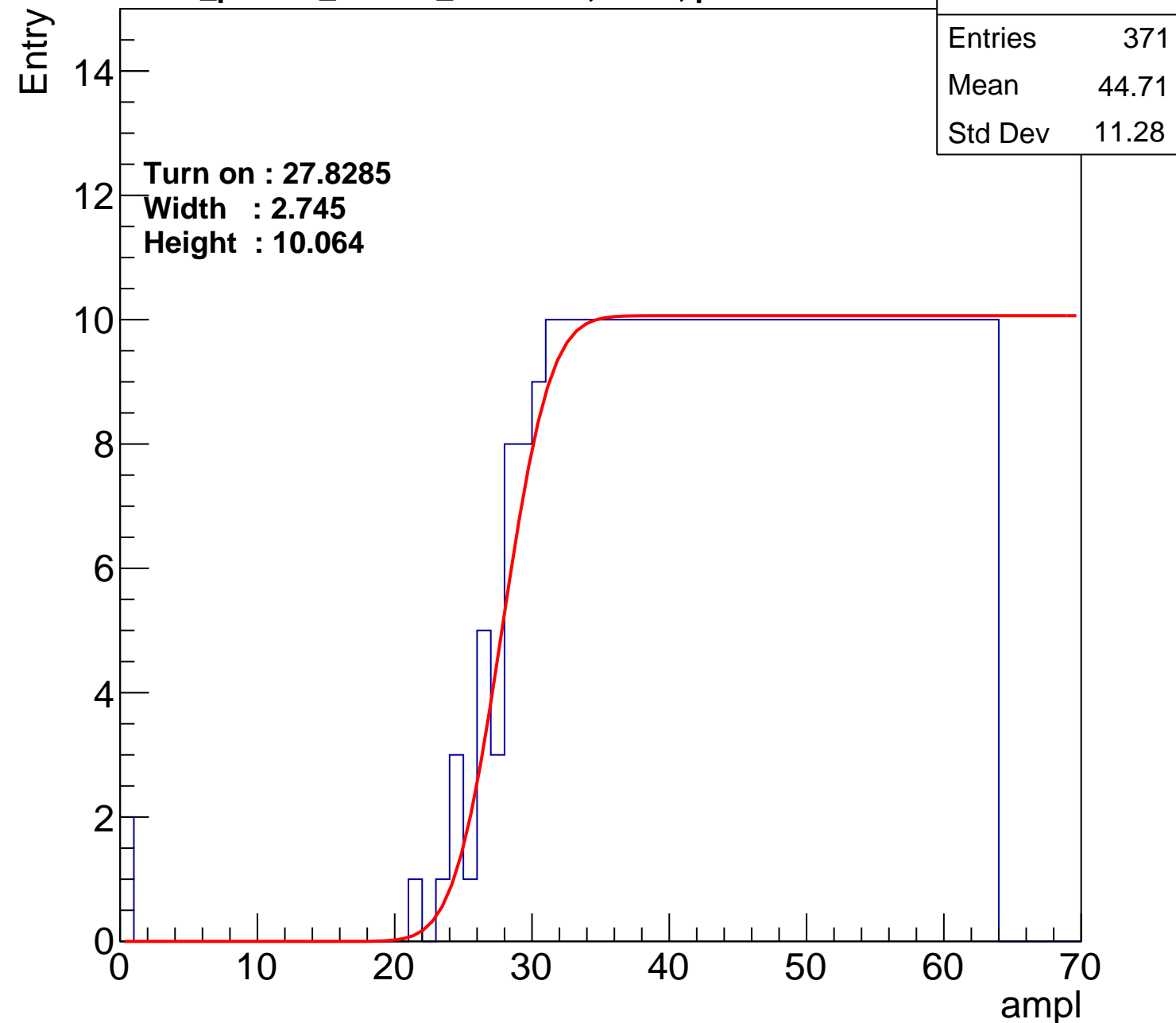
Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L000S, U16-ch114

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.09
Std Dev	11.71

Turn on : 25.4074

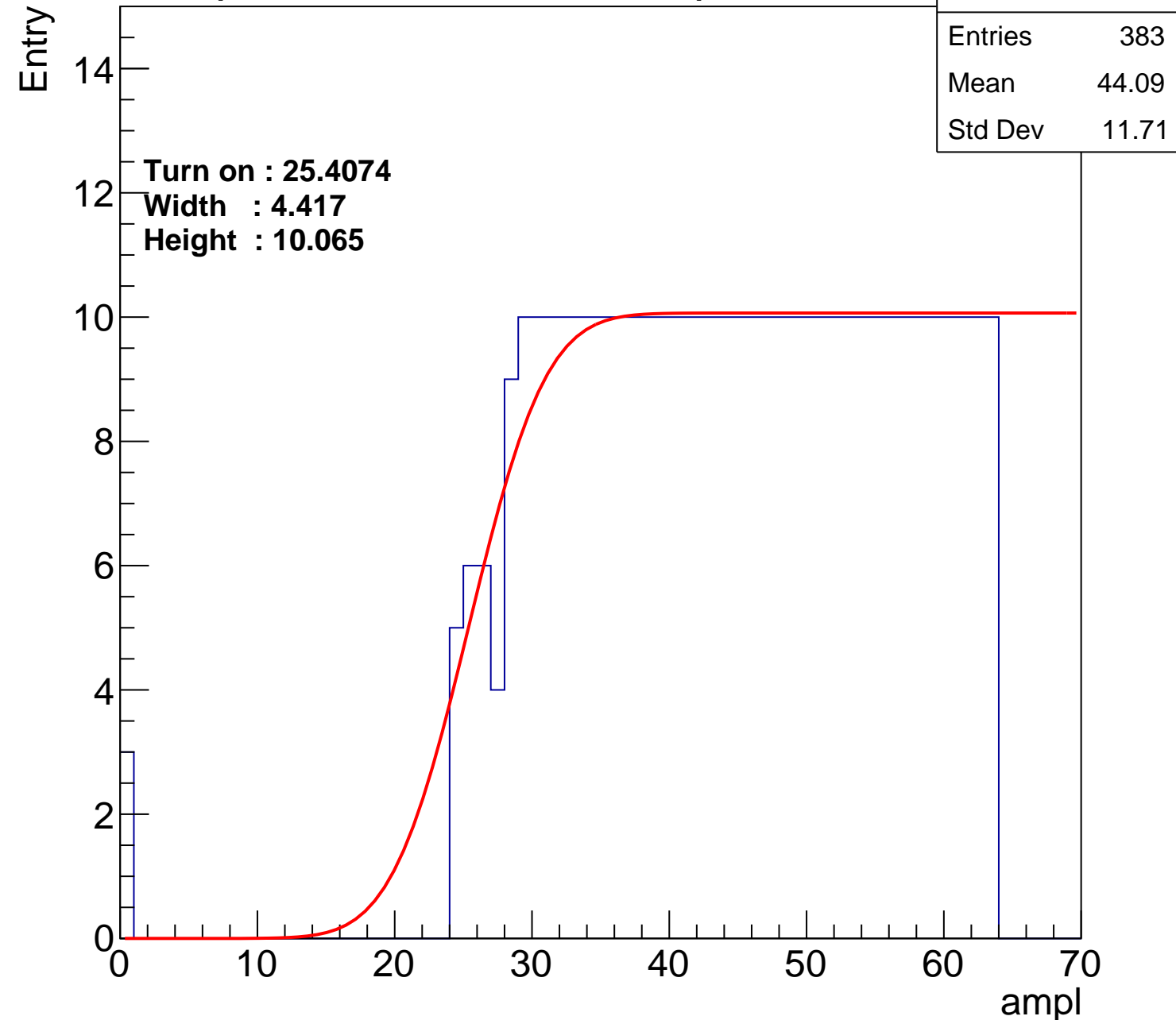
Width : 4.417

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch115

calib_packv5_042523_0143.root, FC#3, port B3

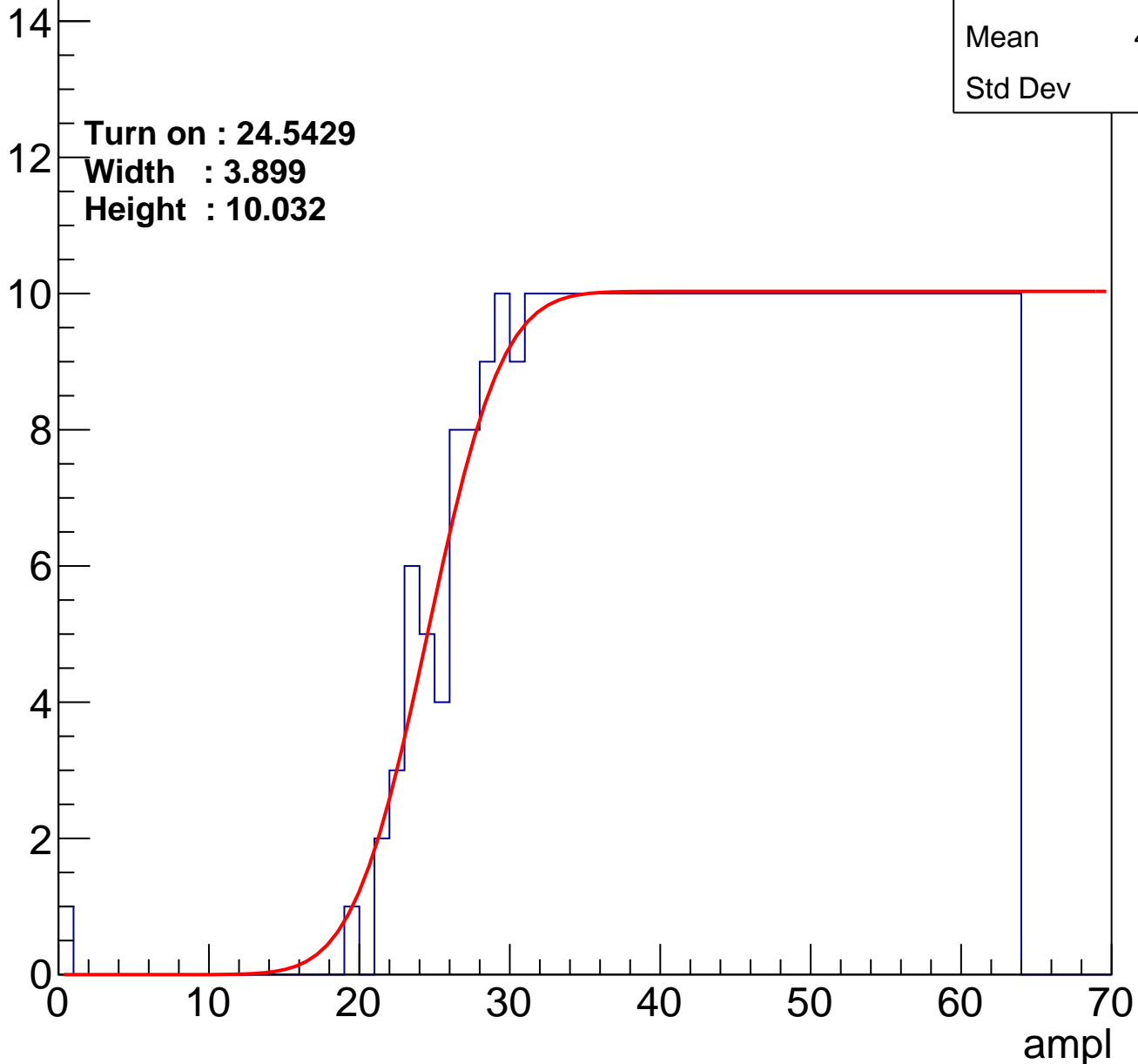
Entries	396
Mean	43.51
Std Dev	11.8

Turn on : 24.5429

Width : 3.899

Height : 10.032

Entry



B1L000S, U16-ch116

calib_packv5_042523_0143.root, FC#3, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 25.4620

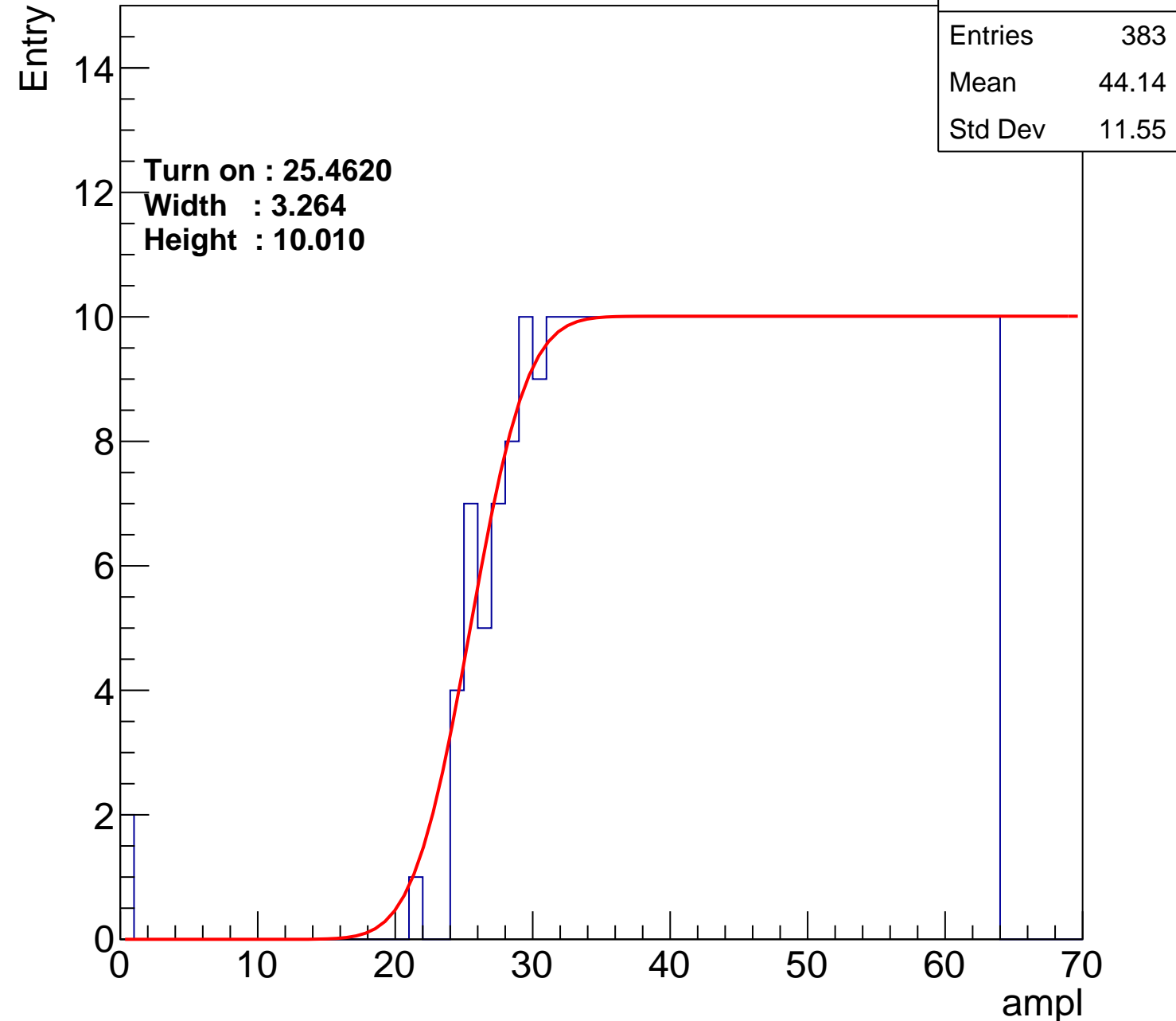
Width : 3.264

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch117

calib_packv5_042523_0143.root, FC#3, port B3

Entries	396
Mean	43.43
Std Dev	12.06

Turn on : 23.9368

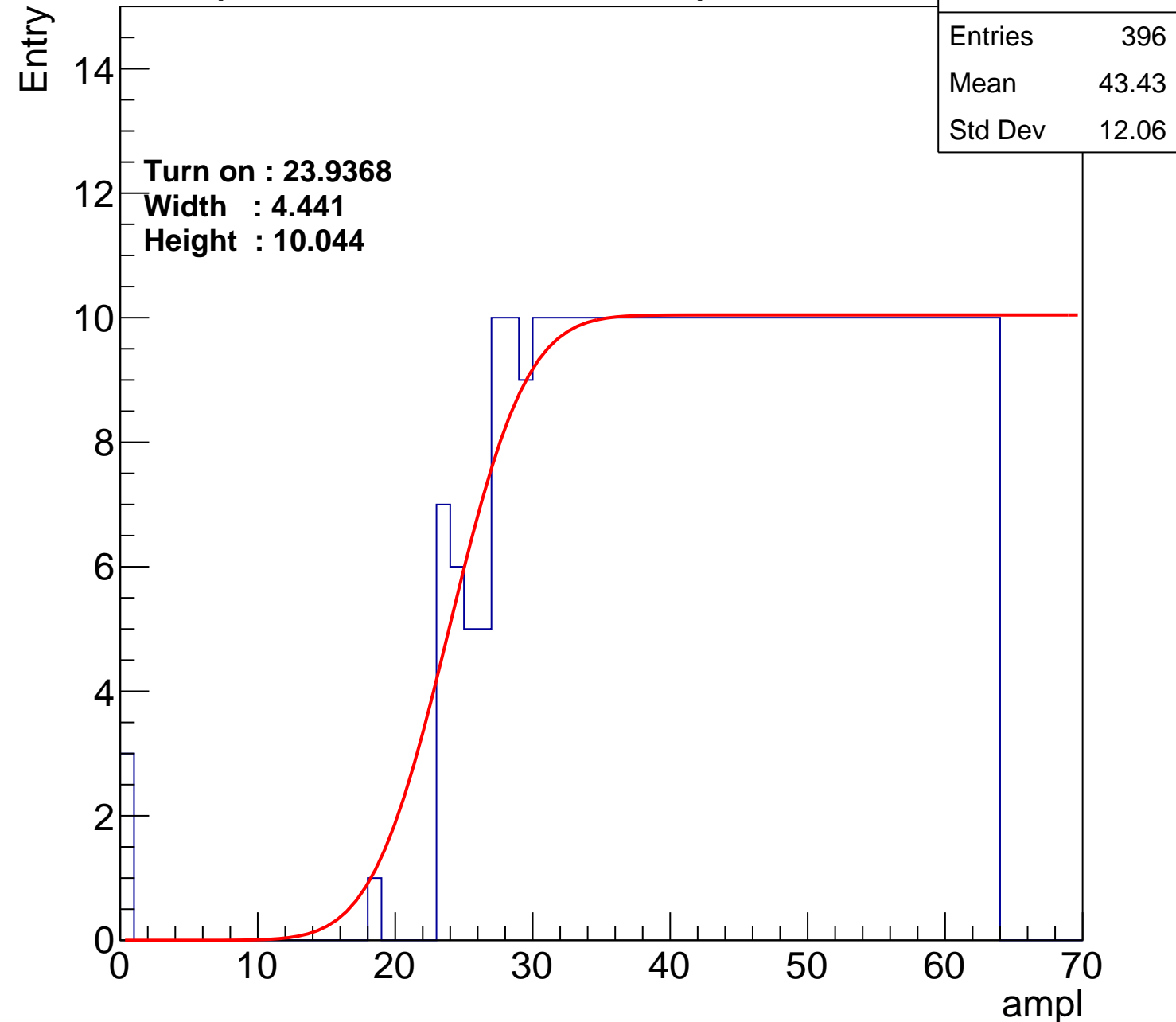
Width : 4.441

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch118

calib_packv5_042523_0143.root, FC#3, port B3

Entries	402
Mean	43.24
Std Dev	11.92

Turn on : 24.2851

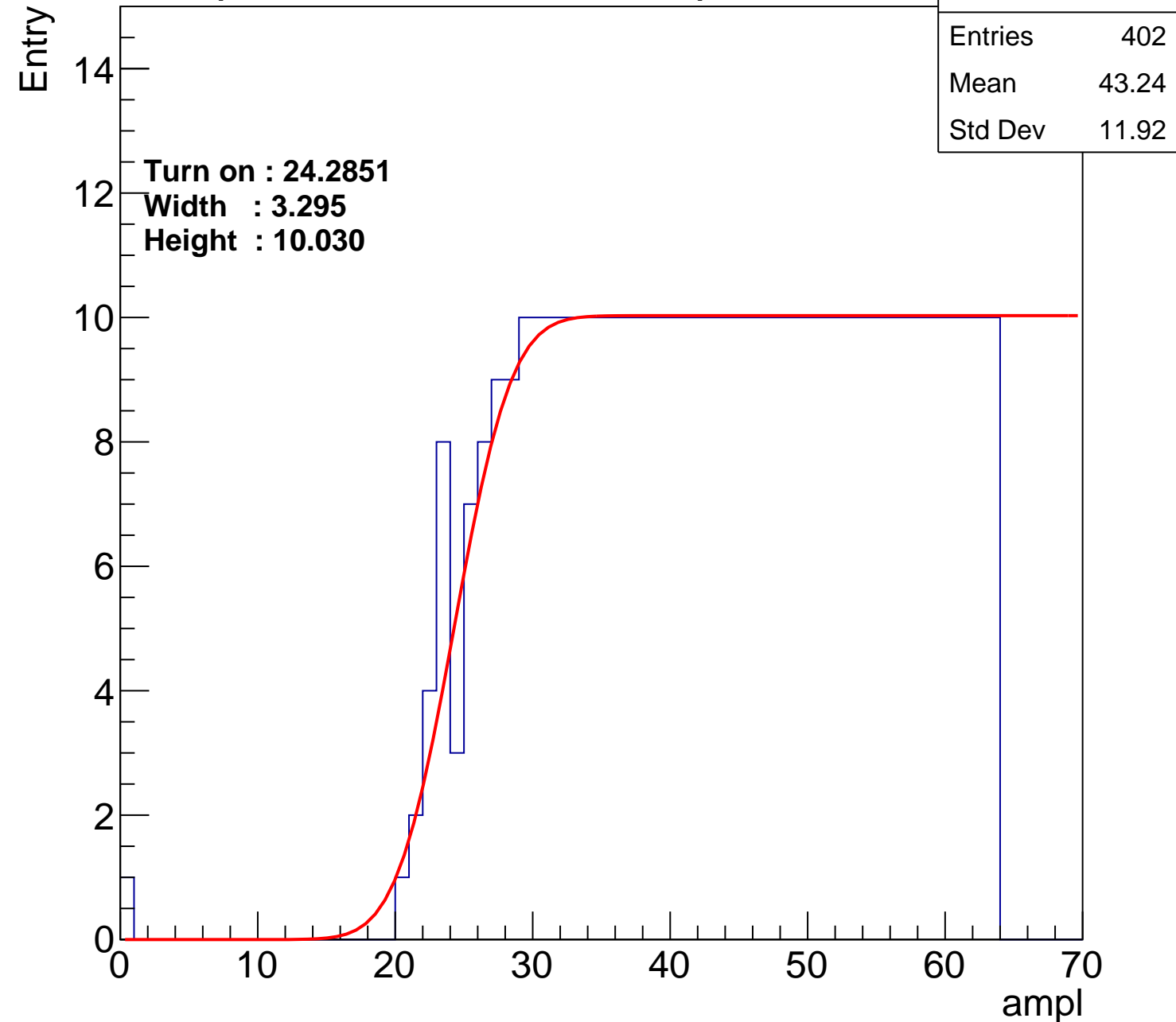
Width : 3.295

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch119

calib_packv5_042523_0143.root, FC#3, port B3

Entries	380
Mean	44.12
Std Dev	11.88

Turn on : 26.6091

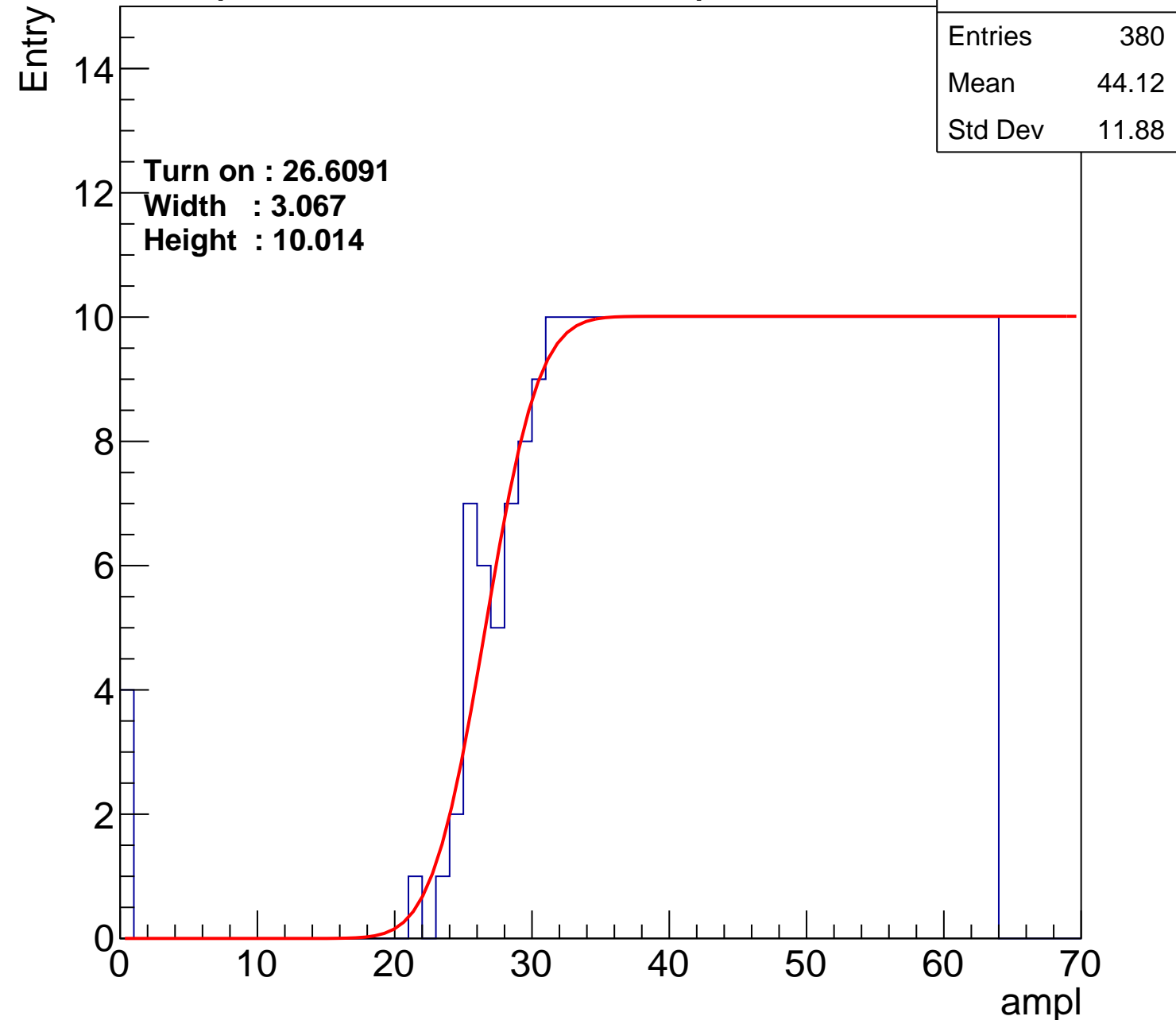
Width : 3.067

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch120

calib_packv5_042523_0143.root, FC#3, port B3

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.8868

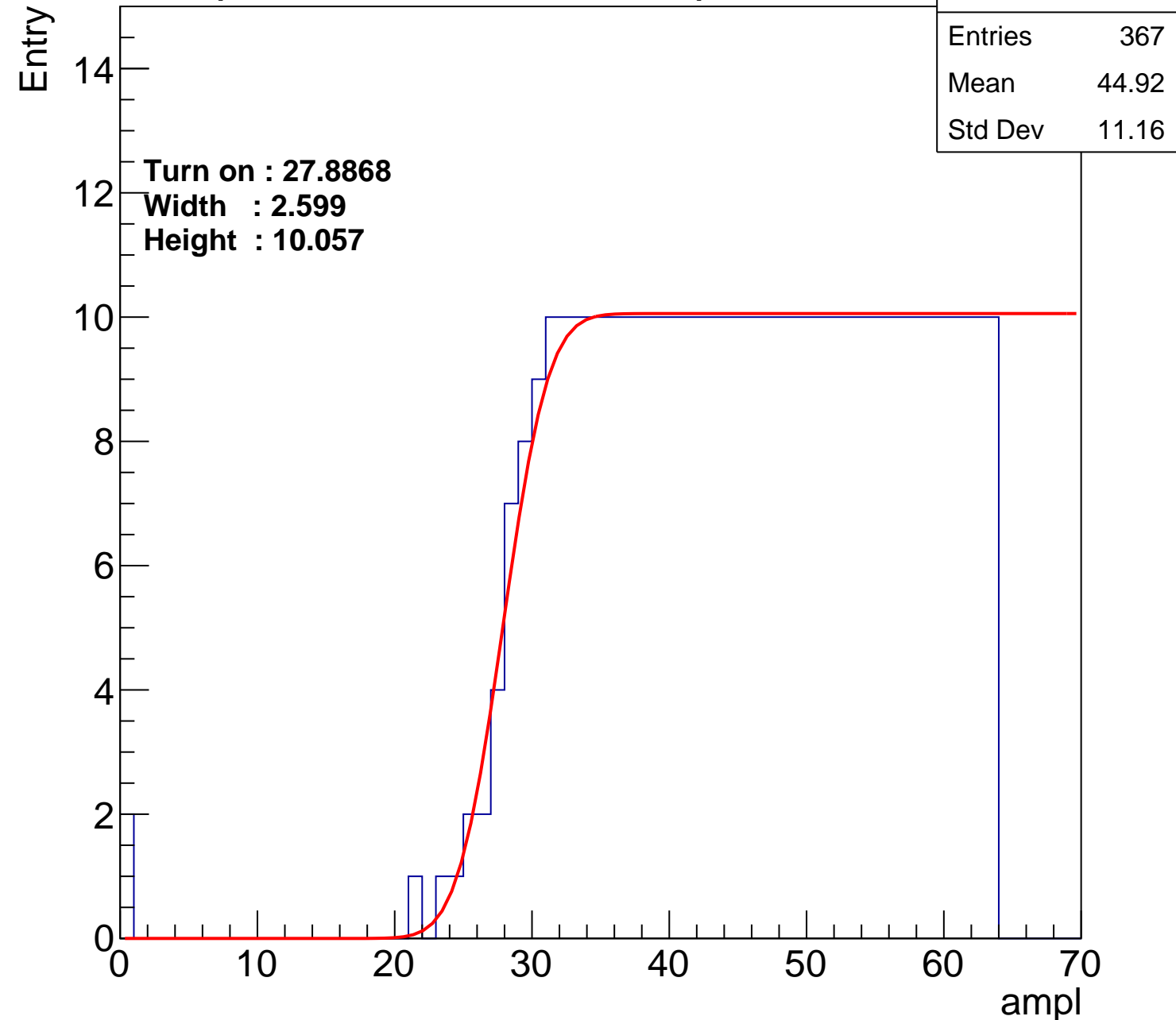
Width : 2.599

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch121

calib_packv5_042523_0143.root, FC#3, port B3

Entries	386
Mean	43.97
Std Dev	11.67

Turn on : 25.6344

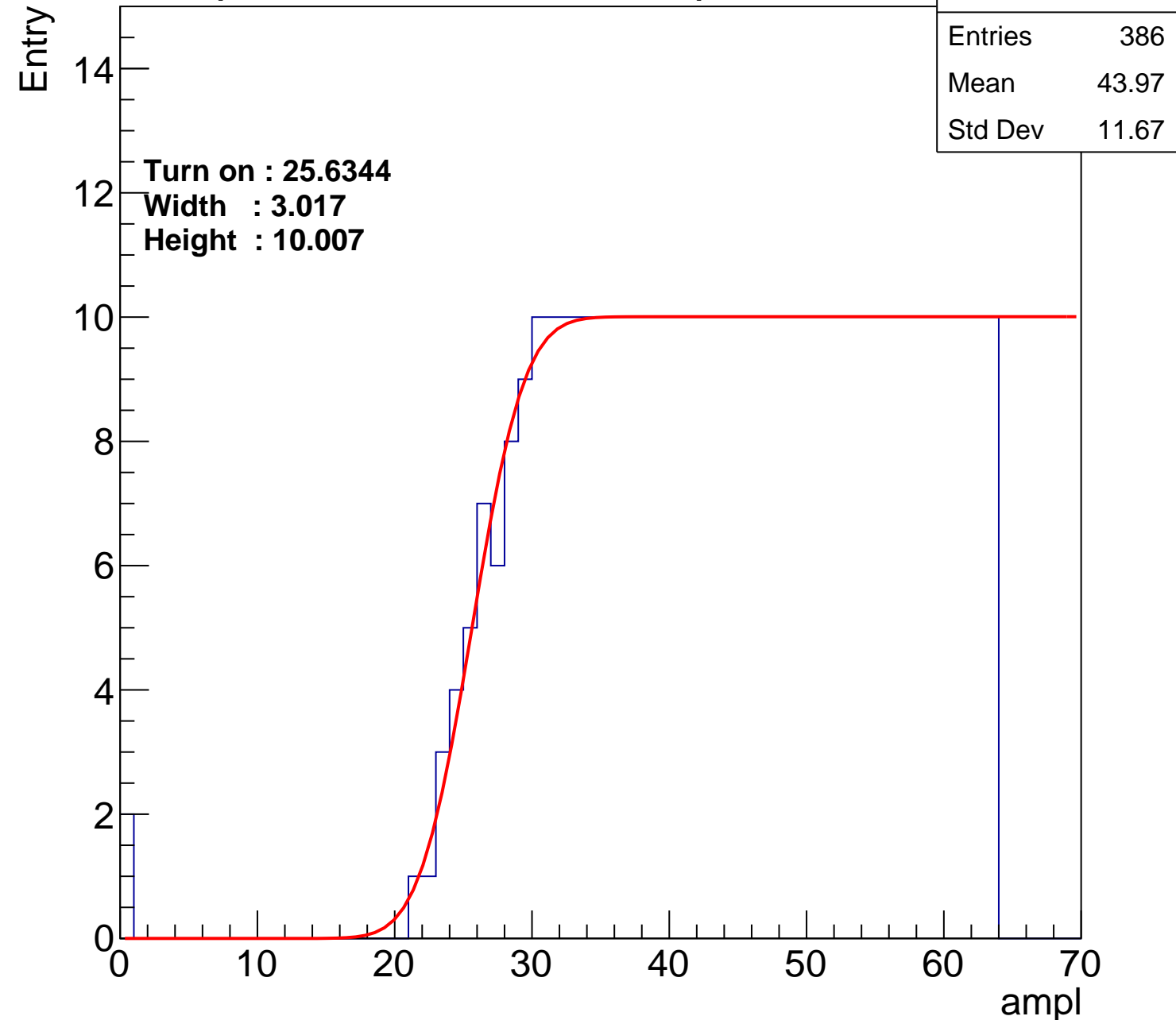
Width : 3.017

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch122

calib_packv5_042523_0143.root, FC#3, port B3

Entries	379
Mean	44.25
Std Dev	11.66

Turn on : 26.5726

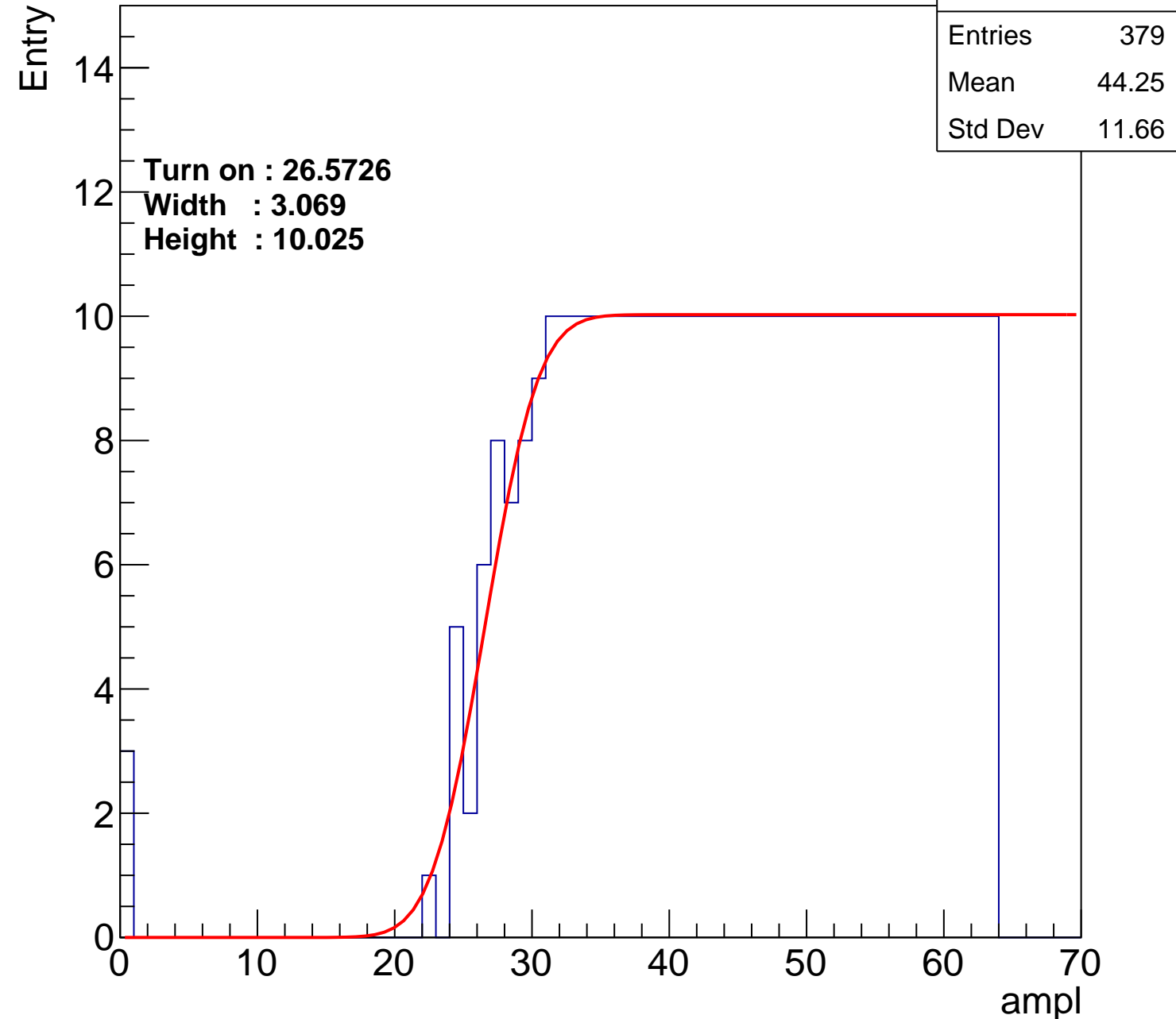
Width : 3.069

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch123

calib_packv5_042523_0143.root, FC#3, port B3

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 25.7648

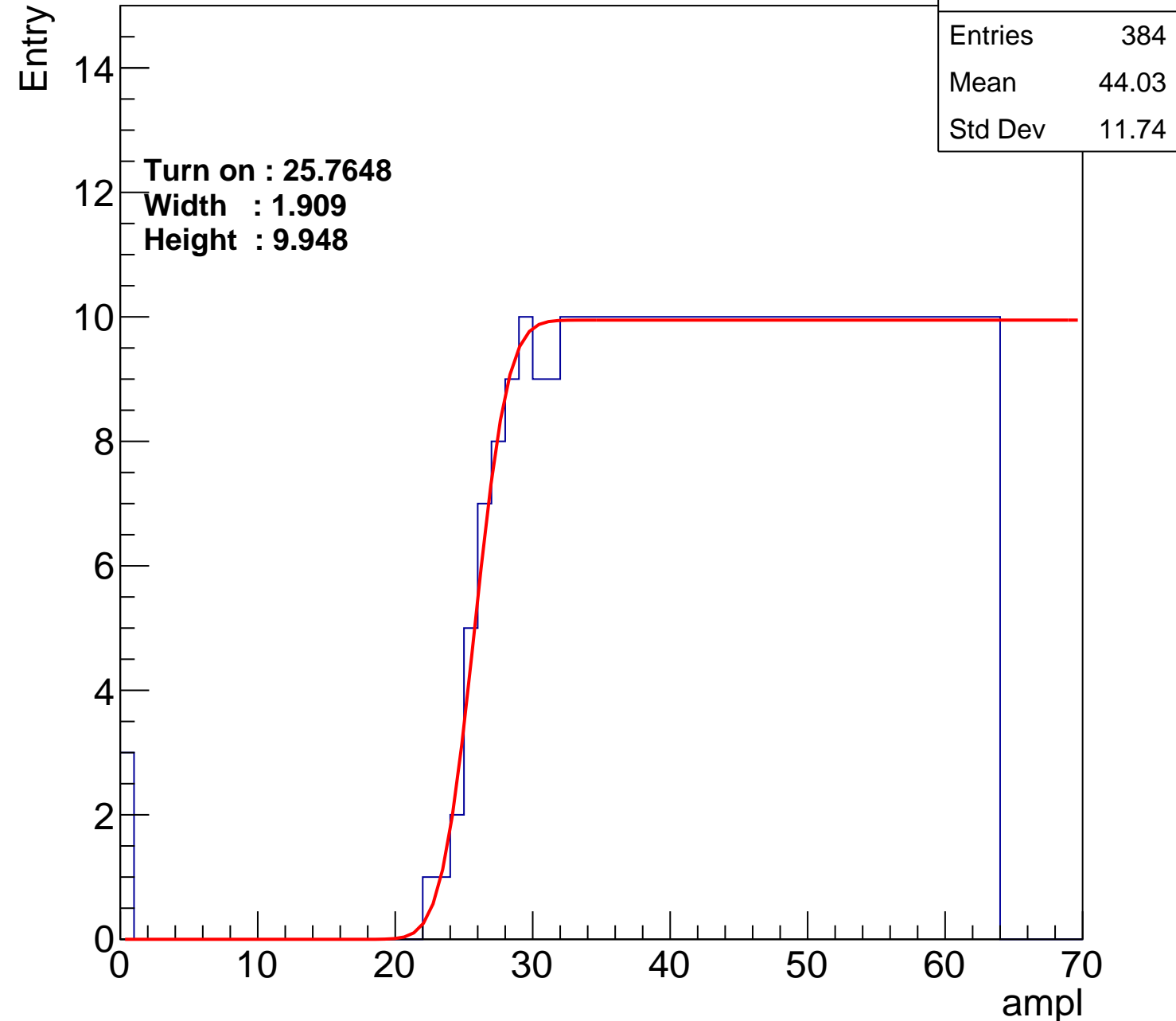
Width : 1.909

Height : 9.948

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch124

calib_packv5_042523_0143.root, FC#3, port B3

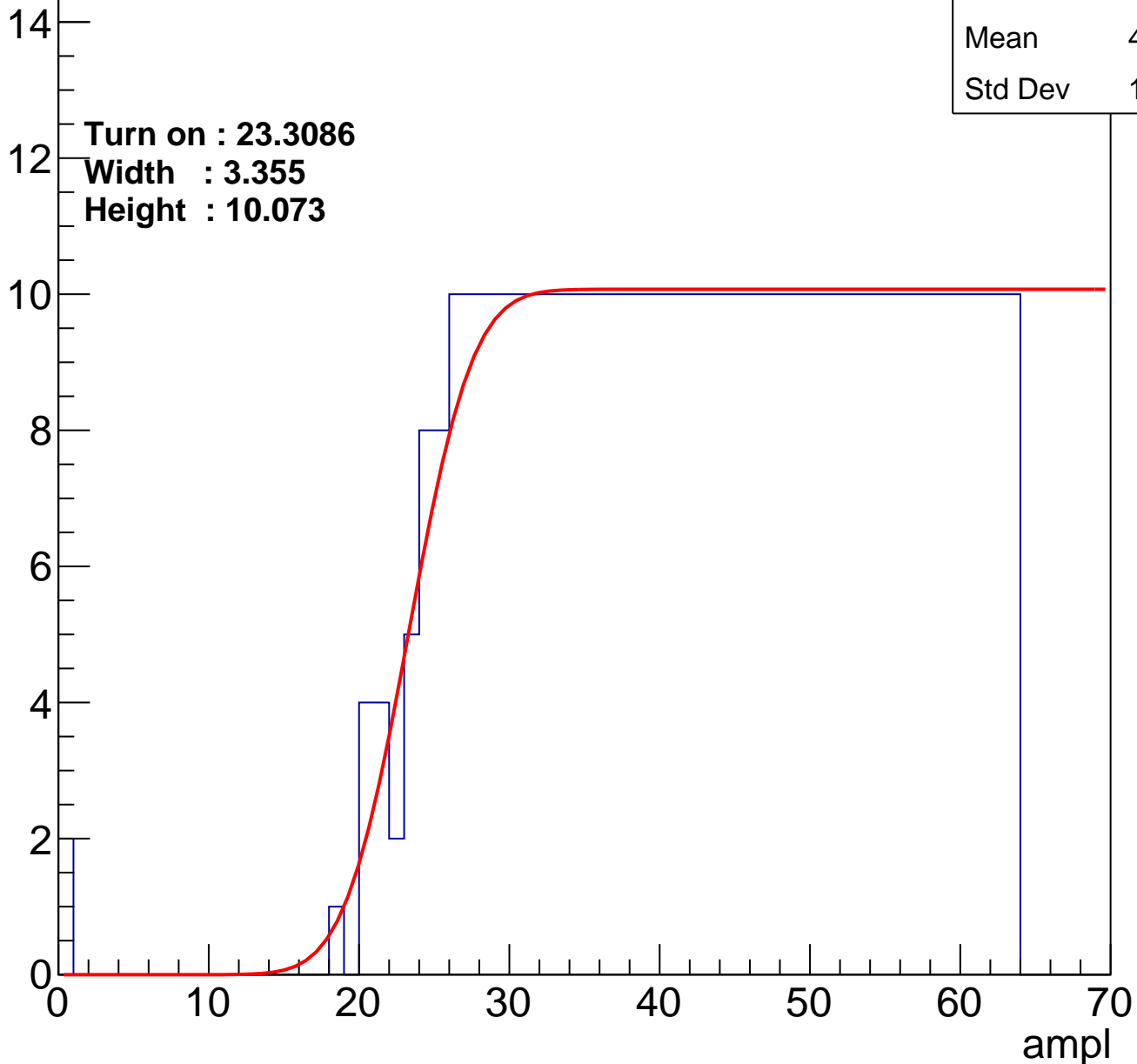
Entries	414
Mean	42.62
Std Dev	12.36

Turn on : 23.3086

Width : 3.355

Height : 10.073

Entry



B1L000S, U16-ch125

calib_packv5_042523_0143.root, FC#3, port B3

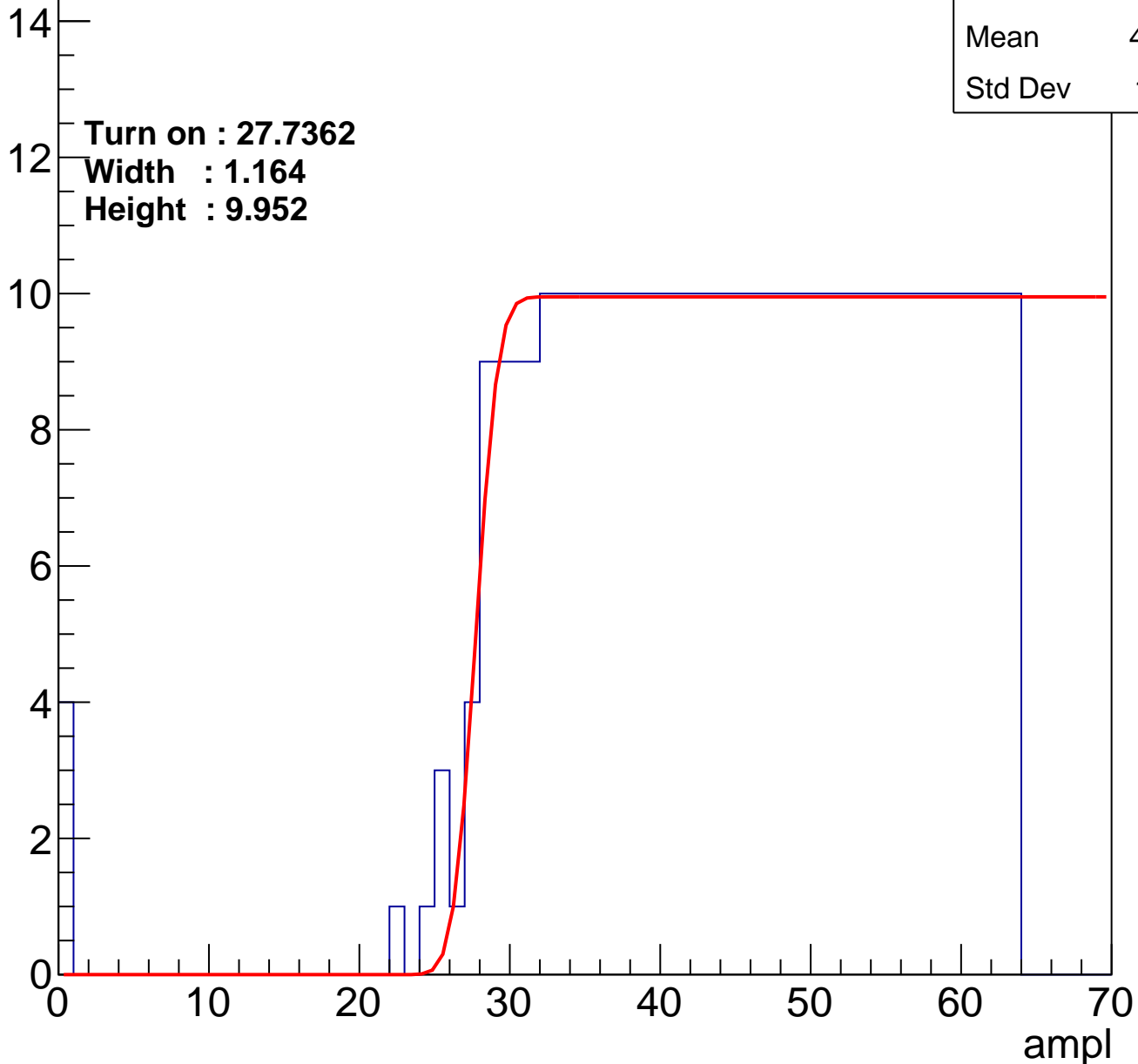
Entries	370
Mean	44.64
Std Dev	11.61

Turn on : 27.7362

Width : 1.164

Height : 9.952

Entry



B1L000S, U16-ch126

calib_packv5_042523_0143.root, FC#3, port B3

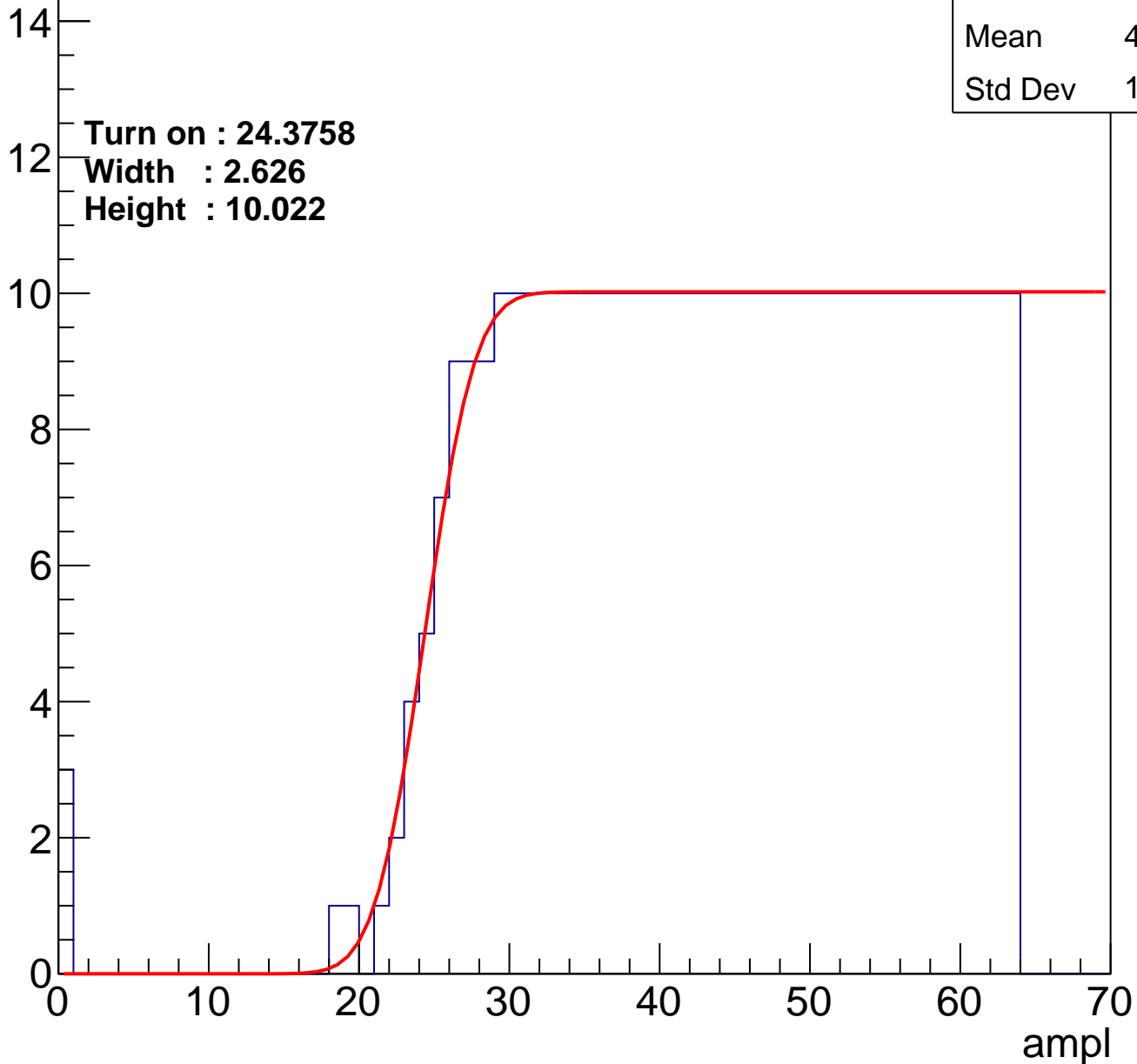
Entries	401
Mean	43.19
Std Dev	12.19

Turn on : 24.3758

Width : 2.626

Height : 10.022

Entry



B1L000S, U16-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.8654

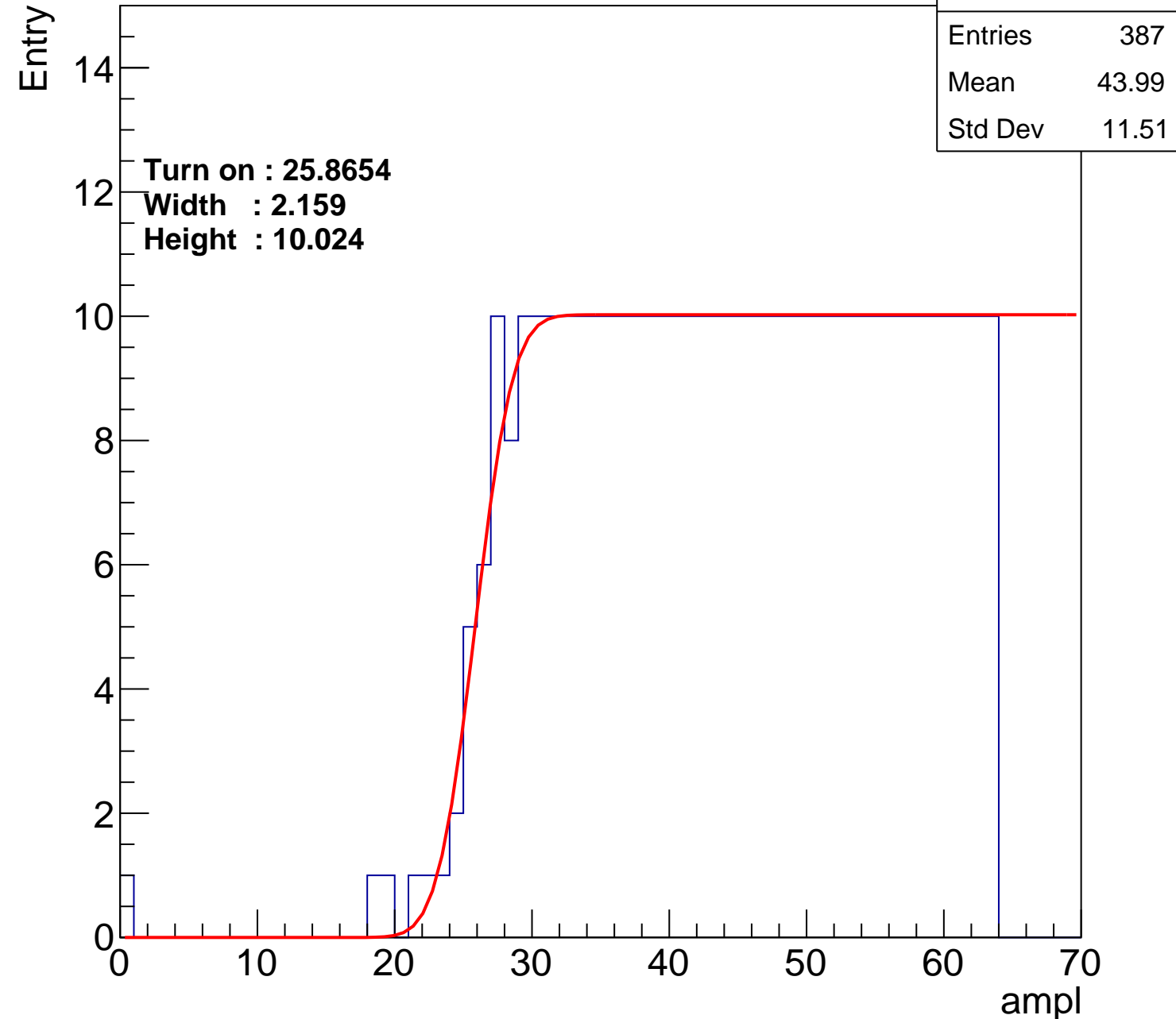
Width : 2.159

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L000S, U16-ch127

calib_packv5_042523_0143.root, FC#3, port B3

Entries	387
Mean	43.99
Std Dev	11.51

Turn on : 25.8654

Width : 2.159

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

