



# B0L002S, U19-ch0

calib\_packv5\_042523\_0143.root, FC#8, port C1

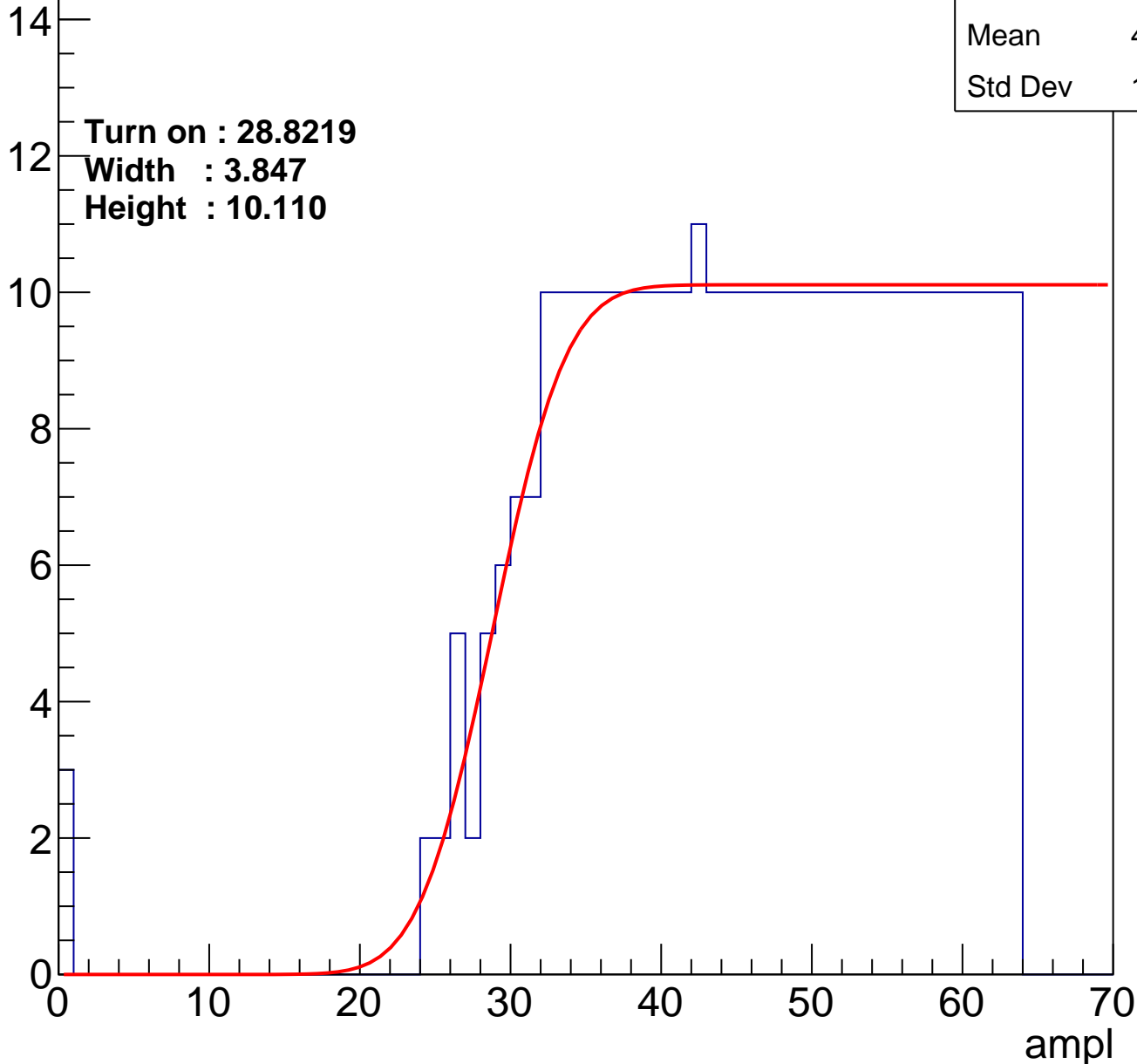
Entry

Entries	360
Mean	45.18
Std Dev	11.23

Turn on : 28.8219

Width : 3.847

Height : 10.110



# B0L002S, U19-ch1

calib\_packv5\_042523\_0143.root, FC#8, port C1

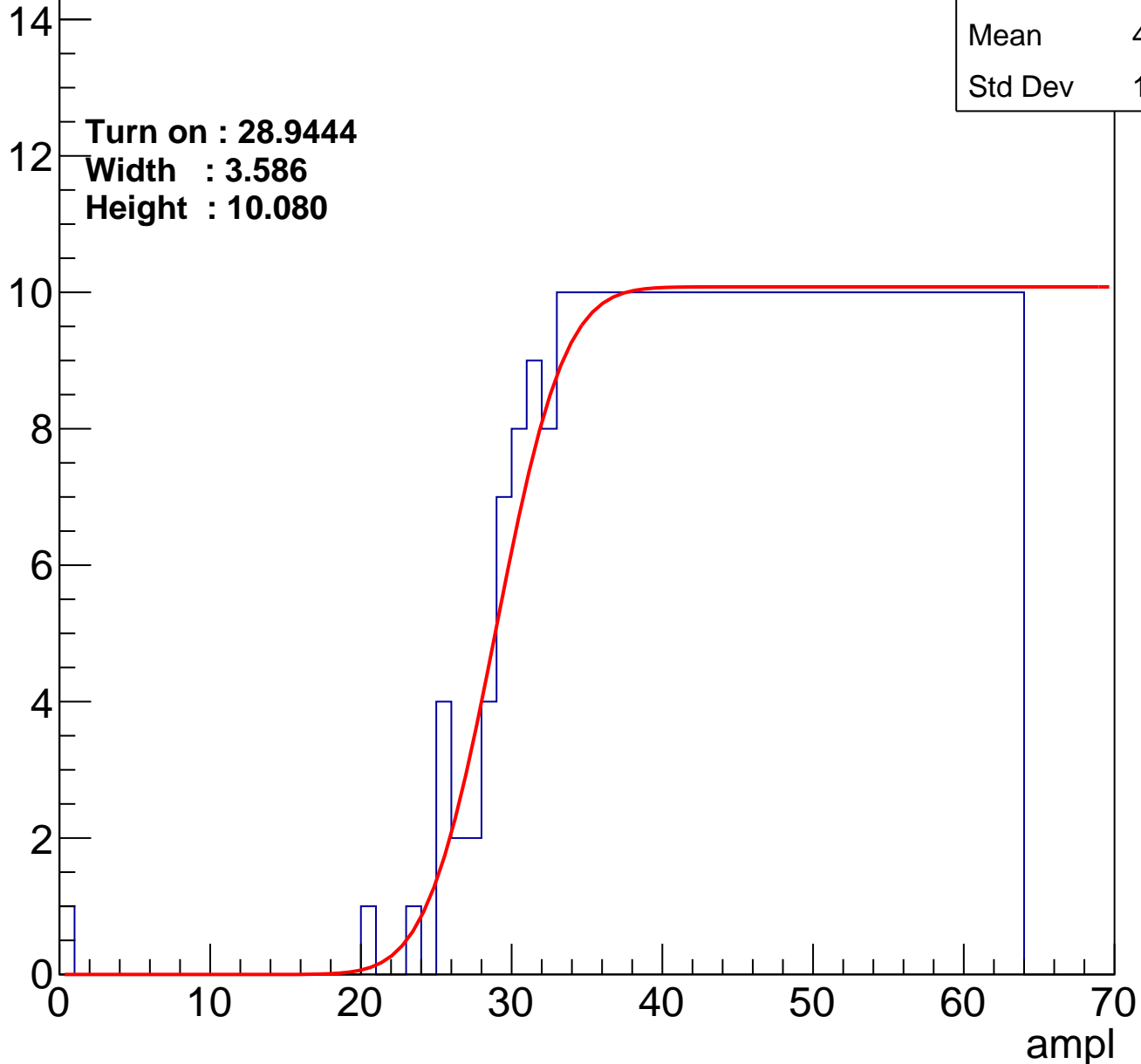
Entries	357
Mean	45.43
Std Dev	10.78

**Turn on : 28.9444**

**Width : 3.586**

**Height : 10.080**

Entry



# B0L002S, U19-ch2

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	352
Mean	45.62
Std Dev	10.85

Turn on : 29.8111

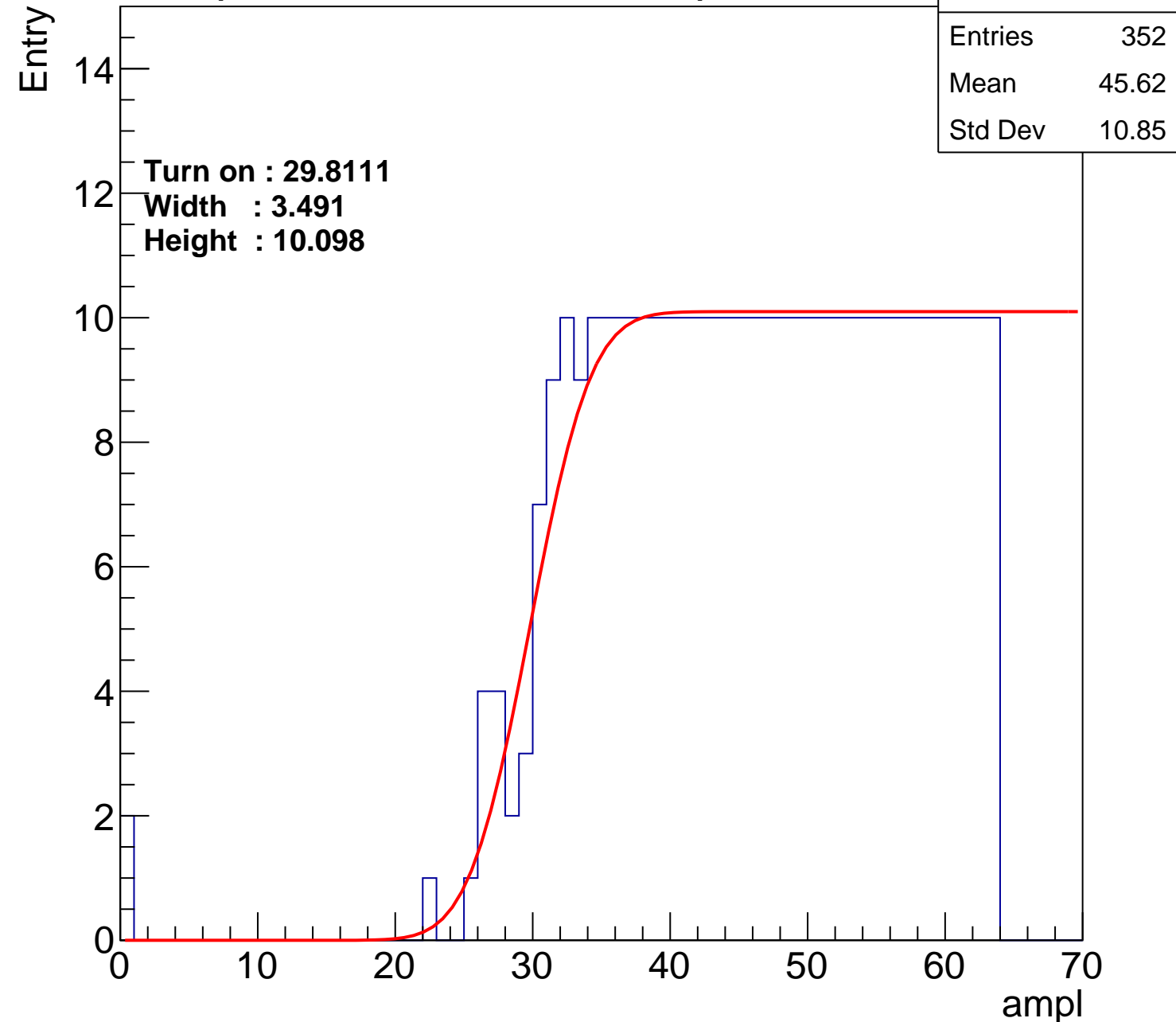
Width : 3.491

Height : 10.098

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch3

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	354
Mean	45.63
Std Dev	10.61

Turn on : 29.3943

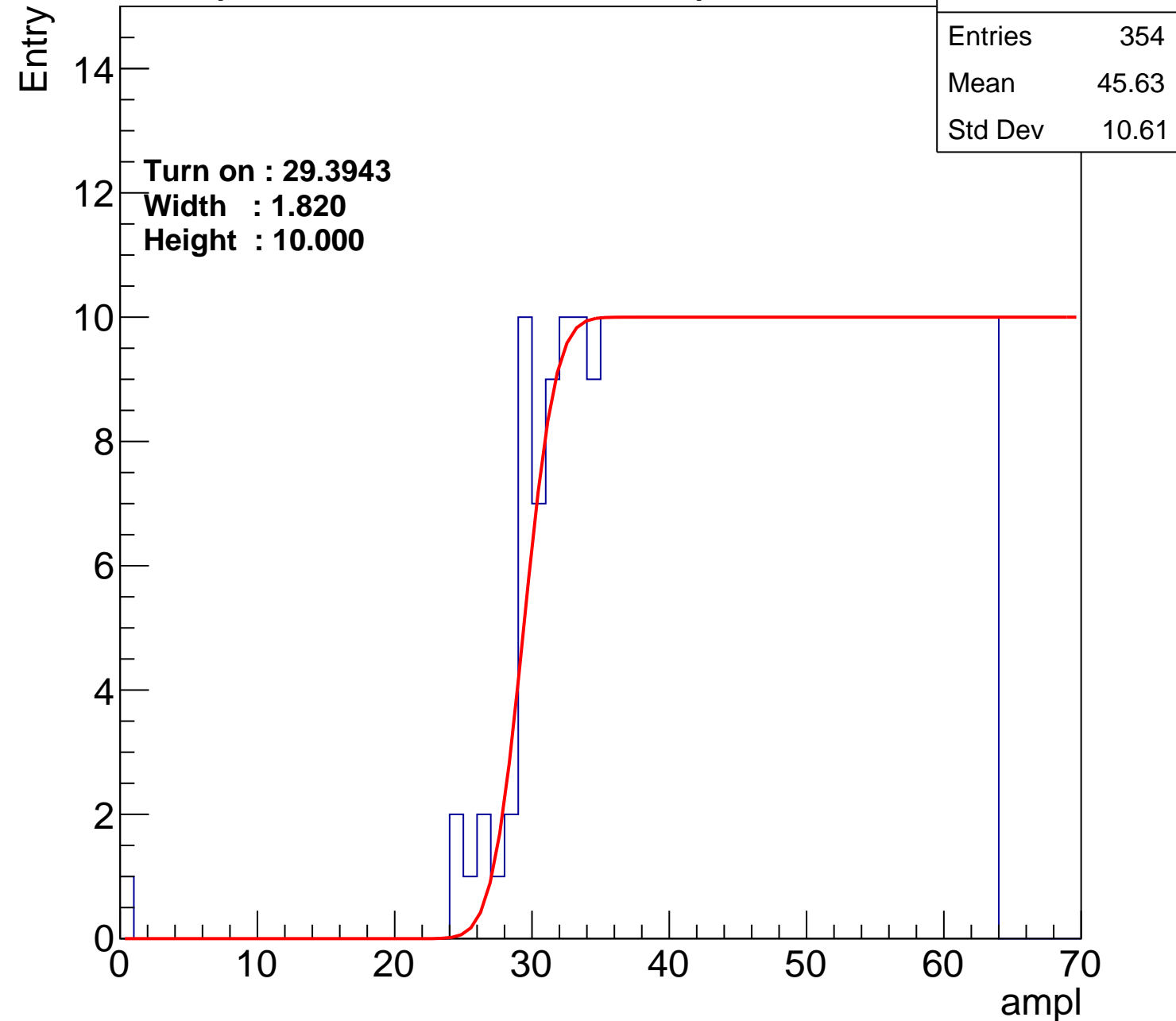
Width : 1.820

Height : 10.000

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch4

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.57
Std Dev	11.4

Turn on : 27.3093

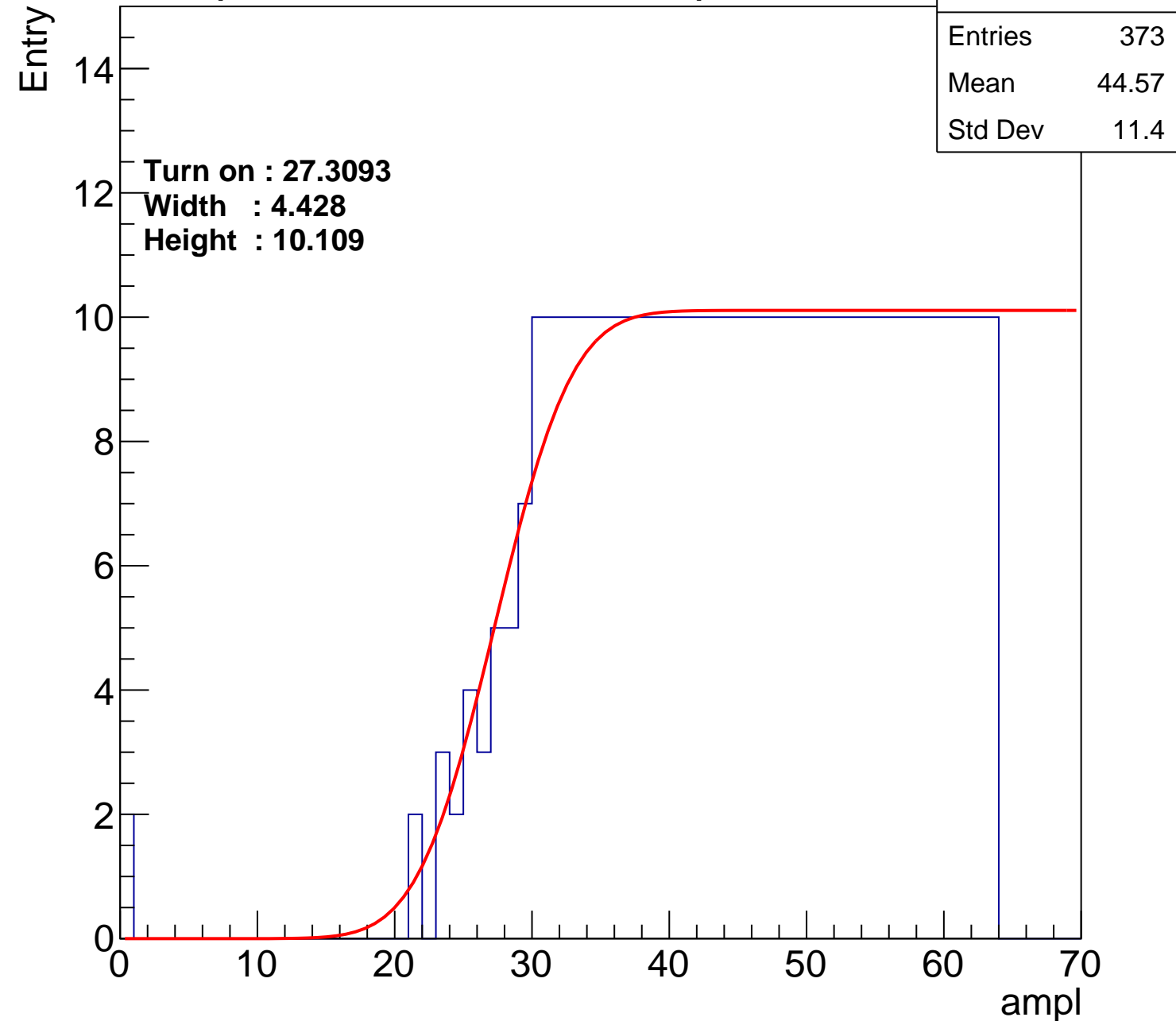
Width : 4.428

Height : 10.109

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch5

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.48
Std Dev	11.25

Turn on : 27.1255

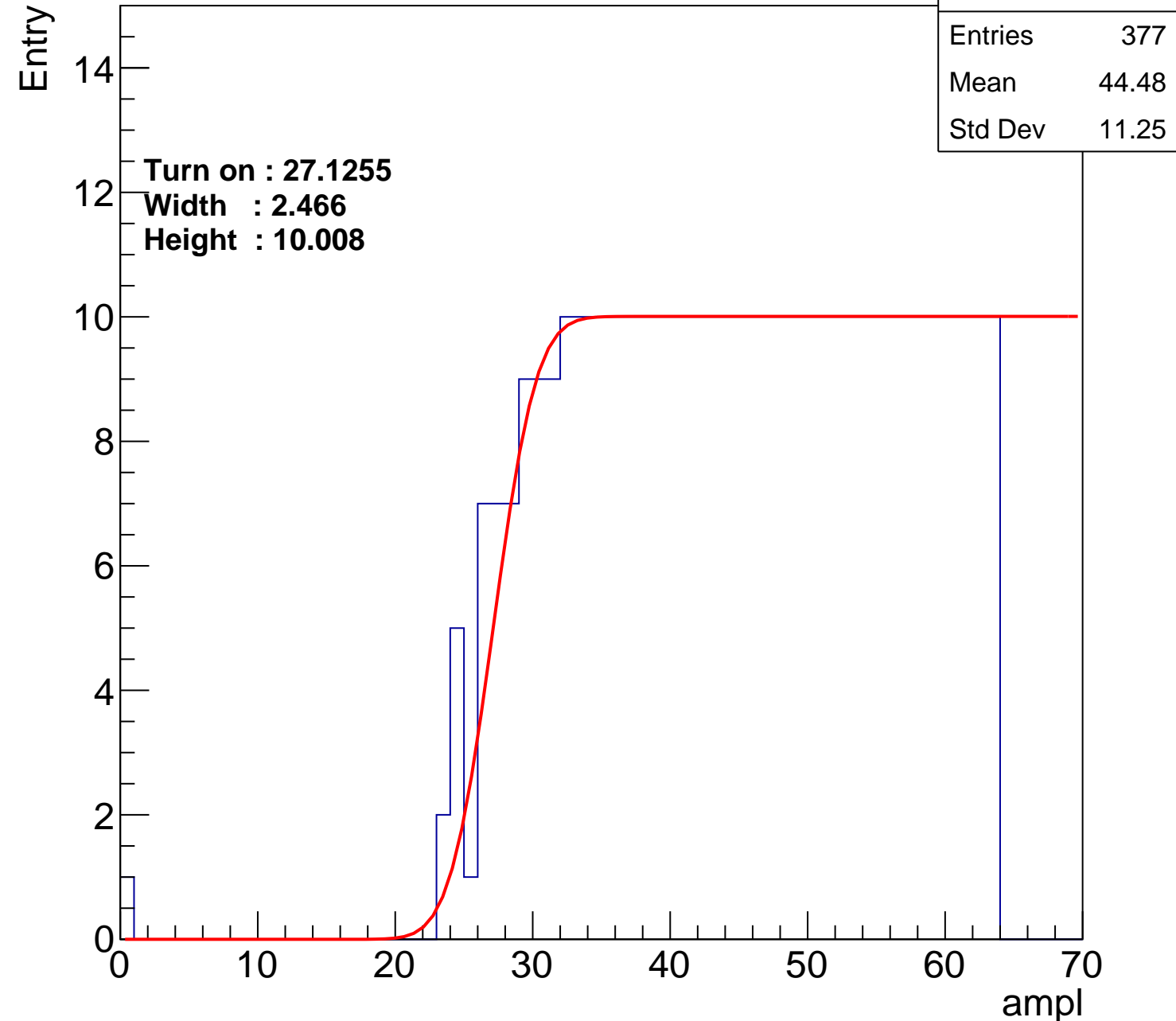
Width : 2.466

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch6

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.81
Std Dev	11.03

**Turn on : 27.2680**

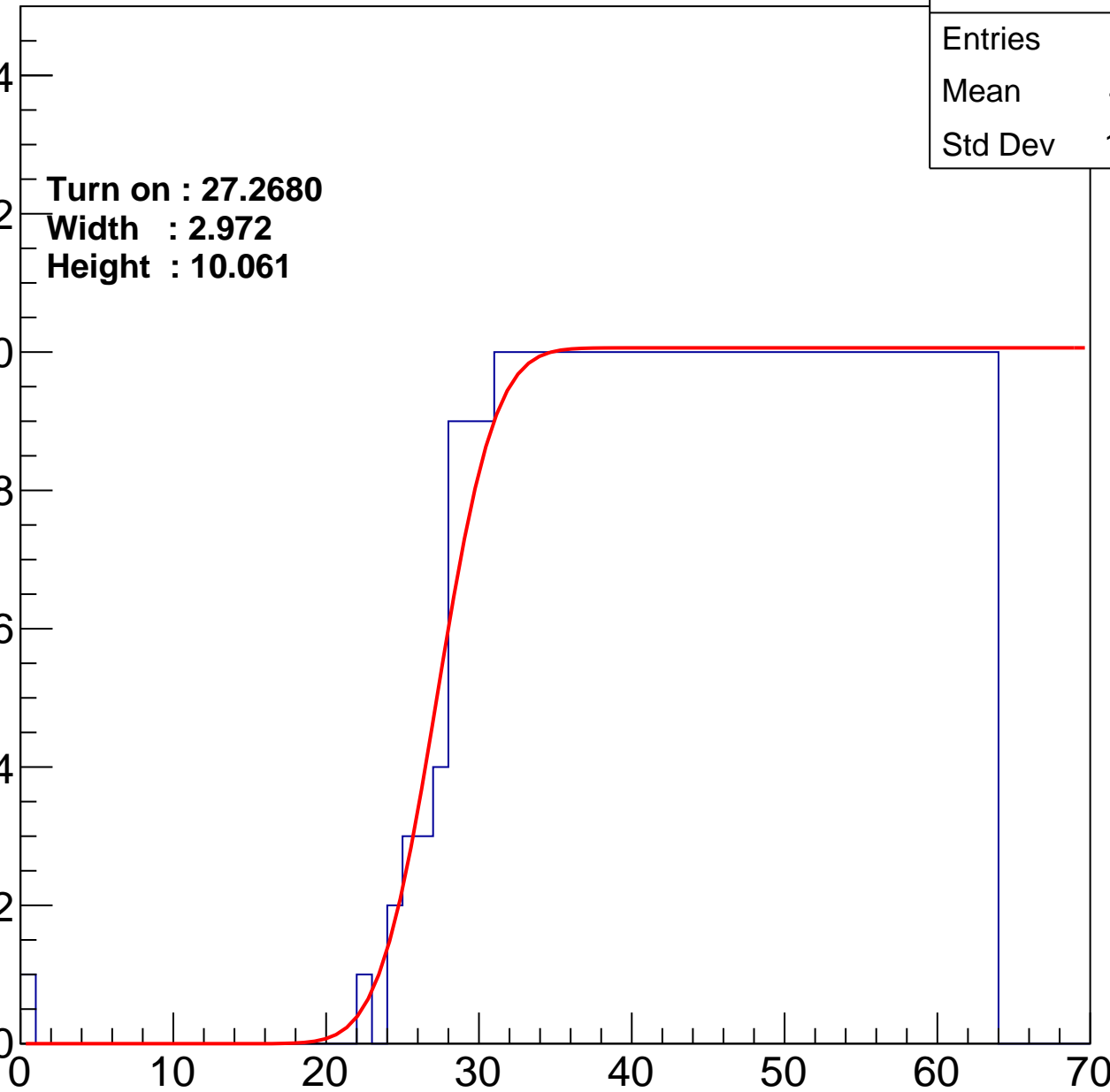
**Width : 2.972**

**Height : 10.061**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch7

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	45.04
Std Dev	10.93

**Turn on : 27.7460**

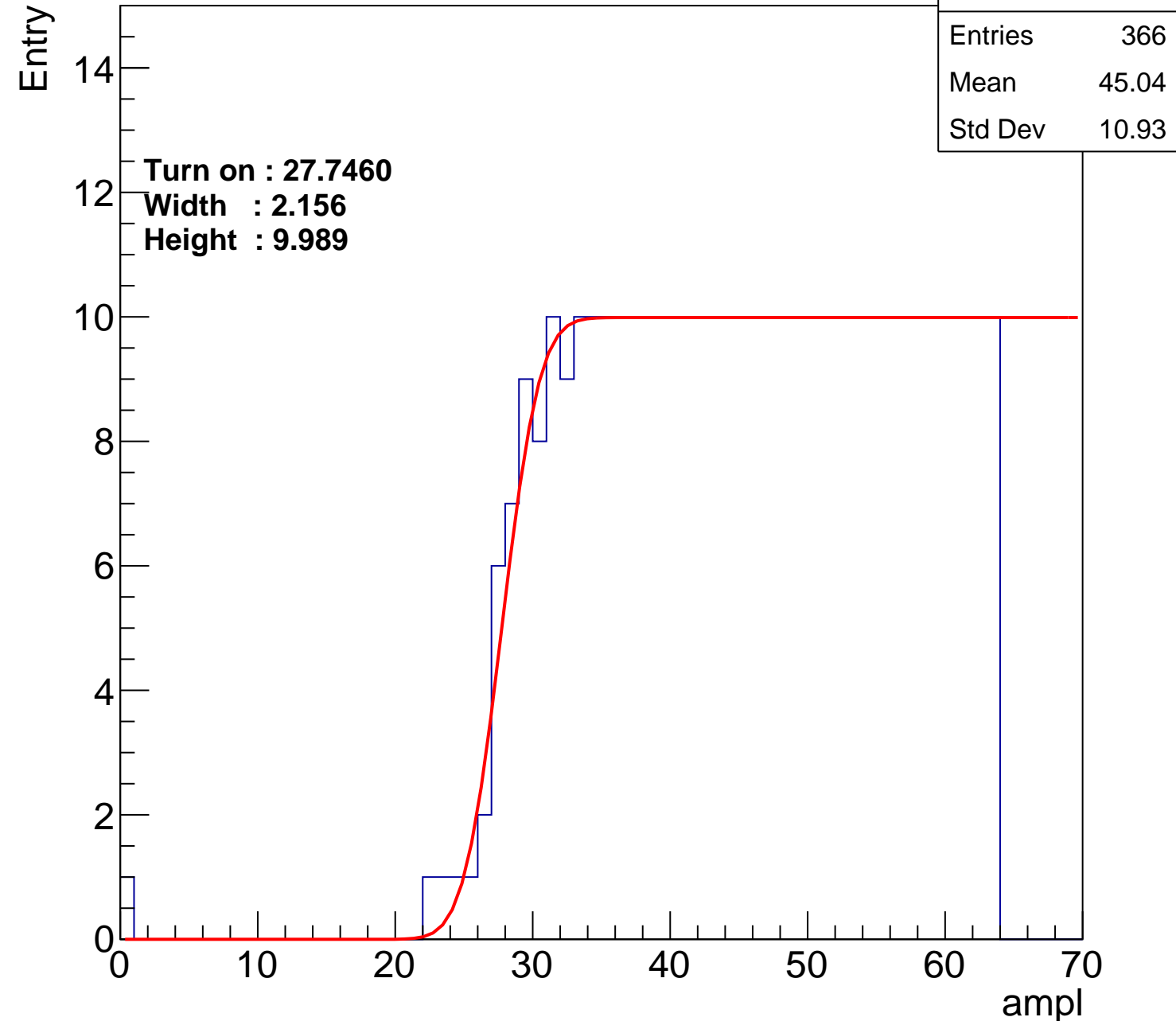
**Width : 2.156**

**Height : 9.989**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch8

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	354
Mean	45.6
Std Dev	10.65

Turn on : 28.4036

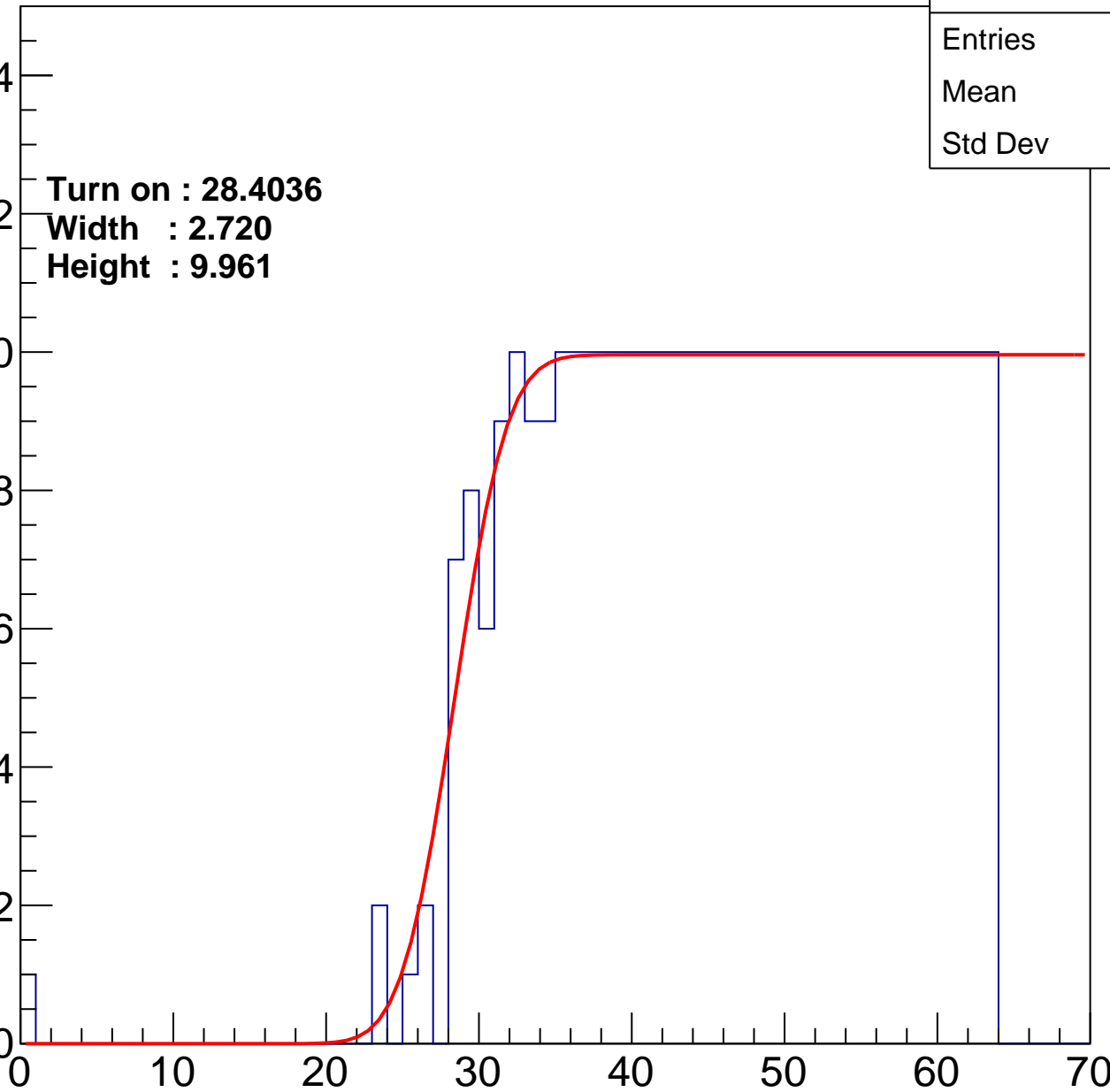
Width : 2.720

Height : 9.961

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch9

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.23
Std Dev	10.82

**Turn on : 27.8853**

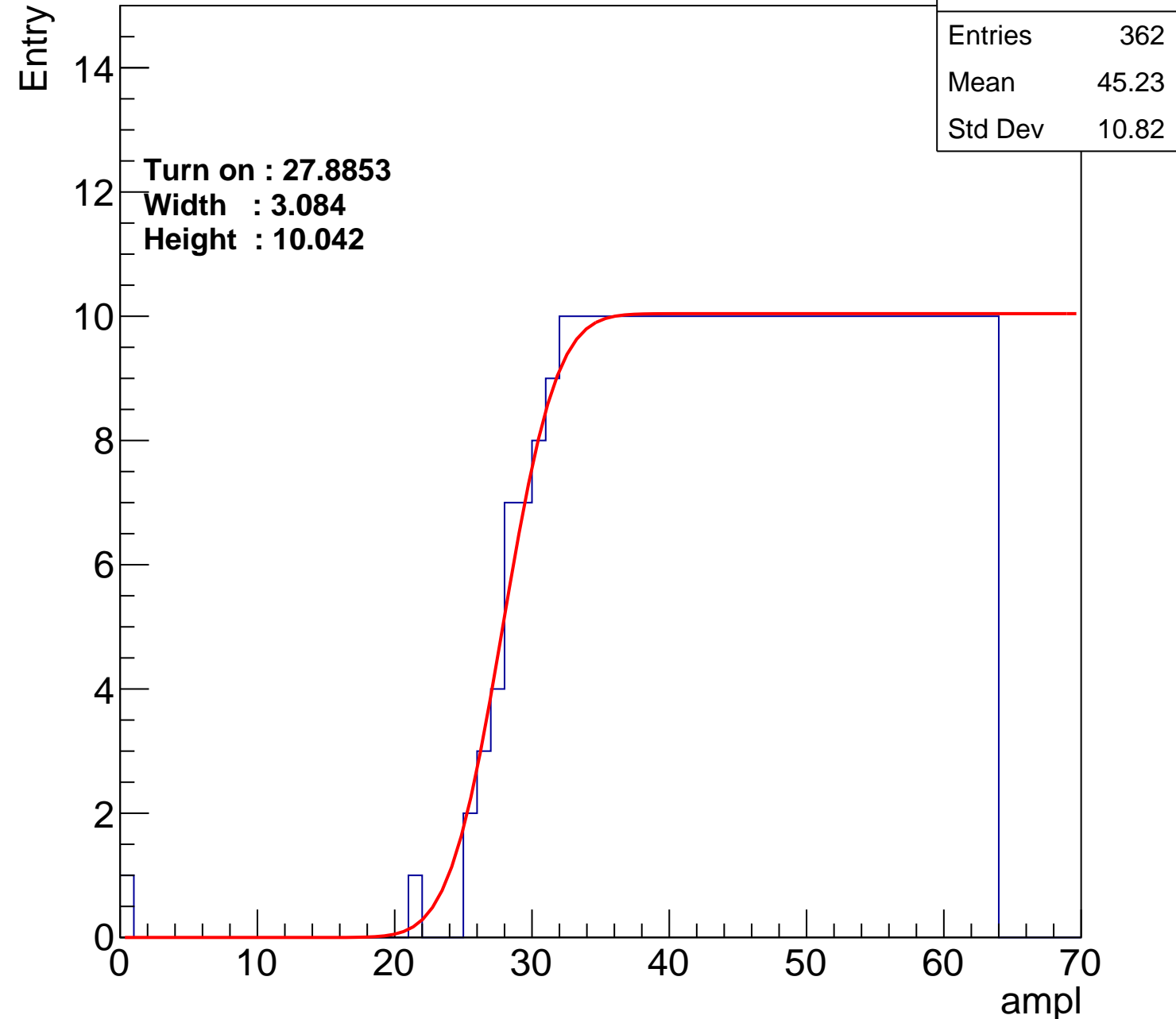
**Width : 3.084**

**Height : 10.042**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch10

calib\_packv5\_042523\_0143.root, FC#8, port C1

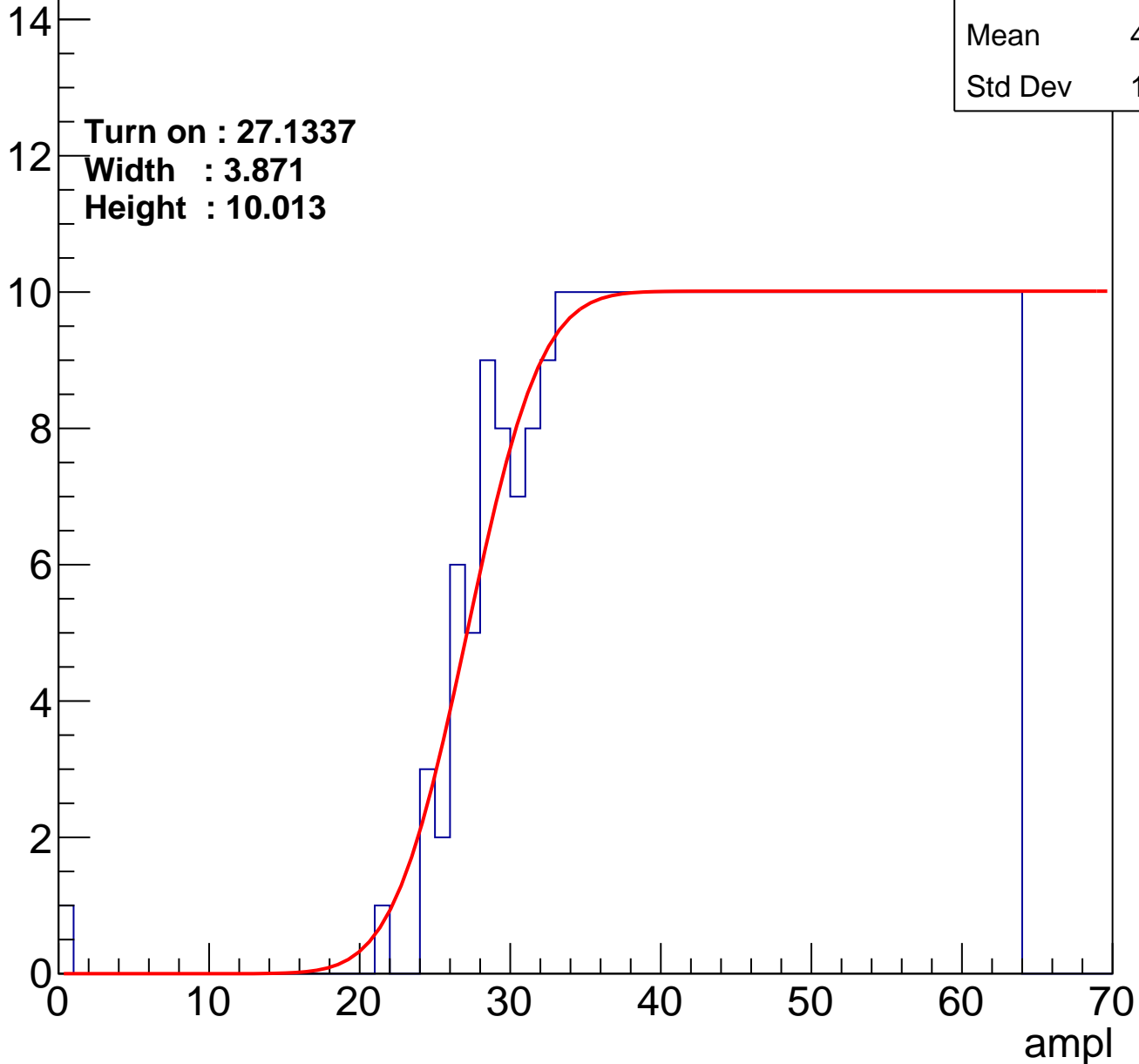
Entries	369
Mean	44.83
Std Dev	11.09

Turn on : 27.1337

Width : 3.871

Height : 10.013

Entry



# B0L002S, U19-ch11

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.51
Std Dev	11.19

Turn on : 26.2847

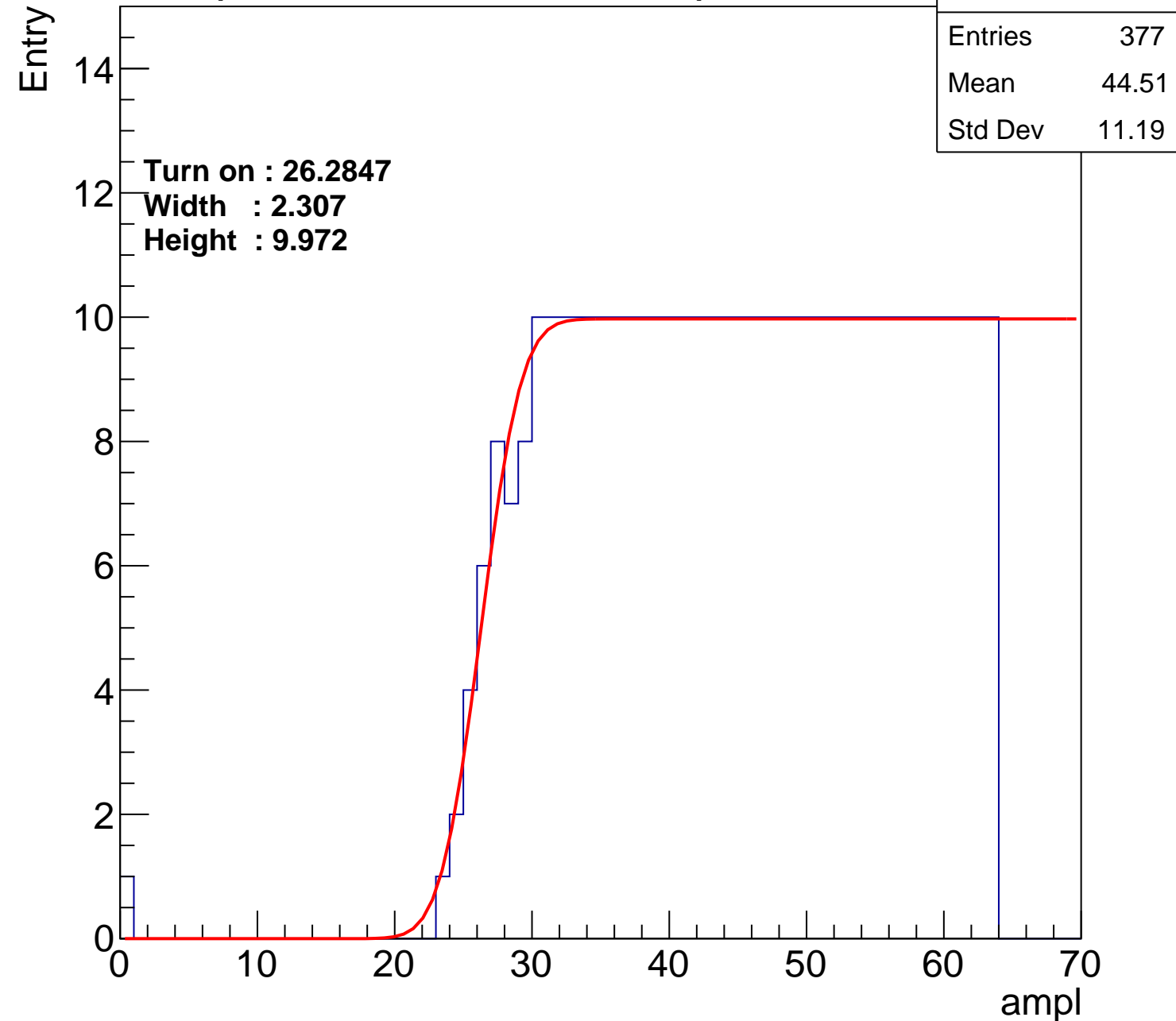
Width : 2.307

Height : 9.972

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch12

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	360
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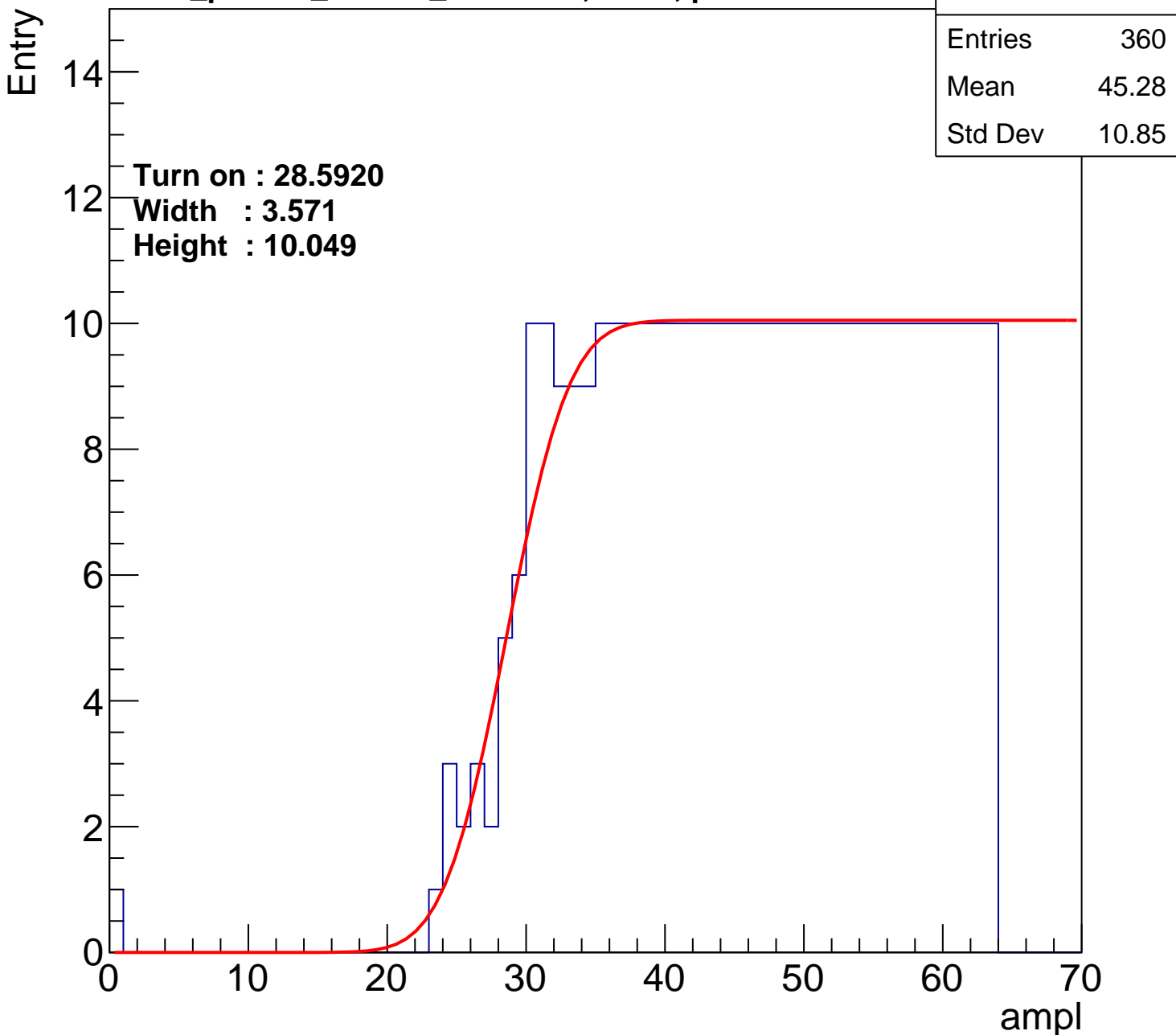
Mean	45.28
------	-------

Std Dev	10.85
---------	-------

**Turn on : 28.5920**

**Width : 3.571**

**Height : 10.049**



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 26.8631  
Width : 4.023  
Height : 10.074



# B0L002S, U19-ch14

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.33
Std Dev	11.34

Turn on : 26.7238

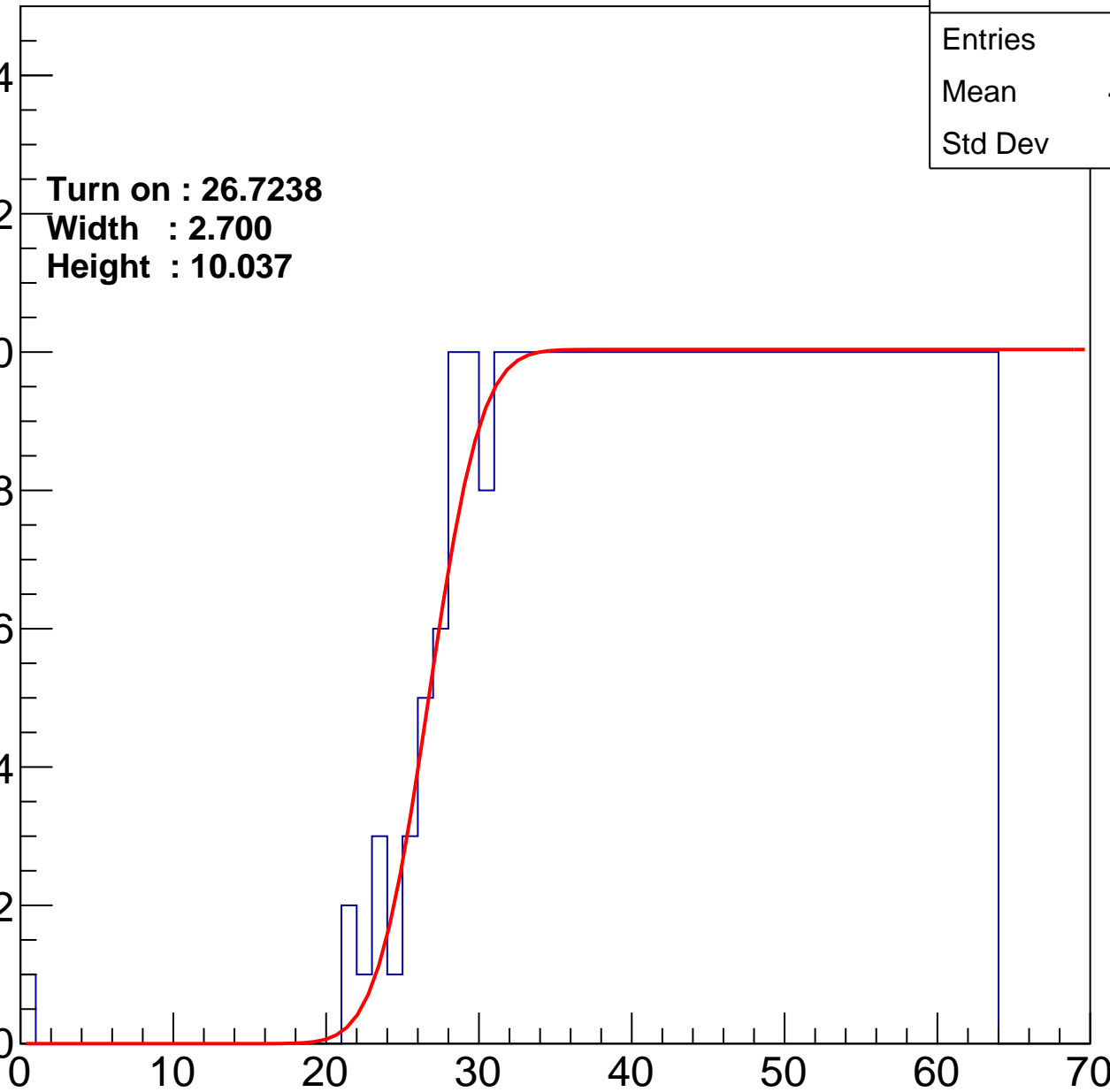
Width : 2.700

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.4772  
Width : 3.456  
Height : 10.029



# B0L002S, U19-ch16

calib\_packv5\_042523\_0143.root, FC#8, port C1

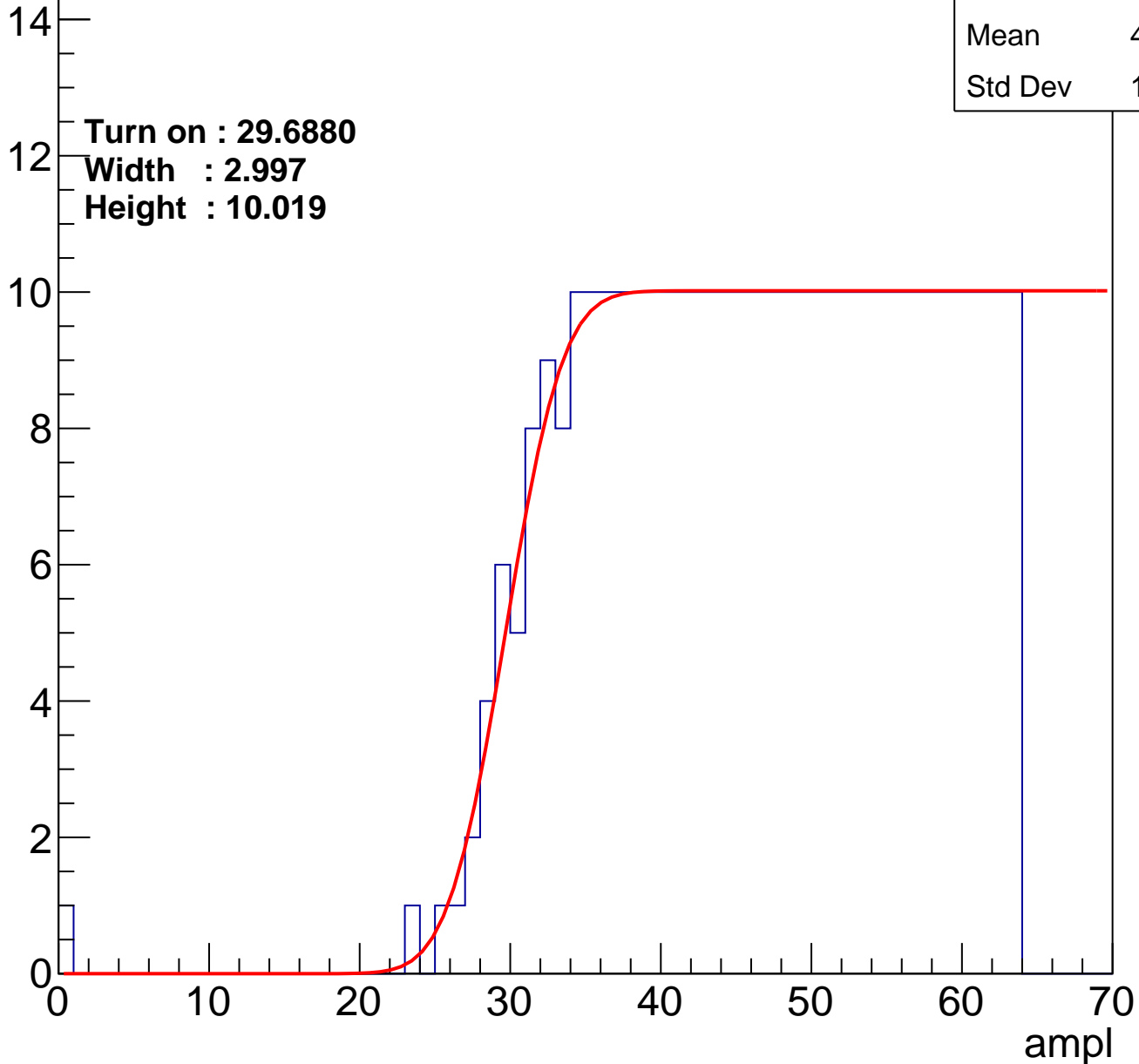
Entries	346
Mean	45.99
Std Dev	10.45

**Turn on : 29.6880**

**Width : 2.997**

**Height : 10.019**

Entry



# B0L002S, U19-ch17

calib\_packv5\_042523\_0143.root, FC#8, port C1

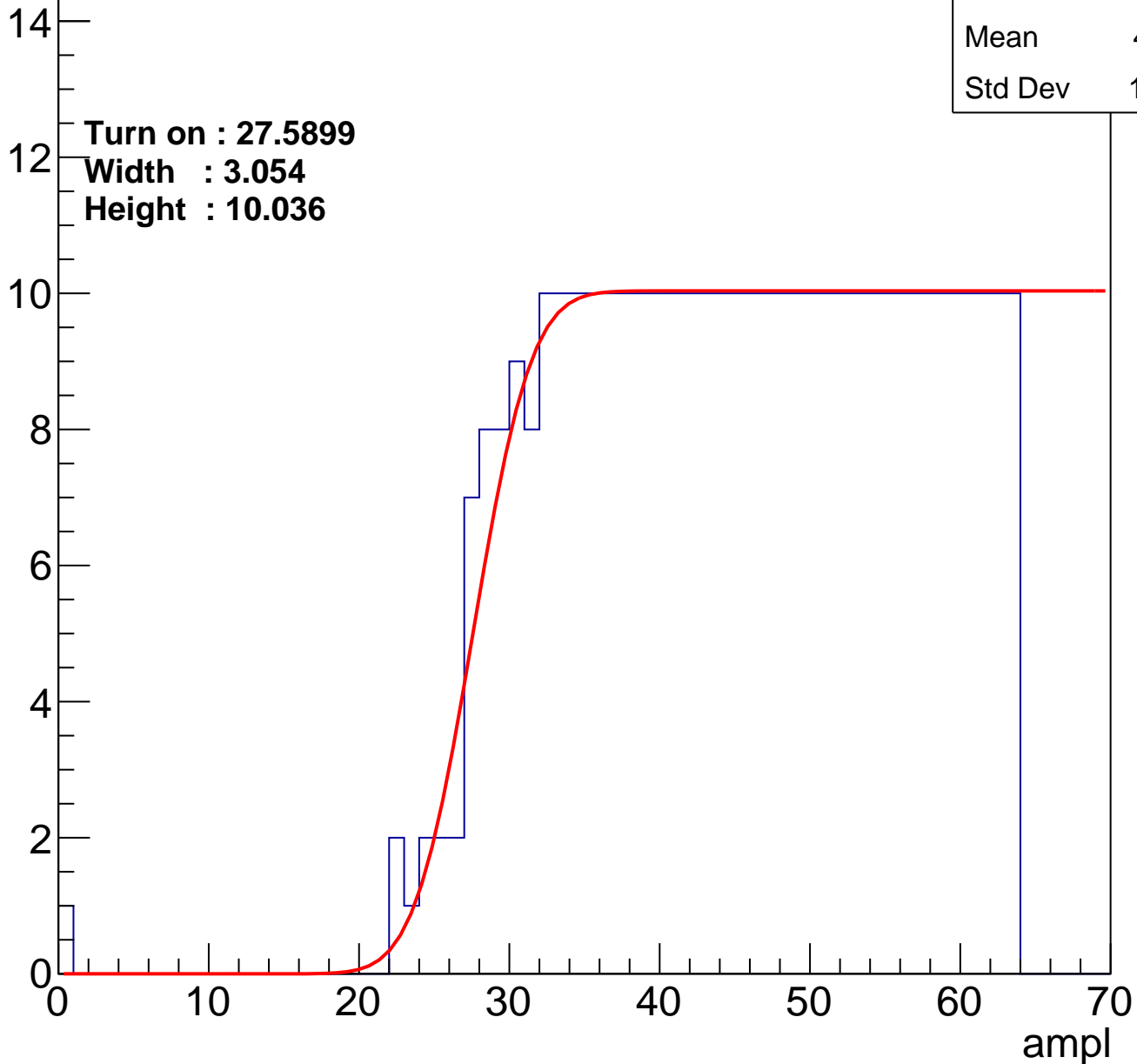
Entries	370
Mean	44.81
Std Dev	11.08

Turn on : 27.5899

Width : 3.054

Height : 10.036

Entry



# B0L002S, U19-ch18

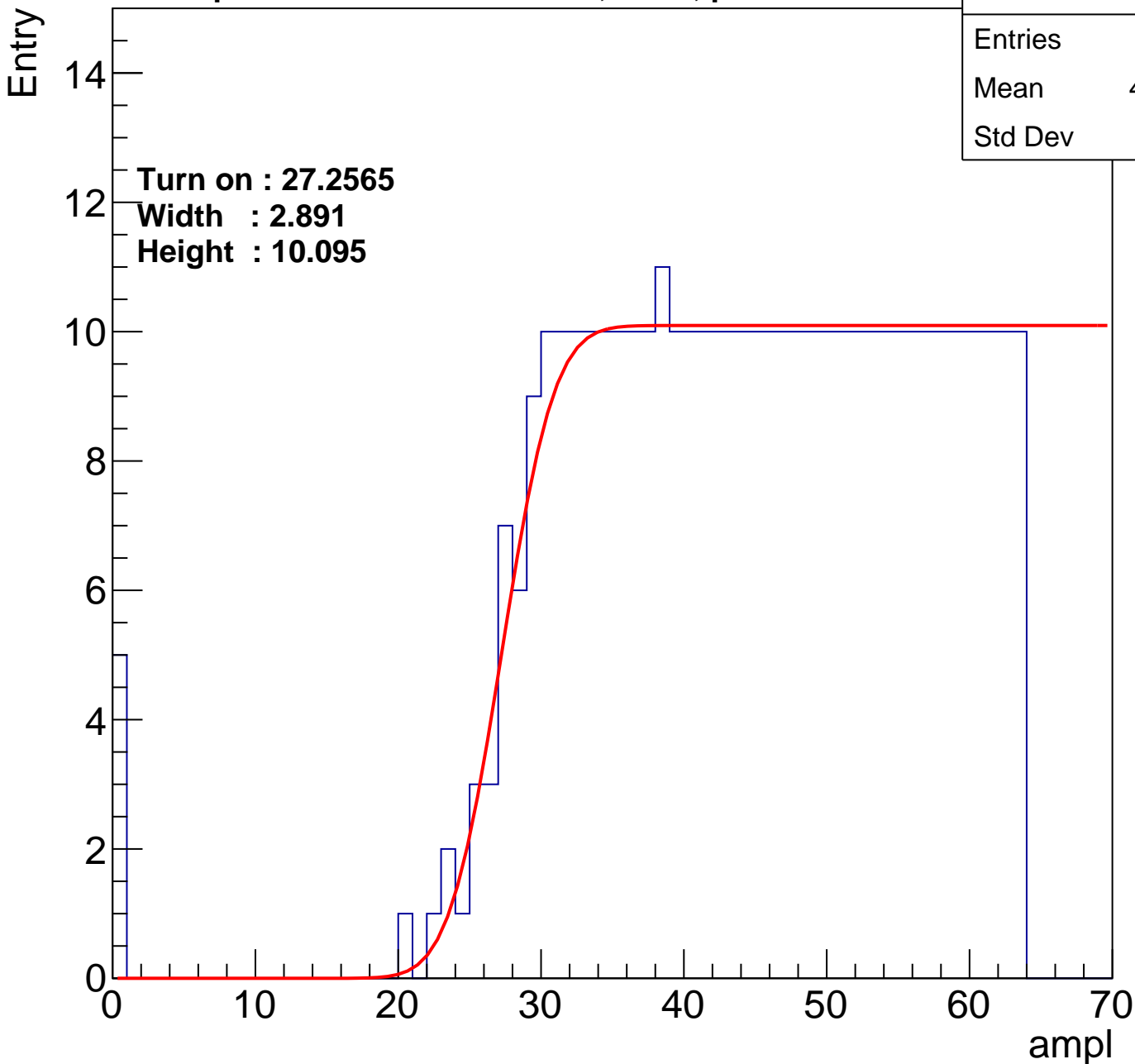
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	379
Mean	44.15
Std Dev	12

Turn on : 27.2565

Width : 2.891

Height : 10.095



# B0L002S, U19-ch19

calib\_packv5\_042523\_0143.root, FC#8, port C1

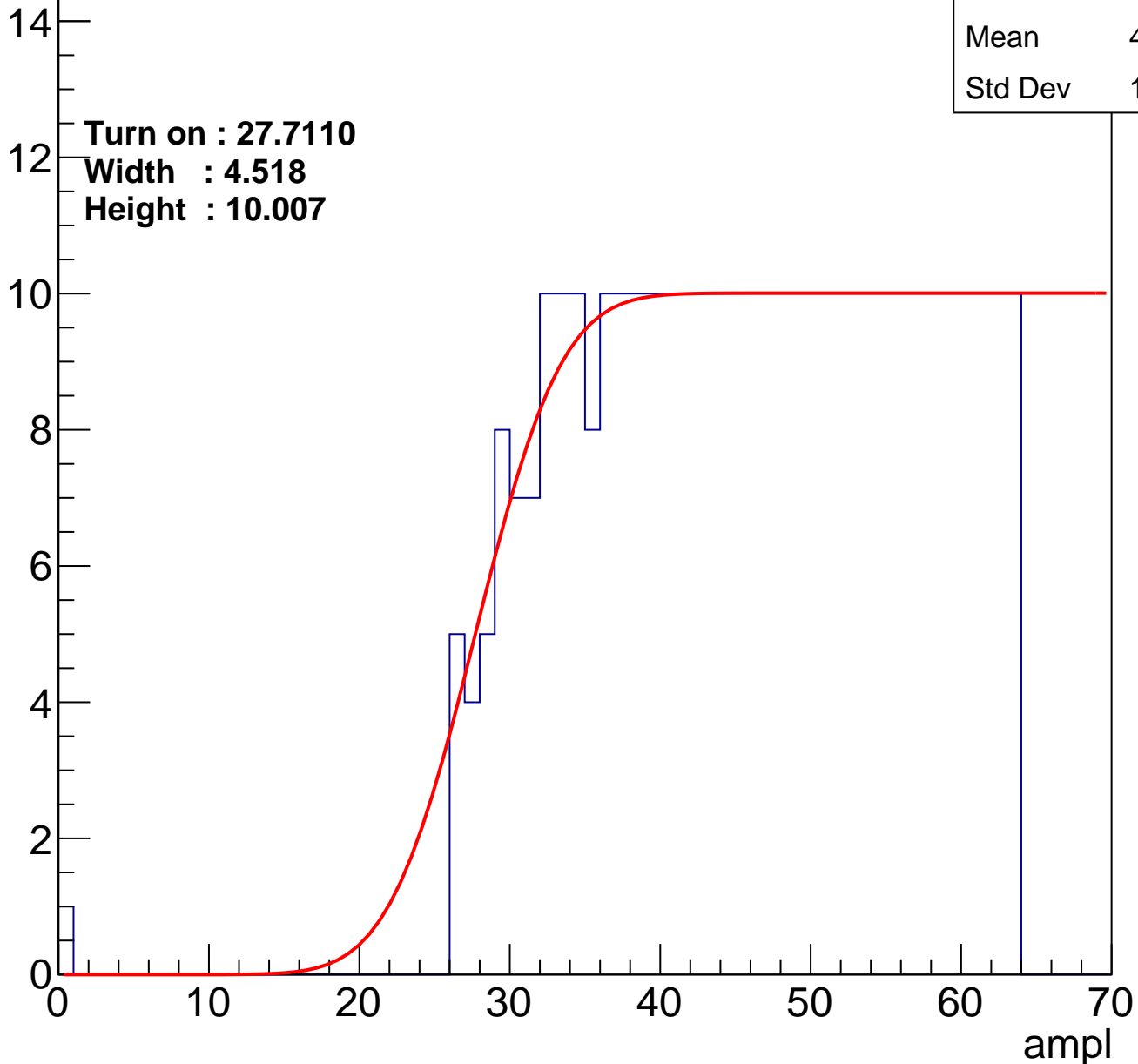
Entries	355
Mean	45.54
Std Dev	10.69

Turn on : 27.7110

Width : 4.518

Height : 10.007

Entry



# B0L002S, U19-ch20

calib\_packv5\_042523\_0143.root, FC#8, port C1

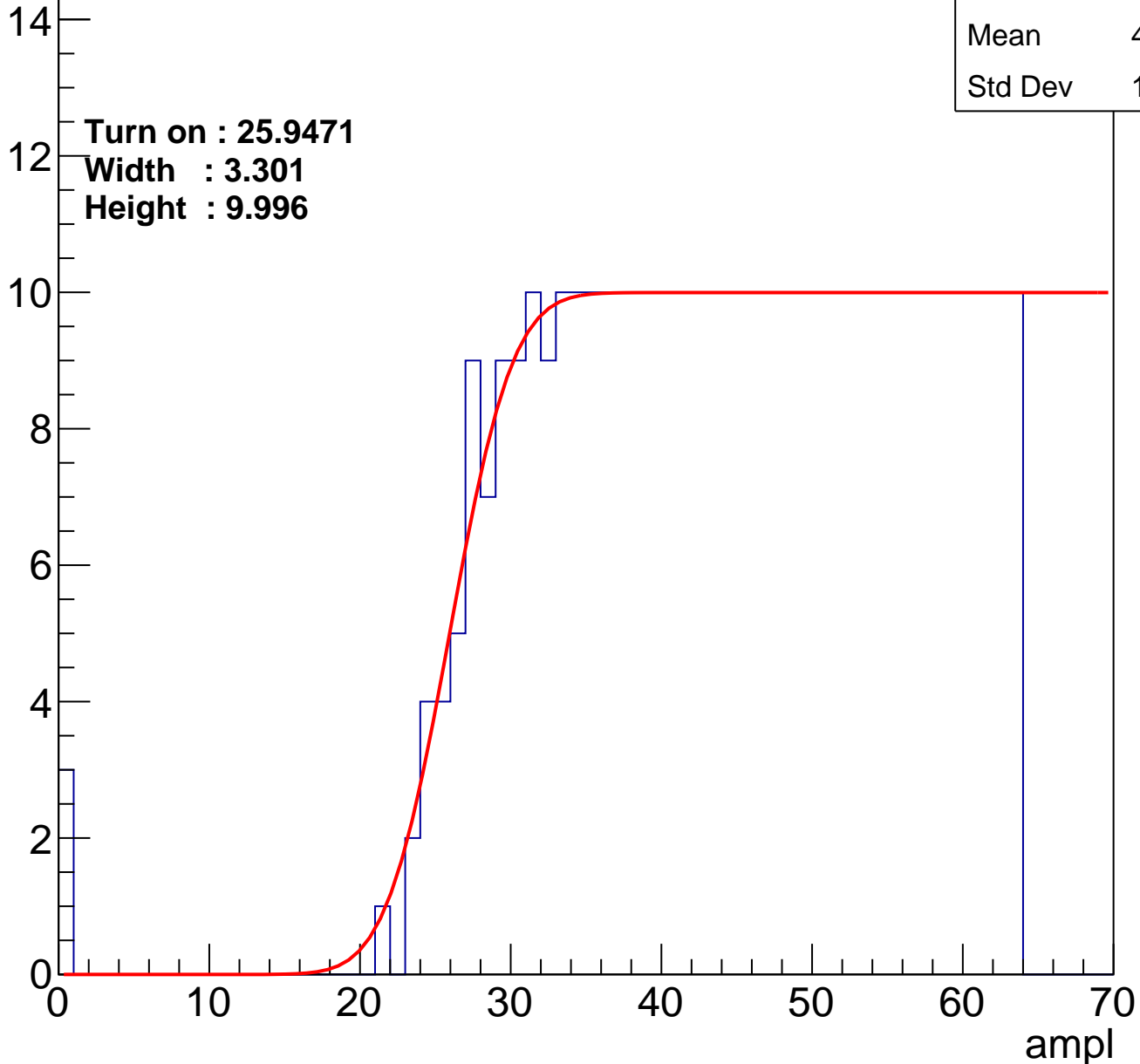
Entries	382
Mean	44.09
Std Dev	11.76

Turn on : 25.9471

Width : 3.301

Height : 9.996

Entry



# B0L002S, U19-ch21

calib\_packv5\_042523\_0143.root, FC#8, port C1

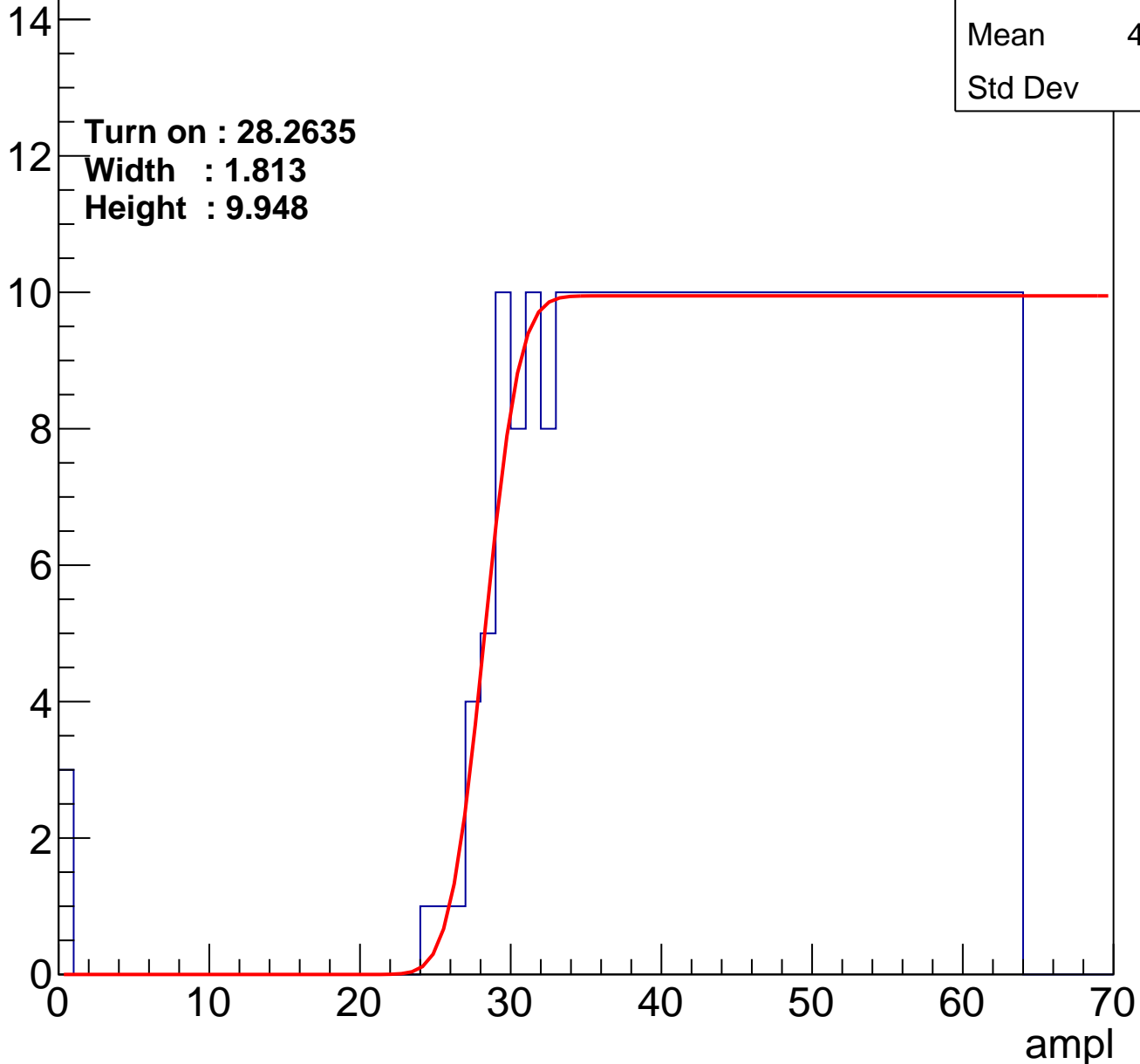
Entries	361
Mean	45.15
Std Dev	11.2

Turn on : 28.2635

Width : 1.813

Height : 9.948

Entry



# B0L002S, U19-ch22

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.13
Std Dev	10.88

**Turn on : 27.6790**

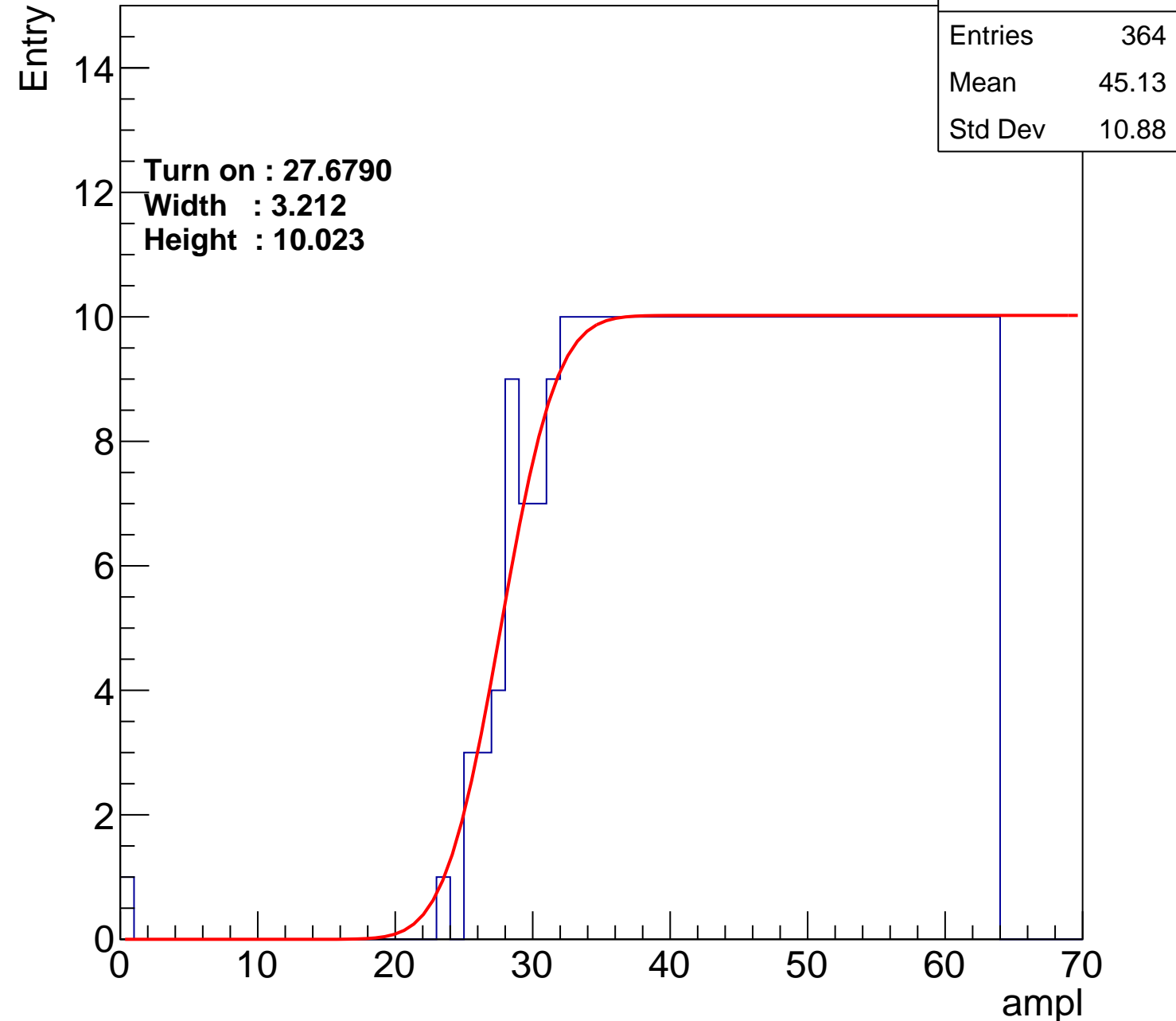
**Width : 3.212**

**Height : 10.023**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch23

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.31
Std Dev	10.81

Turn on : 28.0154

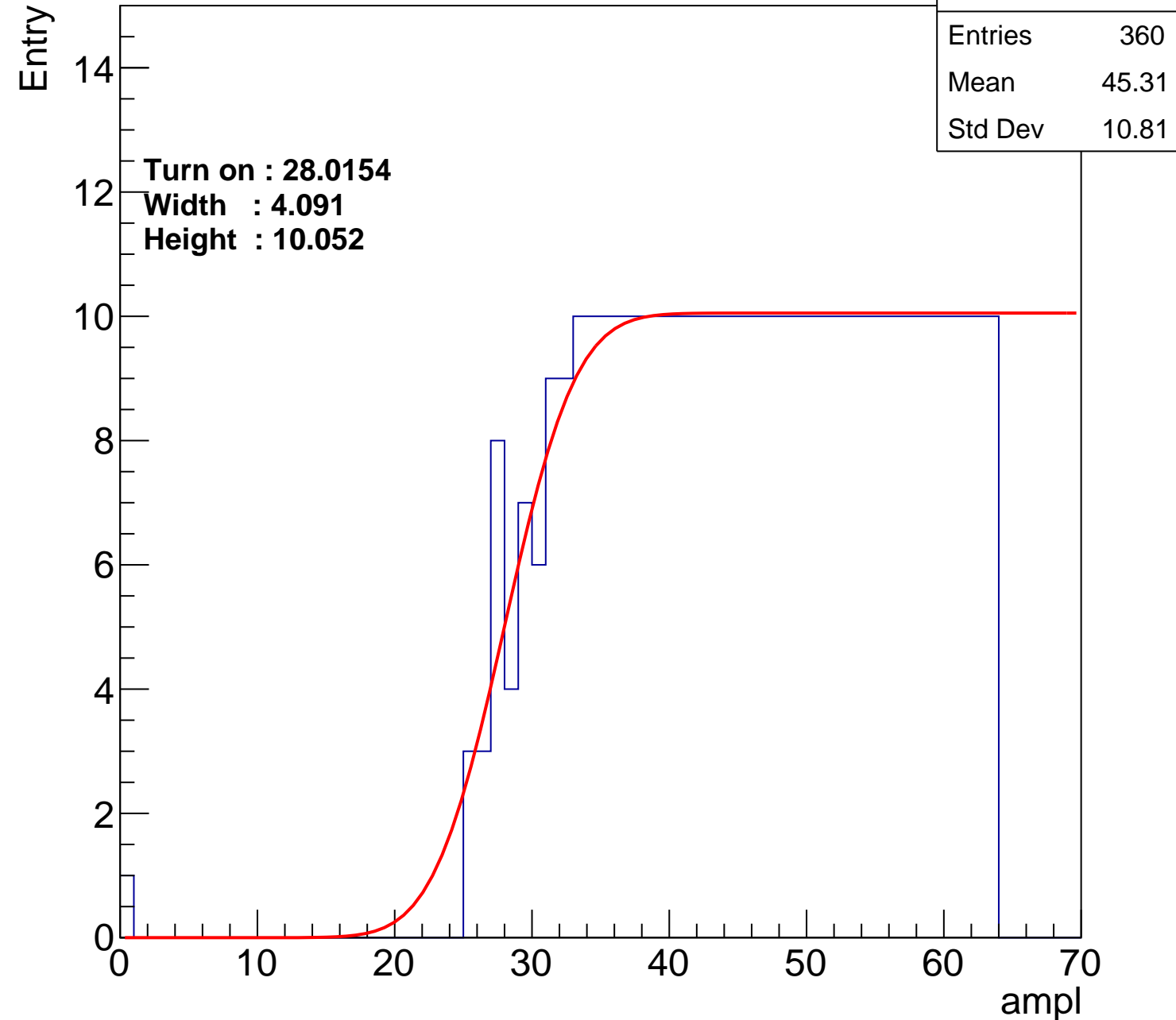
Width : 4.091

Height : 10.052

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch24

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.42
Std Dev	11.56

Turn on : 26.7369

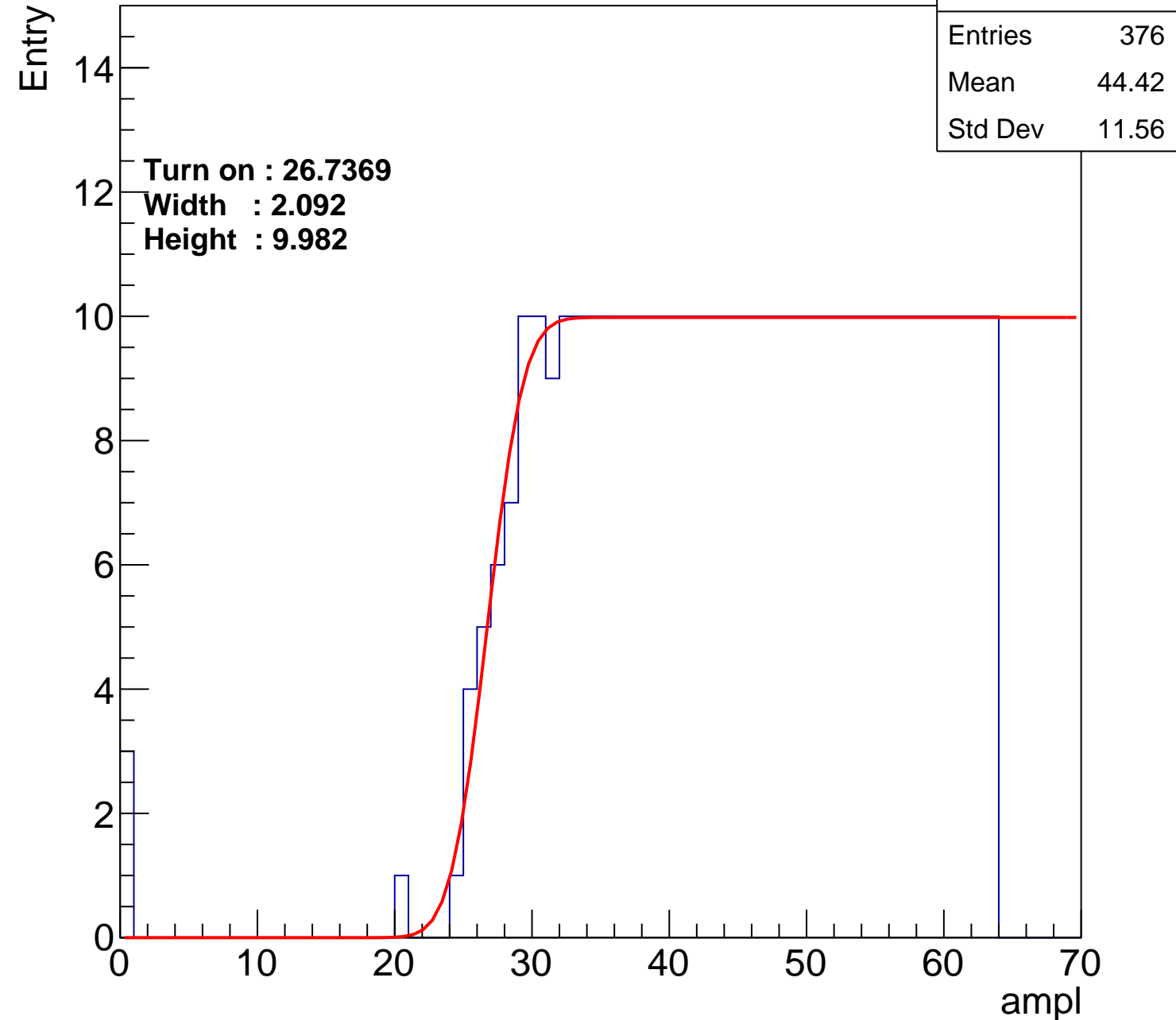
Width : 2.092

Height : 9.982

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch25

calib\_packv5\_042523\_0143.root, FC#8, port C1

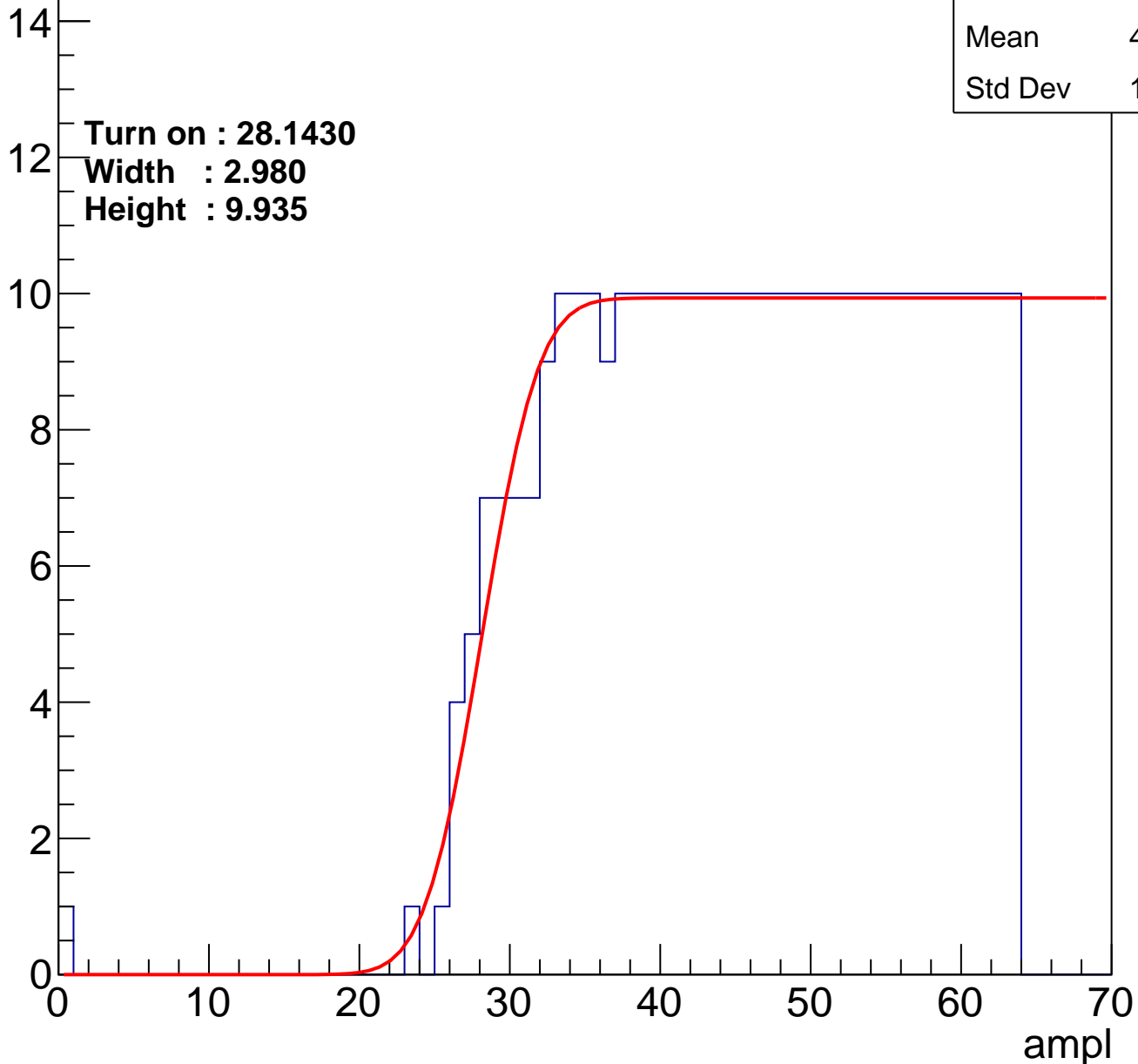
Entries	358
Mean	45.38
Std Dev	10.79

Turn on : 28.1430

Width : 2.980

Height : 9.935

Entry



# B0L002S, U19-ch26

calib\_packv5\_042523\_0143.root, FC#8, port C1

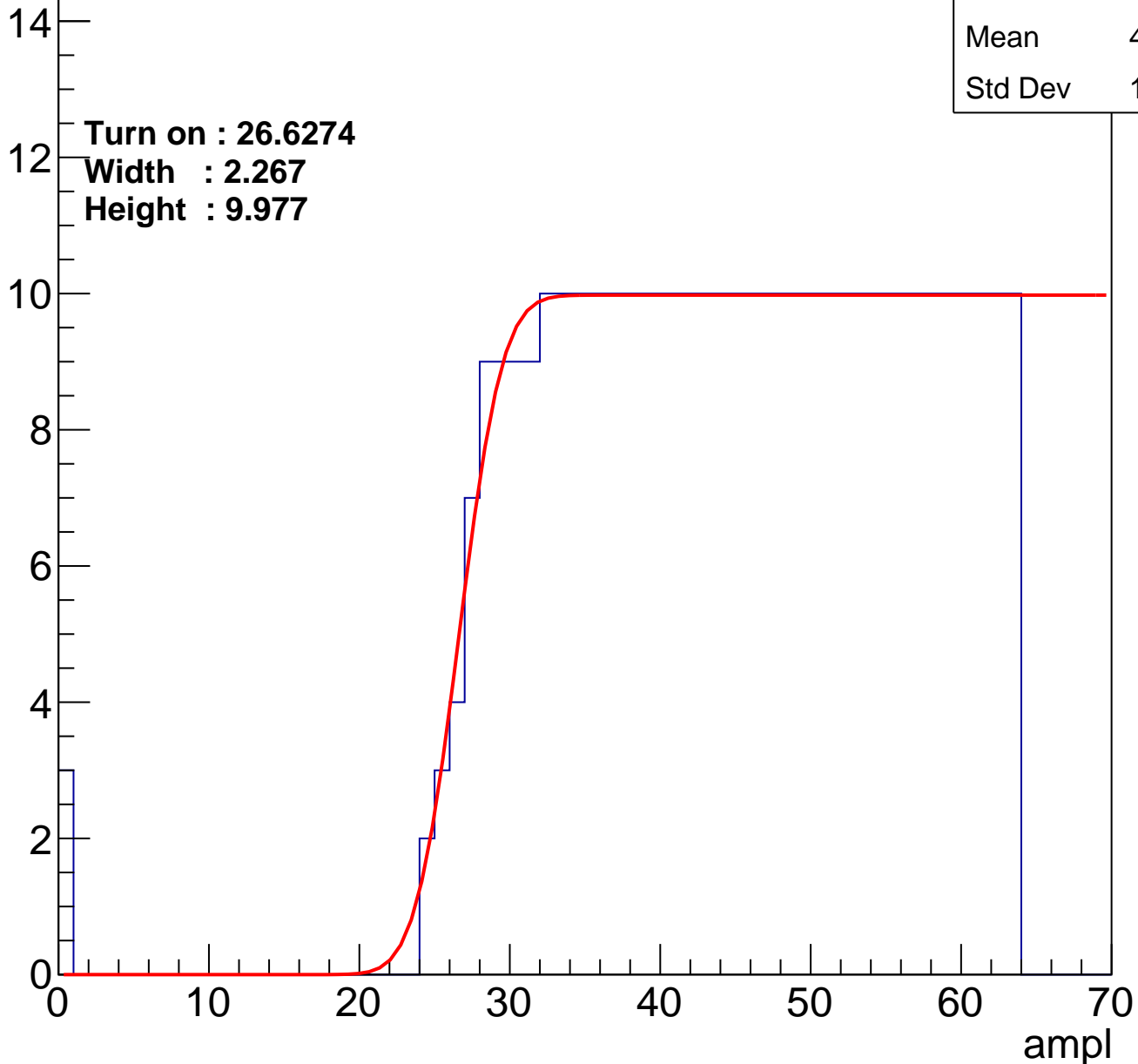
Entries	375
Mean	44.47
Std Dev	11.52

Turn on : 26.6274

Width : 2.267

Height : 9.977

Entry



# B0L002S, U19-ch27

calib\_packv5\_042523\_0143.root, FC#8, port C1

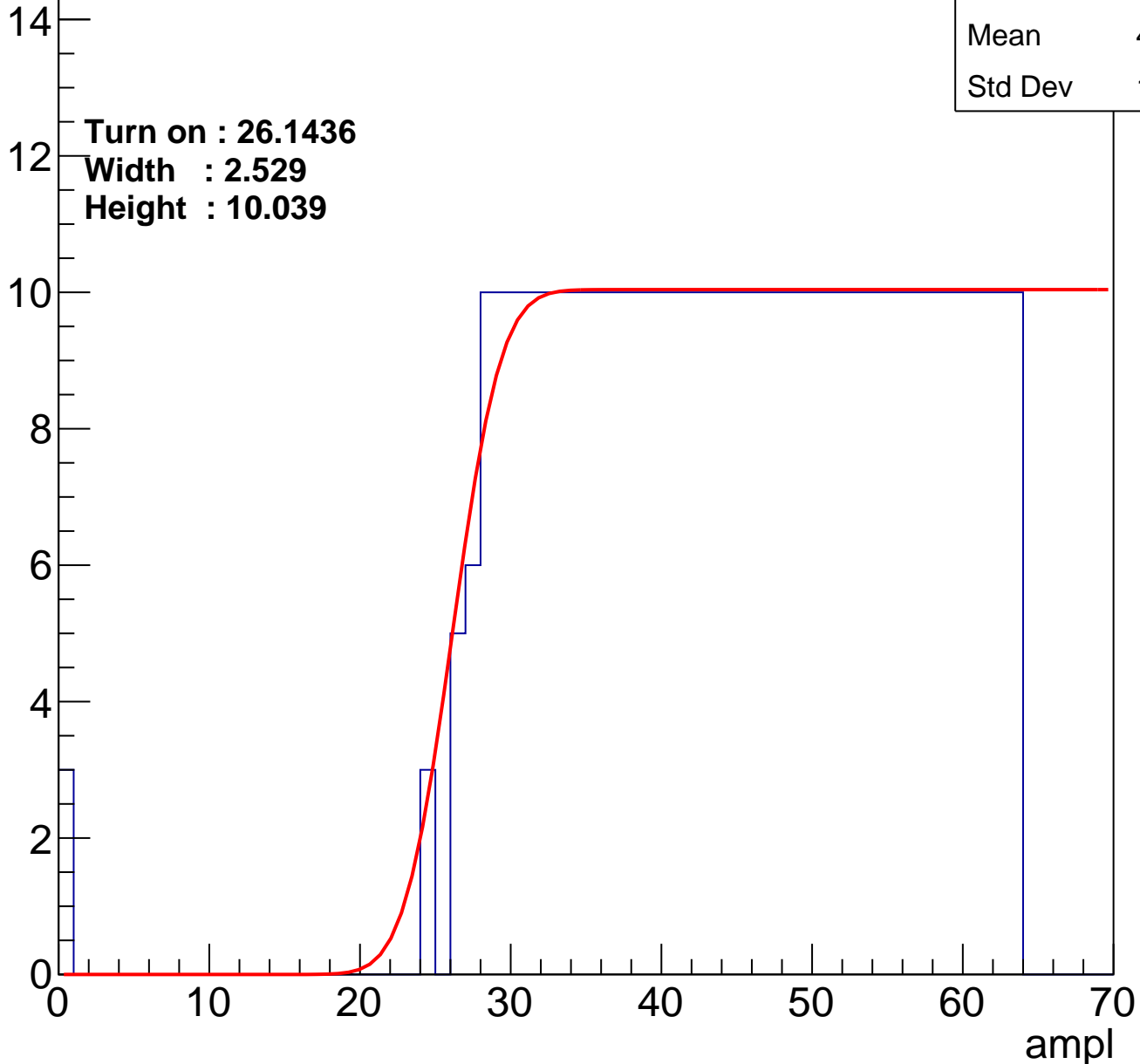
Entries	377
Mean	44.41
Std Dev	11.51

Turn on : 26.1436

Width : 2.529

Height : 10.039

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**Turn on : 26.6739**  
**Width : 2.915**  
**Height : 10.014**



# B0L002S, U19-ch29

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.81
Std Dev	11.46

Turn on : 27.7345

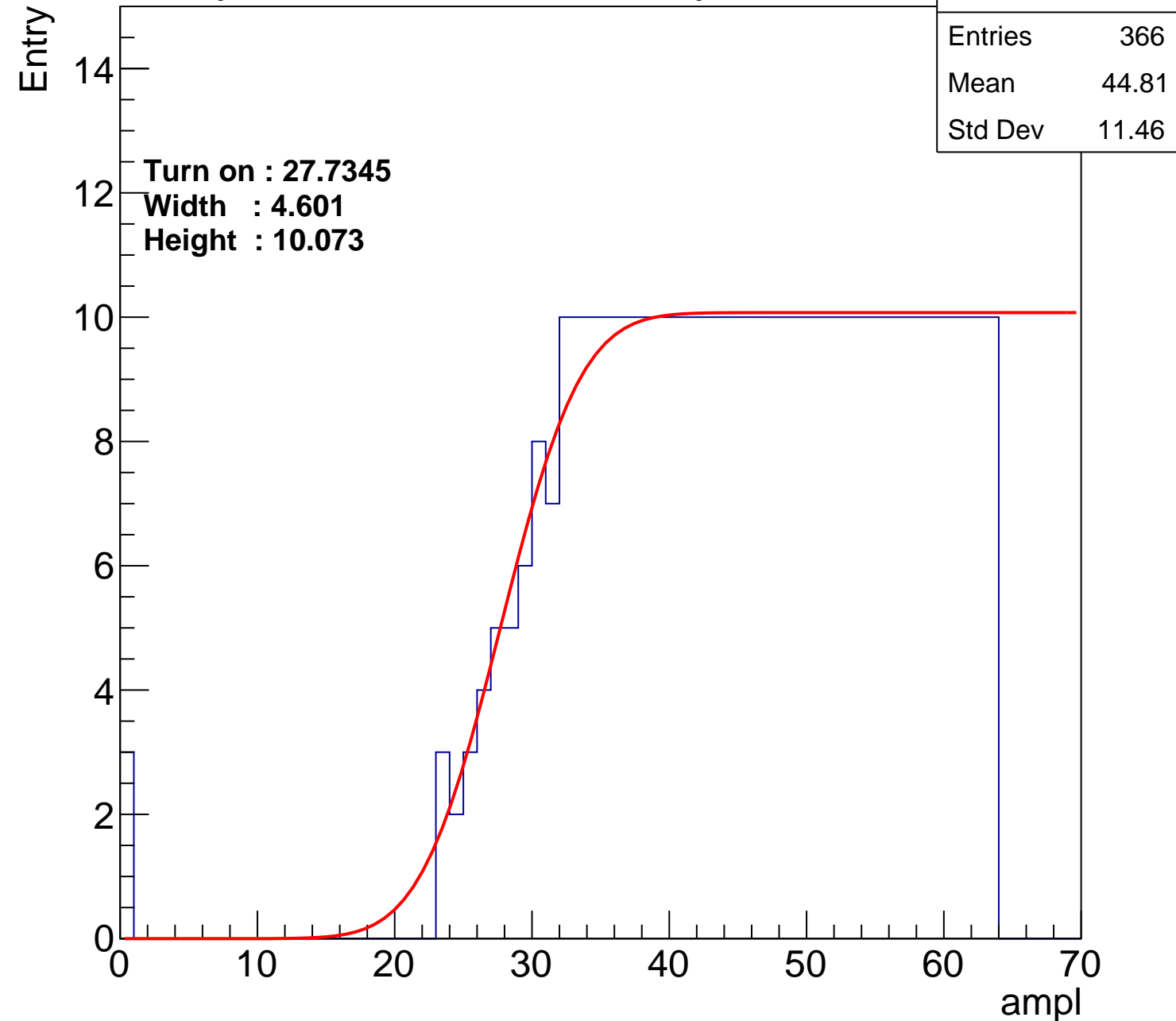
Width : 4.601

Height : 10.073

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch30

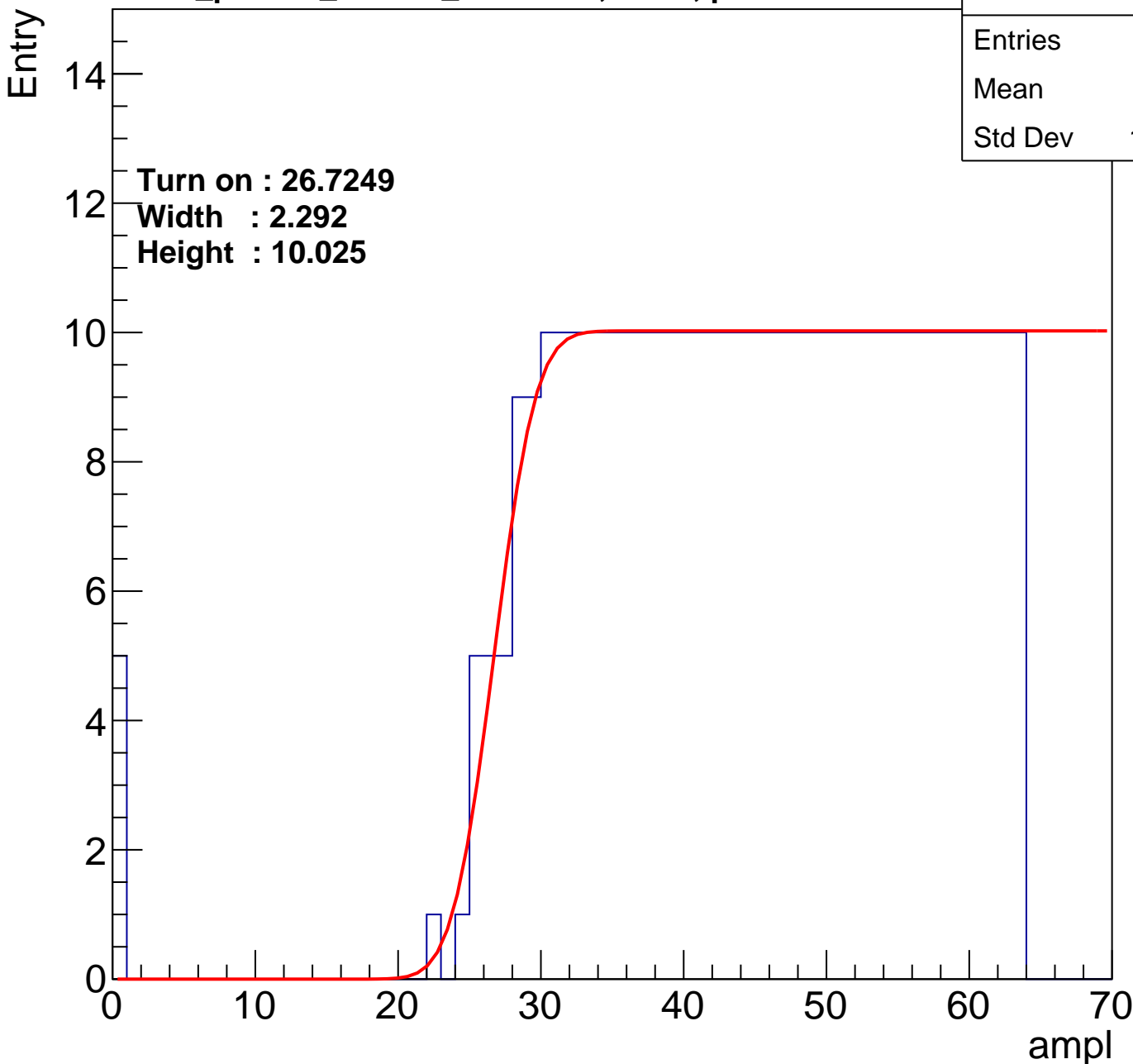
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	380
Mean	44.1
Std Dev	11.99

**Turn on : 26.7249**

**Width : 2.292**

**Height : 10.025**





# B0L002S, U19-ch31

calib\_packv5\_042523\_0143.root, FC#8, port C1

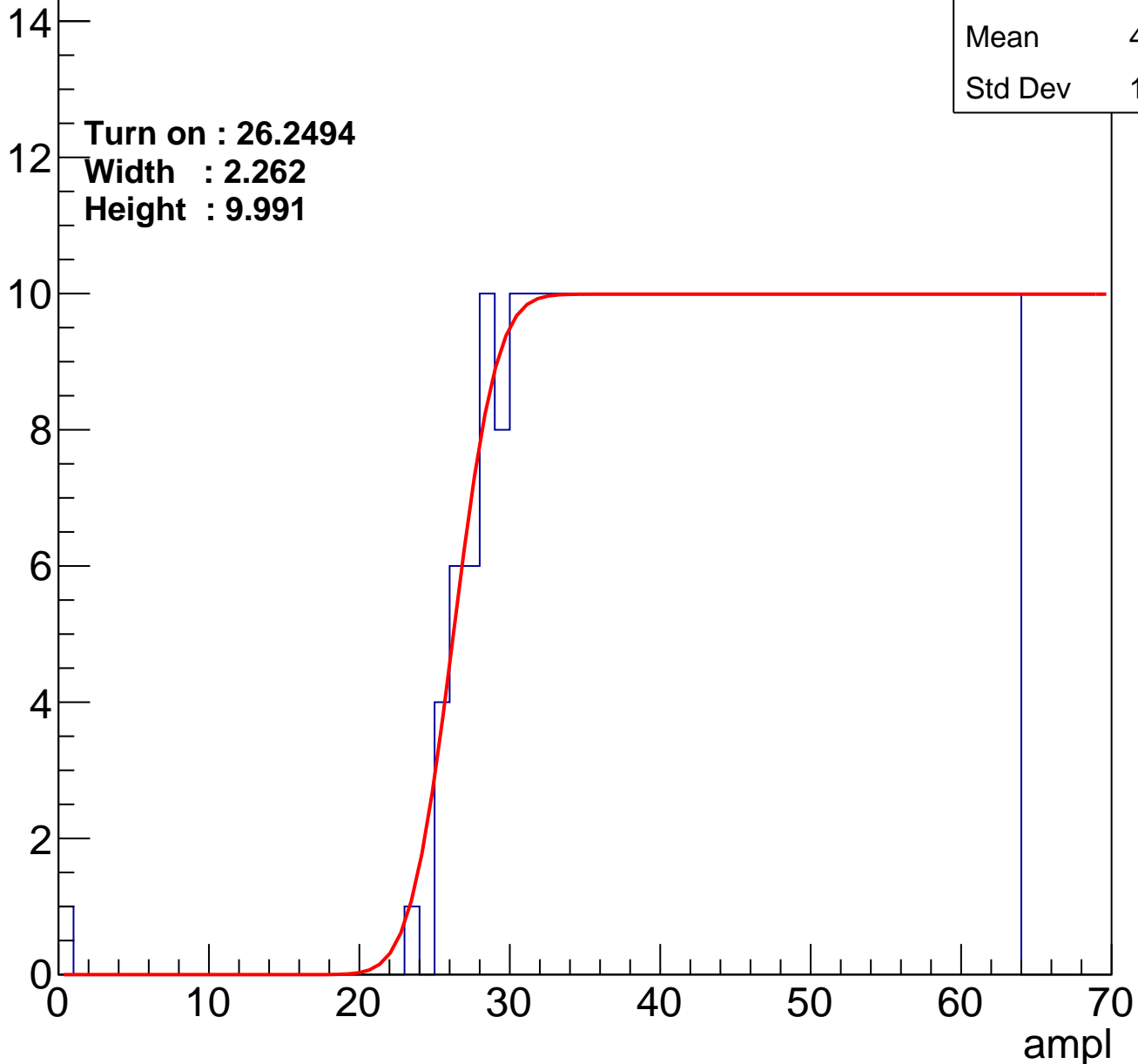
Entries	376
Mean	44.58
Std Dev	11.13

Turn on : 26.2494

Width : 2.262

Height : 9.991

Entry



# B0L002S, U19-ch32

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.61
Std Dev	11.34

**Turn on : 27.2047**

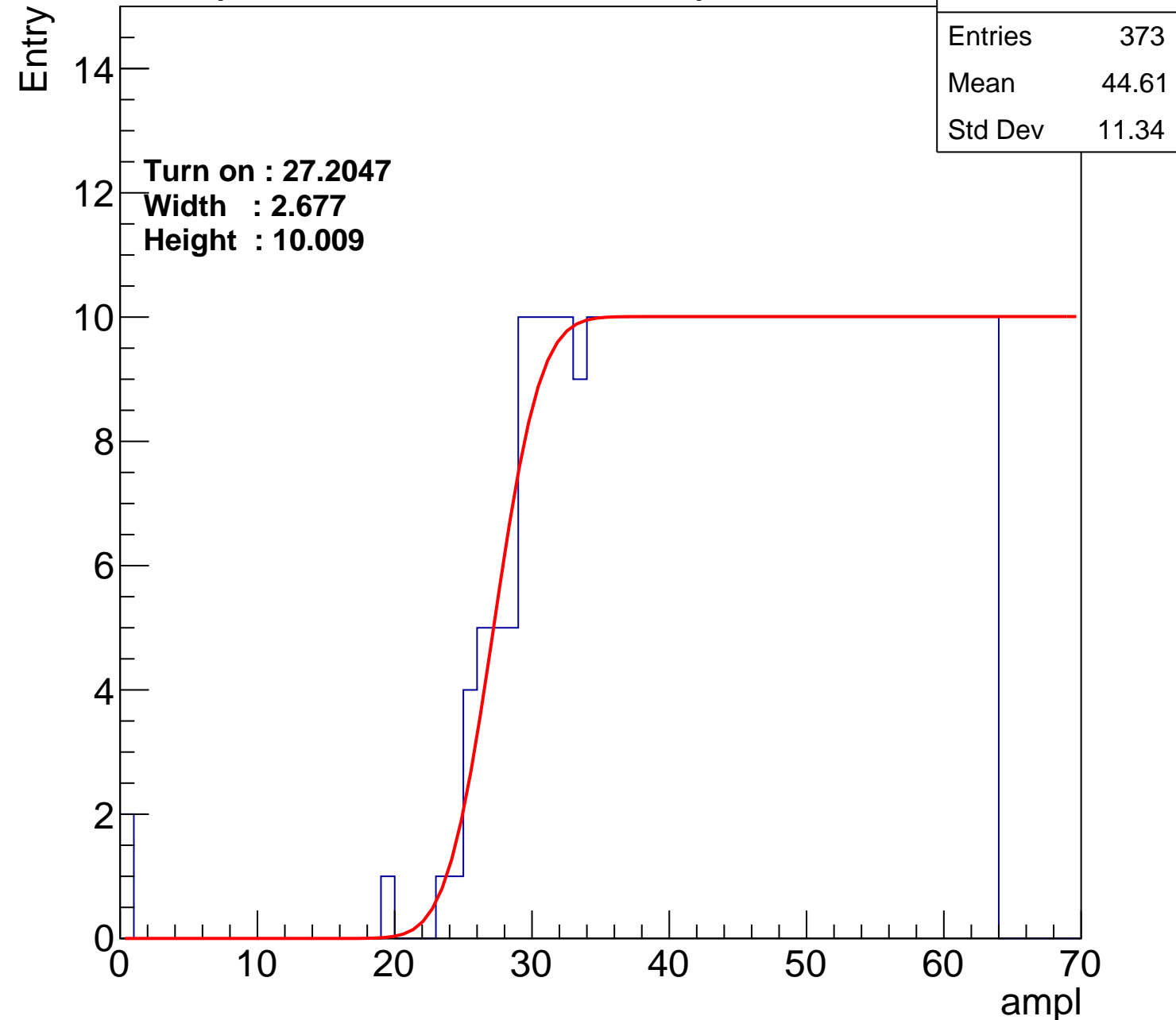
**Width : 2.677**

**Height : 10.009**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch33

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	372
Mean	44.65
Std Dev	11.32

Turn on : 27.1181

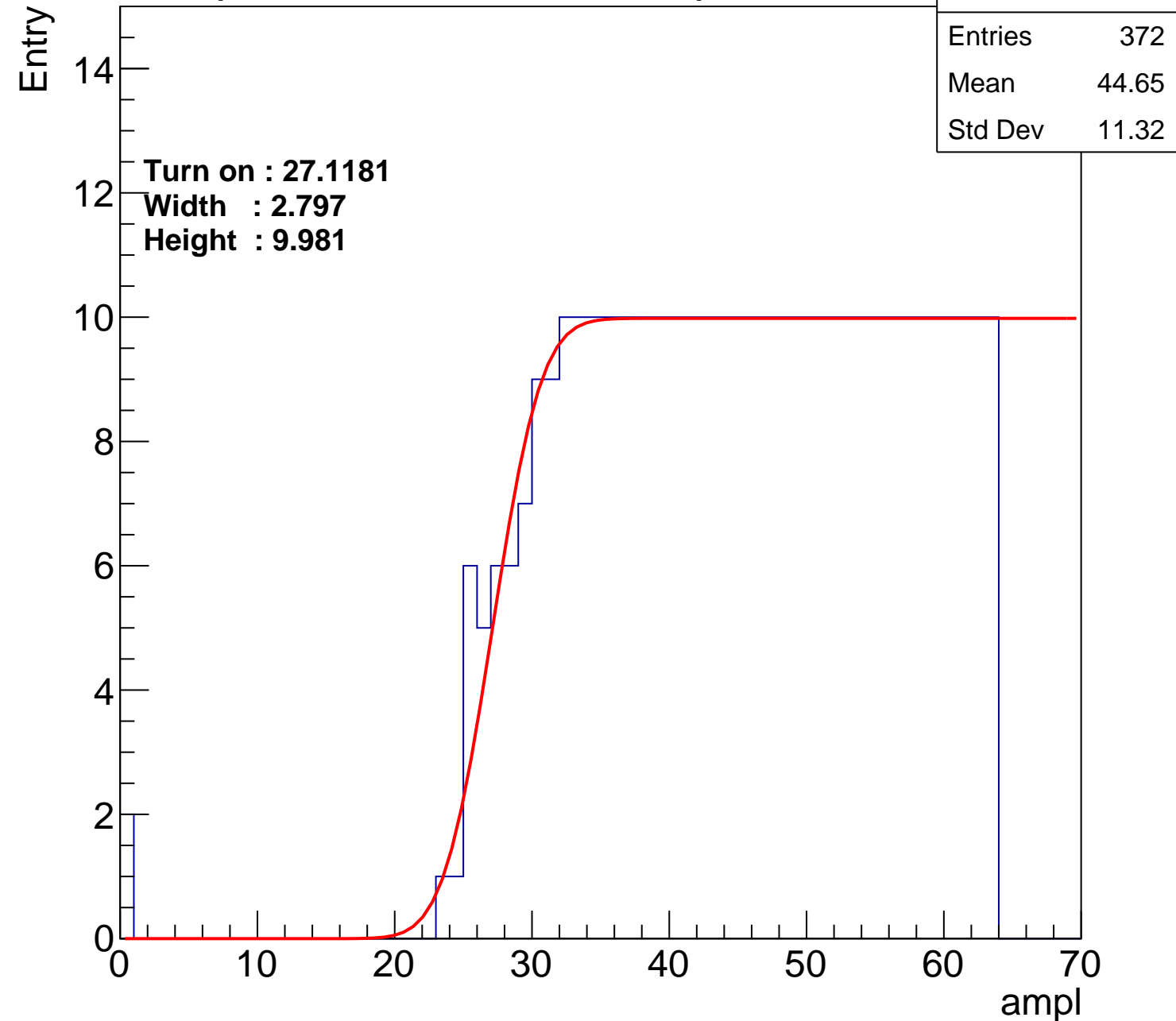
Width : 2.797

Height : 9.981

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch34

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	362
Mean	45.18
Std Dev	11.01

**Turn on : 28.3088**

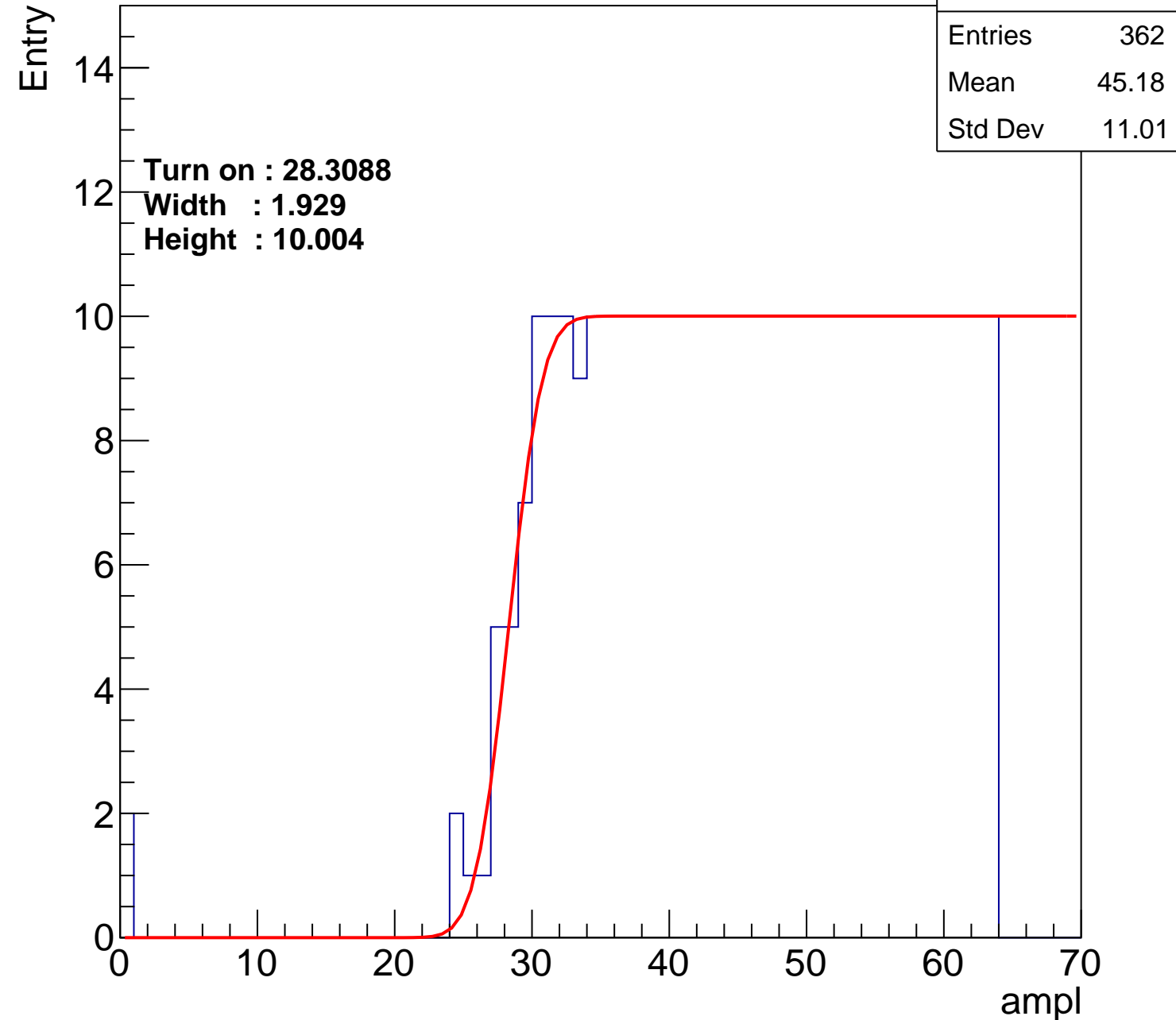
**Width : 1.929**

**Height : 10.004**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch35

calib\_packv5\_042523\_0143.root, FC#8, port C1

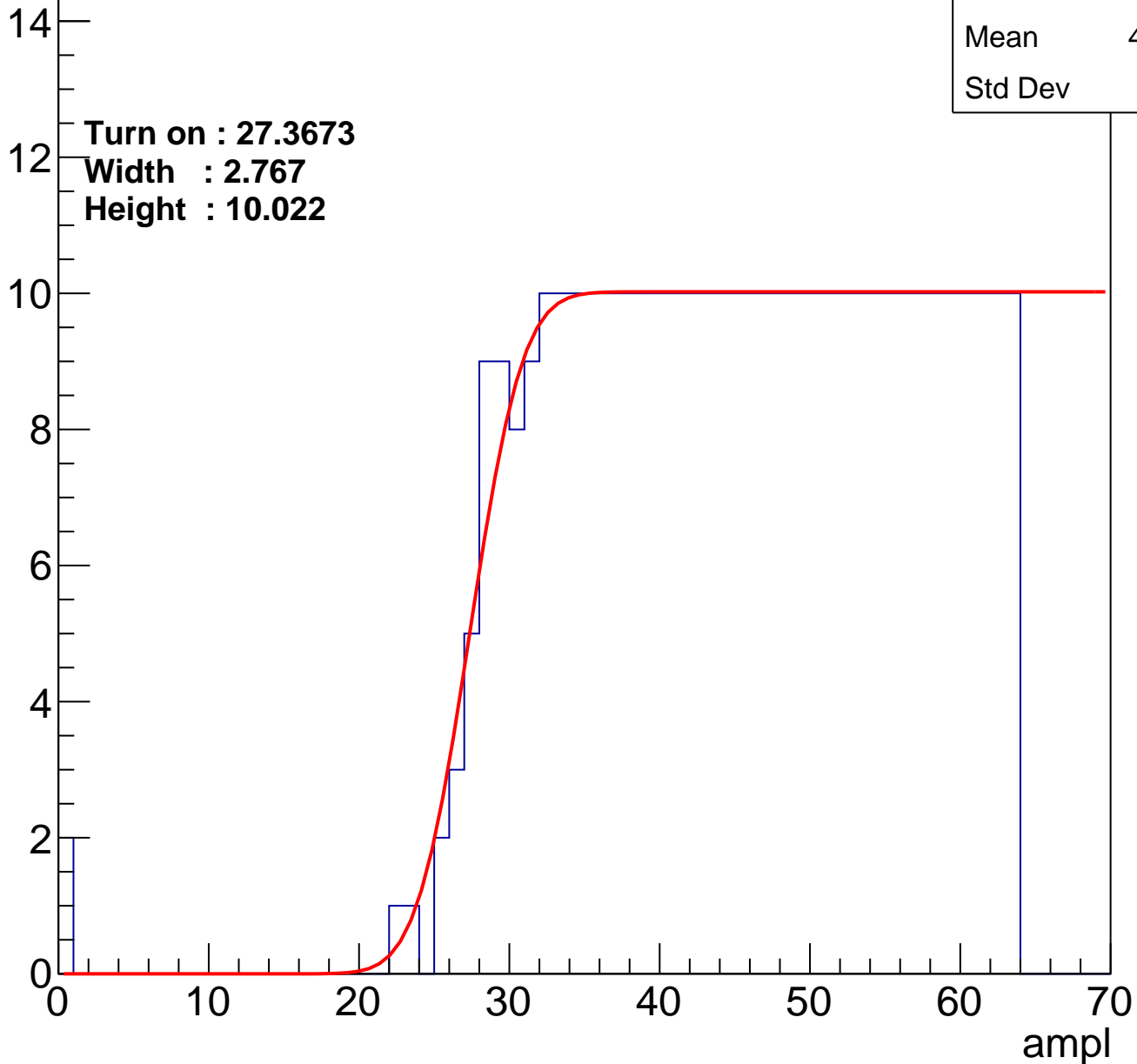
Entries	369
Mean	44.82
Std Dev	11.2

Turn on : 27.3673

Width : 2.767

Height : 10.022

Entry



# B0L002S, U19-ch36

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.57
Std Dev	11.33

**Turn on : 26.8683**

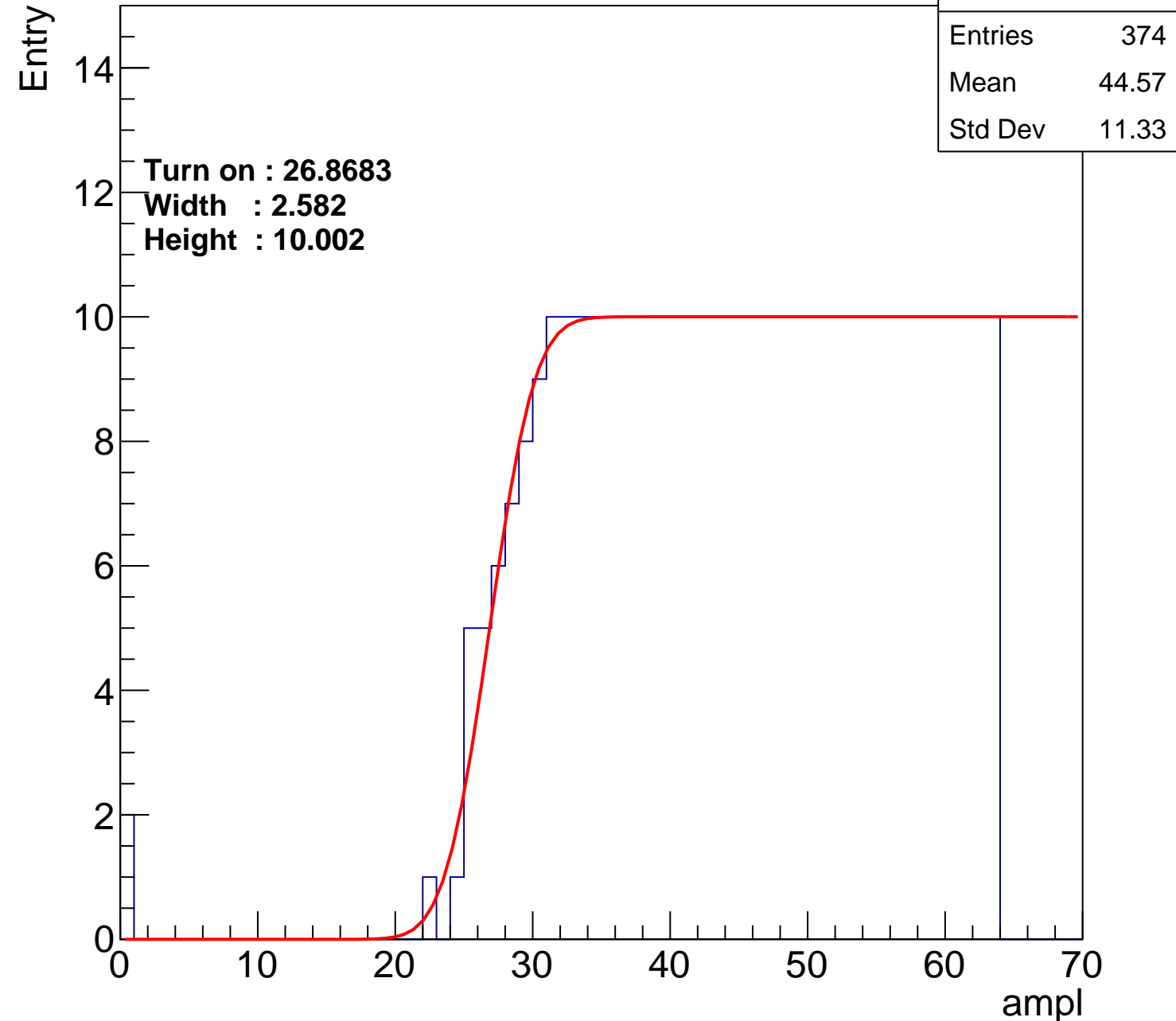
**Width : 2.582**

**Height : 10.002**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch37

calib\_packv5\_042523\_0143.root, FC#8, port C1

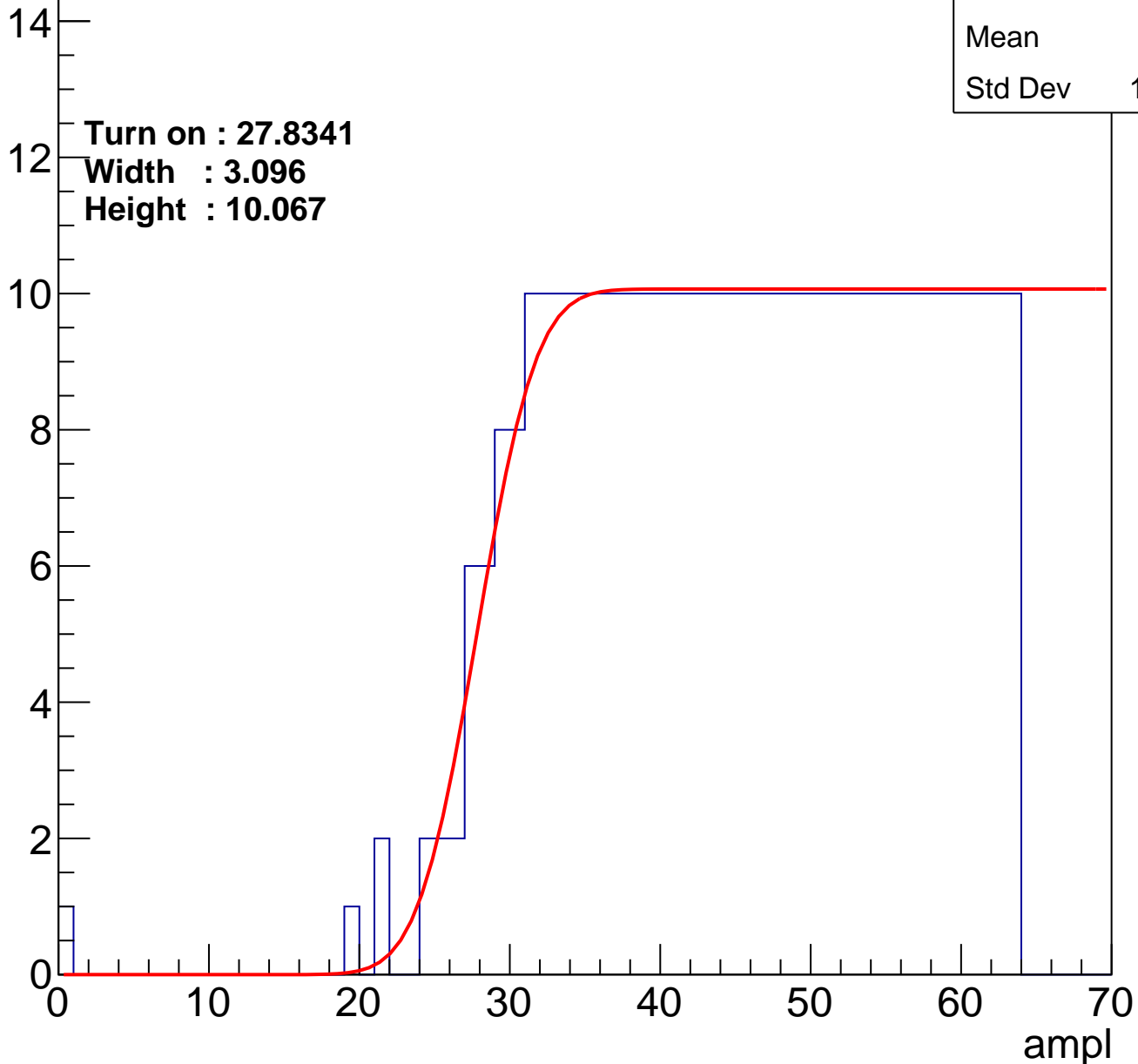
Entries	368
Mean	44.9
Std Dev	11.06

Turn on : 27.8341

Width : 3.096

Height : 10.067

Entry



# B0L002S, U19-ch38

calib\_packv5\_042523\_0143.root, FC#8, port C1

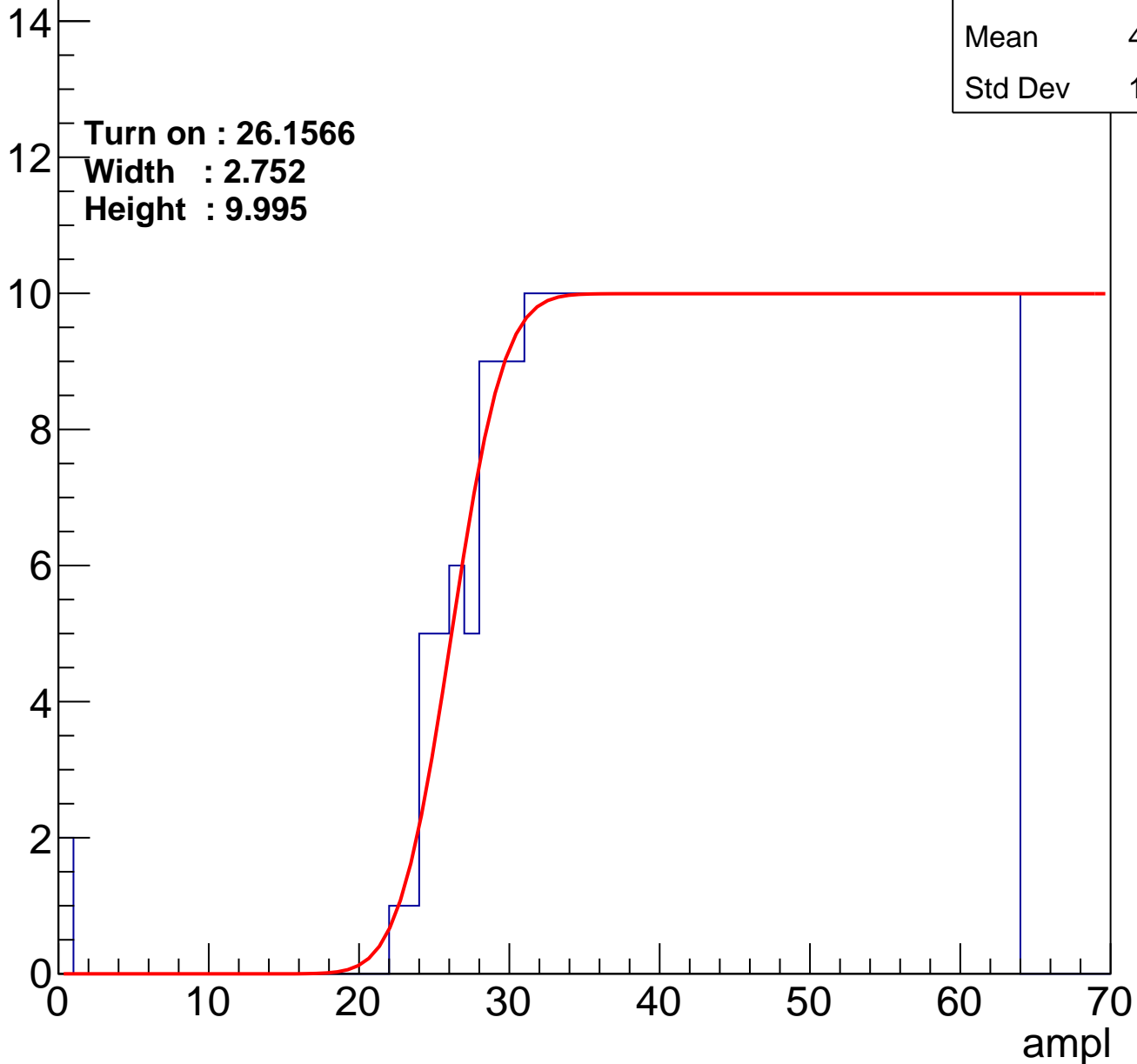
Entries	382
Mean	44.17
Std Dev	11.55

Turn on : 26.1566

Width : 2.752

Height : 9.995

Entry





# B0L002S, U19-ch39

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.75
Std Dev	11.27

Turn on : 26.7550

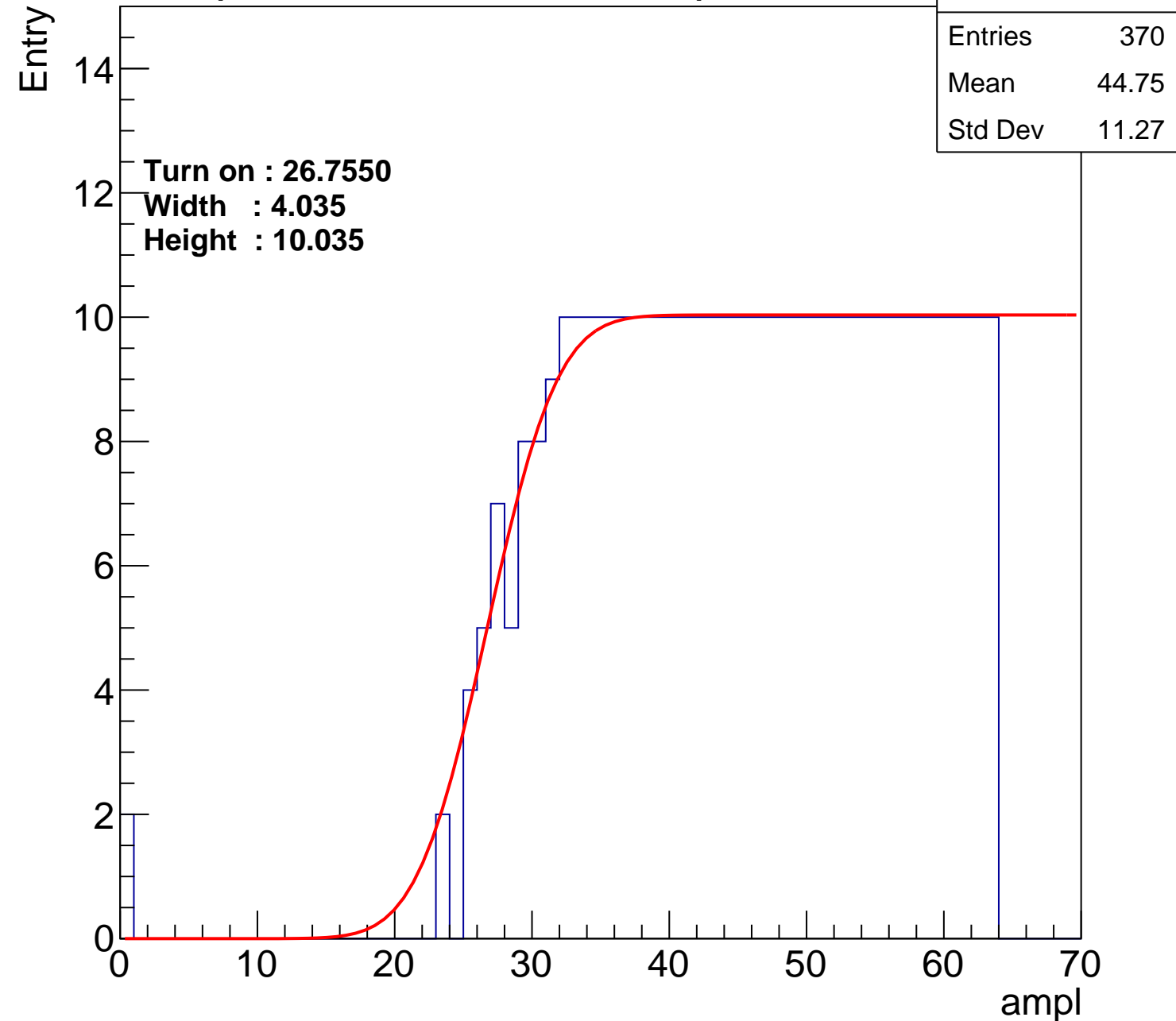
Width : 4.035

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch40

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.49
Std Dev	11.23

Turn on : 26.2777

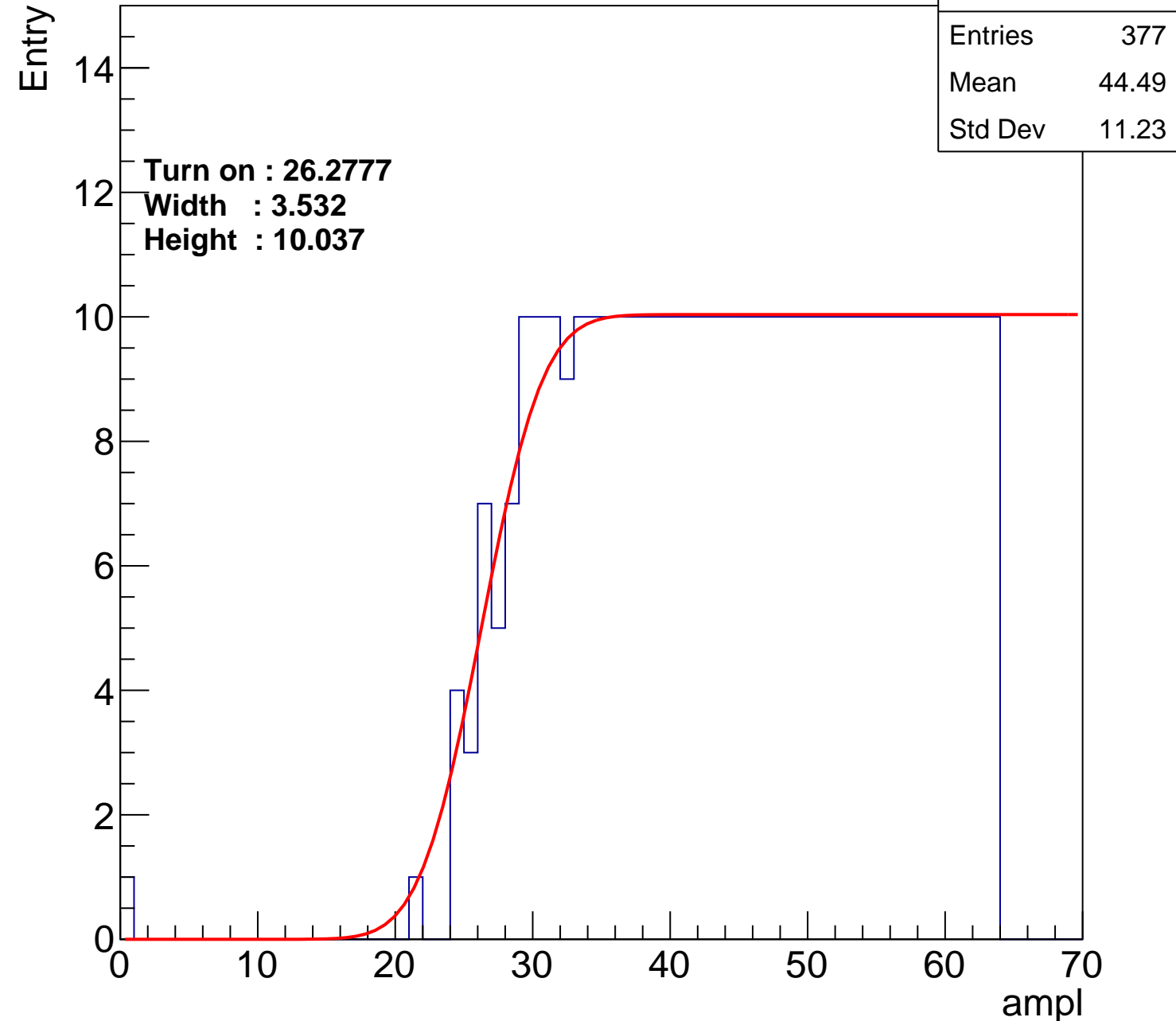
Width : 3.532

Height : 10.037

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch41

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	360
Mean	45.33
Std Dev	10.78

**Turn on : 28.6883**

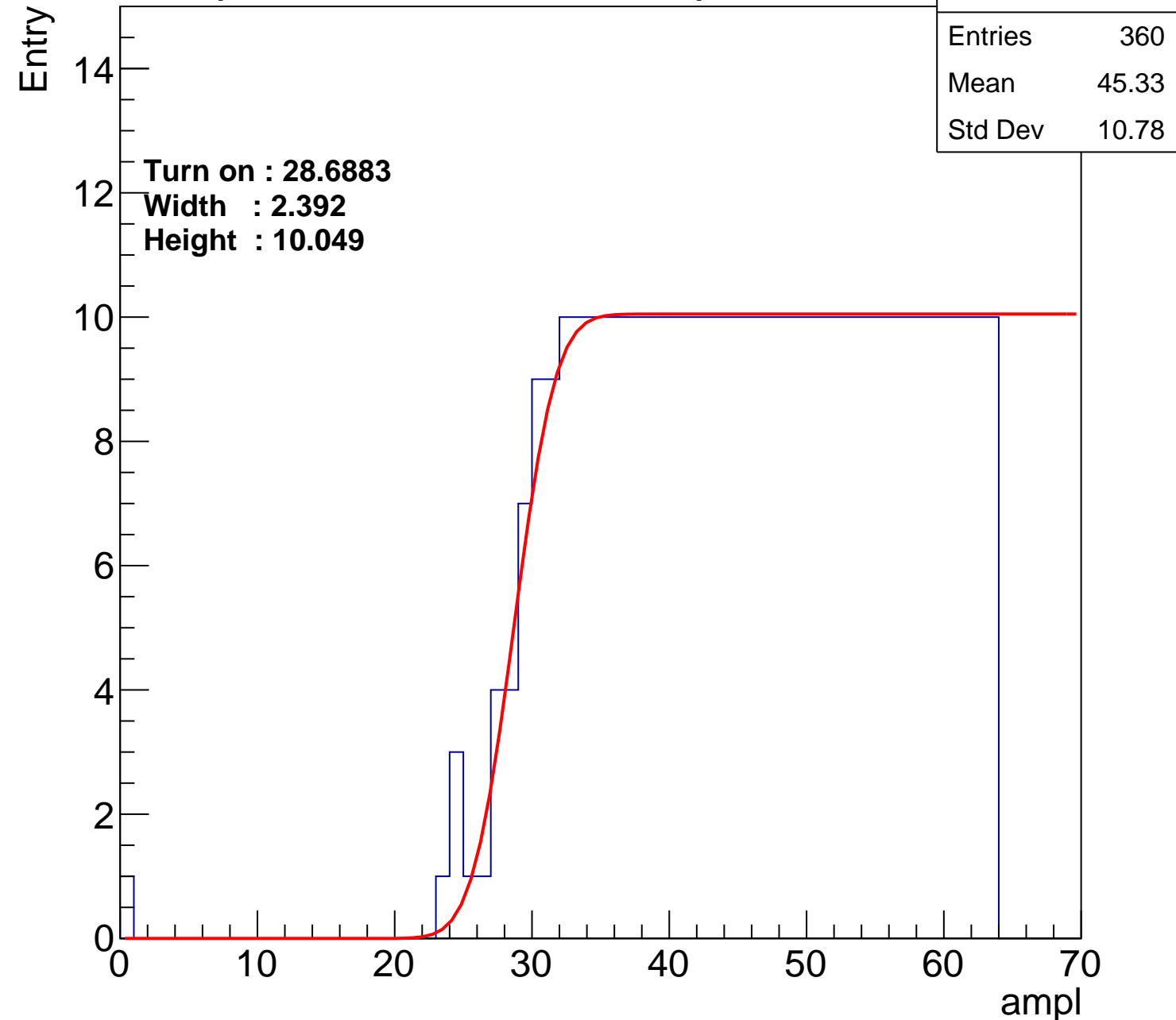
**Width : 2.392**

**Height : 10.049**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch42

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.59
Std Dev	11.37

Turn on : 27.2923

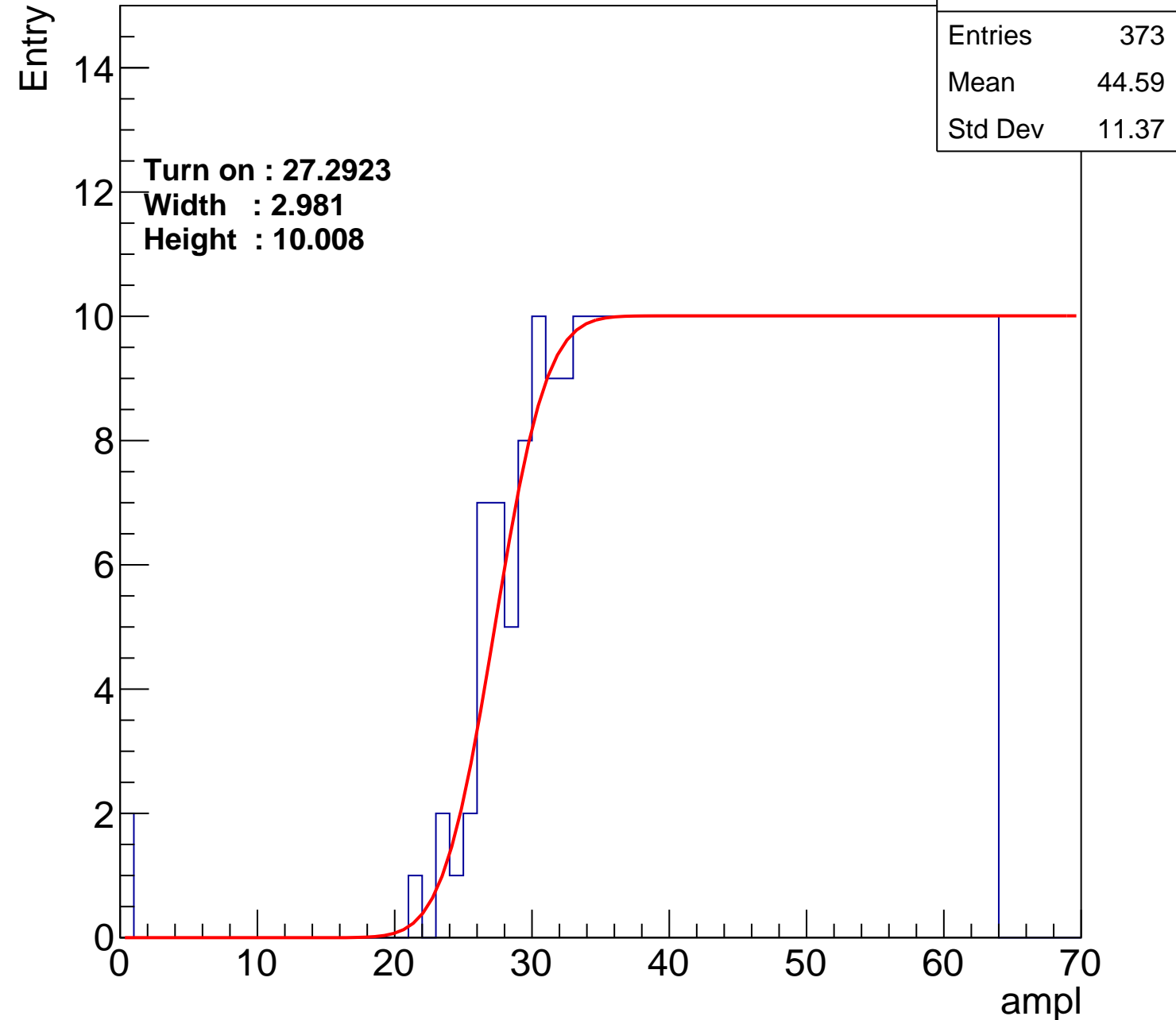
Width : 2.981

Height : 10.008

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch43

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.47
Std Dev	10.71

Turn on : 29.2219

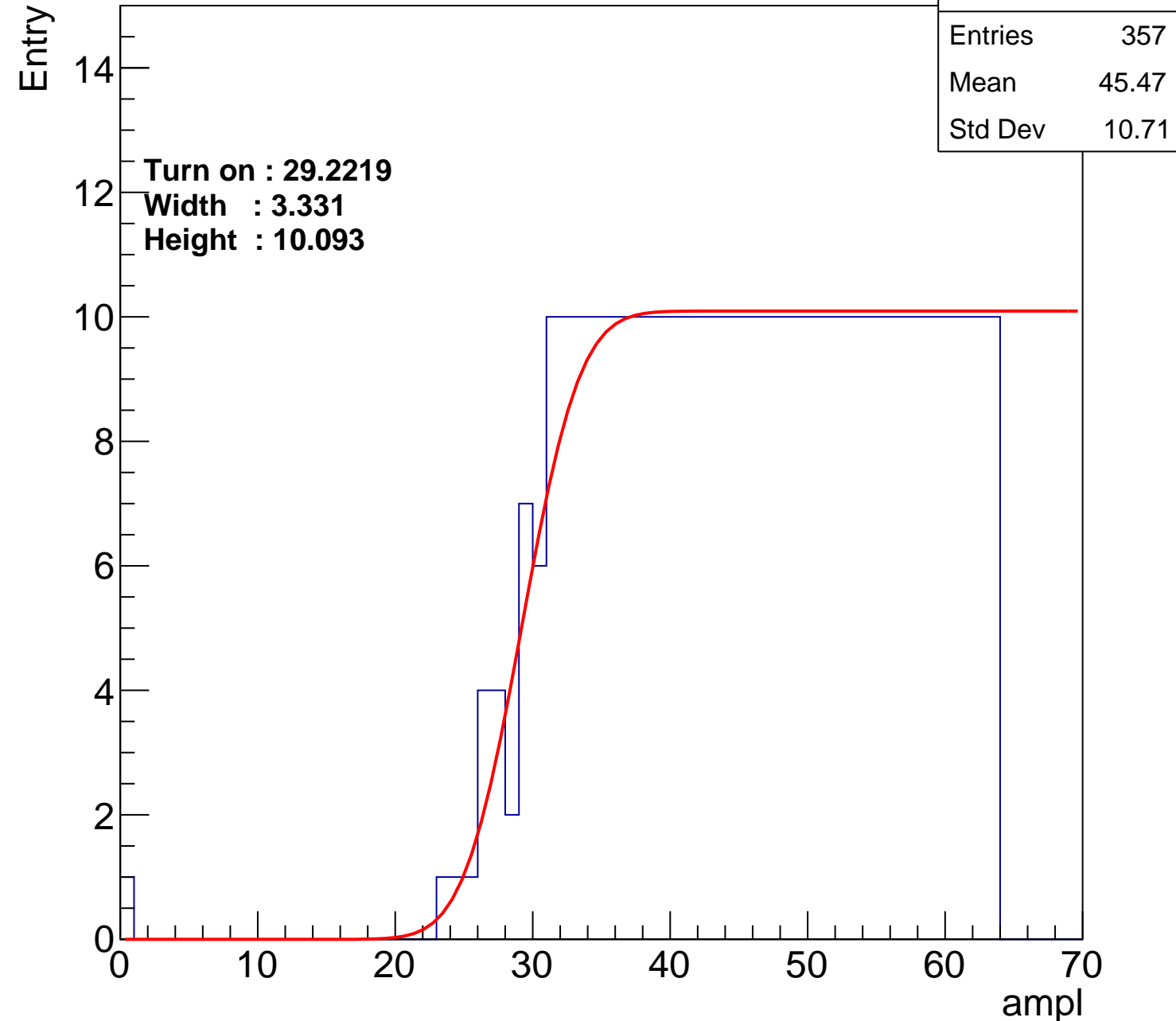
Width : 3.331

Height : 10.093

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch44

calib\_packv5\_042523\_0143.root, FC#8, port C1

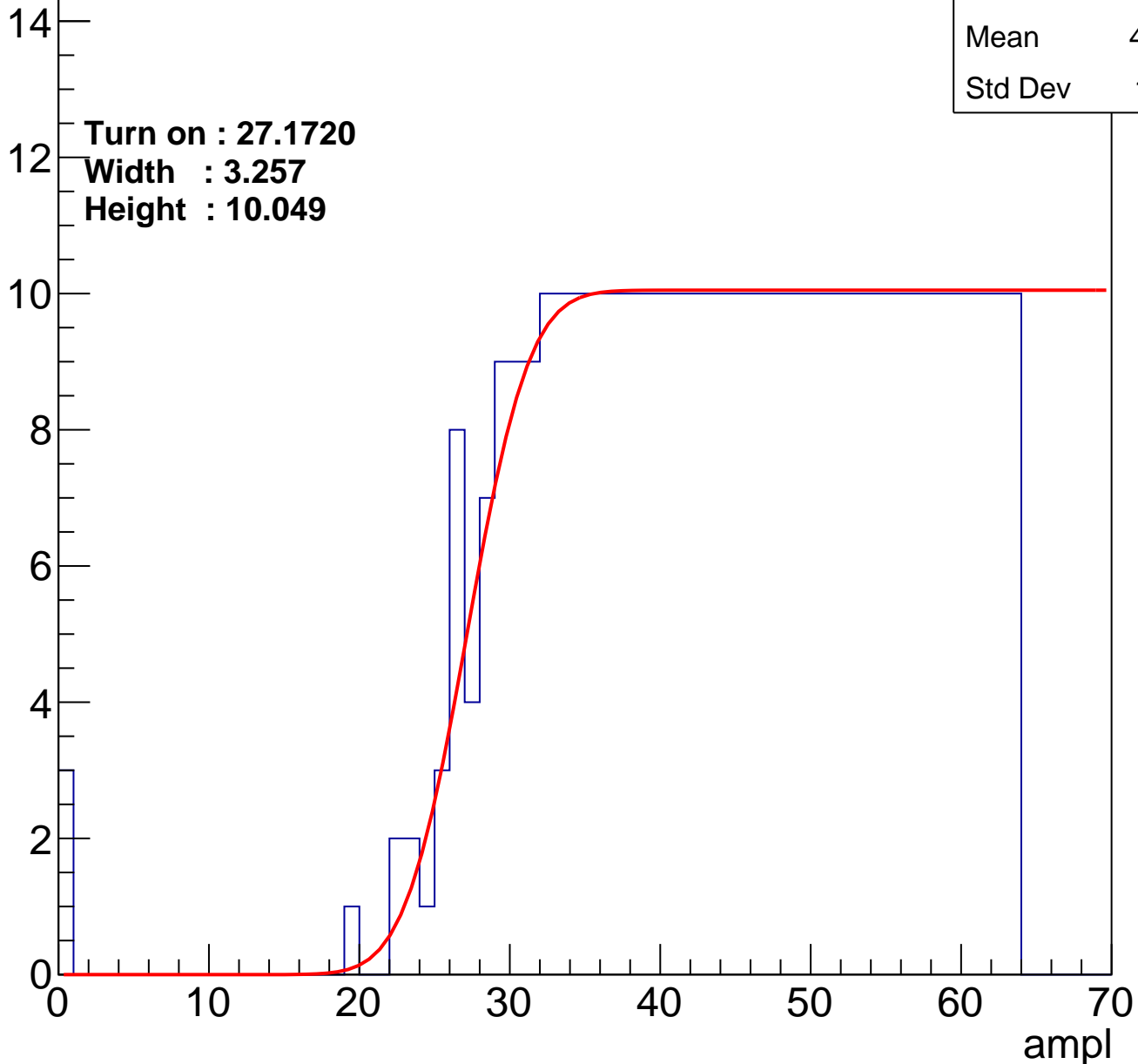
Entries	378
Mean	44.26
Std Dev	11.71

Turn on : 27.1720

Width : 3.257

Height : 10.049

Entry



# B0L002S, U19-ch45

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	359
Mean	45.41
Std Dev	10.7

Turn on : 28.5836

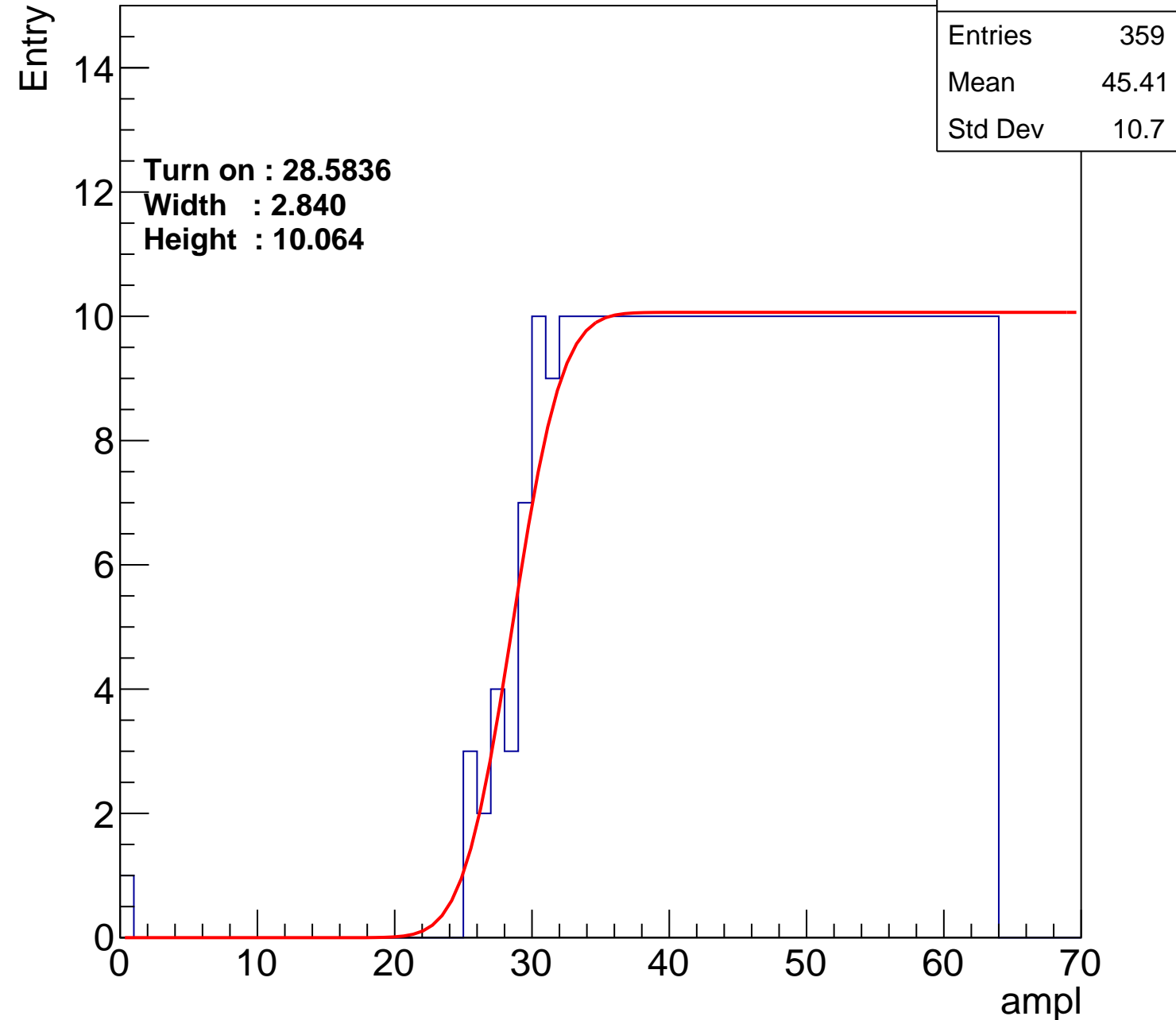
Width : 2.840

Height : 10.064

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch46

calib\_packv5\_042523\_0143.root, FC#8, port C1

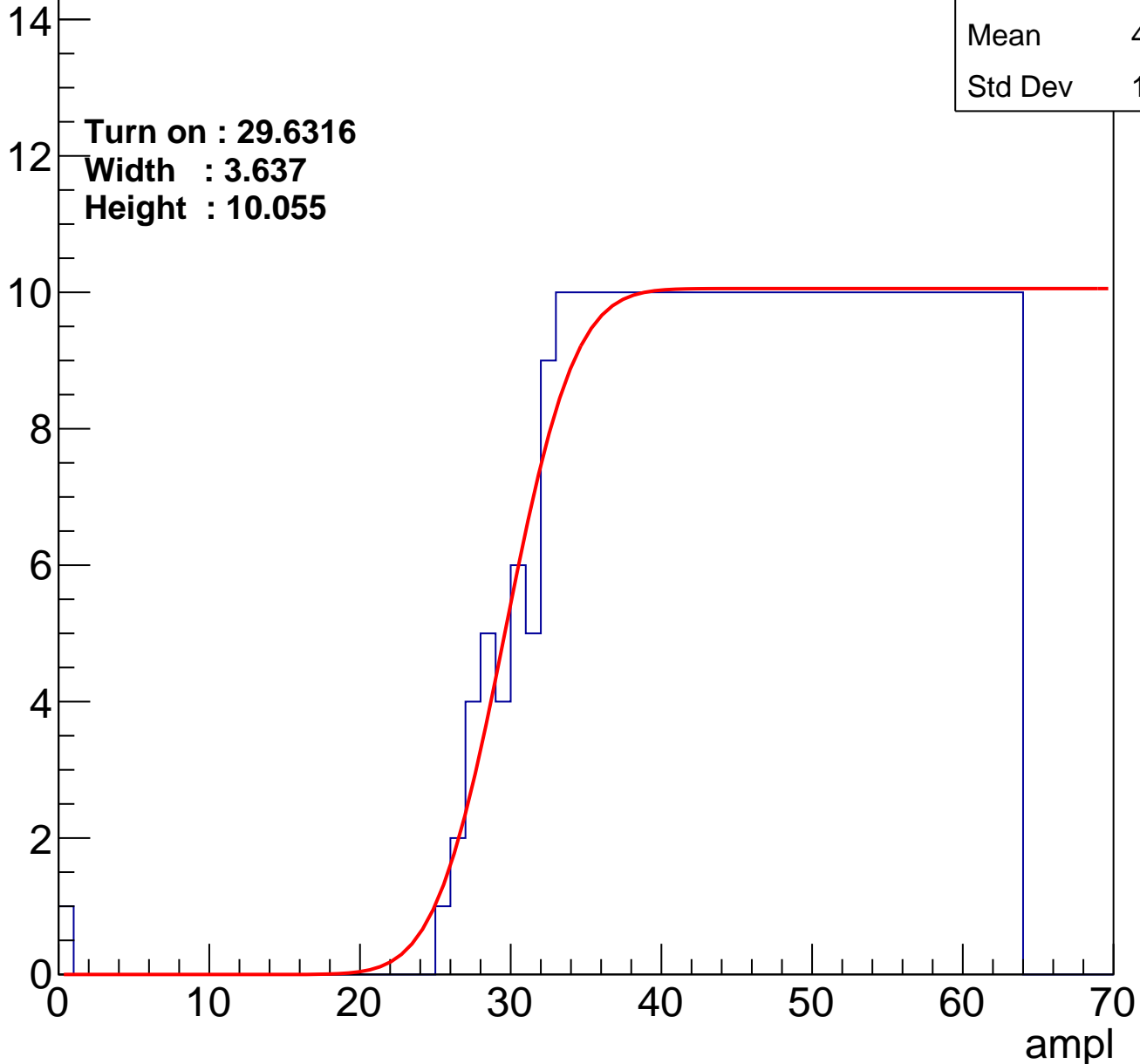
Entries	347
Mean	45.95
Std Dev	10.47

Turn on : 29.6316

Width : 3.637

Height : 10.055

Entry





# B0L002S, U19-ch47

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.42
Std Dev	10.89

**Turn on : 28.3703**

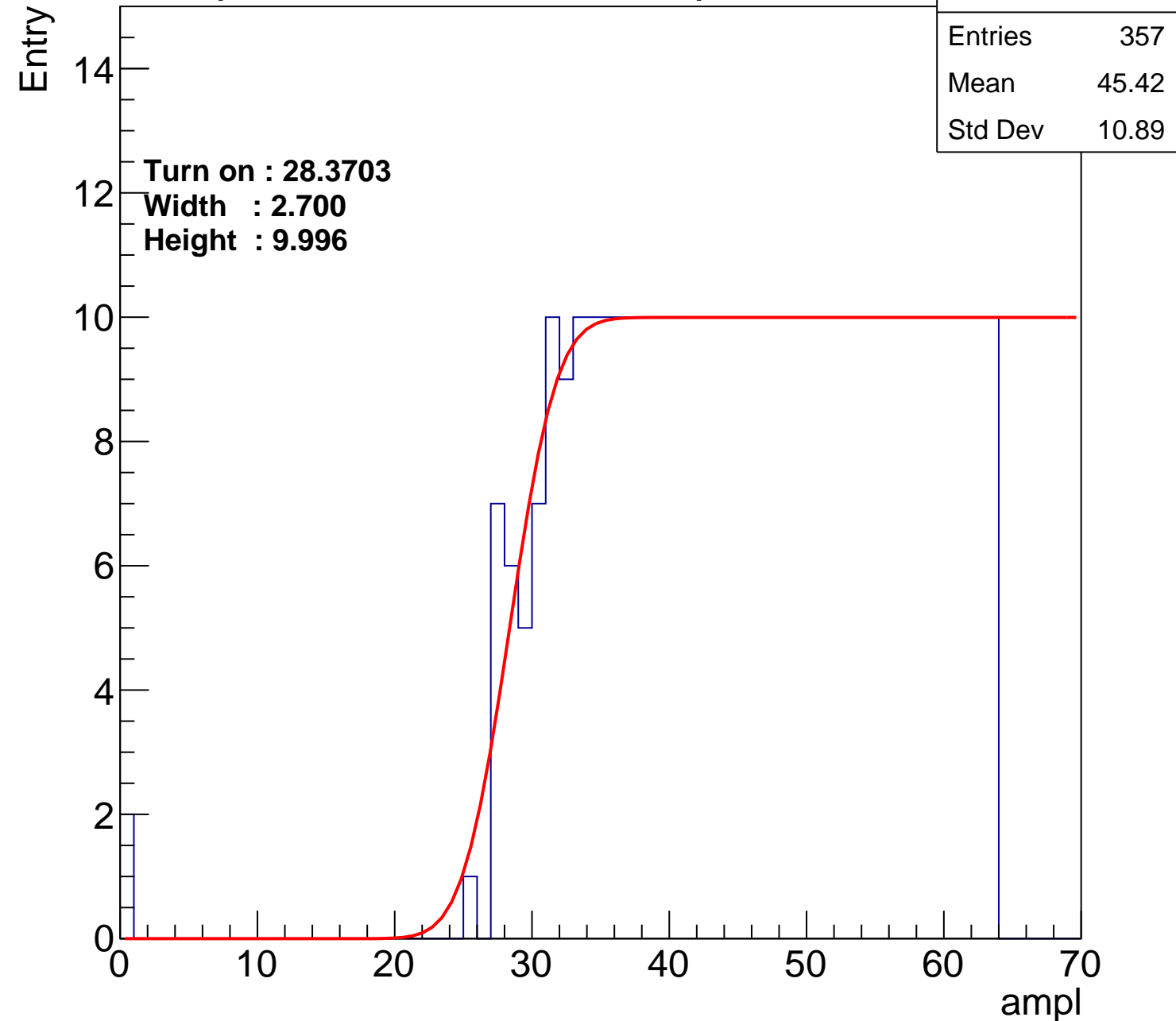
**Width : 2.700**

**Height : 9.996**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch48

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.7
Std Dev	11.62

Turn on : 28.2009

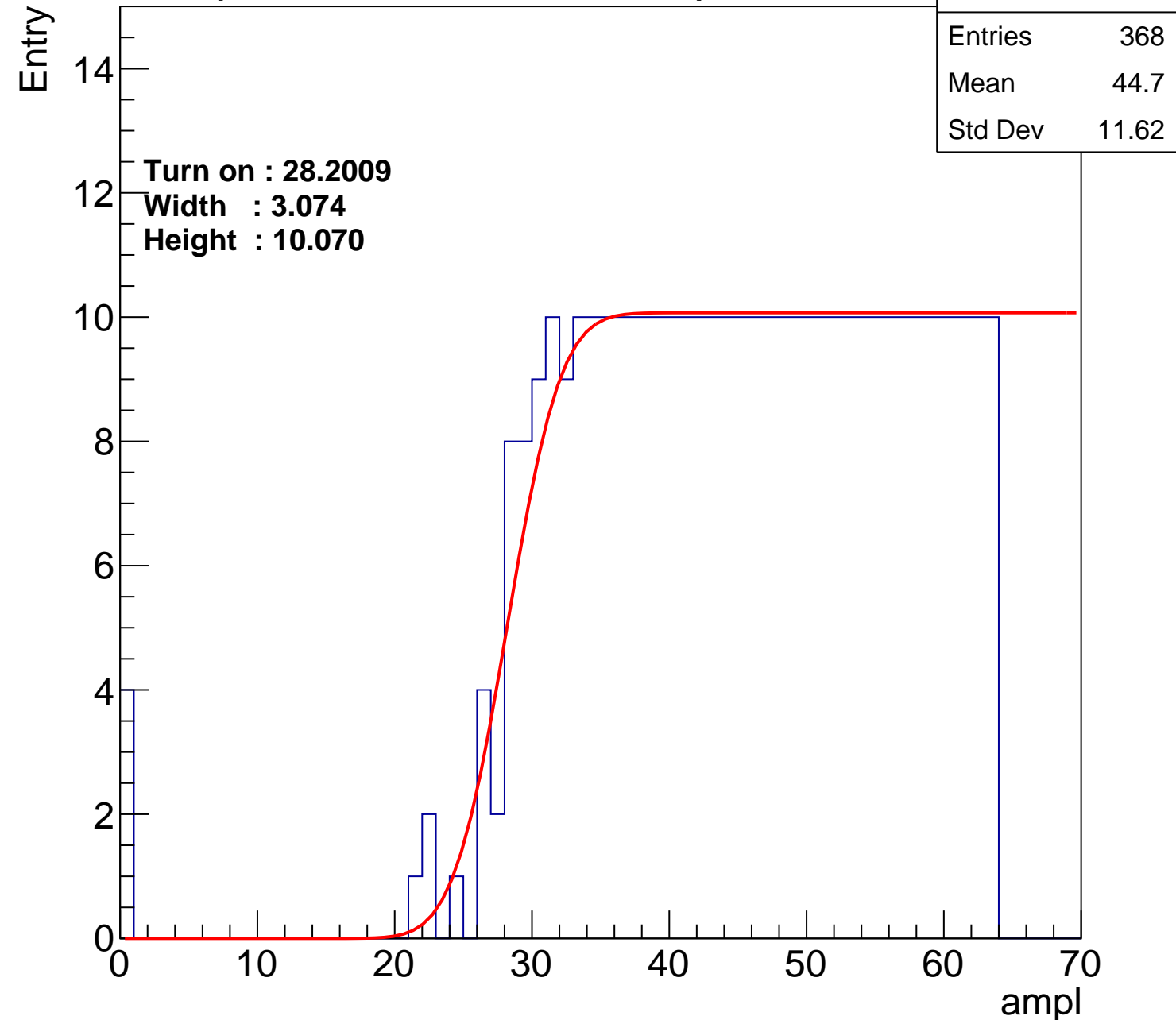
Width : 3.074

Height : 10.070

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch49

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	385
Mean	44.02
Std Dev	11.65

**Turn on : 25.9879**

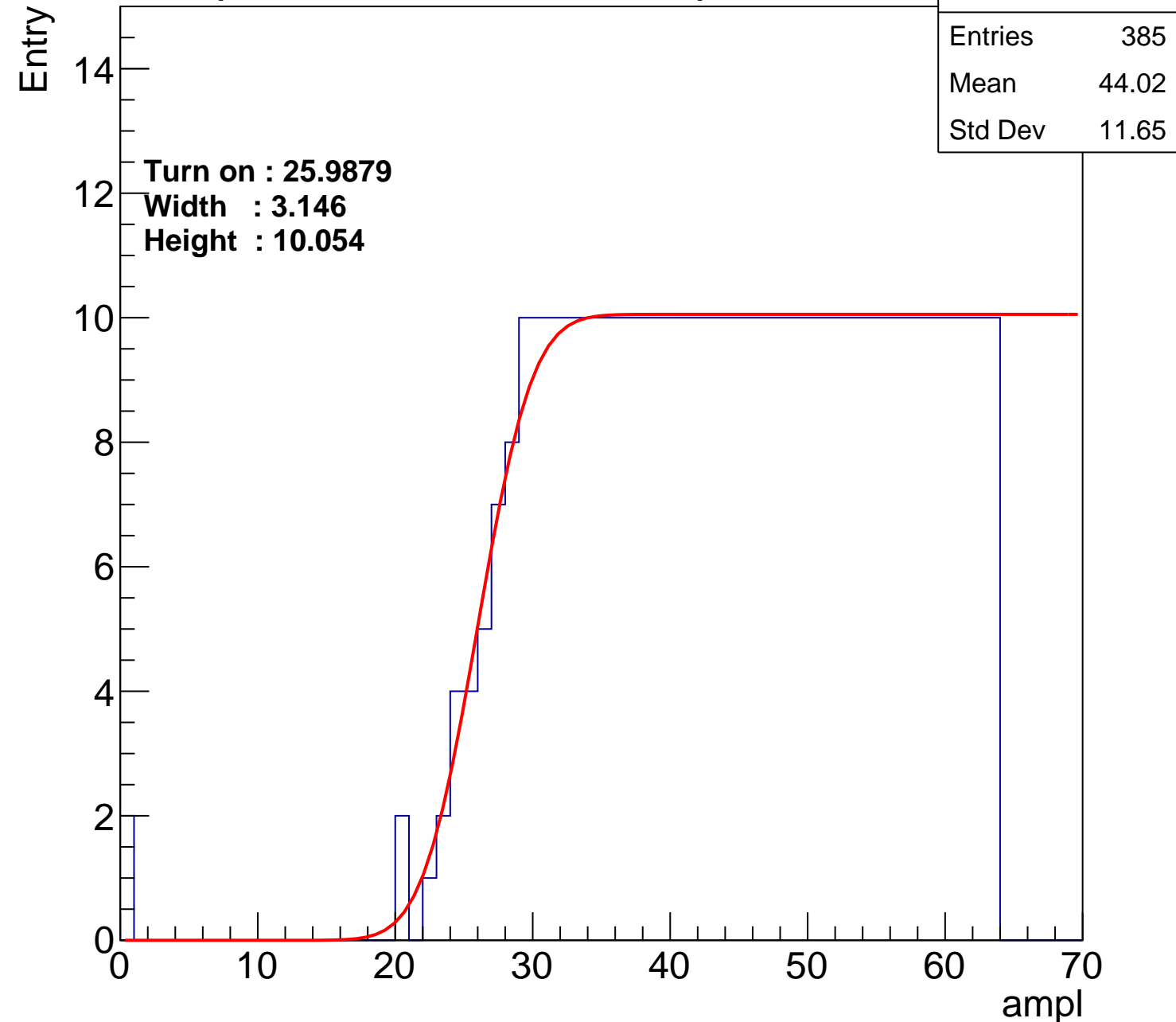
**Width : 3.146**

**Height : 10.054**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch50

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	369
Mean	44.8
Std Dev	11.25

Turn on : 27.8921

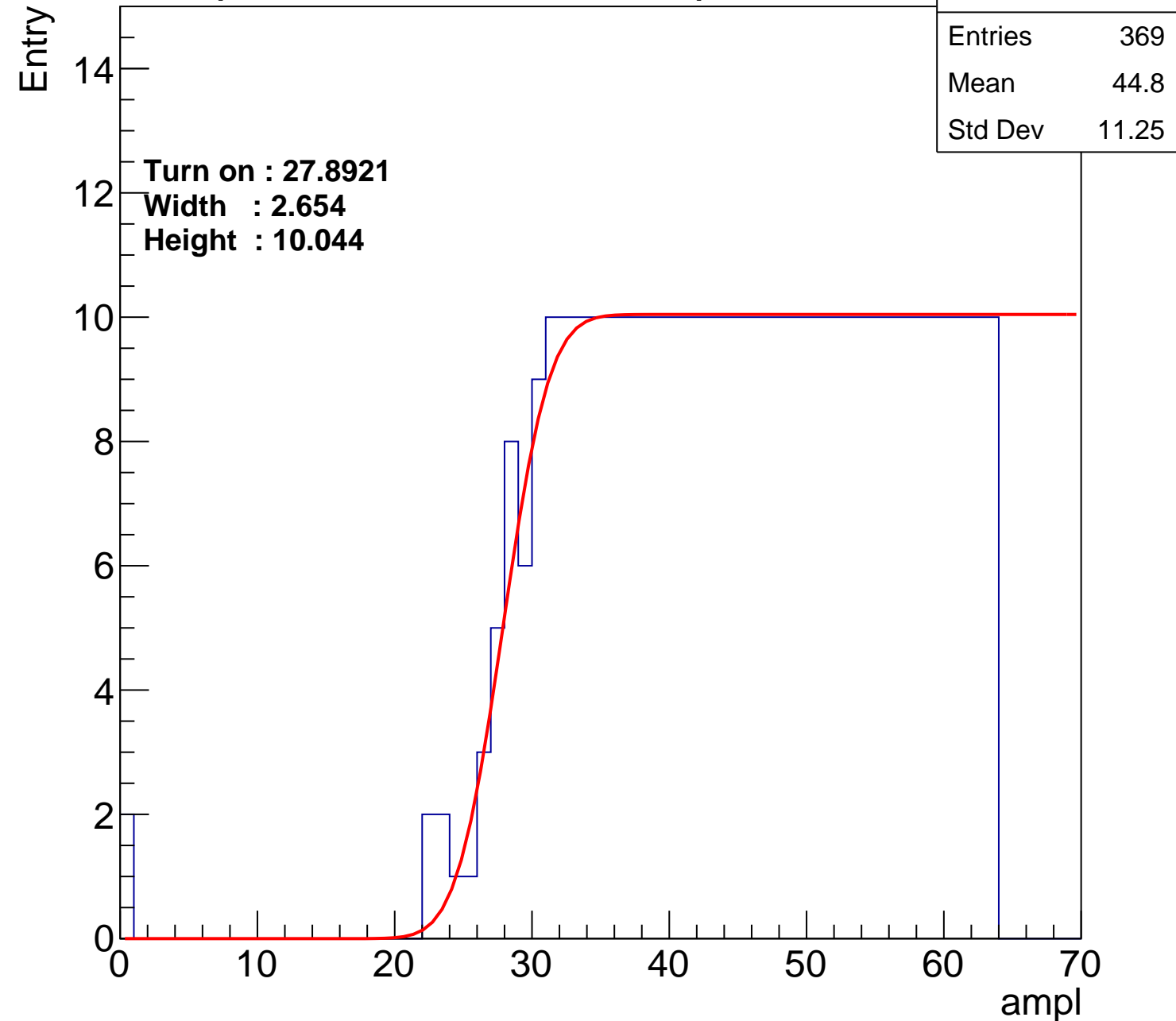
Width : 2.654

Height : 10.044

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch51

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	396
Mean	43.43
Std Dev	12.06

**Turn on : 24.7544**

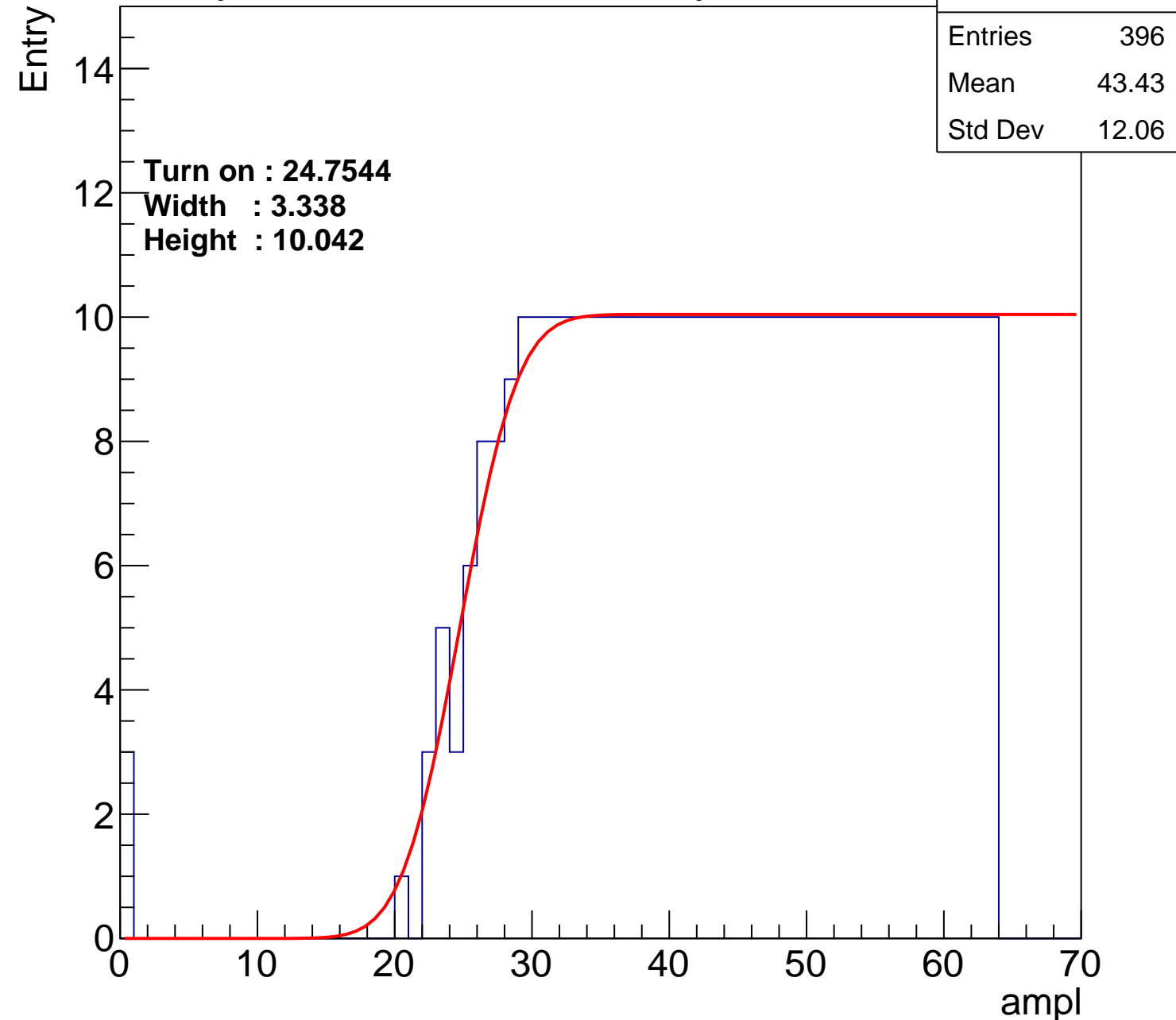
**Width : 3.338**

**Height : 10.042**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch52

calib\_packv5\_042523\_0143.root, FC#8, port C1

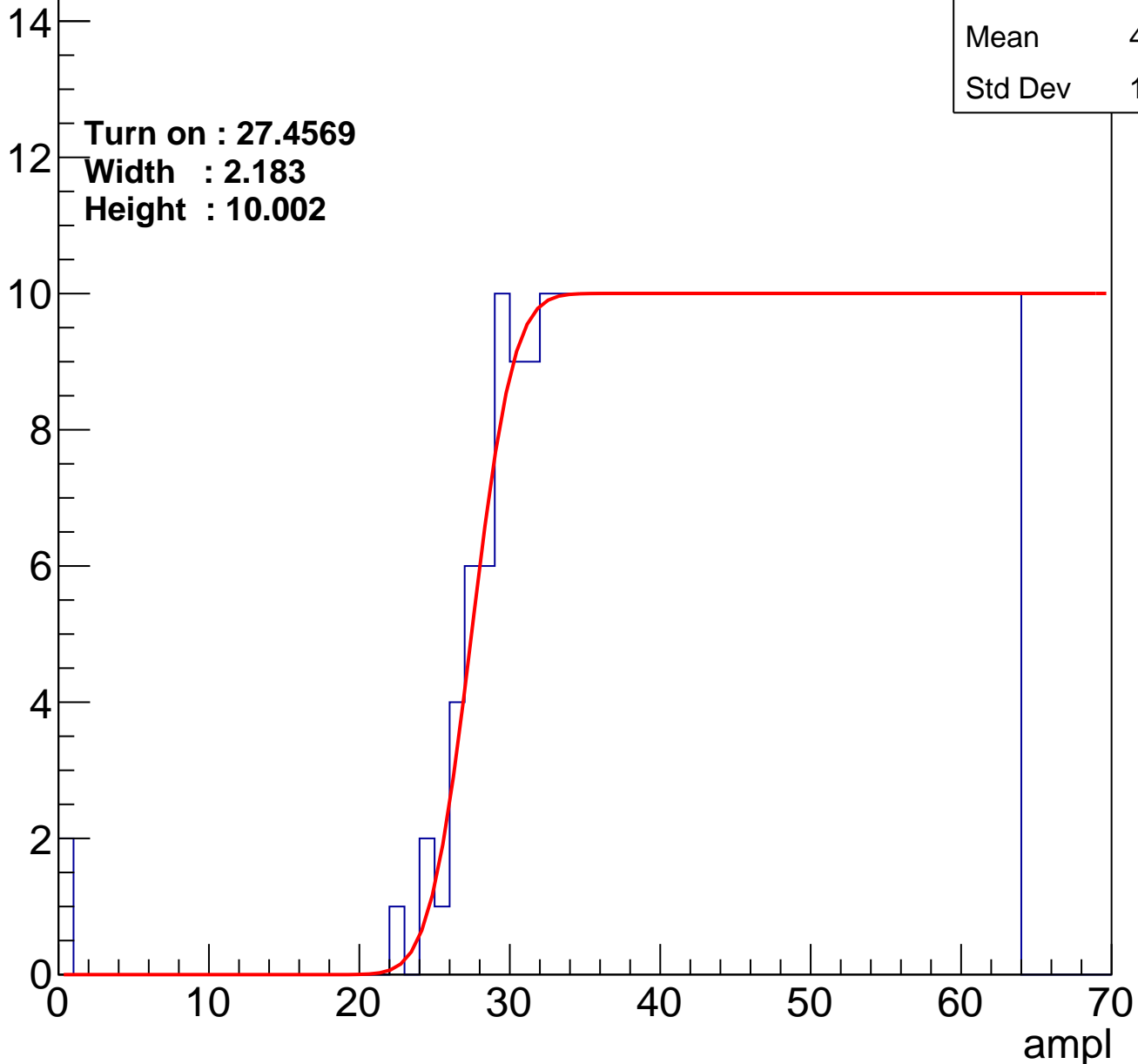
Entries	370
Mean	44.78
Std Dev	11.22

Turn on : 27.4569

Width : 2.183

Height : 10.002

Entry



# B0L002S, U19-ch53

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	44.83
Std Dev	11.52

Turn on : 27.9120

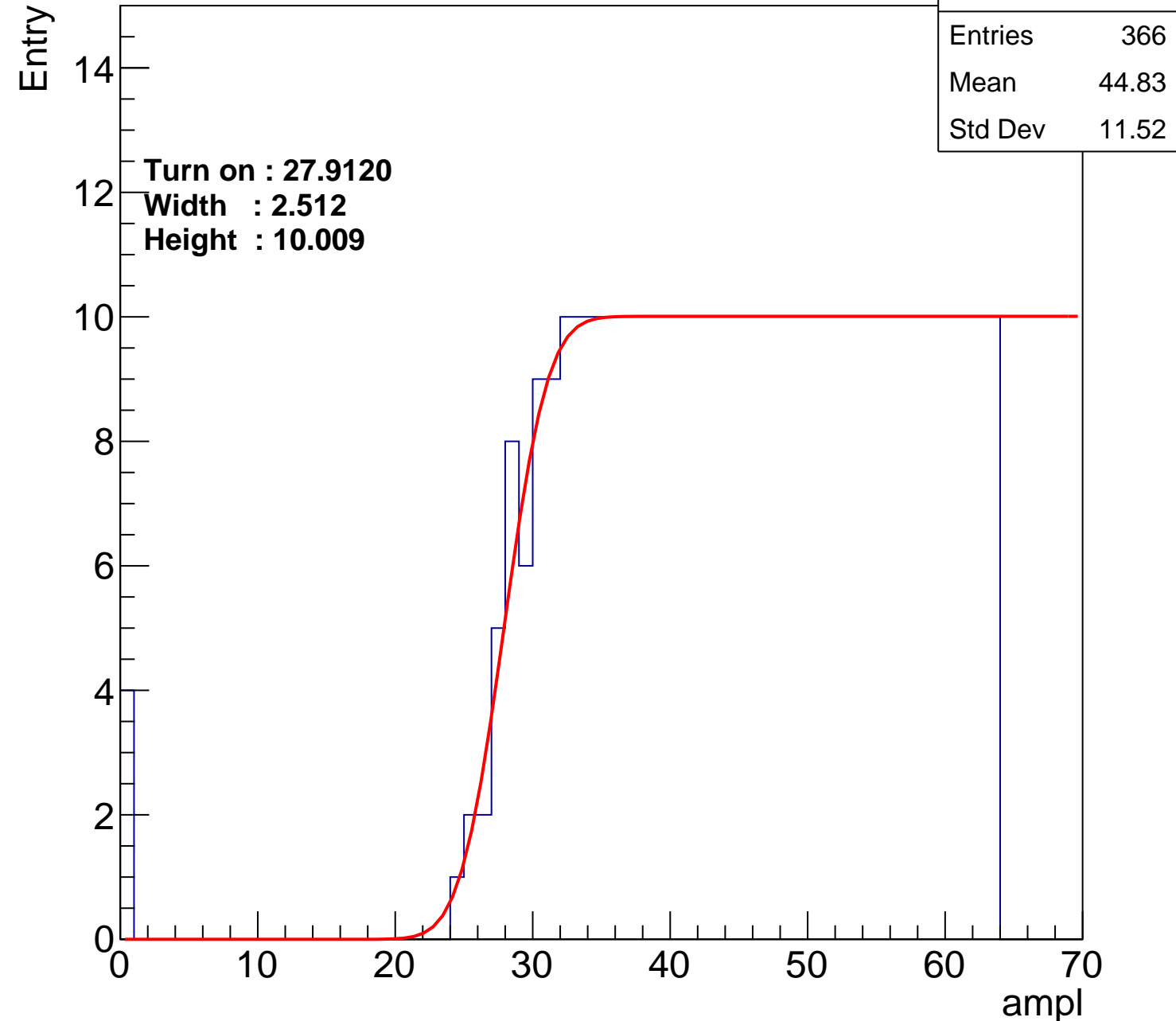
Width : 2.512

Height : 10.009

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch54

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.69
Std Dev	11.63

Turn on : 27.7457

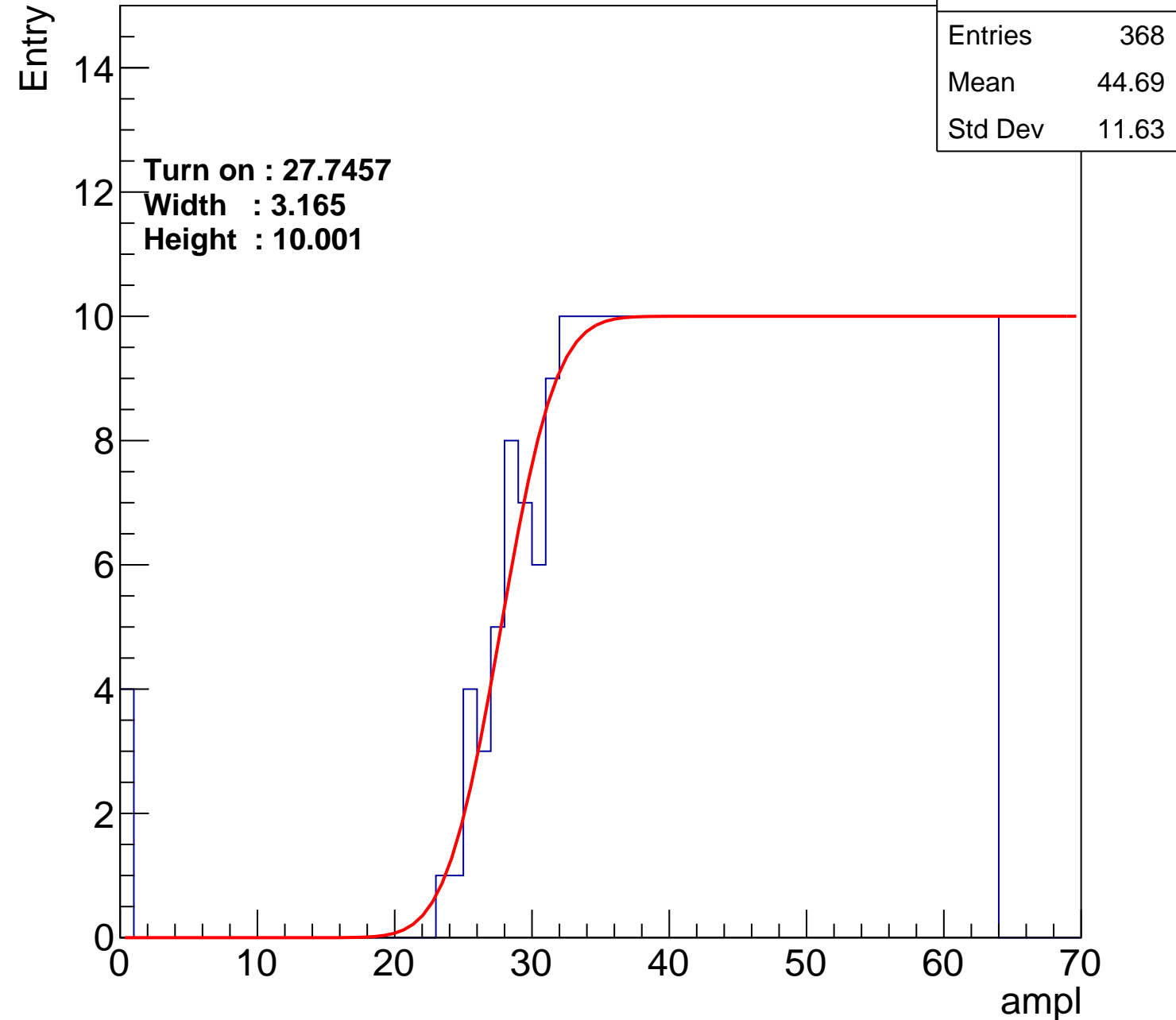
Width : 3.165

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch55

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.67
Std Dev	11.48

**Turn on : 27.2982**

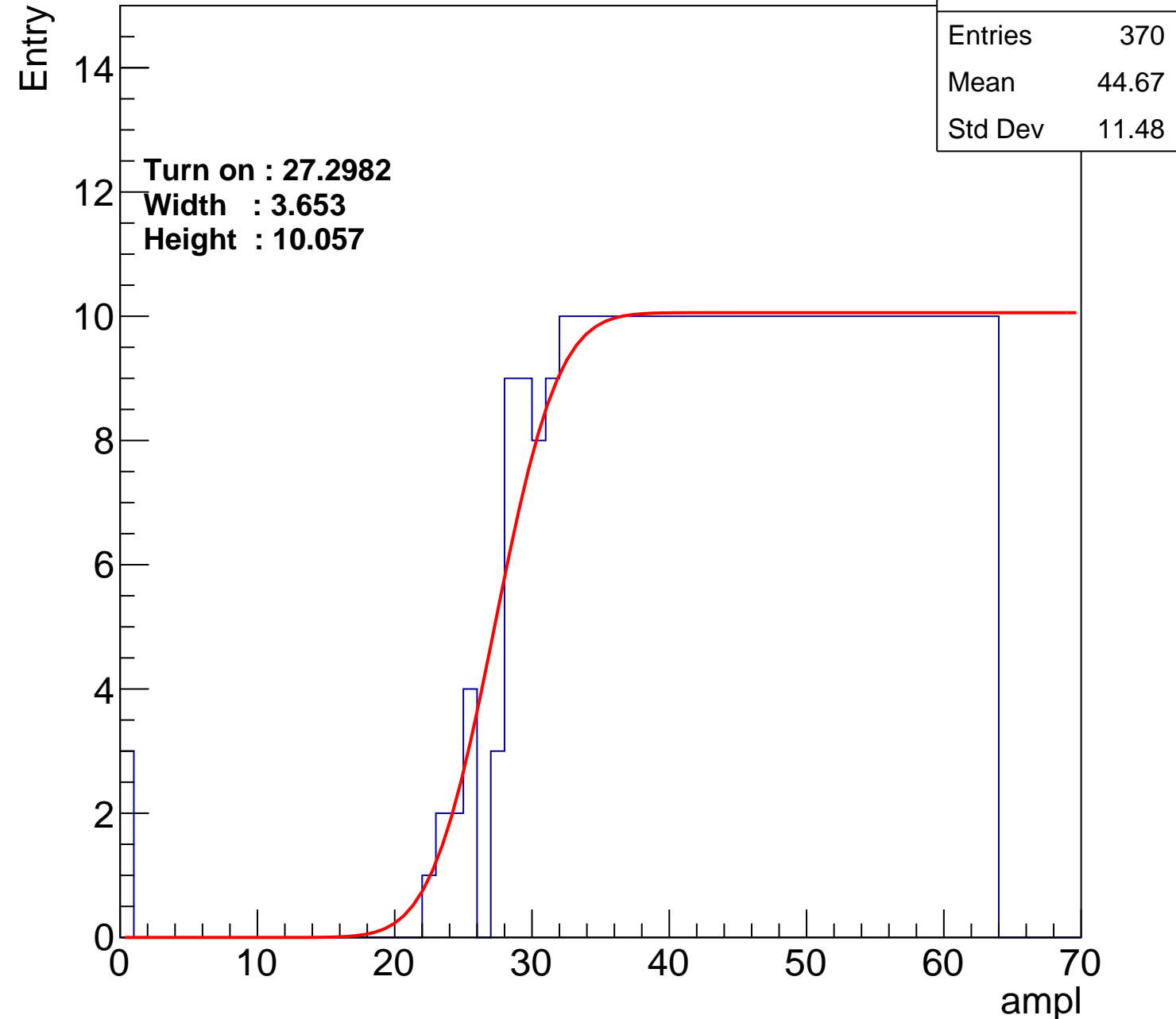
**Width : 3.653**

**Height : 10.057**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch56

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	377
Mean	44.37
Std Dev	11.58

Turn on : 26.8524

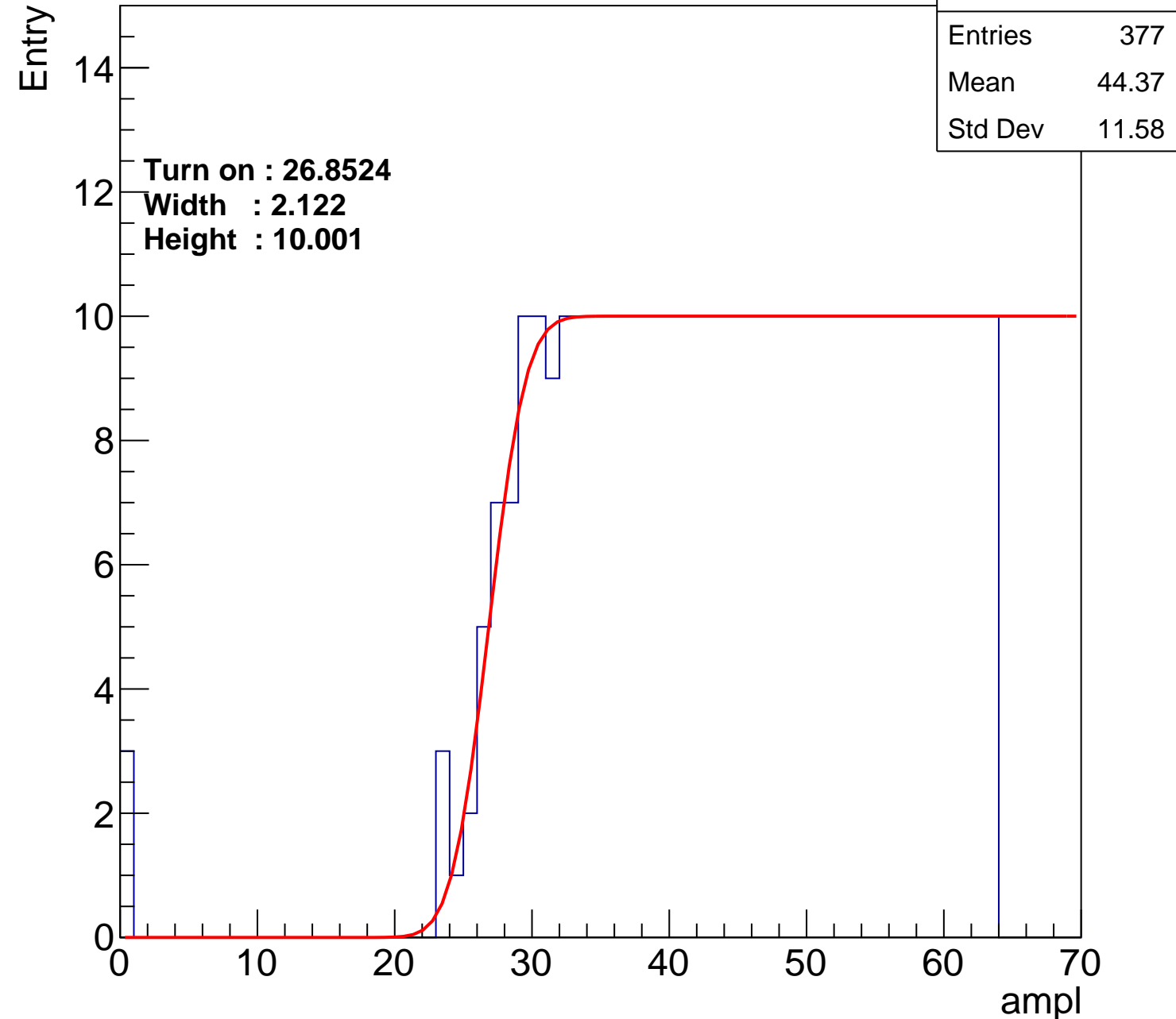
Width : 2.122

Height : 10.001

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch57

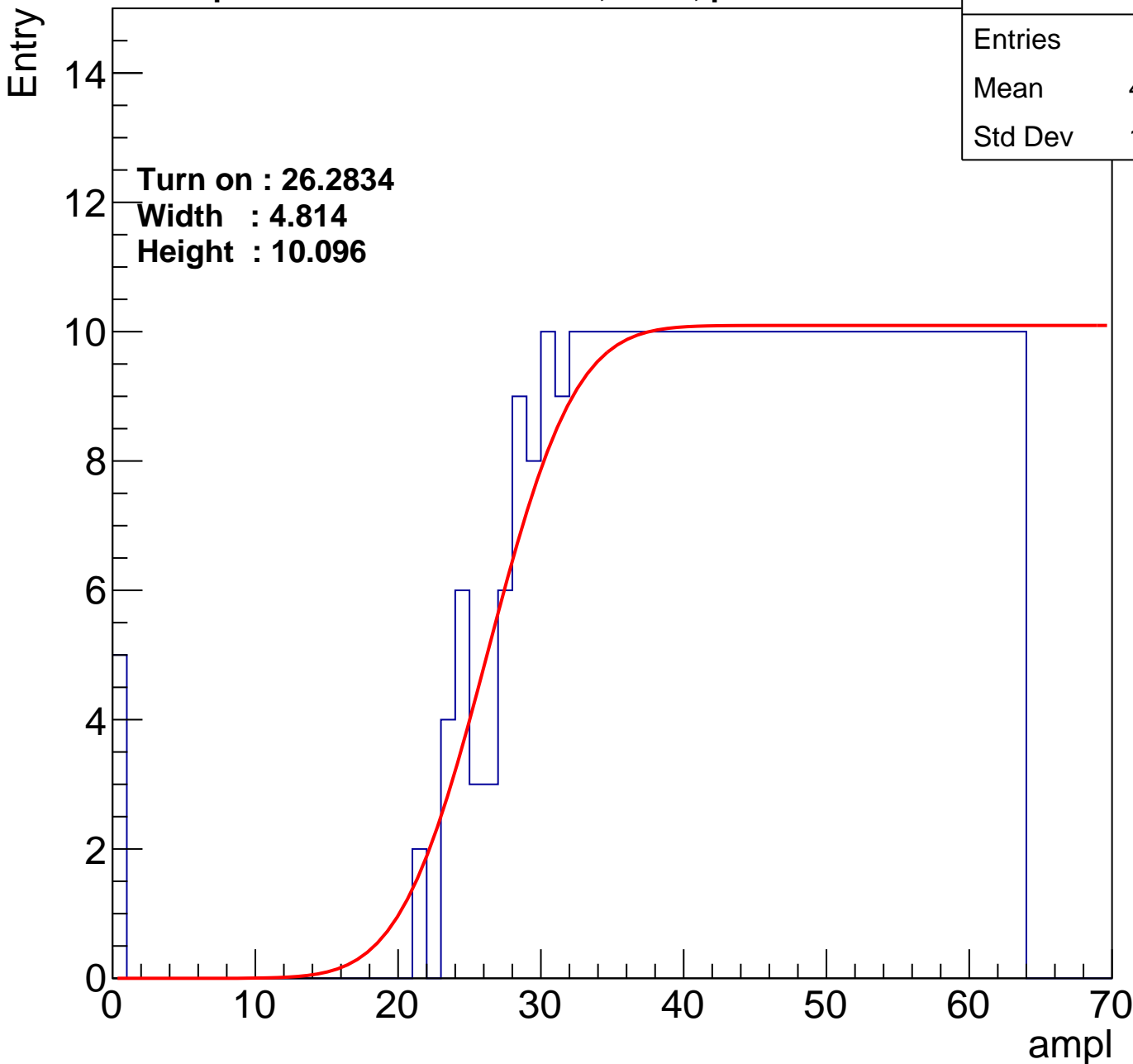
calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	385
Mean	43.78
Std Dev	12.22

Turn on : 26.2834

Width : 4.814

Height : 10.096



# B0L002S, U19-ch58

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.56
Std Dev	11.8

Turn on : 27.7431

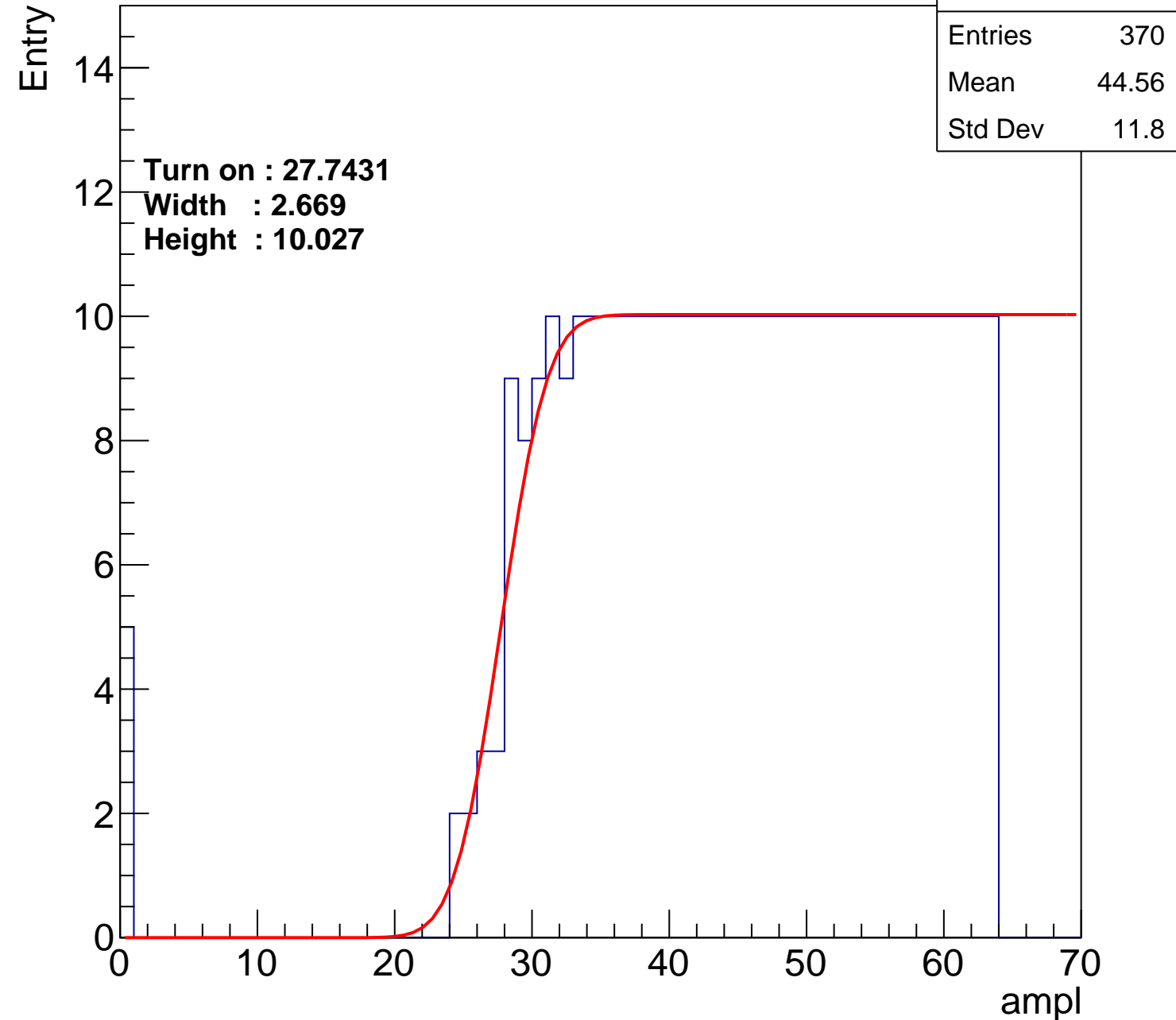
Width : 2.669

Height : 10.027

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch59

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.28
Std Dev	11.49

**Turn on : 25.8044**

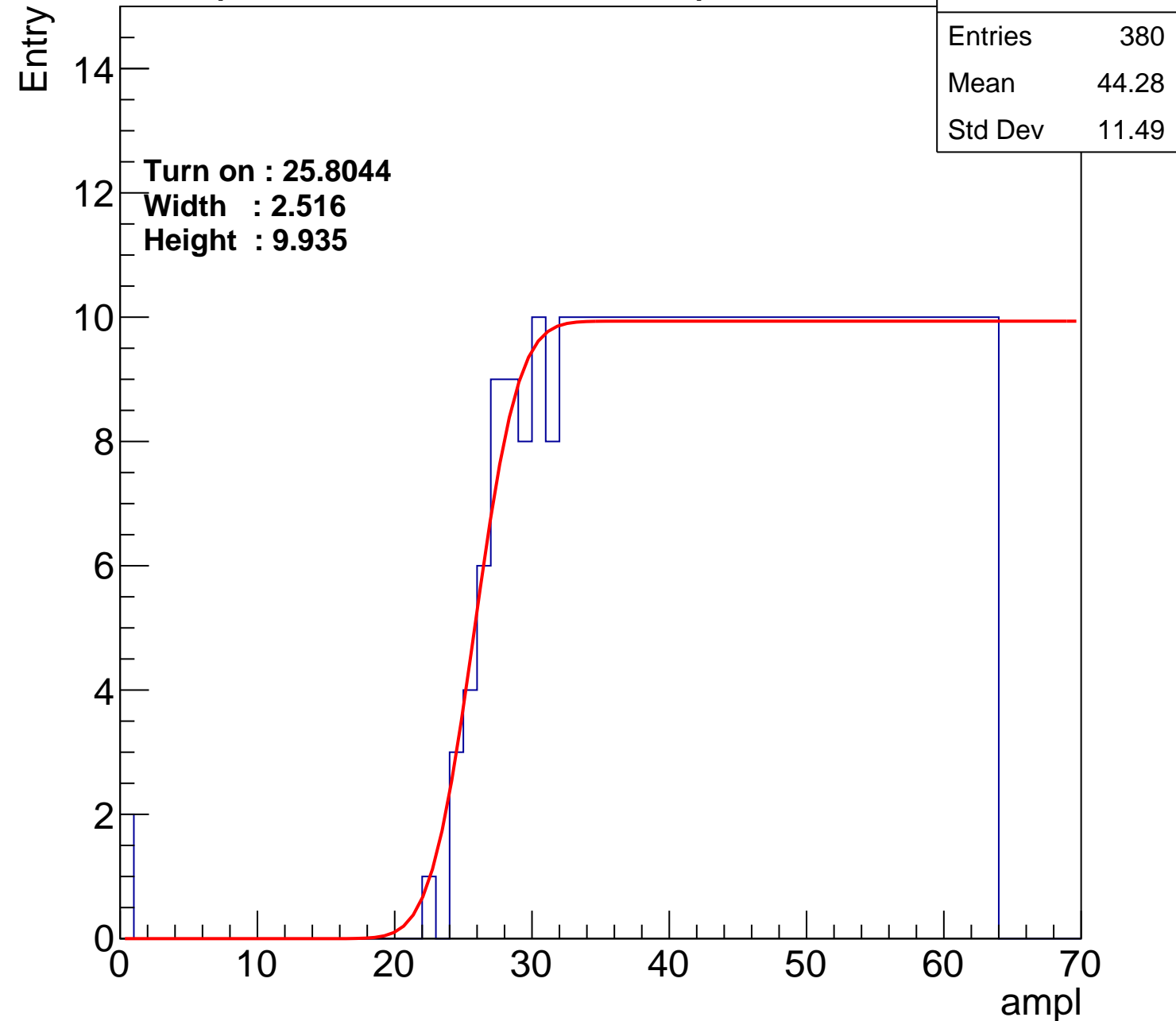
**Width : 2.516**

**Height : 9.935**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch60

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.05
Std Dev	11.04

**Turn on : 27.7397**

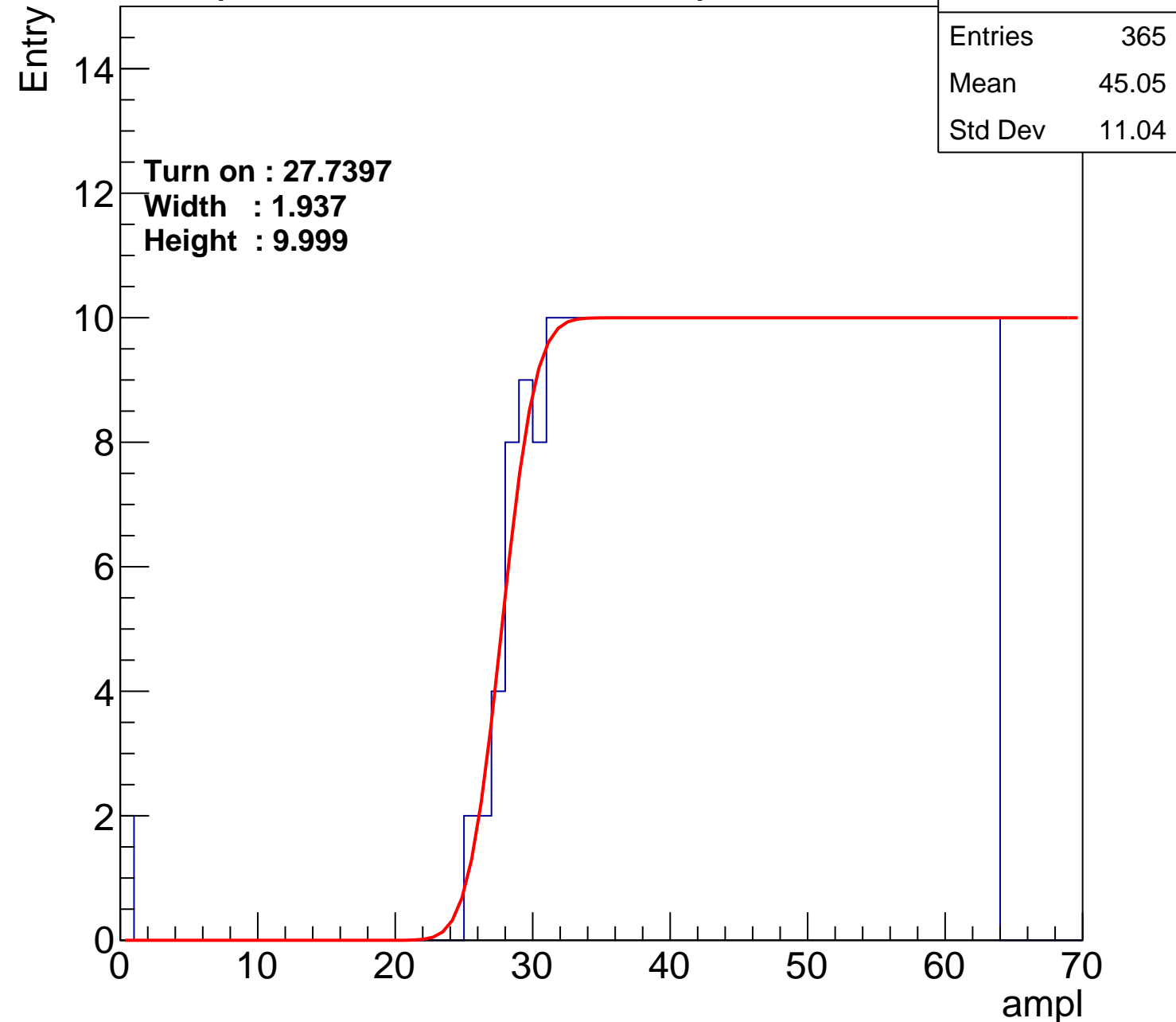
**Width : 1.937**

**Height : 9.999**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch61

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	358
Mean	45.34
Std Dev	10.97

Turn on : 28.6231

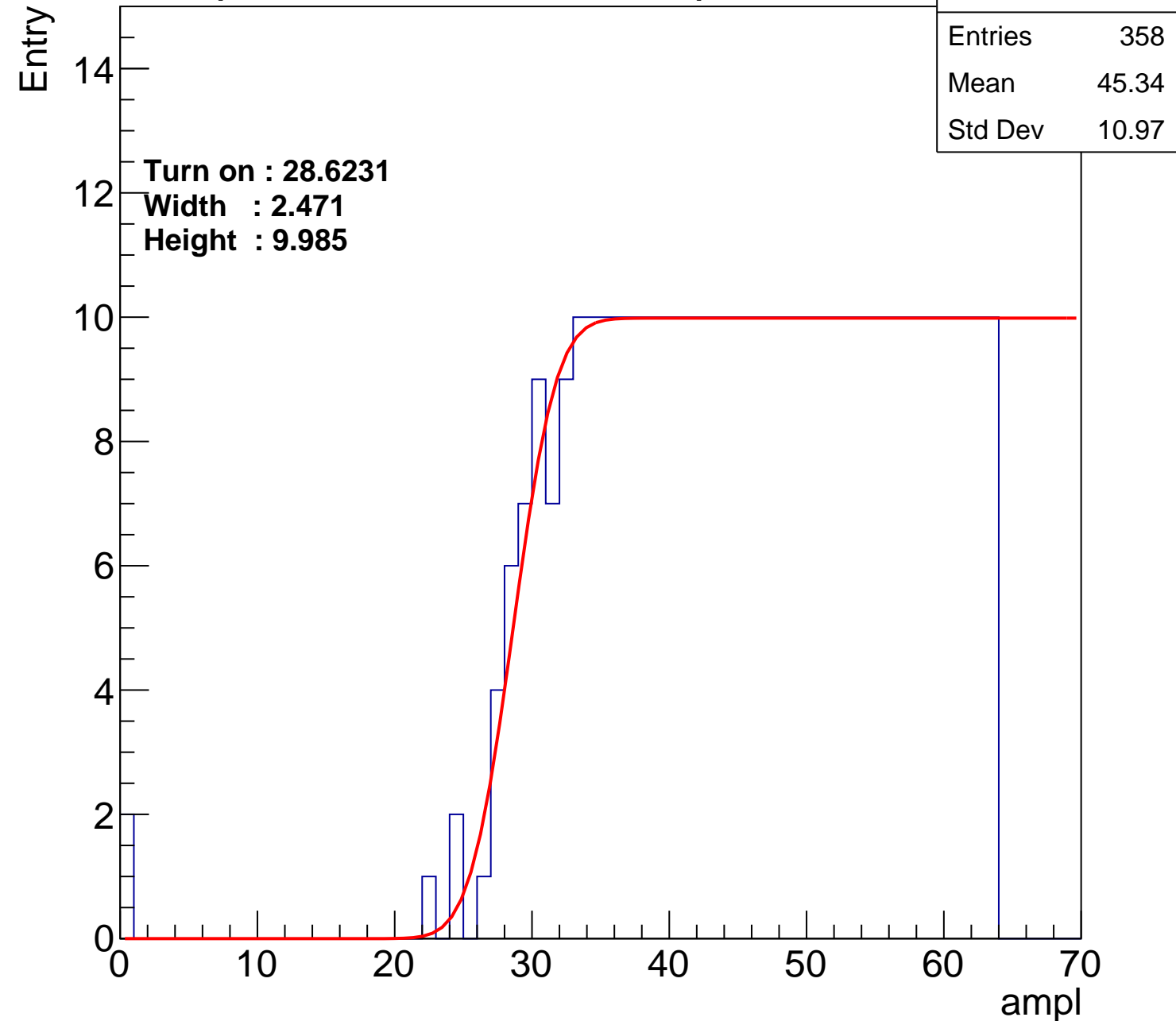
Width : 2.471

Height : 9.985

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch62

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.13
Std Dev	11.79

Turn on : 27.2388

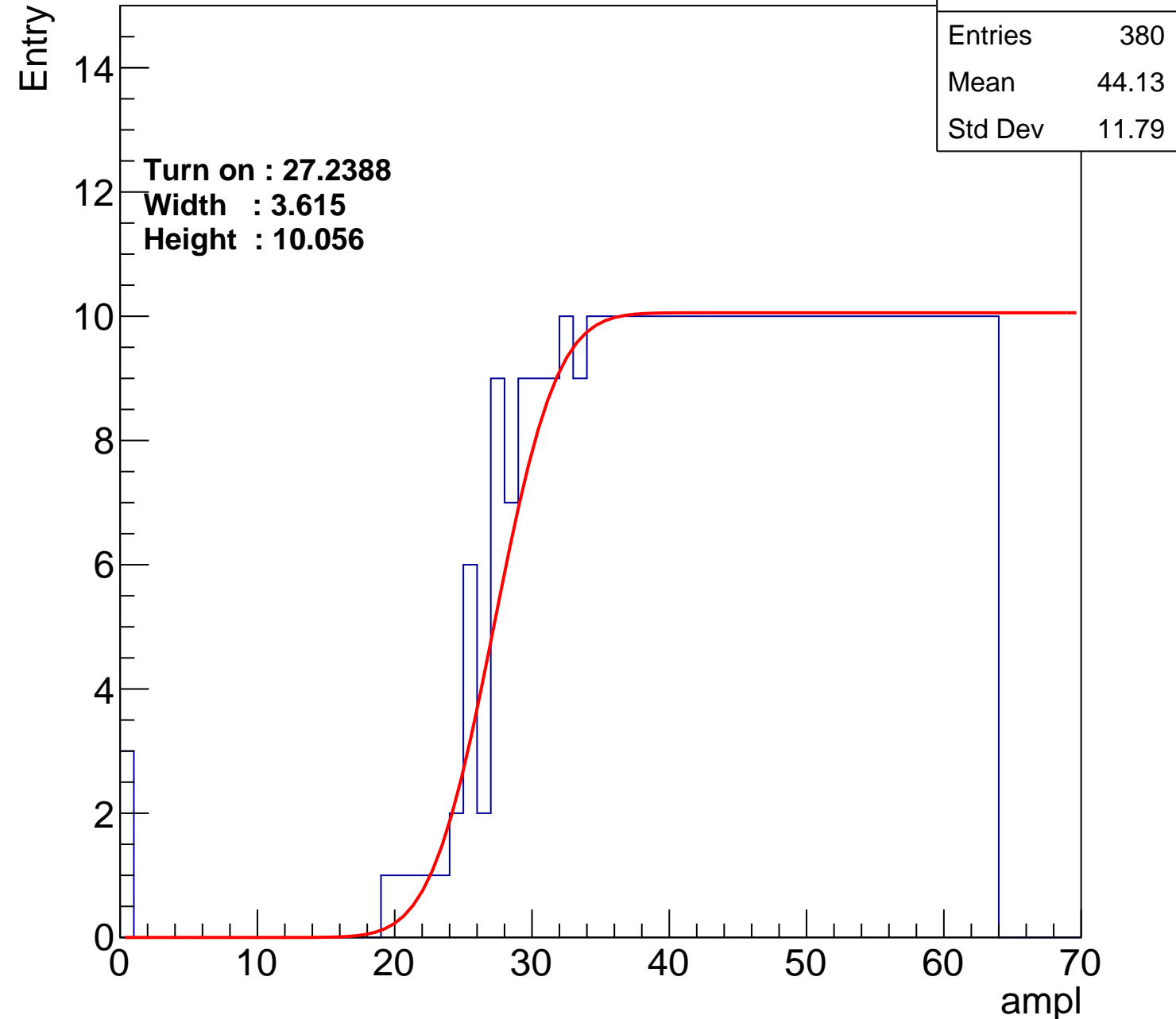
Width : 3.615

Height : 10.056

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch63

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	371
Mean	44.71
Std Dev	11.28

Turn on : 27.1099

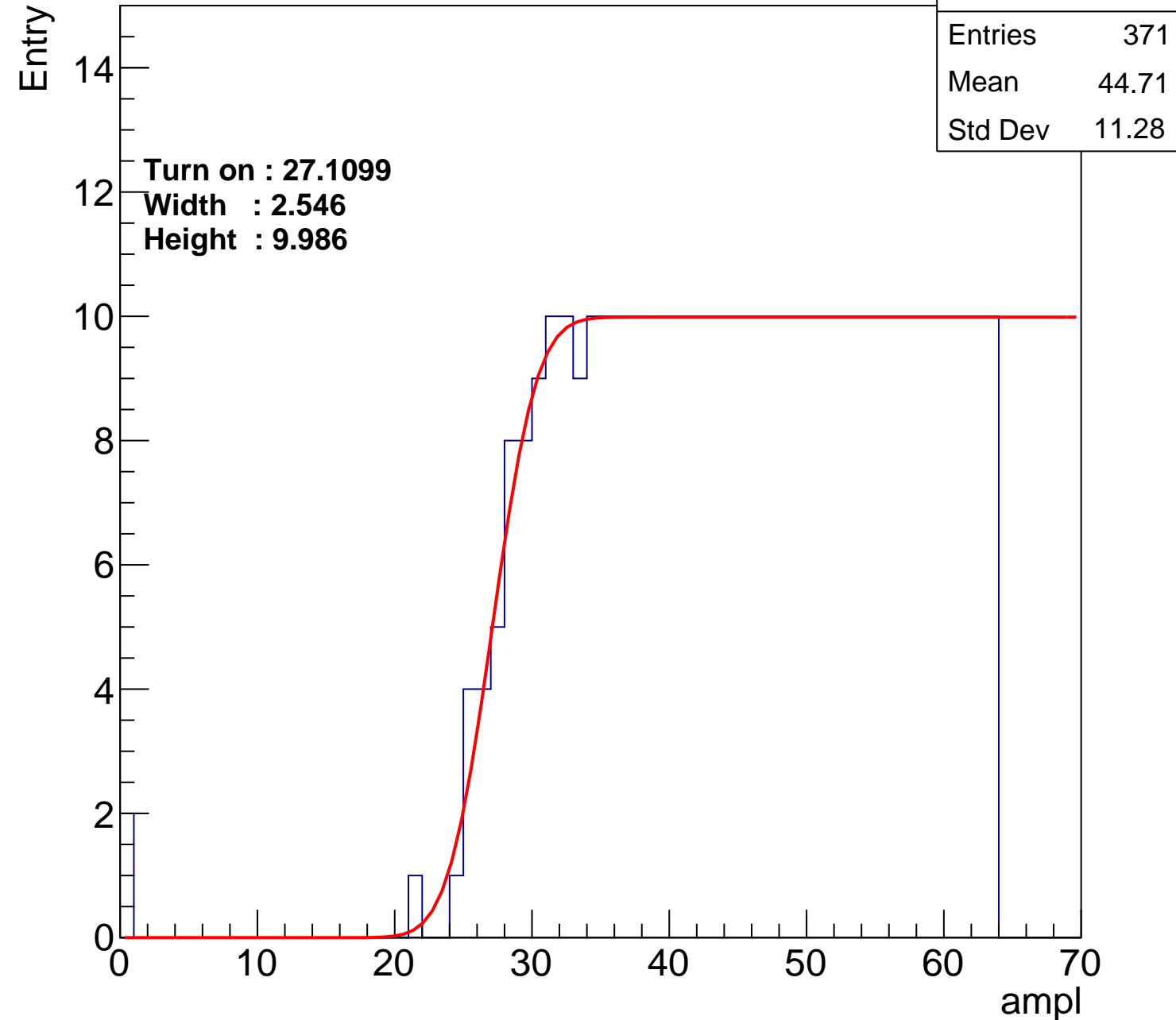
Width : 2.546

Height : 9.986

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch64

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	379
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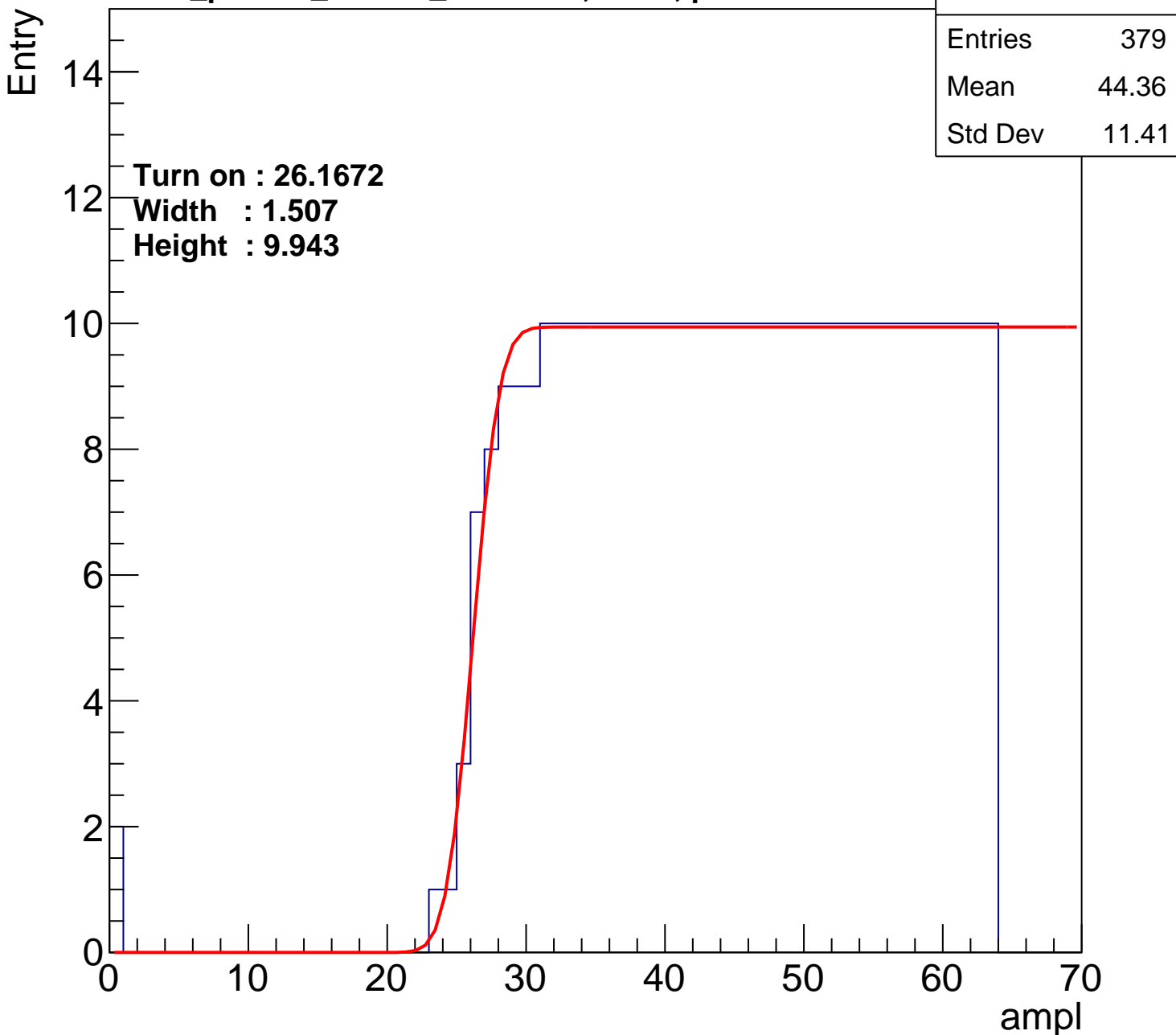
Mean	44.36
------	-------

Std Dev	11.41
---------	-------

**Turn on : 26.1672**

**Width : 1.507**

**Height : 9.943**



# B0L002S, U19-ch65

calib\_packv5\_042523\_0143.root, FC#8, port C1

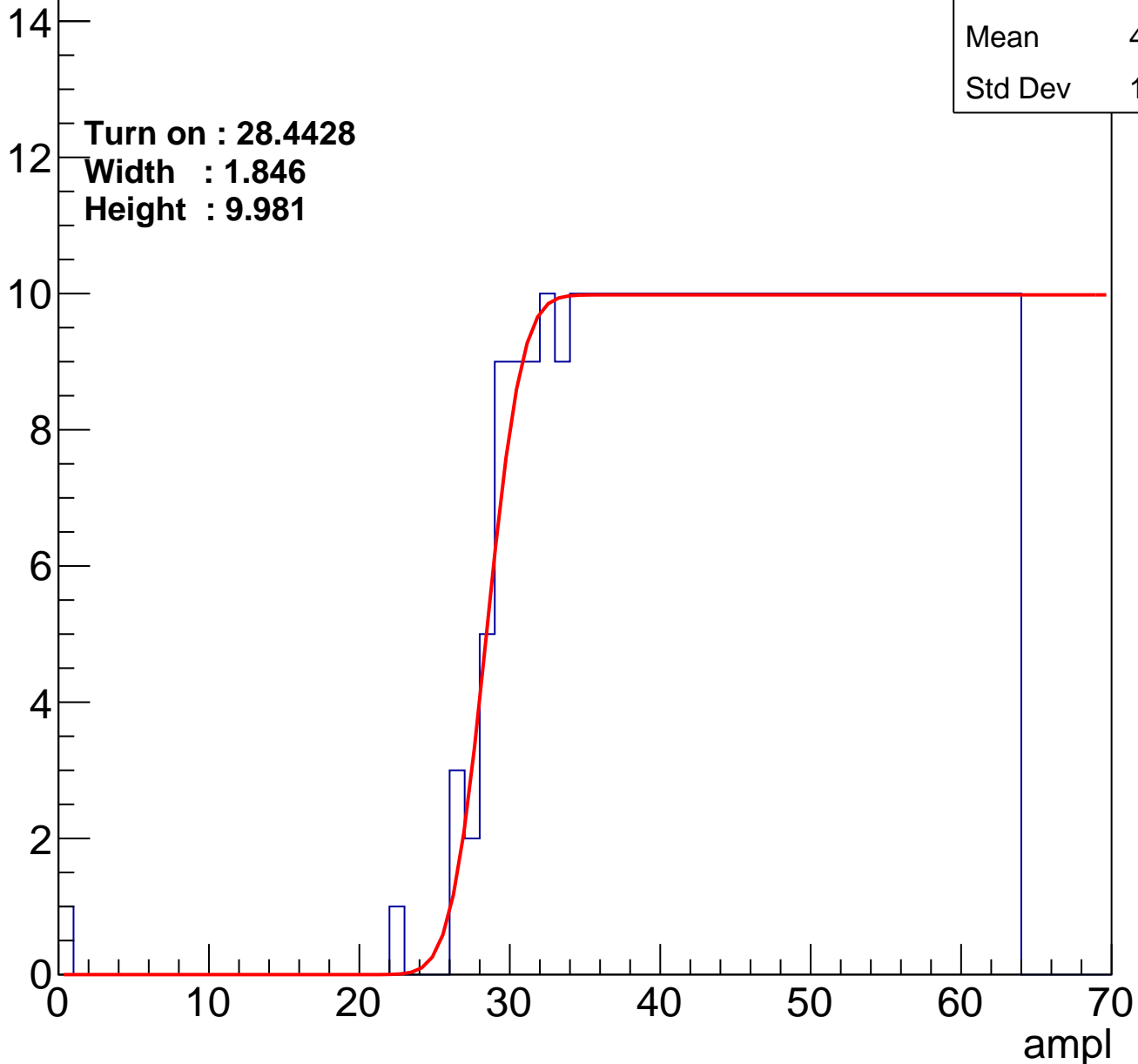
Entries	358
Mean	45.45
Std Dev	10.69

Turn on : 28.4428

Width : 1.846

Height : 9.981

Entry



# B0L002S, U19-ch66

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.15
Std Dev	10.9

Turn on : 28.0282

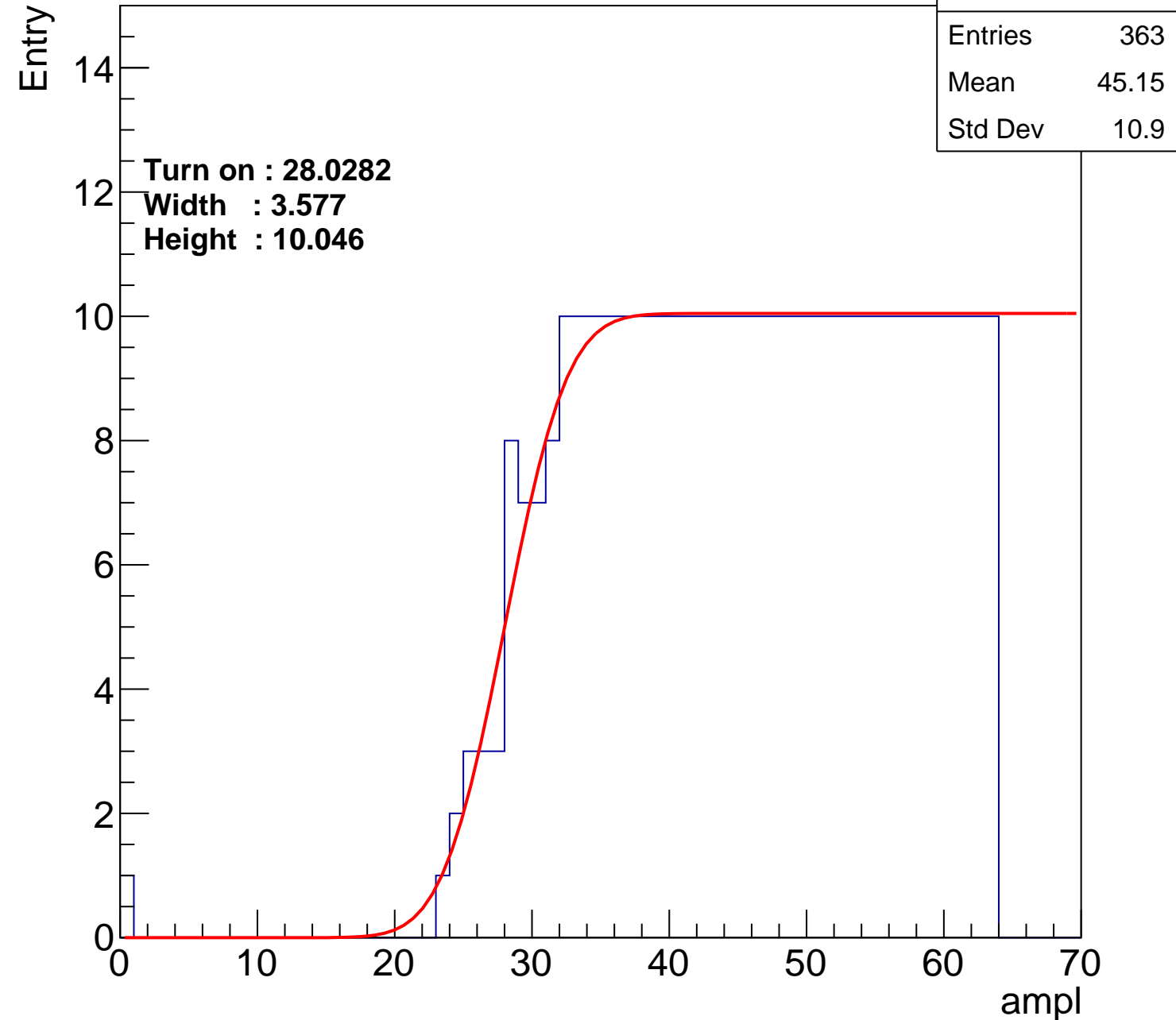
Width : 3.577

Height : 10.046

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch67

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	383
Mean	44.01
Std Dev	11.84

Turn on : 27.4448

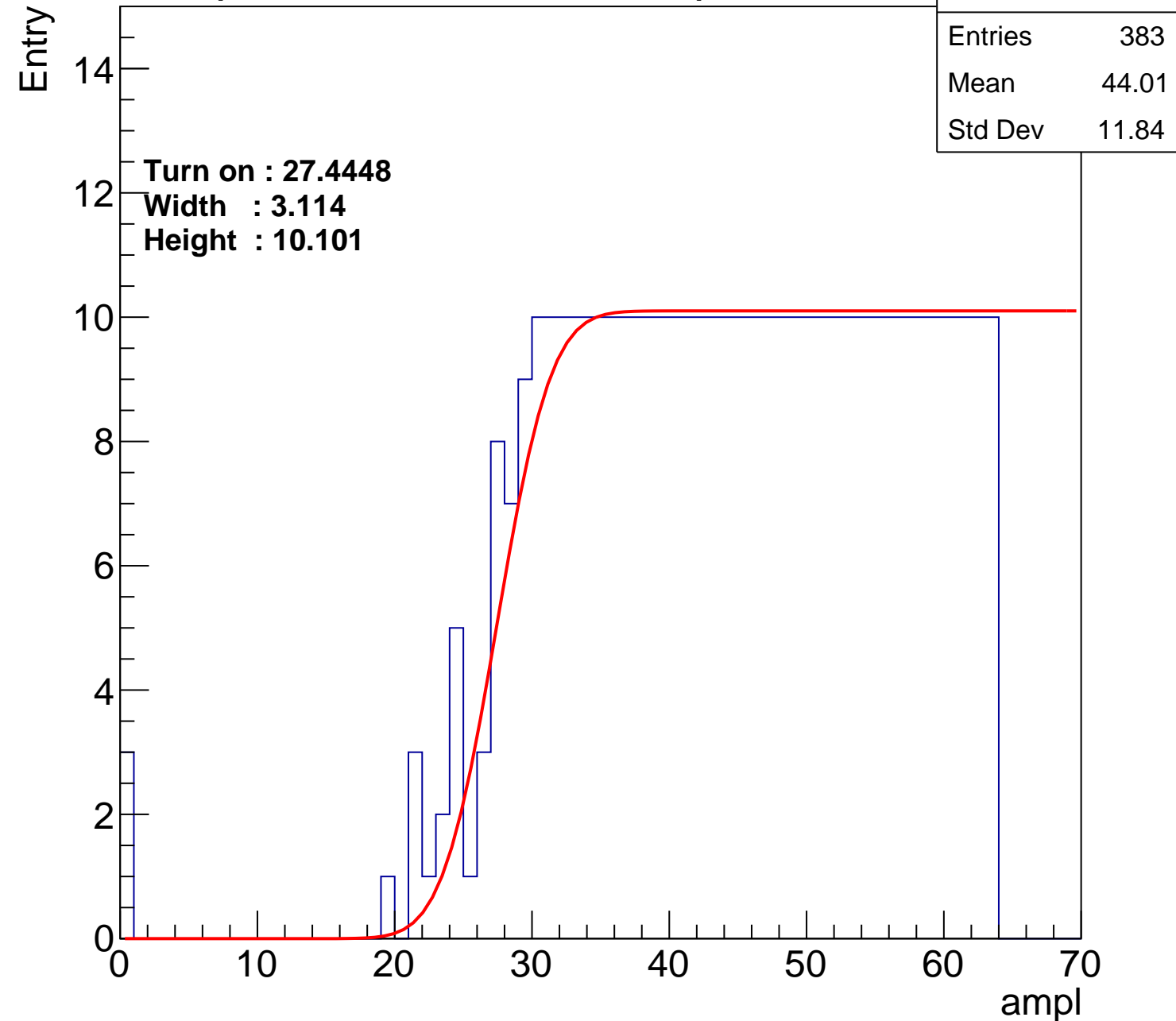
Width : 3.114

Height : 10.101

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch68

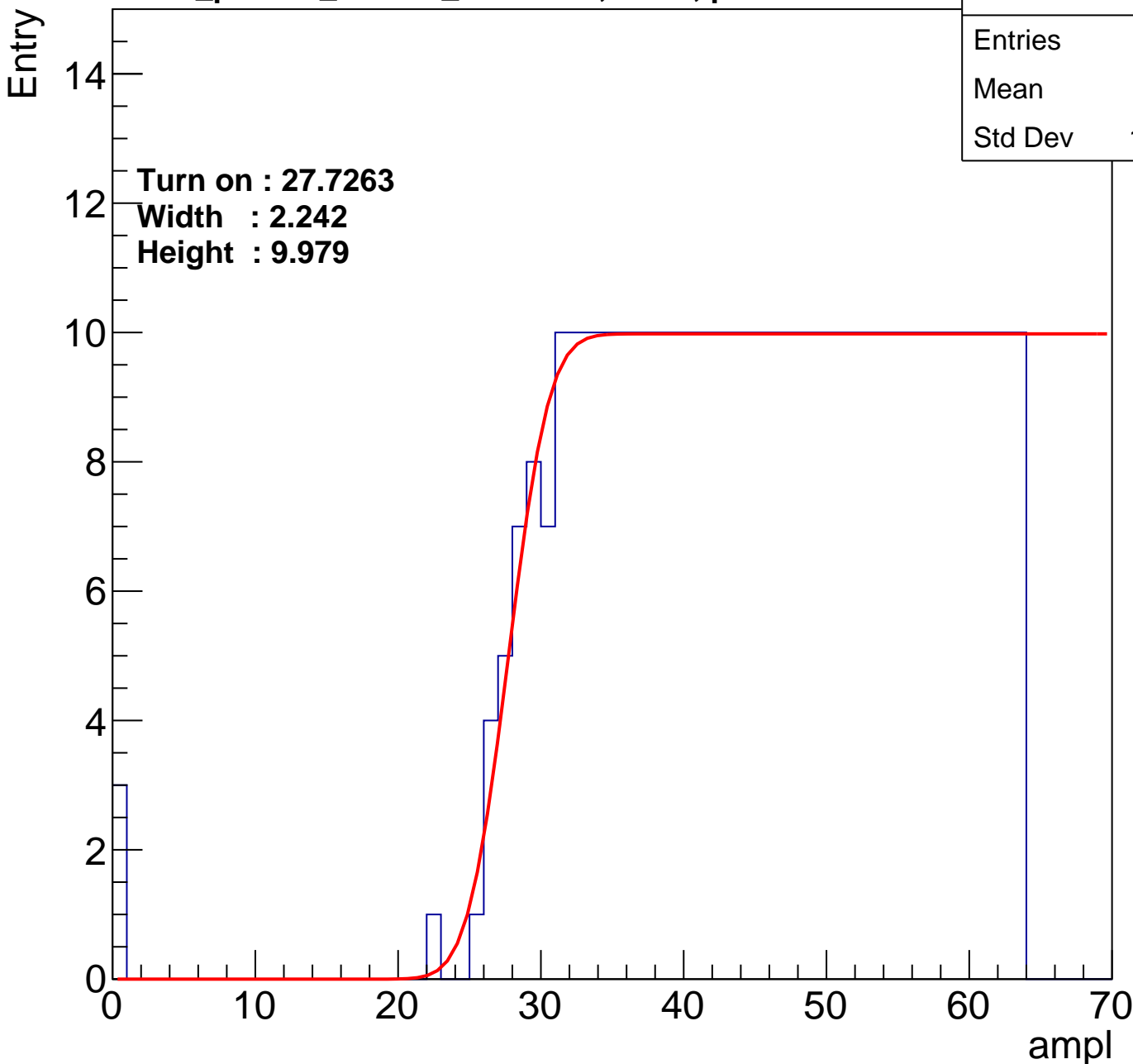
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	366
Mean	44.9
Std Dev	11.33

**Turn on : 27.7263**

**Width : 2.242**

**Height : 9.979**



# B0L002S, U19-ch69

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	374
Mean	44.56
Std Dev	11.35

Turn on : 26.9691

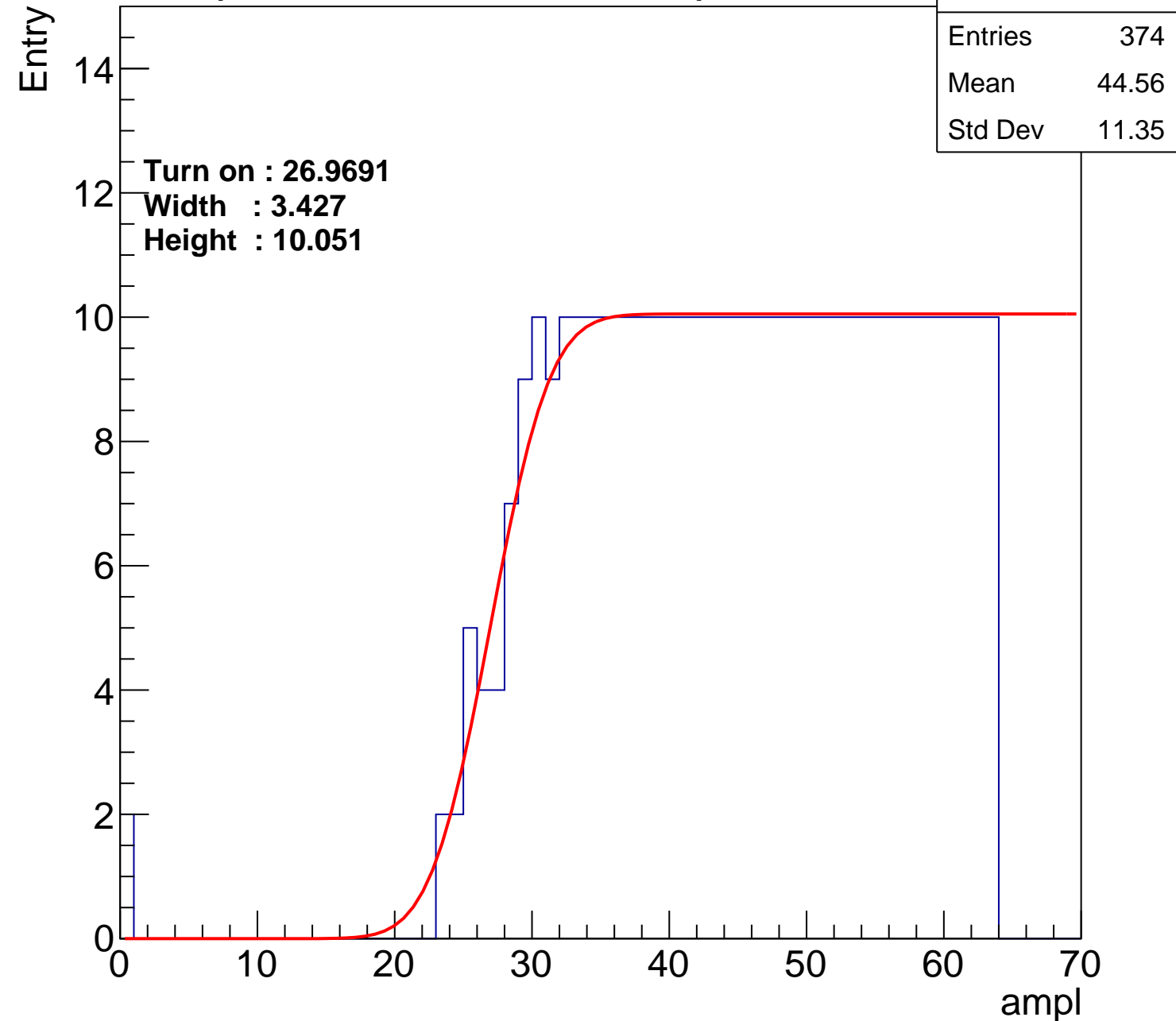
Width : 3.427

Height : 10.051

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch70

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.7207

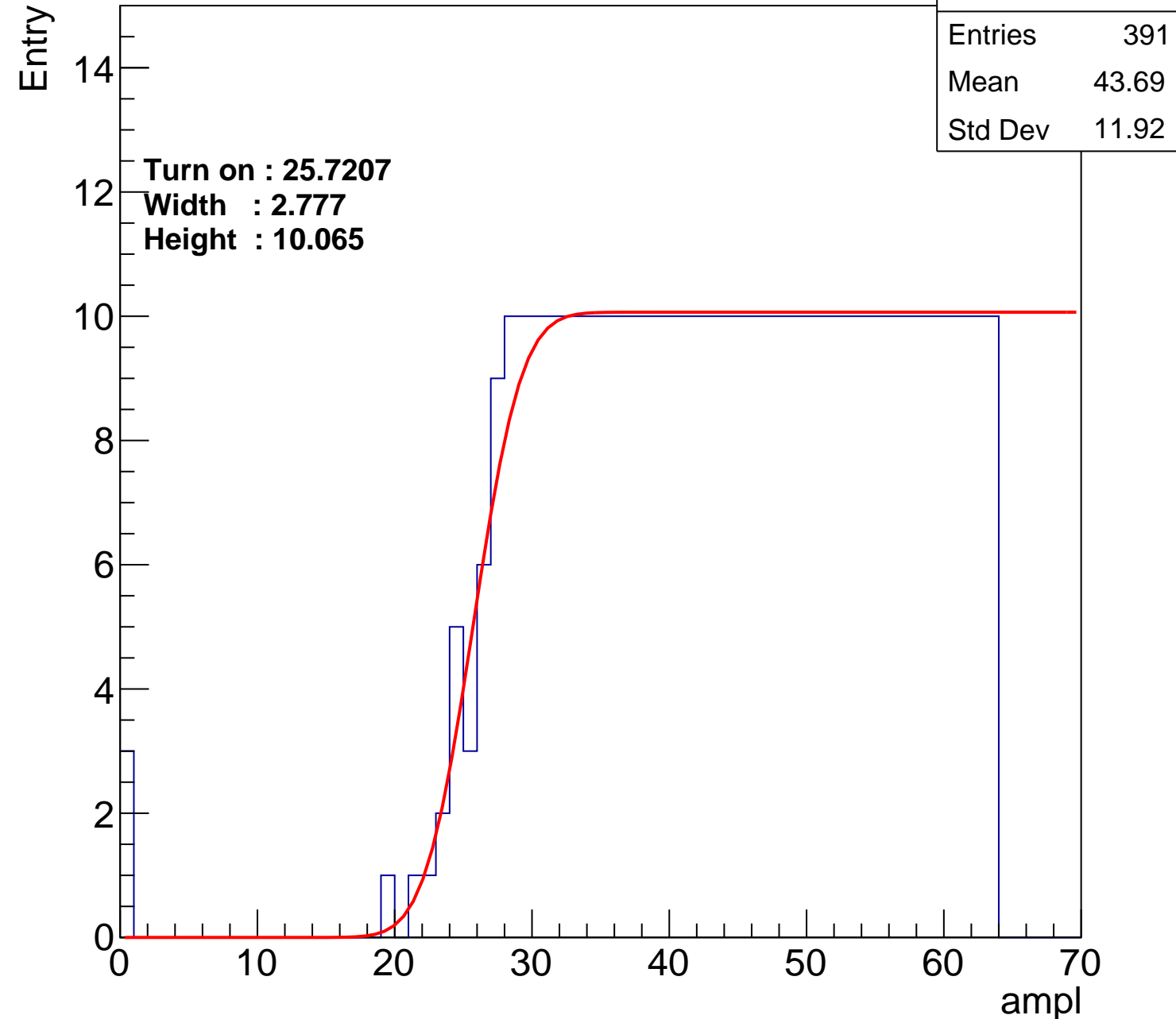
Width : 2.777

Height : 10.065

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch71

calib\_packv5\_042523\_0143.root, FC#8, port C1

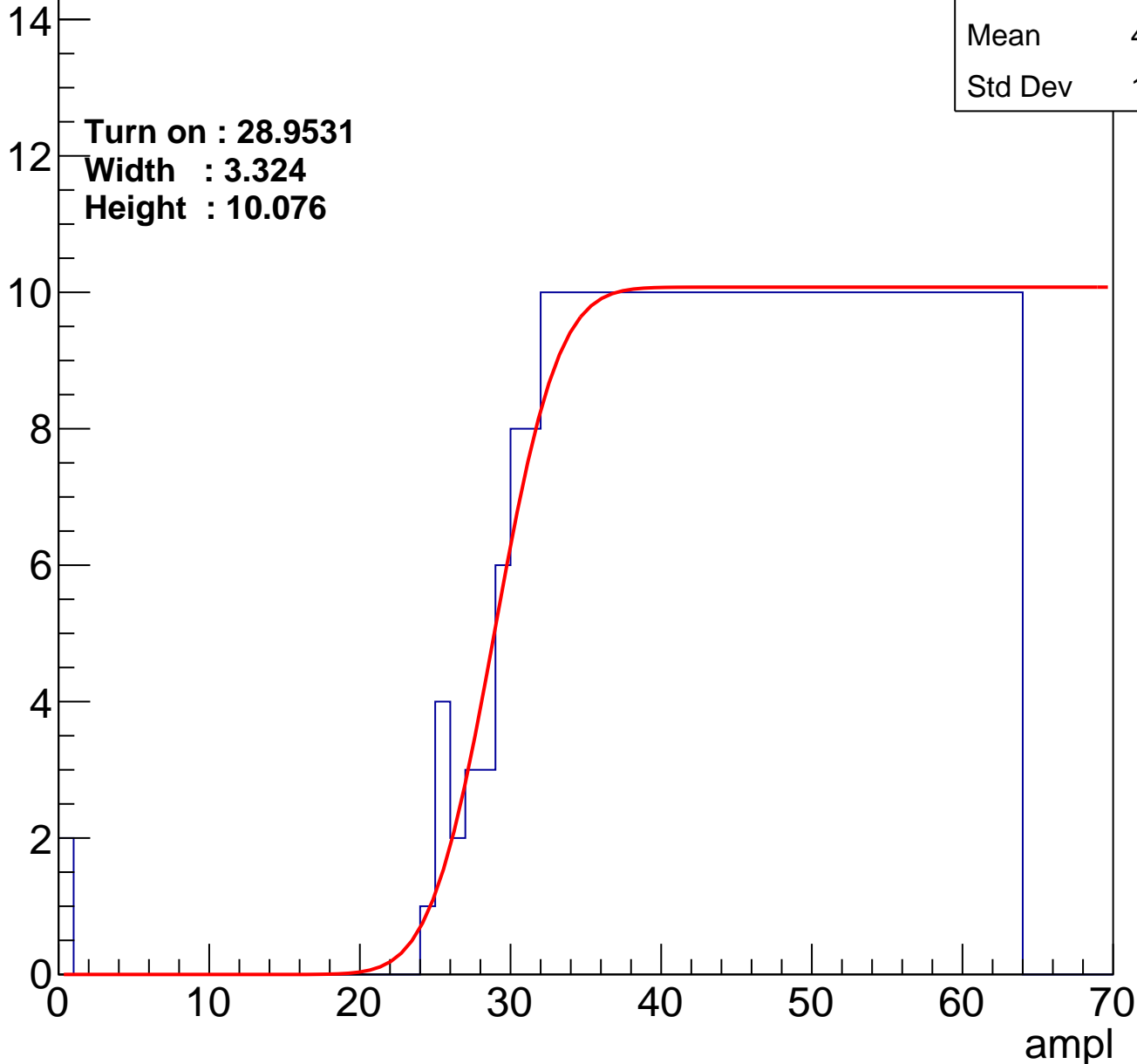
Entries	357
Mean	45.39
Std Dev	10.95

Turn on : 28.9531

Width : 3.324

Height : 10.076

Entry



# B0L002S, U19-ch72

calib\_packv5\_042523\_0143.root, FC#8, port C1

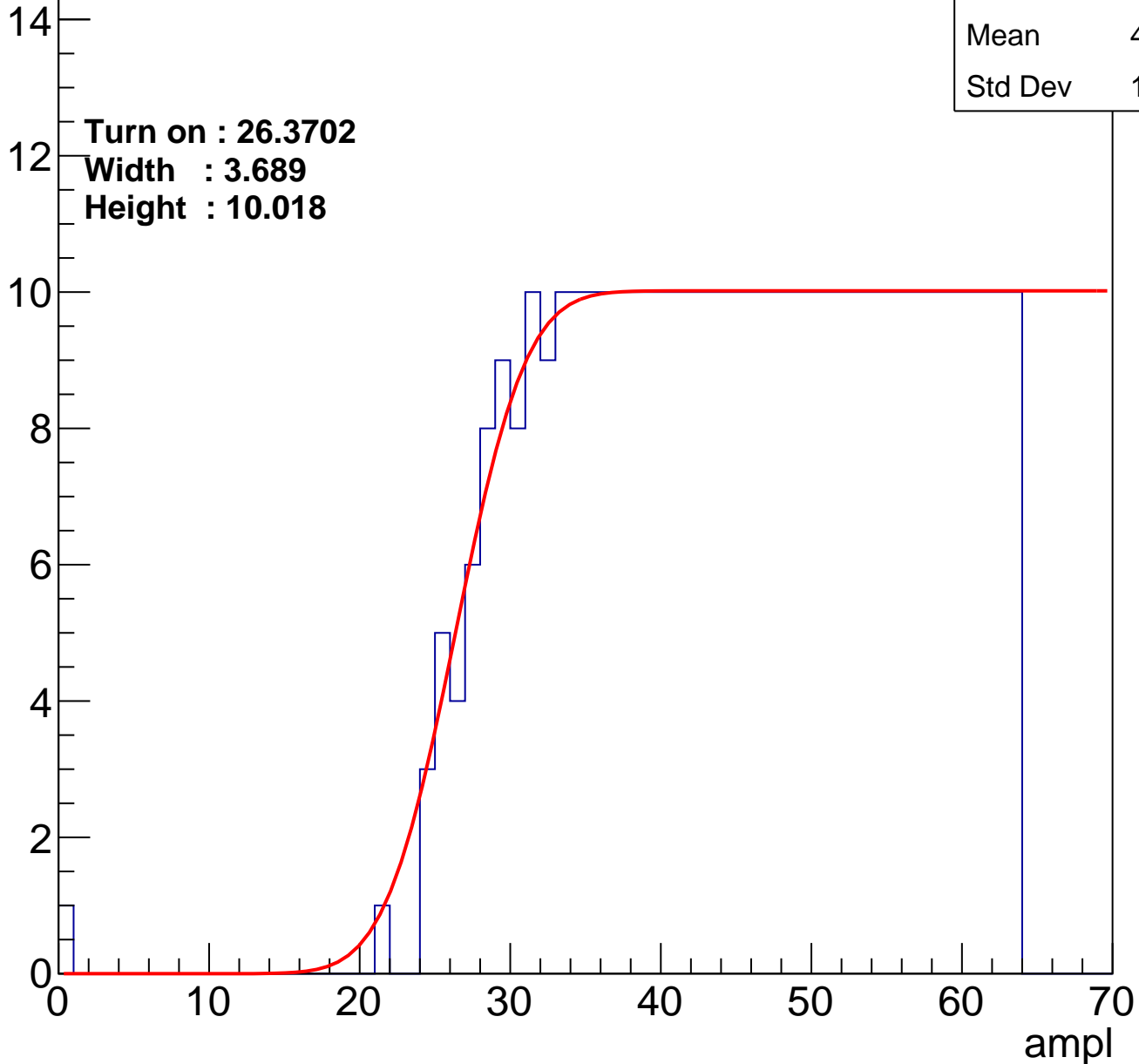
Entries	374
Mean	44.62
Std Dev	11.18

Turn on : 26.3702

Width : 3.689

Height : 10.018

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.2517  
Width : 3.794  
Height : 10.061



# B0L002S, U19-ch74

calib\_packv5\_042523\_0143.root, FC#8, port C1

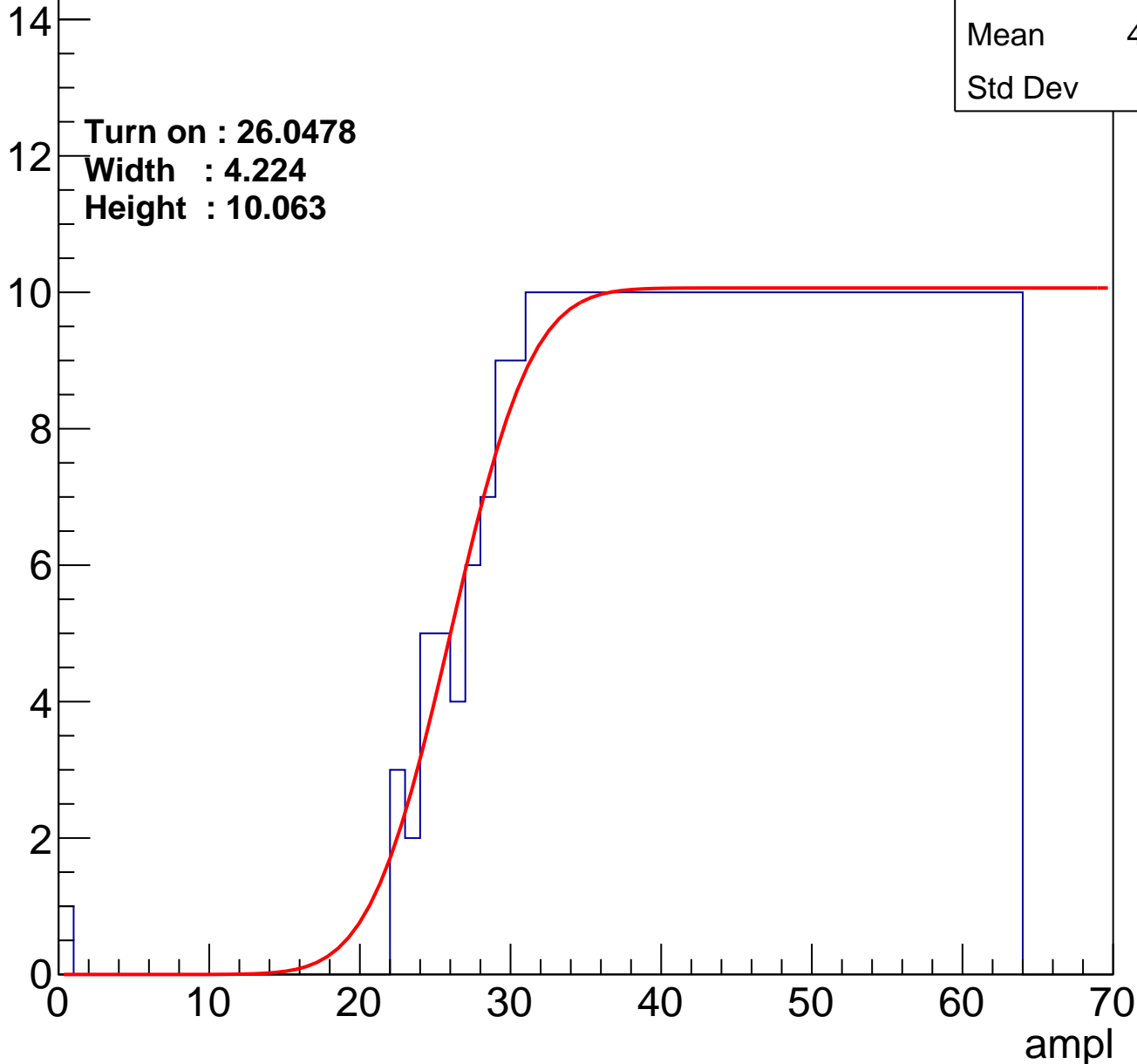
Entries	381
Mean	44.25
Std Dev	11.4

Turn on : 26.0478

Width : 4.224

Height : 10.063

Entry



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Turn on : 27.5217  
Width : 2.256  
Height : 9.965



# B0L002S, U19-ch76

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.39
Std Dev	11.43

**Turn on : 26.7570**

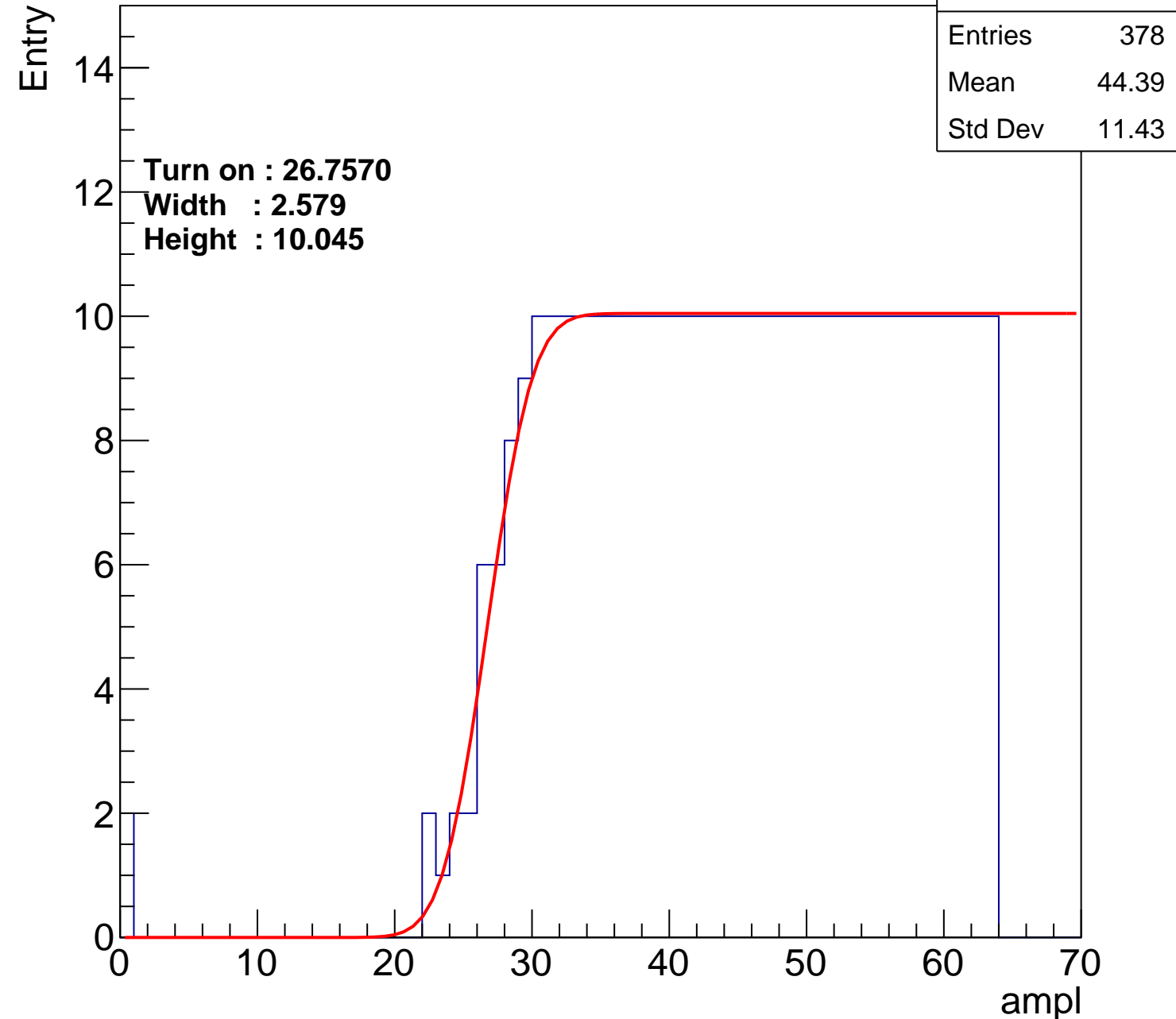
**Width : 2.579**

**Height : 10.045**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch77

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.81
Std Dev	11.09

Turn on : 27.9808

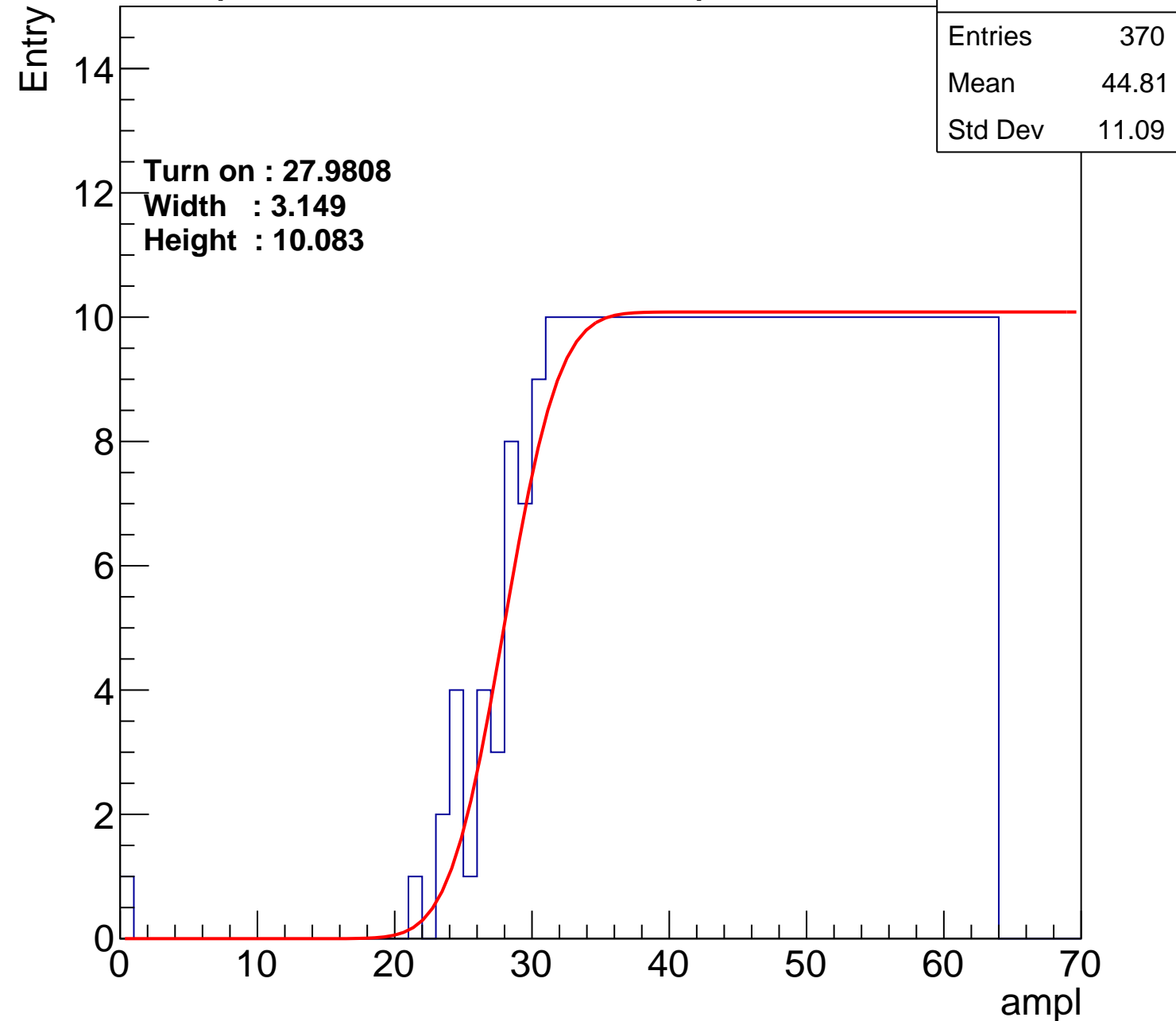
Width : 3.149

Height : 10.083

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch78

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	381
Mean	44.33
Std Dev	11.28

**Turn on : 26.1482**

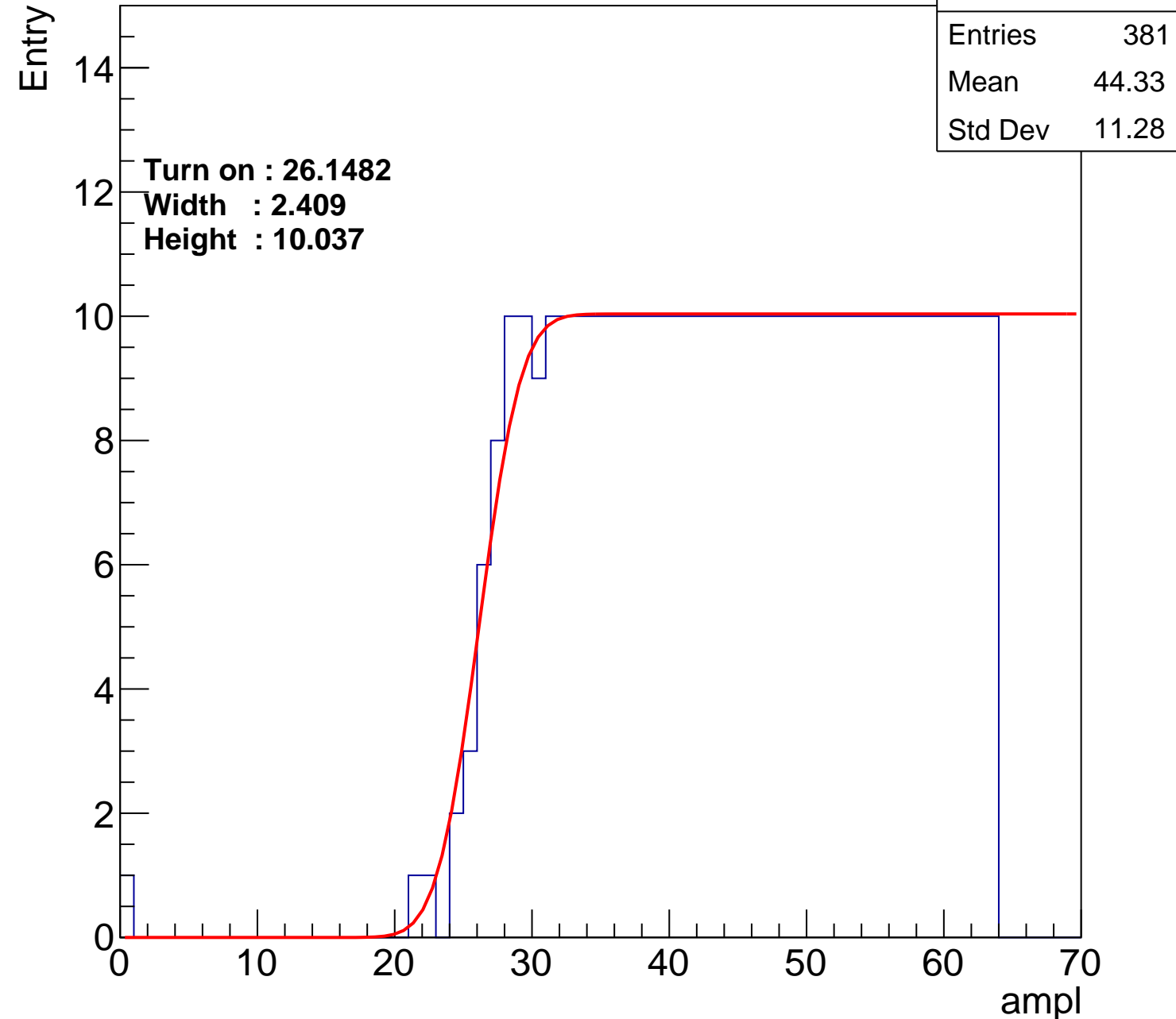
**Width : 2.409**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch79

calib\_packv5\_042523\_0143.root, FC#8, port C1

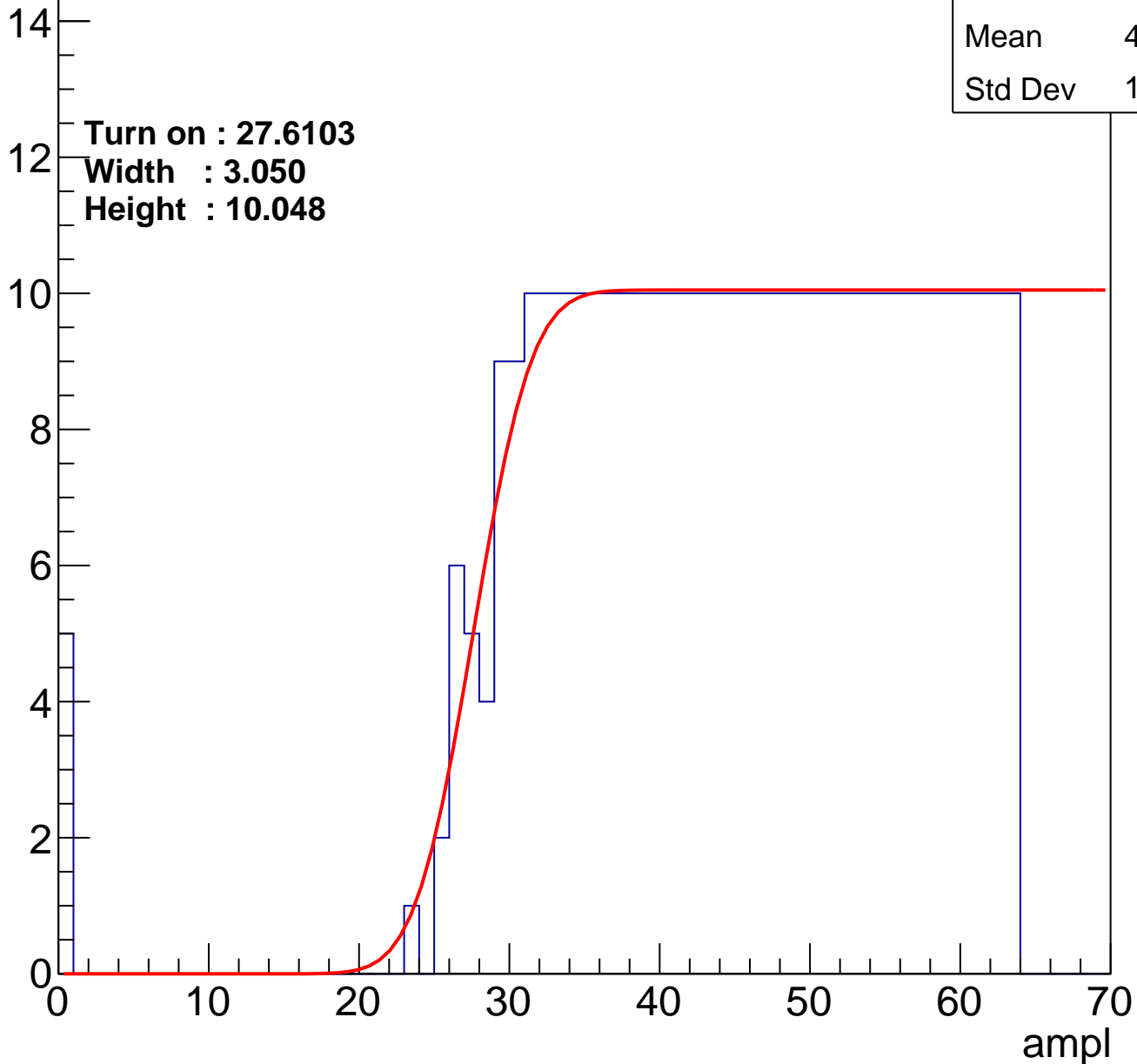
Entries	371
Mean	44.52
Std Dev	11.82

Turn on : 27.6103

Width : 3.050

Height : 10.048

Entry



# B0L002S, U19-ch80

calib\_packv5\_042523\_0143.root, FC#8, port C1

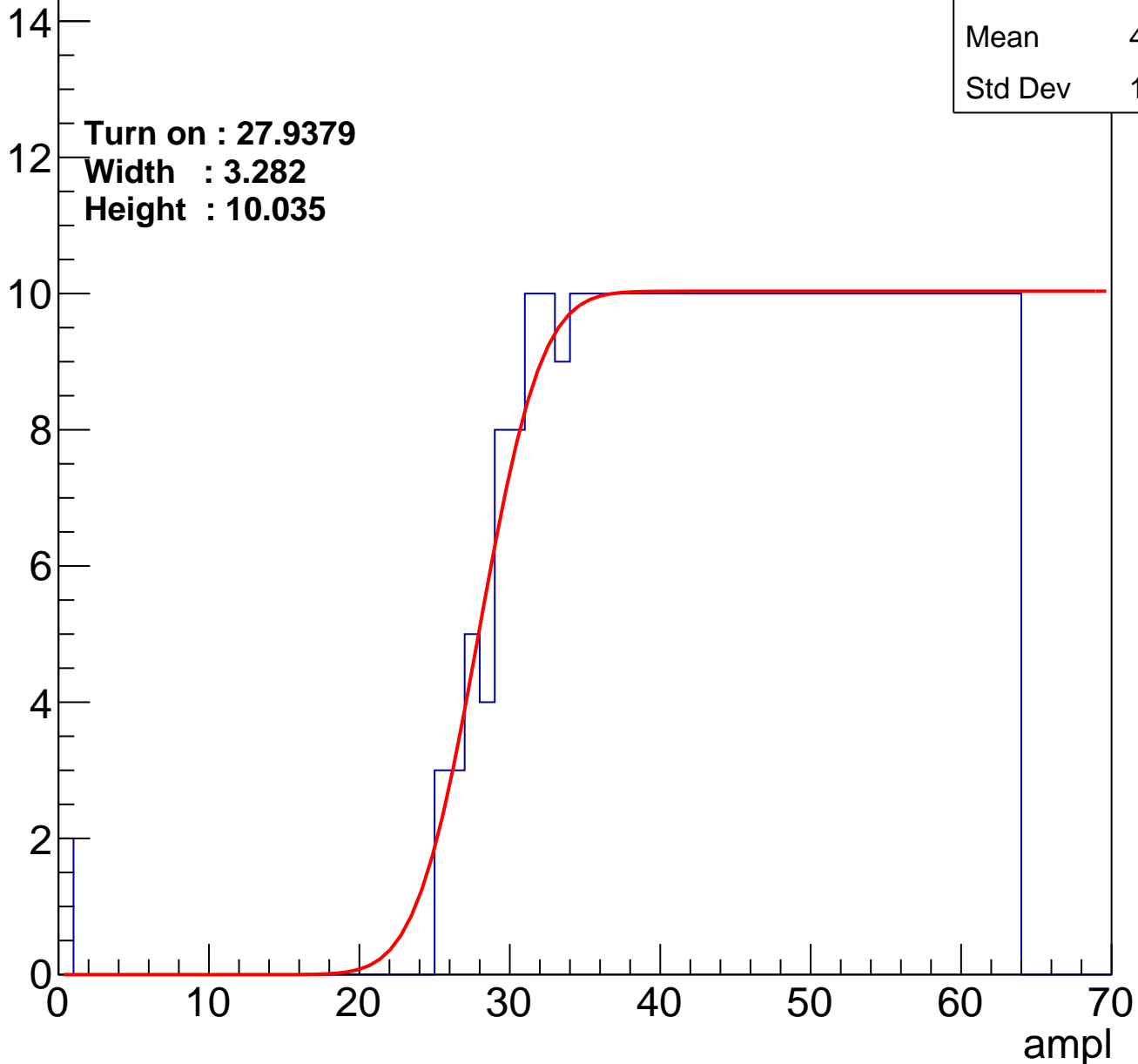
Entries	362
Mean	45.16
Std Dev	11.03

Turn on : 27.9379

Width : 3.282

Height : 10.035

Entry



# B0L002S, U19-ch81

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.25
Std Dev	11.54

Turn on : 26.1624

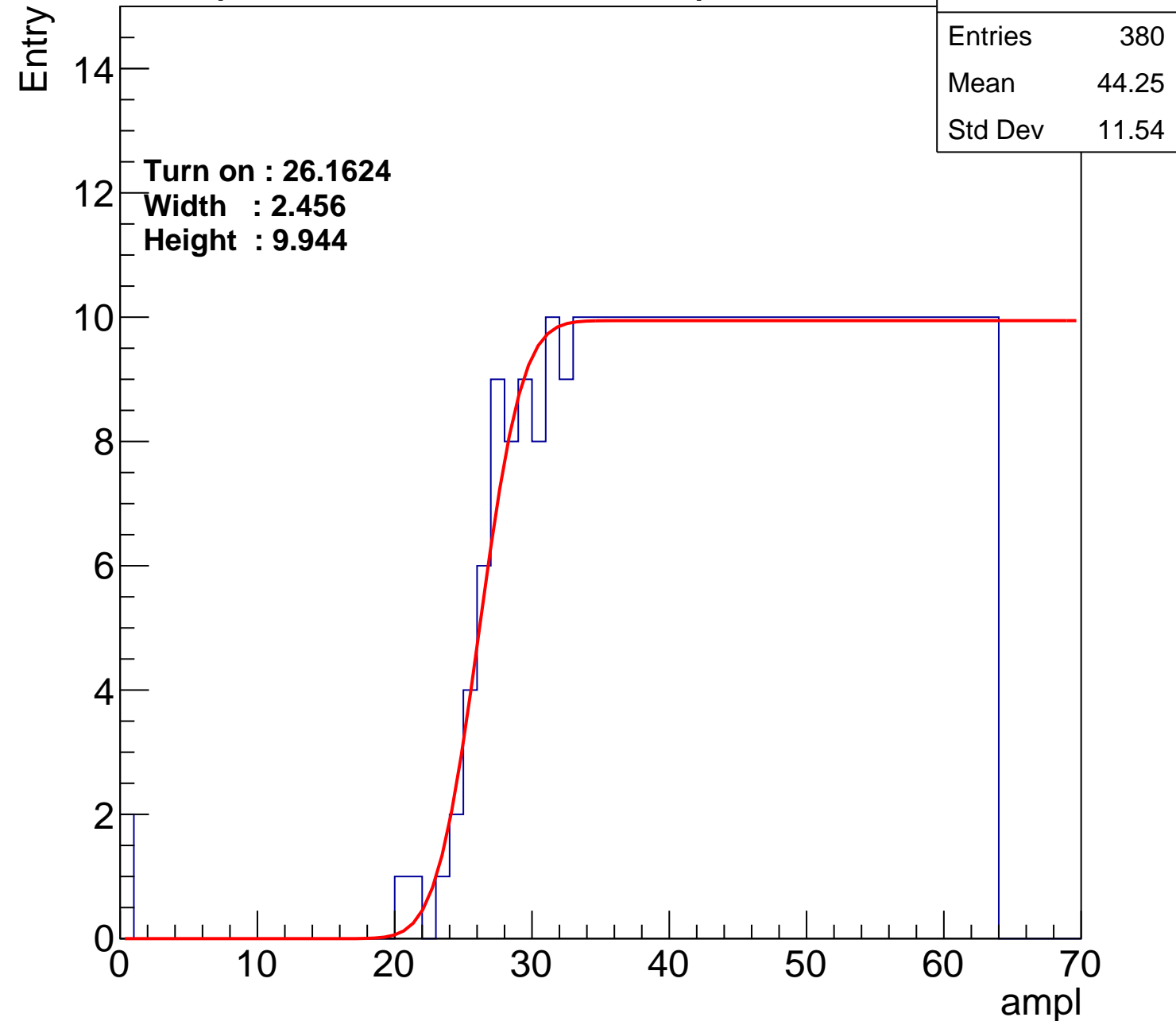
Width : 2.456

Height : 9.944

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch82

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.11
Std Dev	10.86

**Turn on : 28.1508**

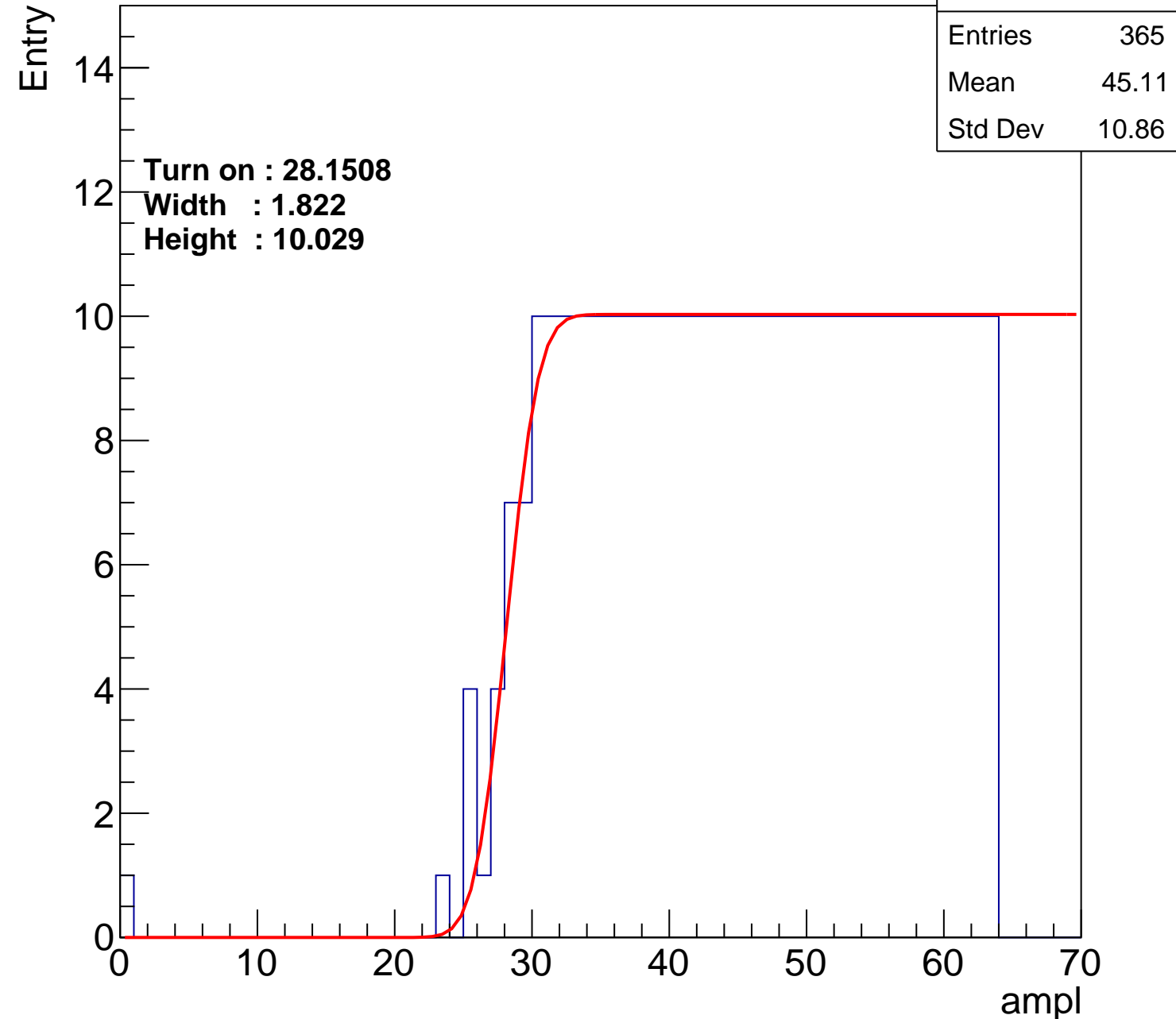
**Width : 1.822**

**Height : 10.029**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch83

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.58
Std Dev	11.38

Turn on : 27.5194

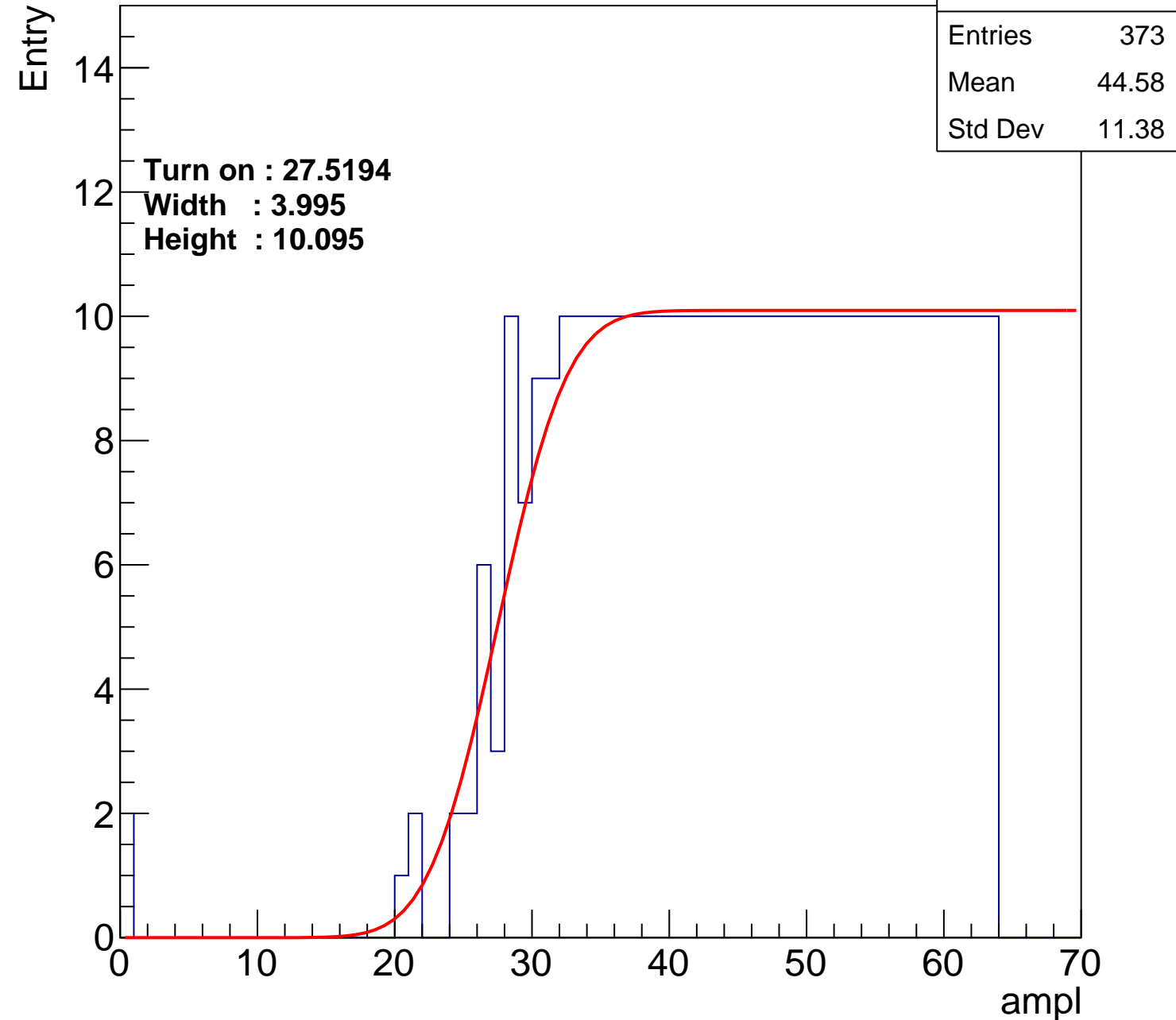
Width : 3.995

Height : 10.095

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch84

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.6
Std Dev	11.36

**Turn on : 26.9788**

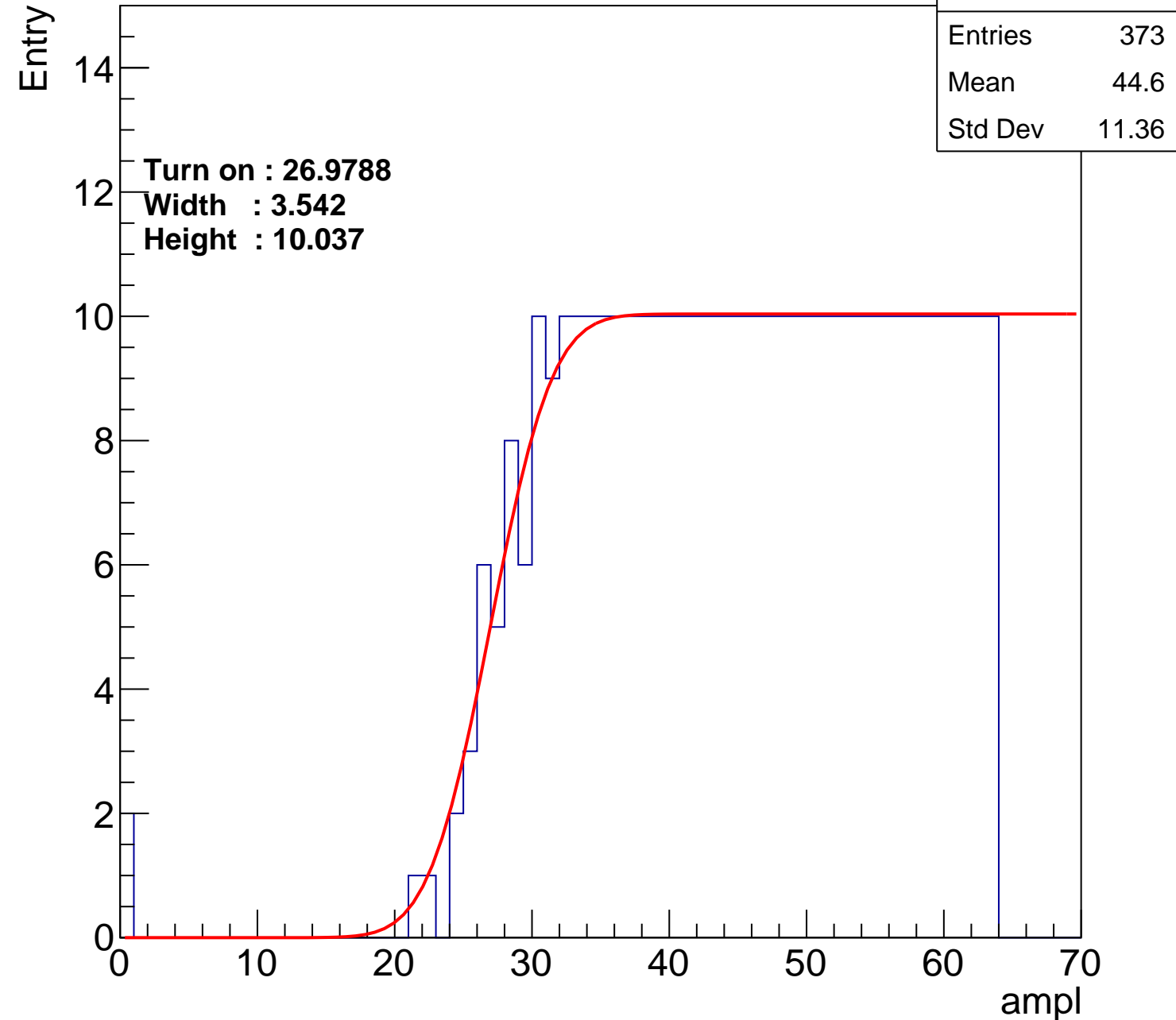
**Width : 3.542**

**Height : 10.037**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch85

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.47
Std Dev	11.72

Turn on : 27.2375

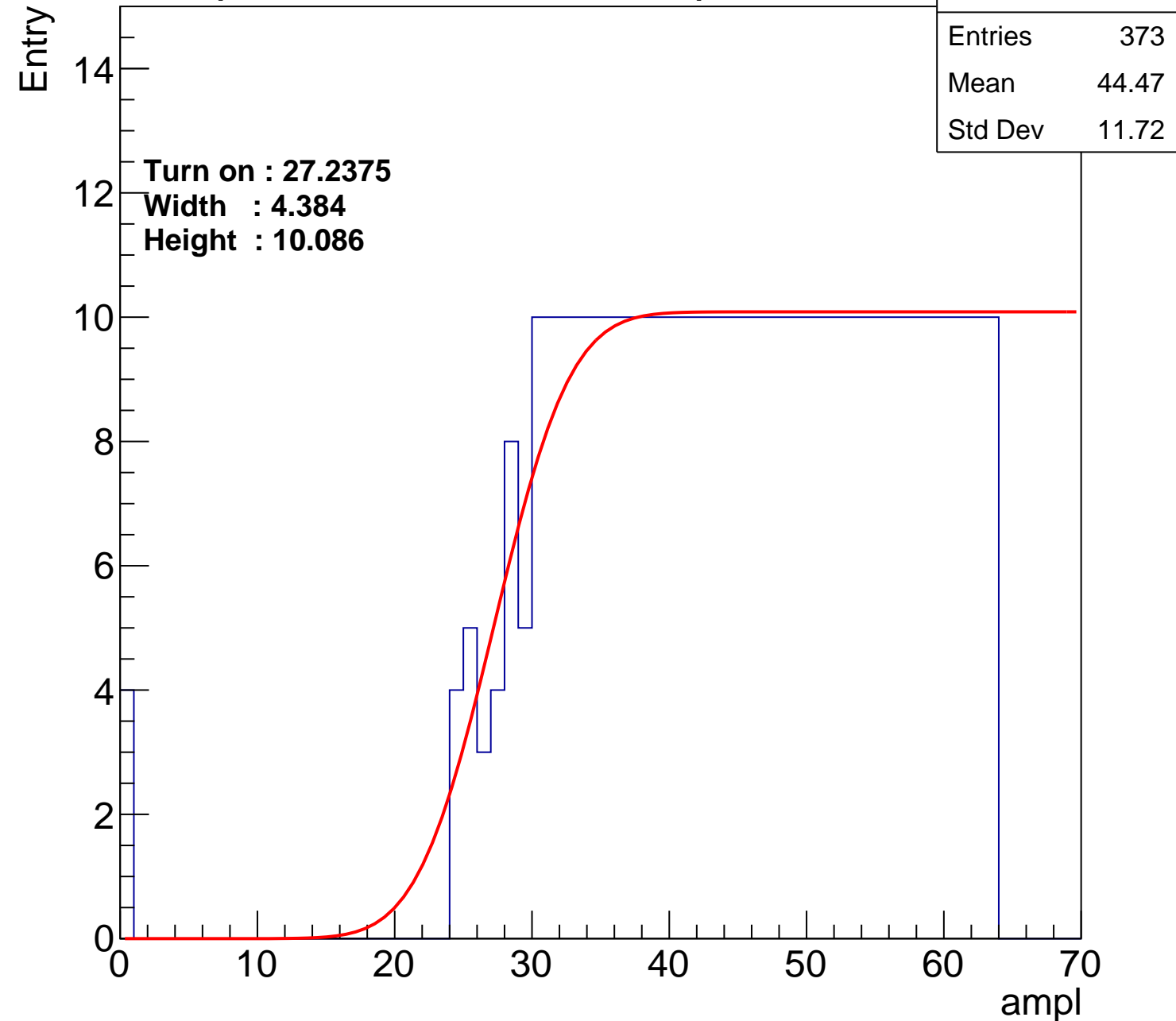
Width : 4.384

Height : 10.086

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch86

calib\_packv5\_042523\_0143.root, FC#8, port C1

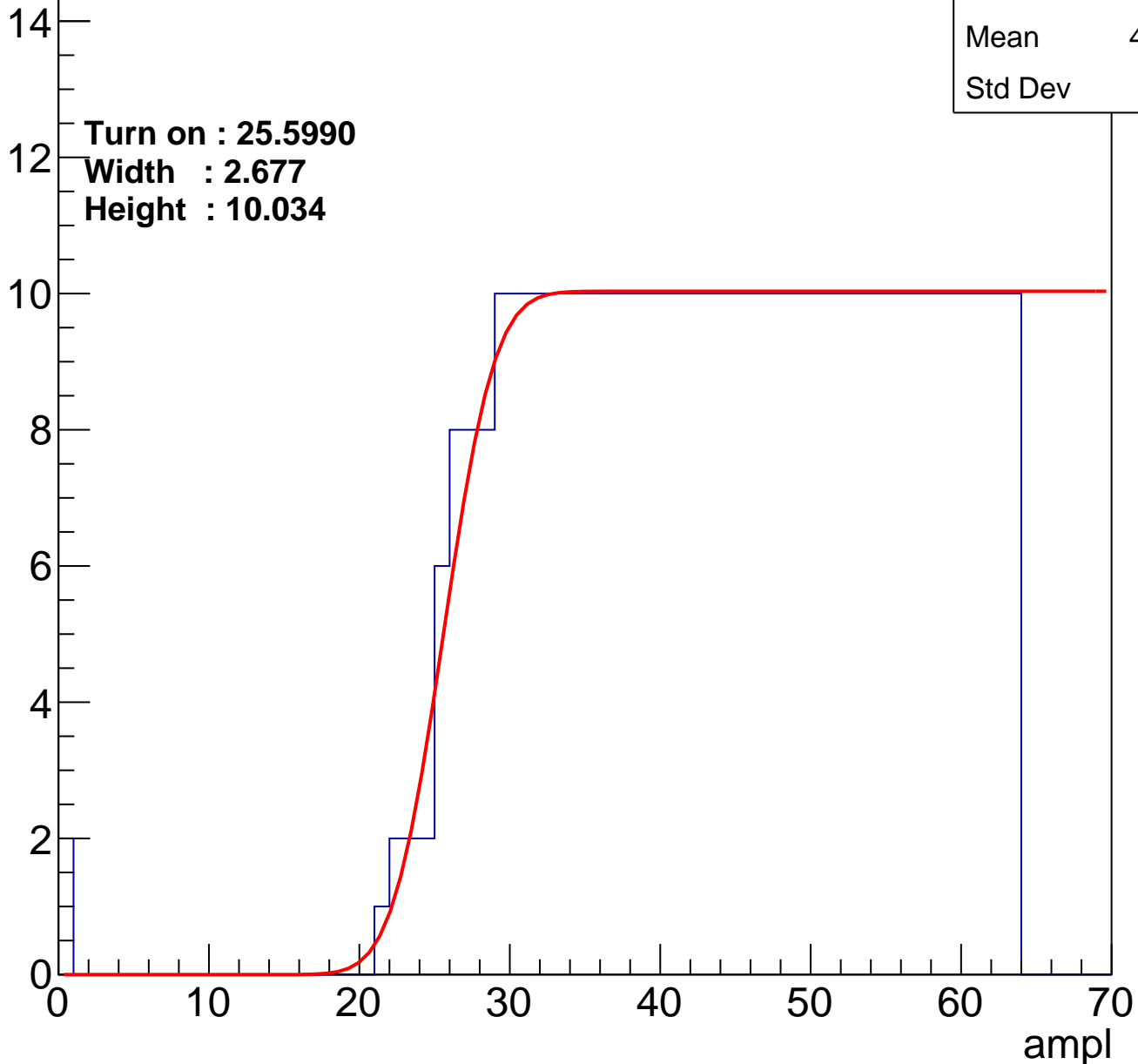
Entries	389
Mean	43.85
Std Dev	11.7

Turn on : 25.5990

Width : 2.677

Height : 10.034

Entry





# B0L002S, U19-ch87

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.42
Std Dev	11.47

**Turn on : 26.5975**

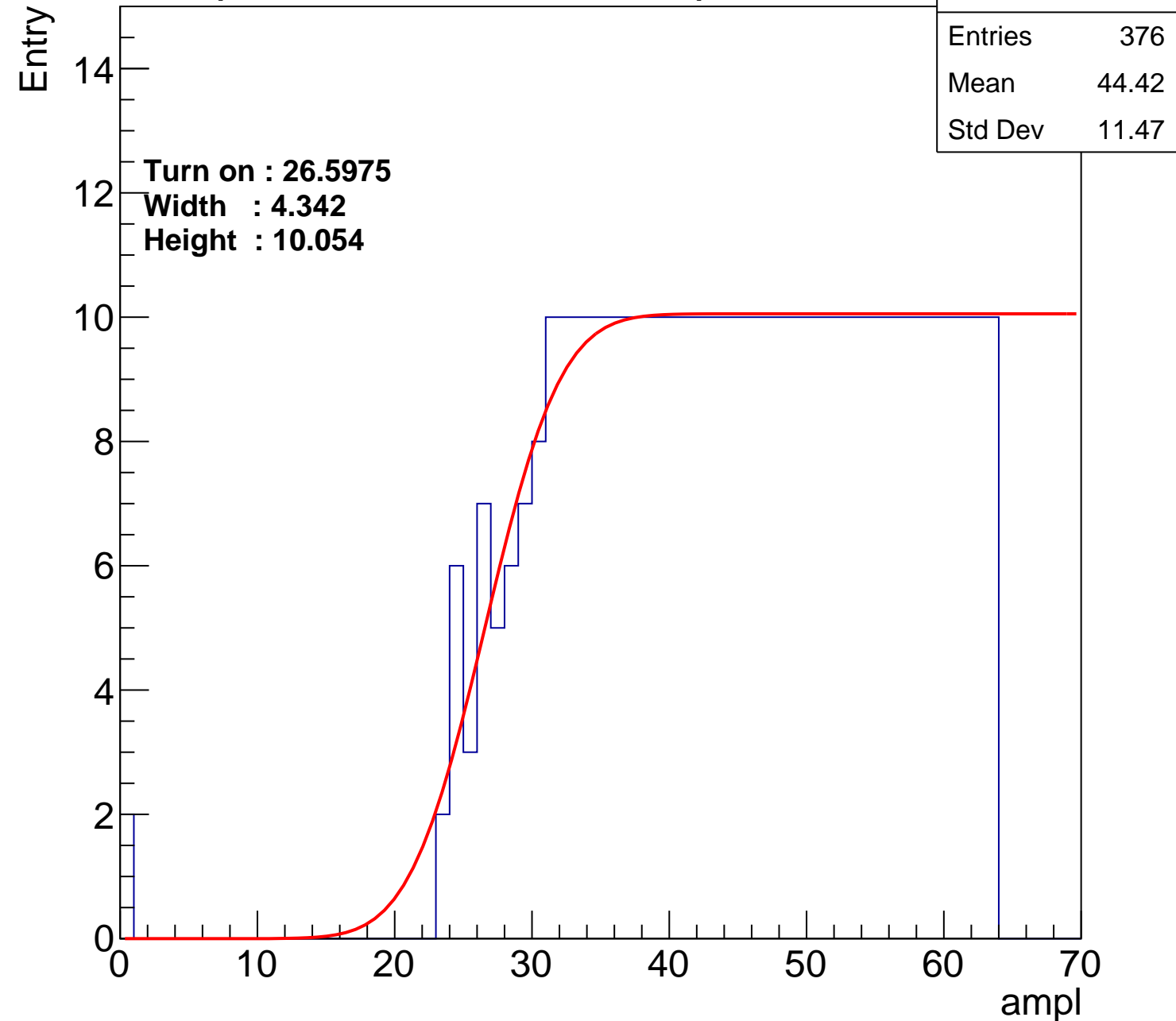
**Width : 4.342**

**Height : 10.054**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch88

calib\_packv5\_042523\_0143.root, FC#8, port C1

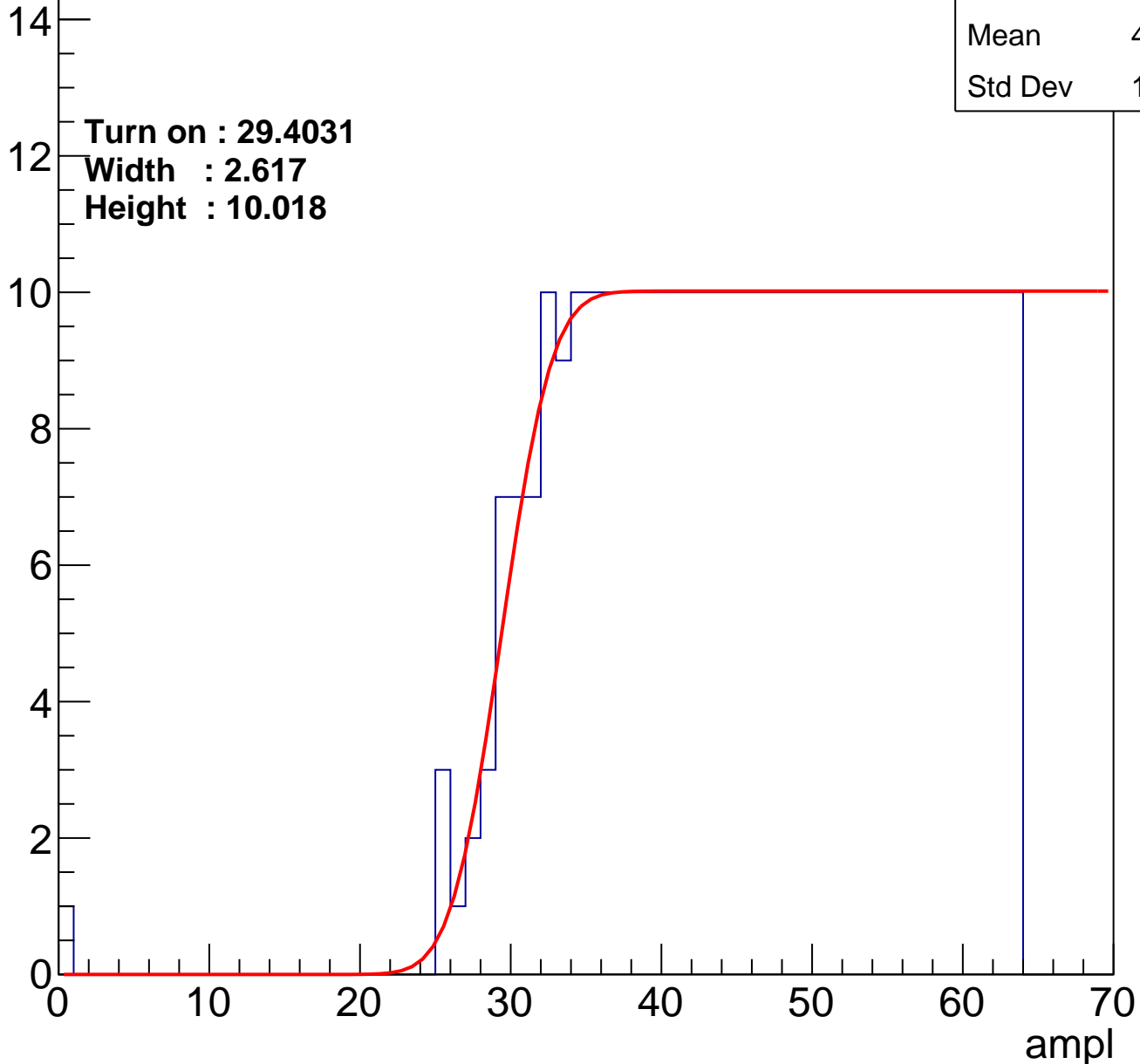
Entries	350
Mean	45.82
Std Dev	10.52

Turn on : 29.4031

Width : 2.617

Height : 10.018

Entry



# B0L002S, U19-ch89

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.07
Std Dev	11.12

Turn on : 28.1703

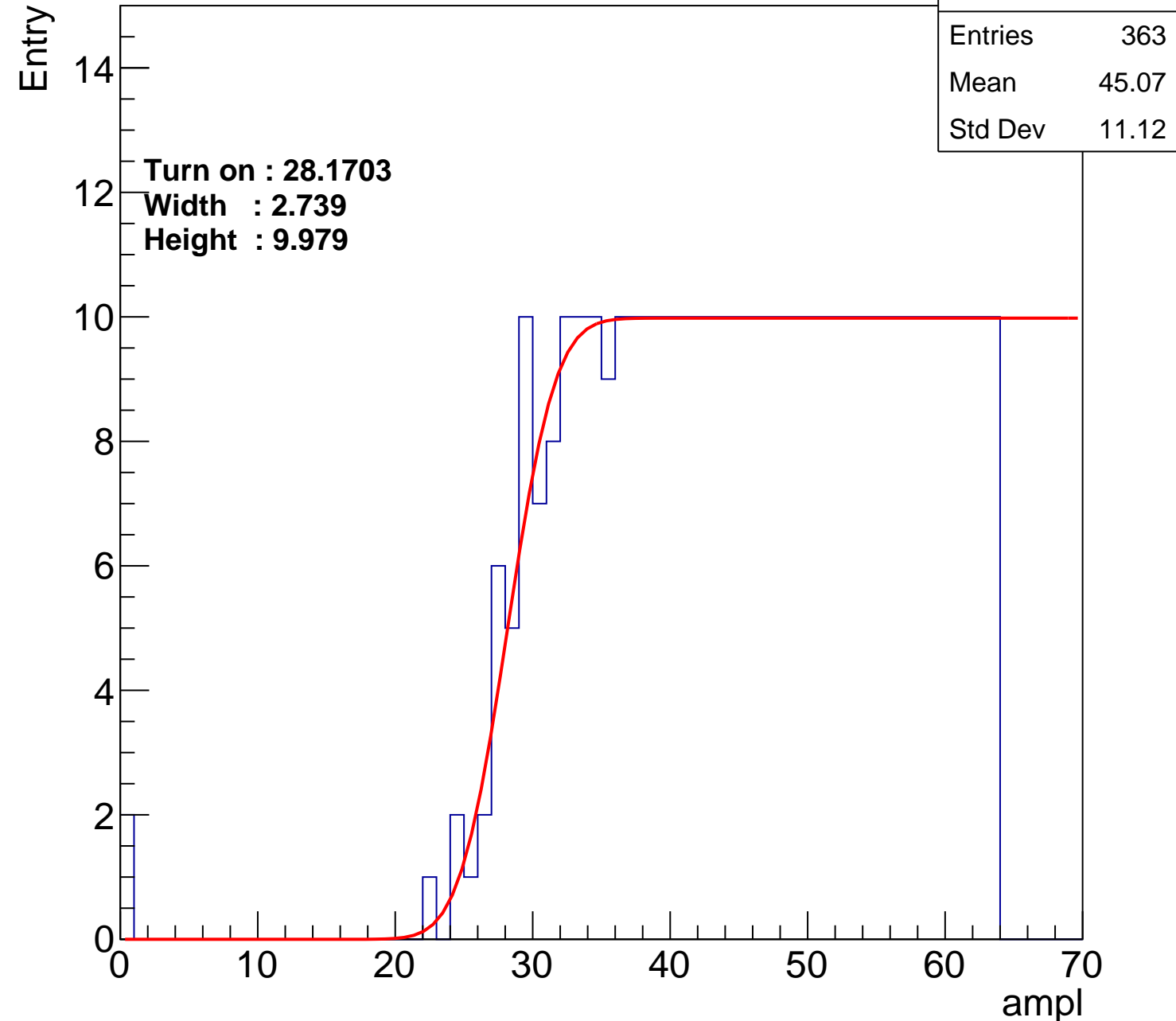
Width : 2.739

Height : 9.979

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch90

calib\_packv5\_042523\_0143.root, FC#8, port C1

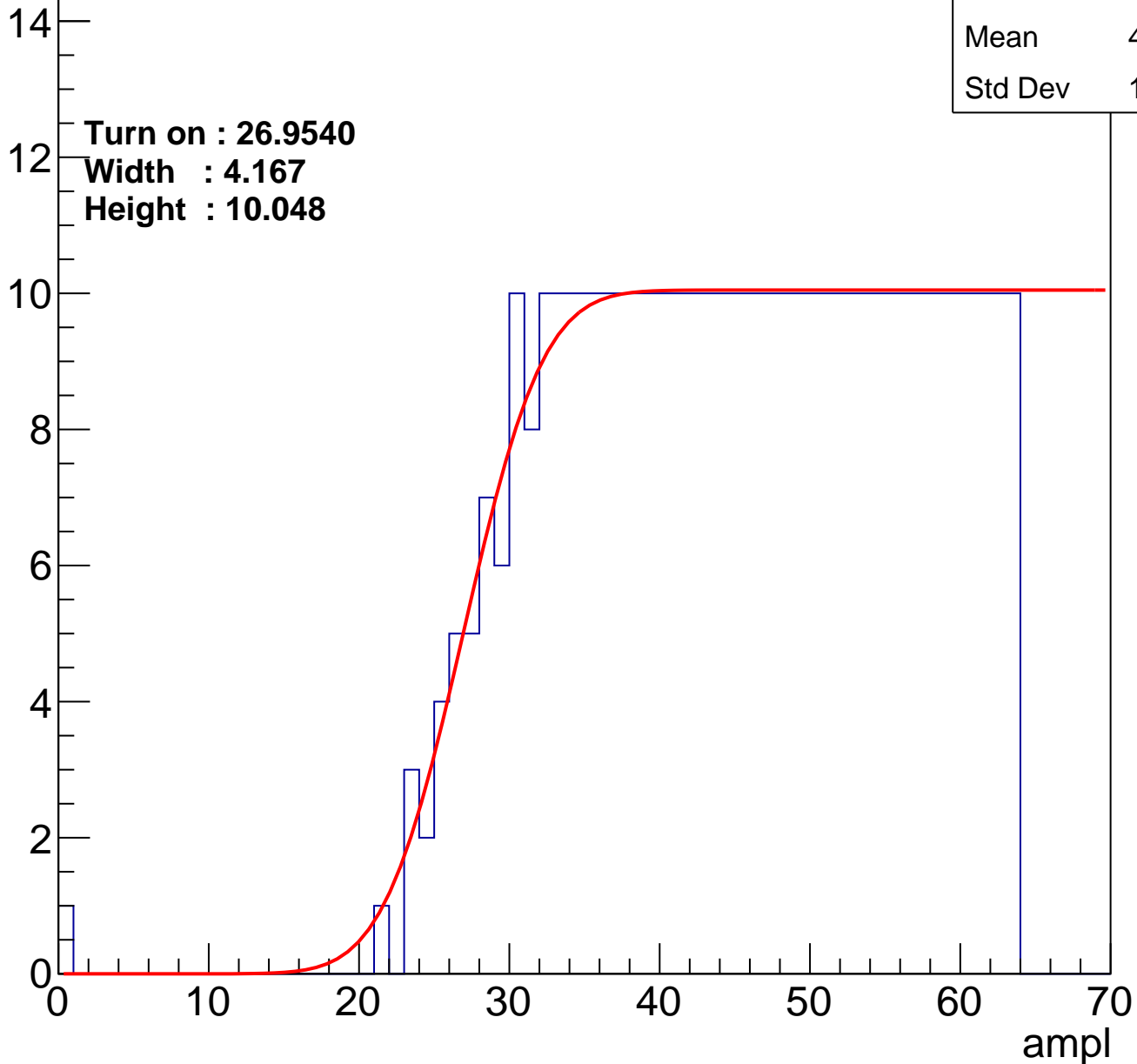
Entries	372
Mean	44.68
Std Dev	11.19

Turn on : 26.9540

Width : 4.167

Height : 10.048

Entry



# B0L002S, U19-ch91

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	349
Mean	45.77
Std Dev	10.66

Turn on : 29.6322

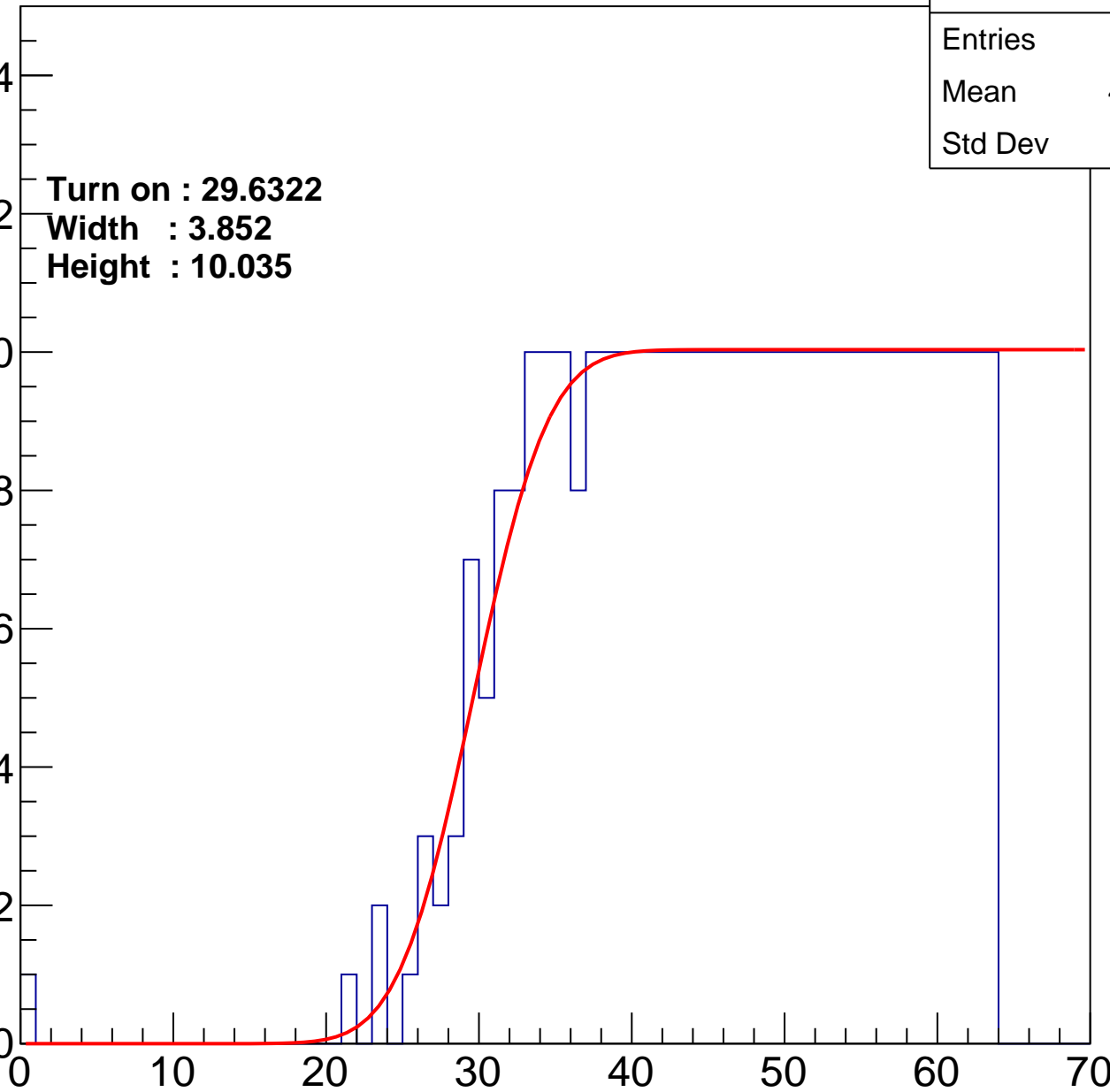
Width : 3.852

Height : 10.035

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch92

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	392
Mean	43.59
Std Dev	12.02

Turn on : 25.3719

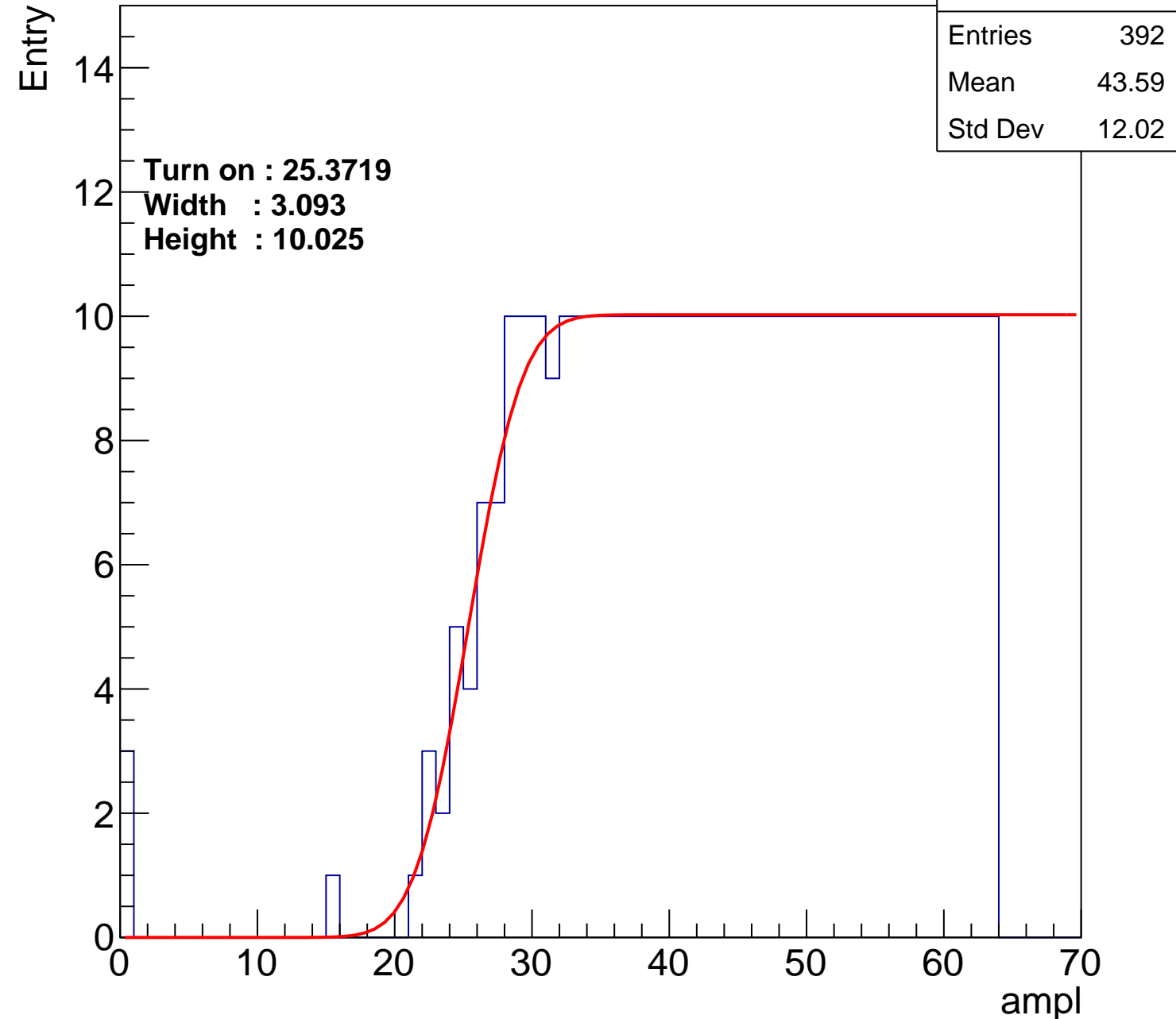
Width : 3.093

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch93

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	45.03
Std Dev	11.09

**Turn on : 27.9083**

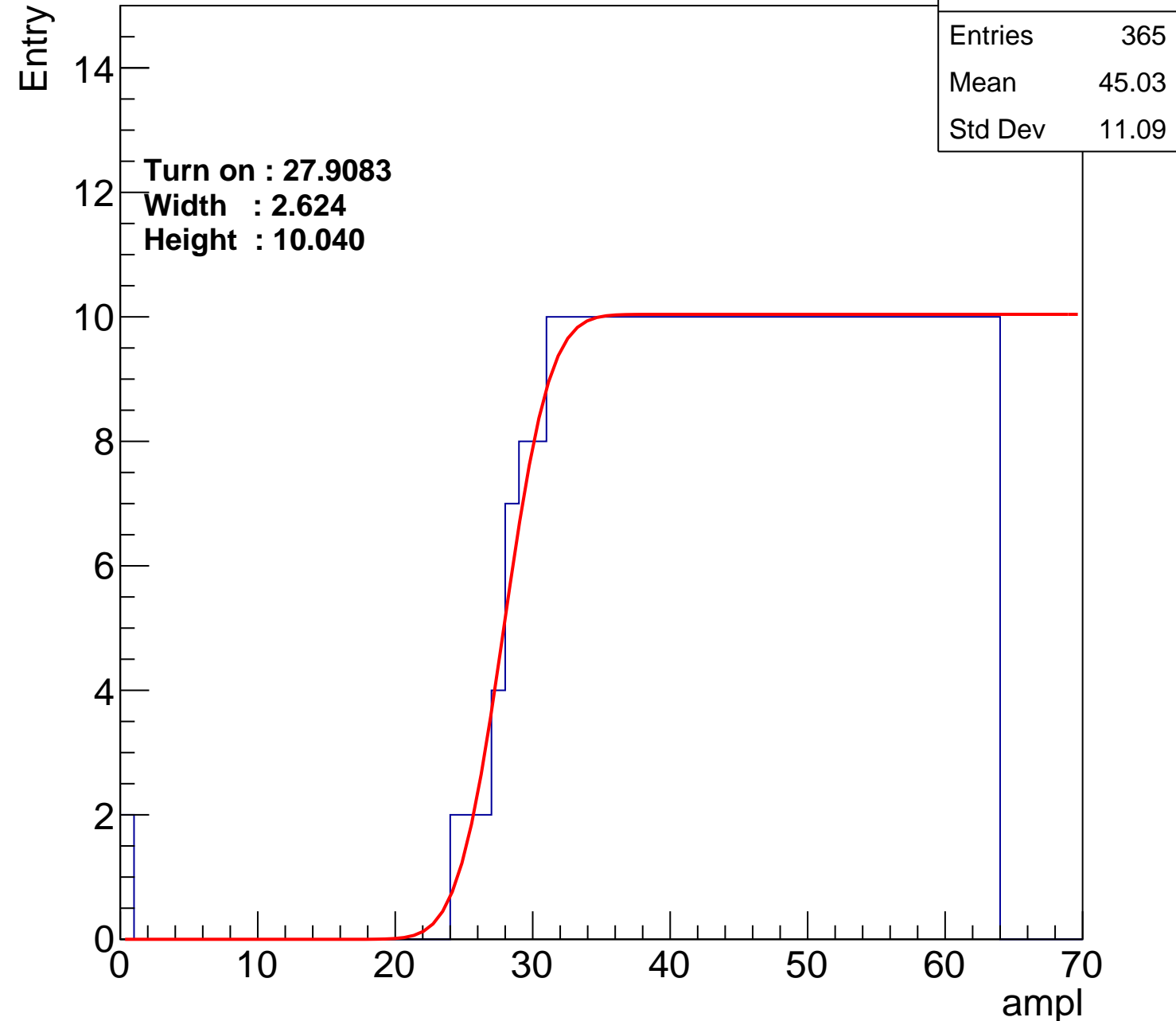
**Width : 2.624**

**Height : 10.040**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**calib\_packv5\_042523\_0143.root, FC#8, port C1**

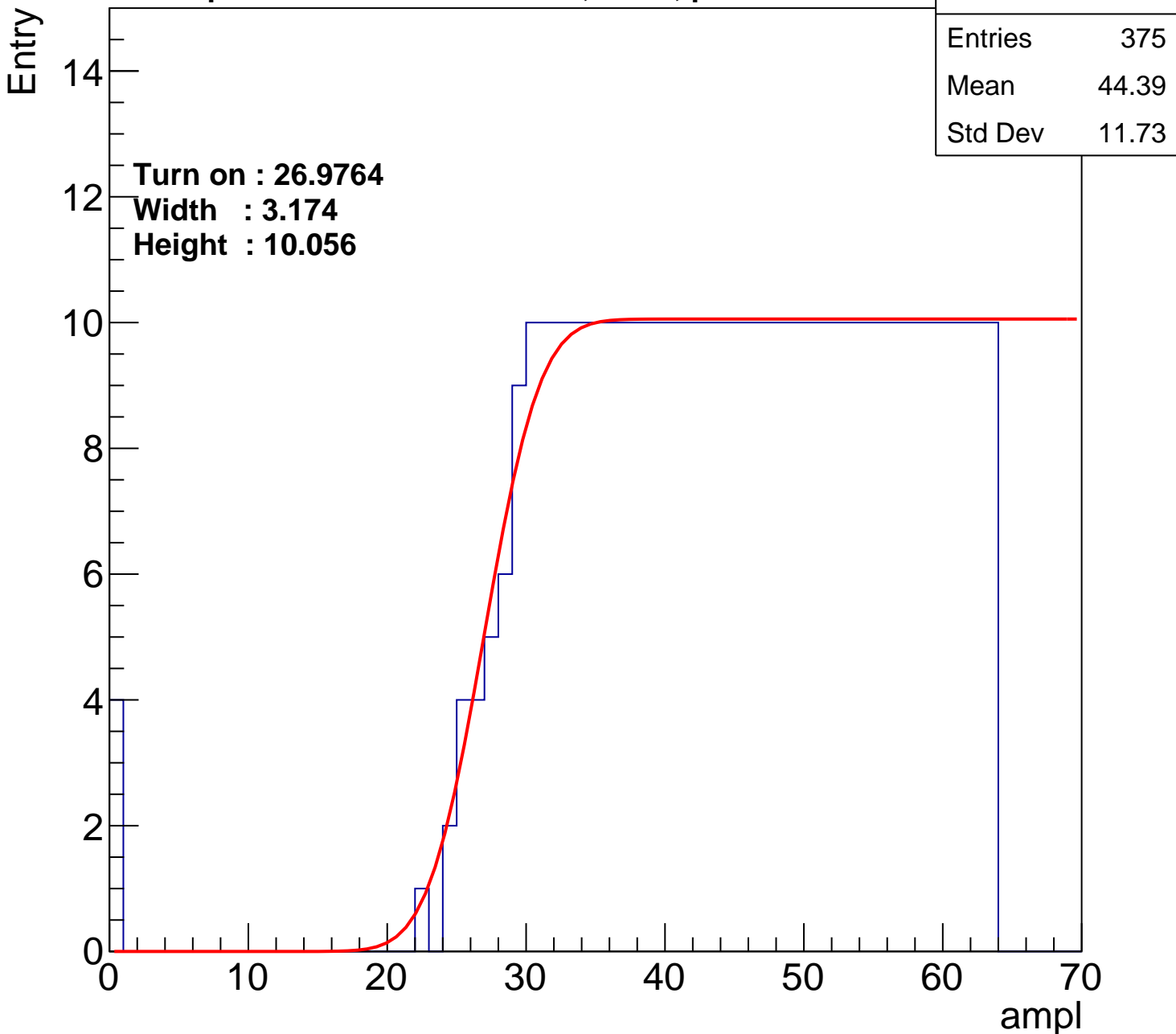
Entries	375
Mean	44.39
Std Dev	11.73

Mean	44.39
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Std Dev	11.73
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**Width : 3.174**

**Height : 10.056**





# B0L002S, U19-ch95

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	357
Mean	45.33
Std Dev	11.14

**Turn on : 29.2395**

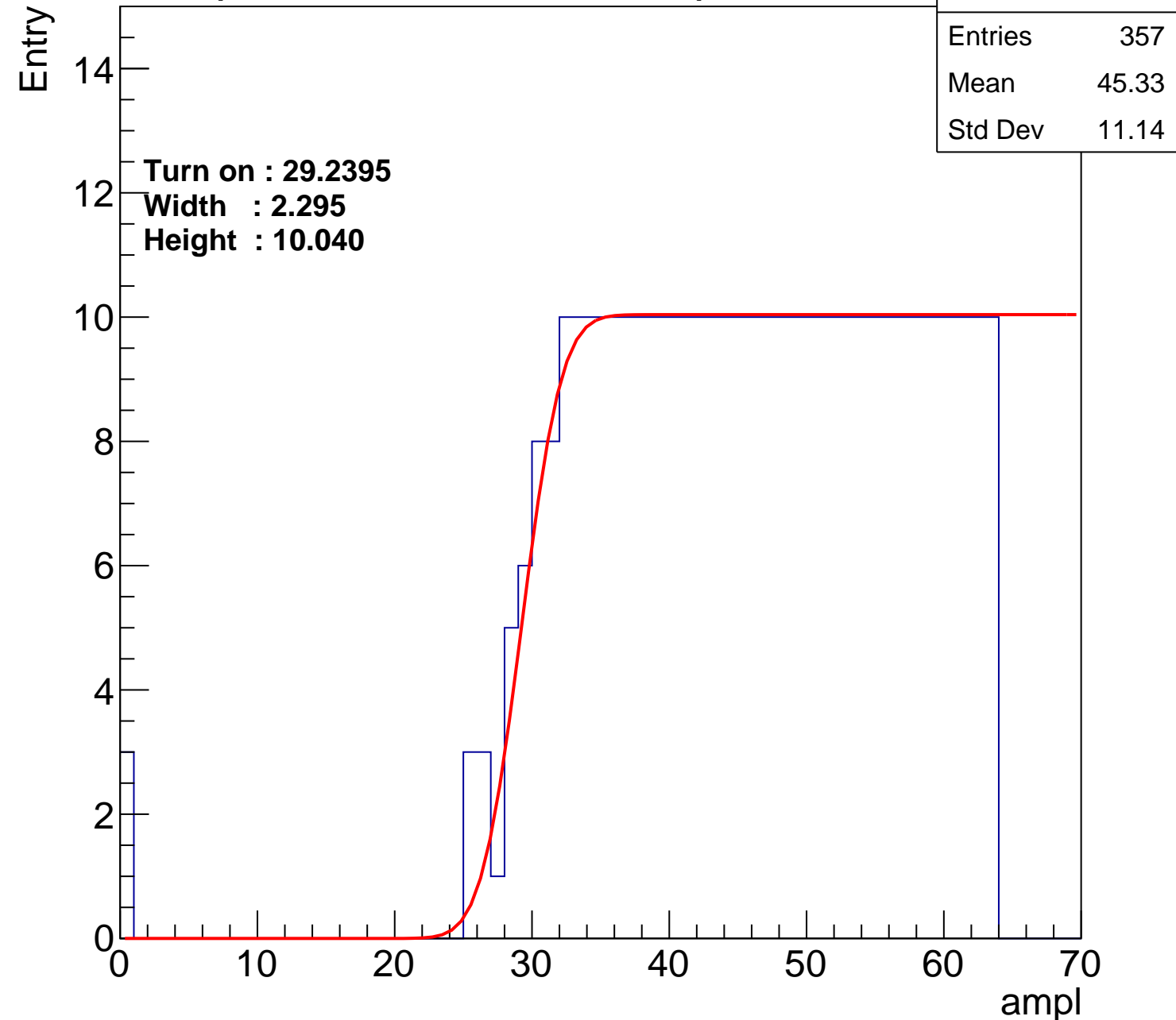
**Width : 2.295**

**Height : 10.040**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



**B0L002S, U19-ch96**

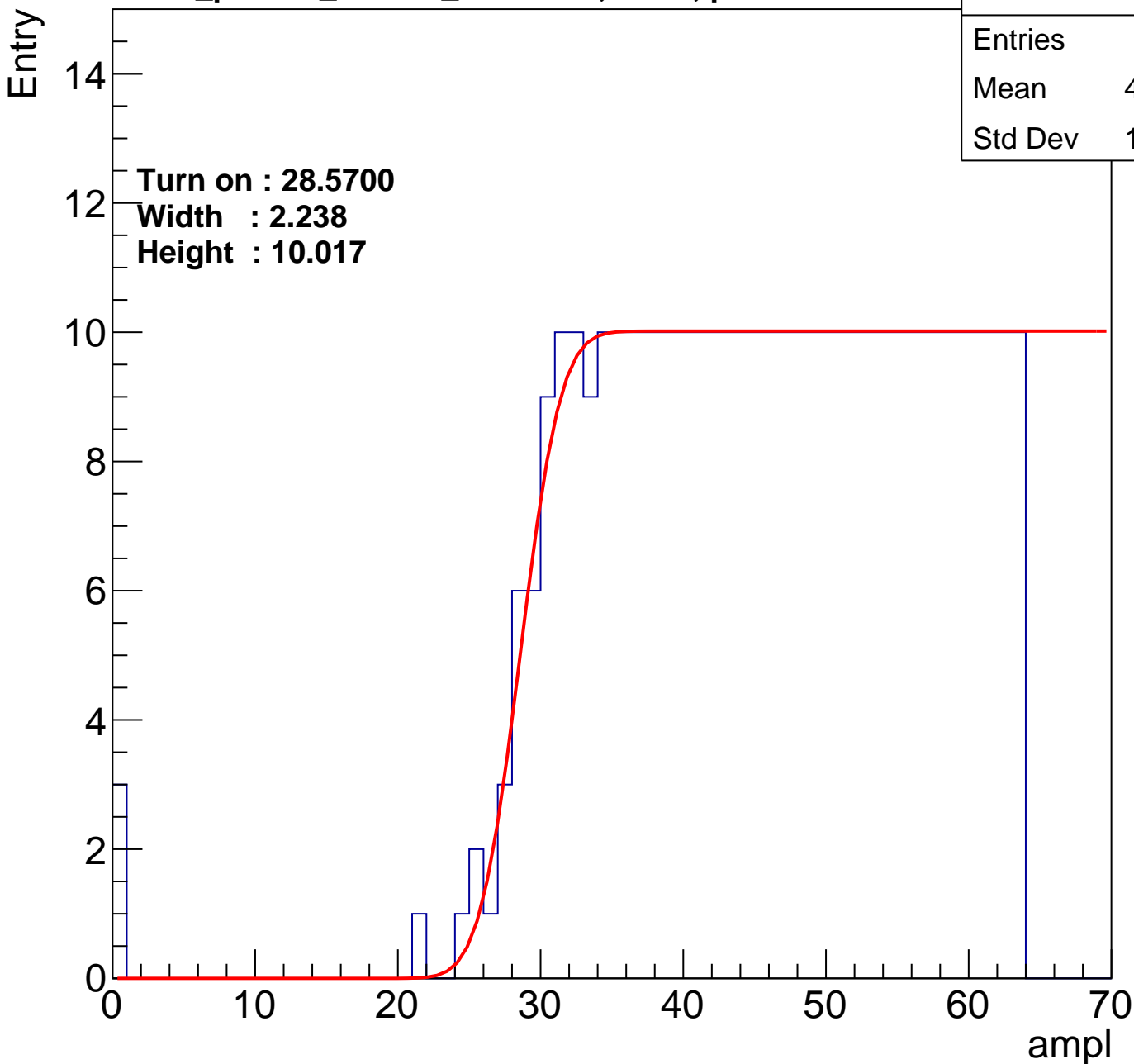
**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	361
Mean	45.13
Std Dev	11.24

**Turn on : 28.5700**

**Width : 2.238**

**Height : 10.017**



# B0L002S, U19-ch97

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	44.99
Std Dev	11.3

Turn on : 28.4929

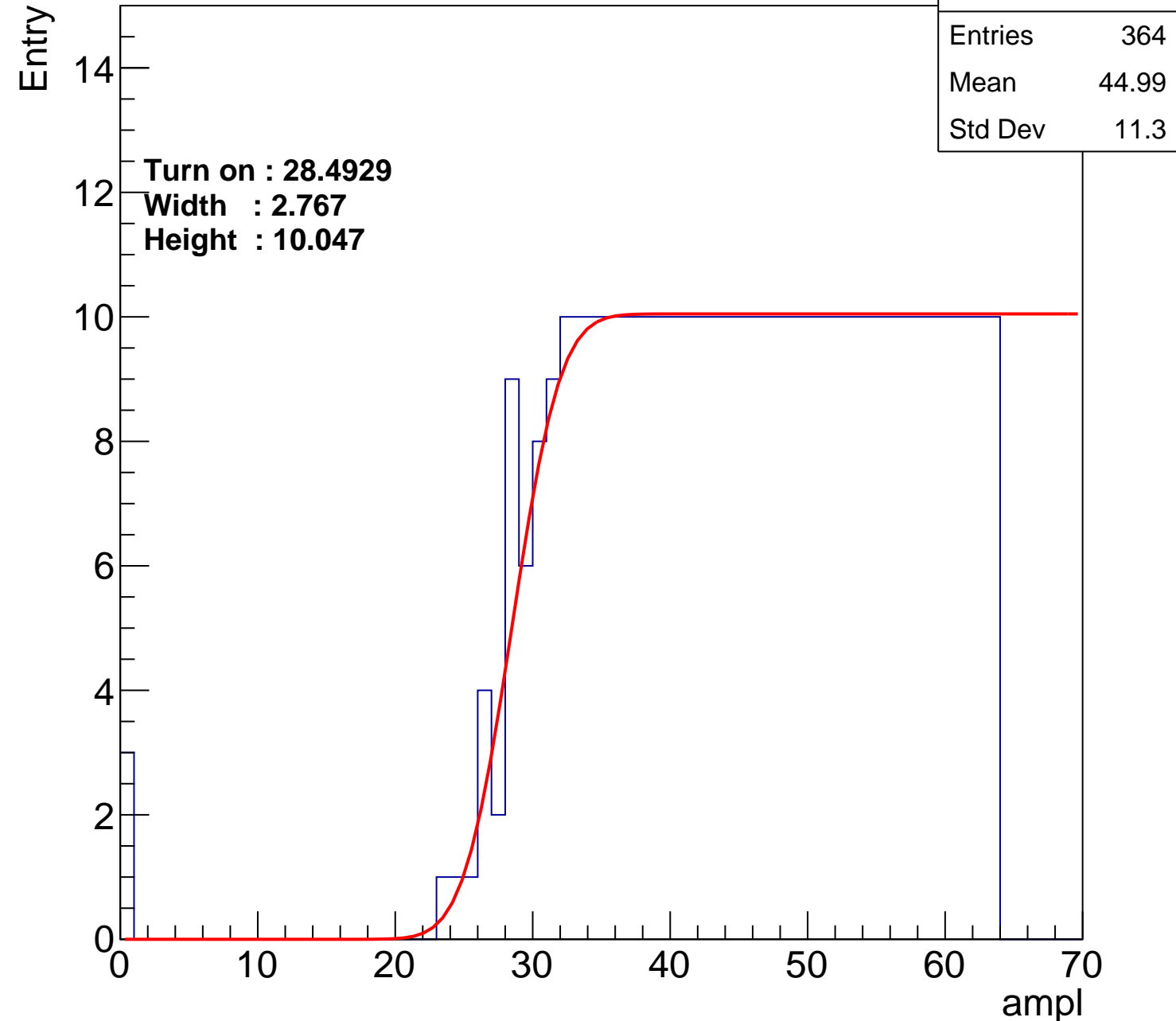
Width : 2.767

Height : 10.047

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch98

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	341
Mean	46.18
Std Dev	10.53

**Turn on : 29.9867**

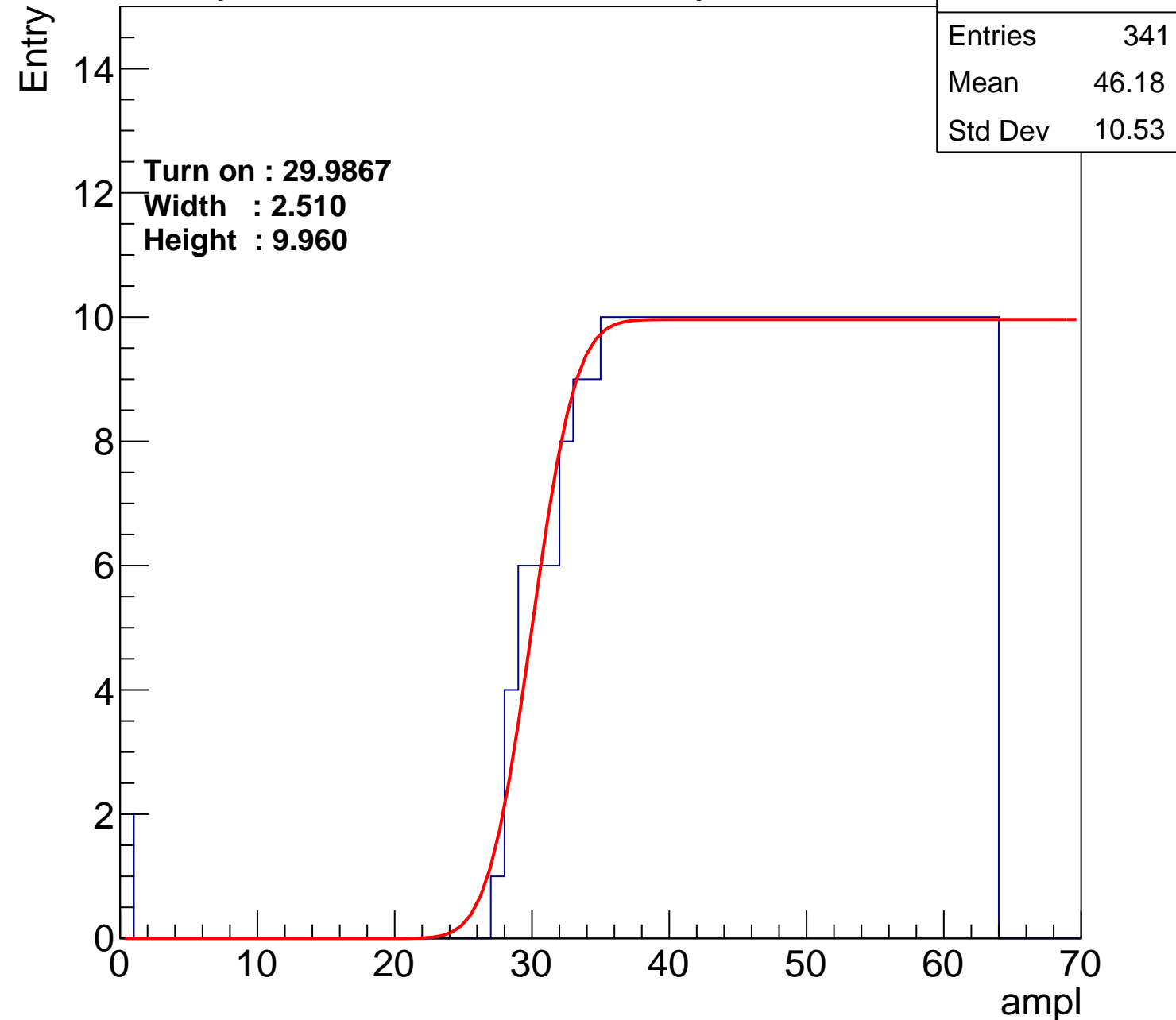
**Width : 2.510**

**Height : 9.960**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch99

calib\_packv5\_042523\_0143.root, FC#8, port C1

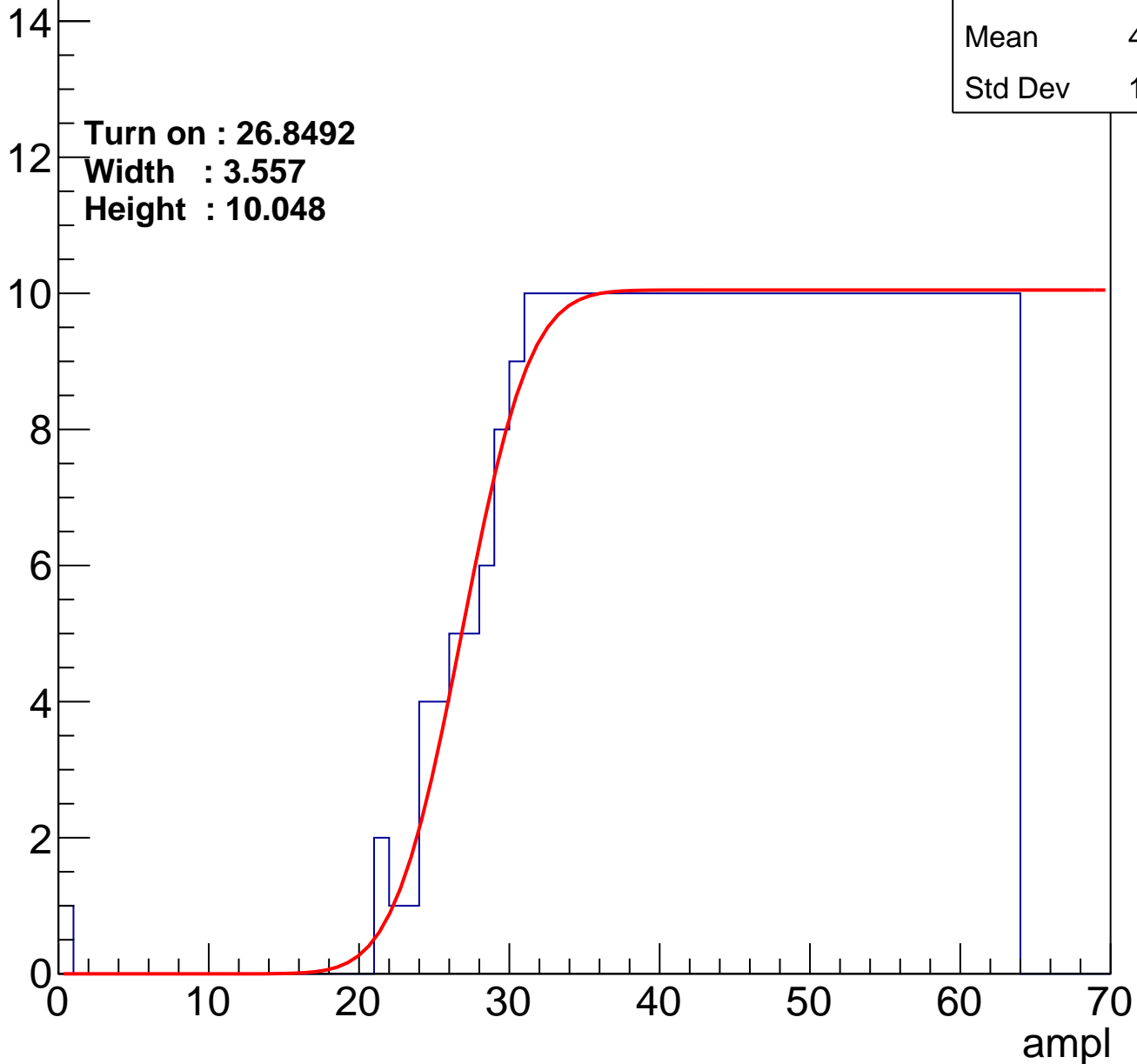
Entries	376
Mean	44.49
Std Dev	11.29

Turn on : 26.8492

Width : 3.557

Height : 10.048

Entry



# B0L002S, U19-ch100

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.14
Std Dev	11.13

**Turn on : 28.3419**

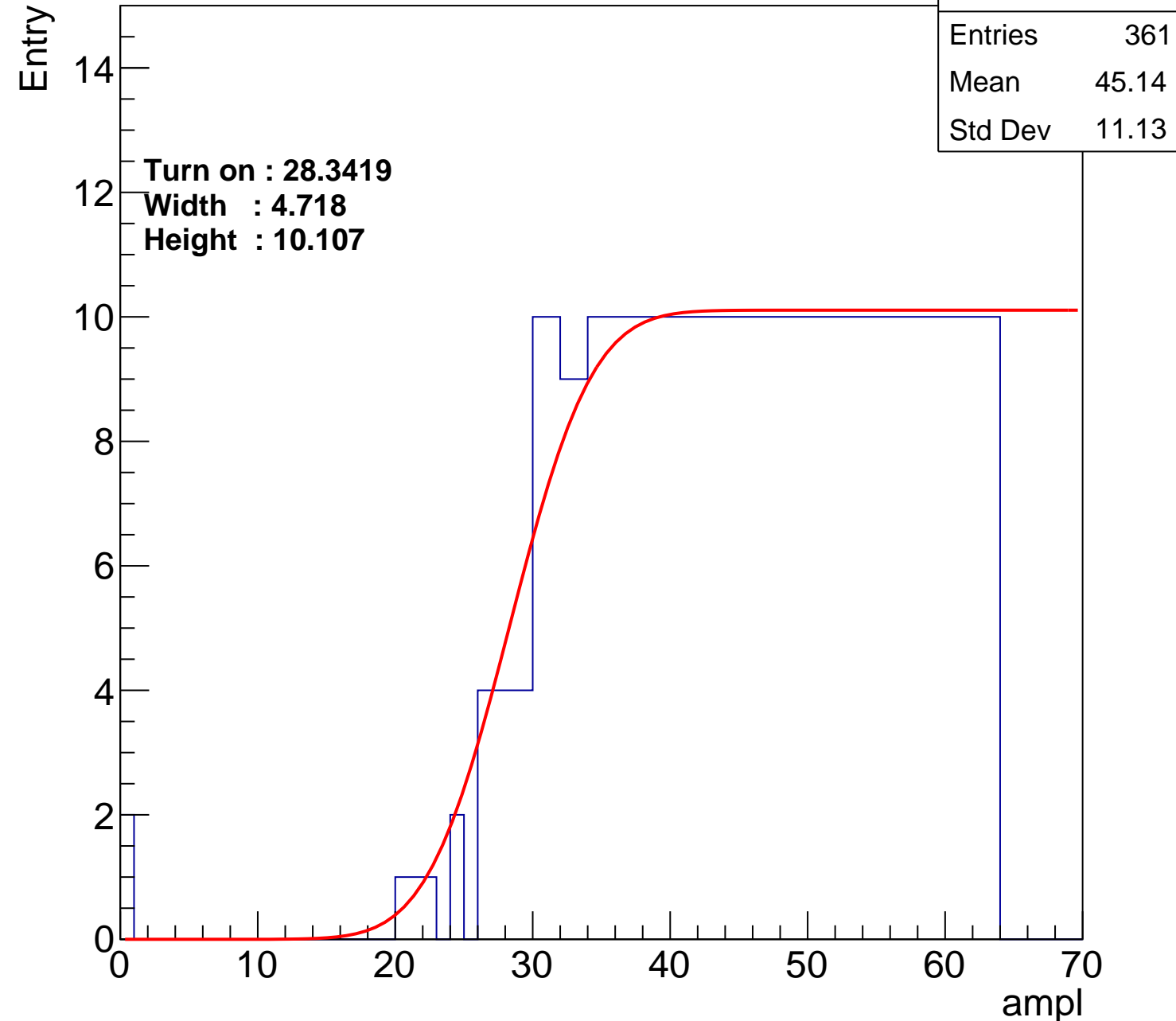
**Width : 4.718**

**Height : 10.107**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch101

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.35
Std Dev	11.42

Turn on : 27.3686

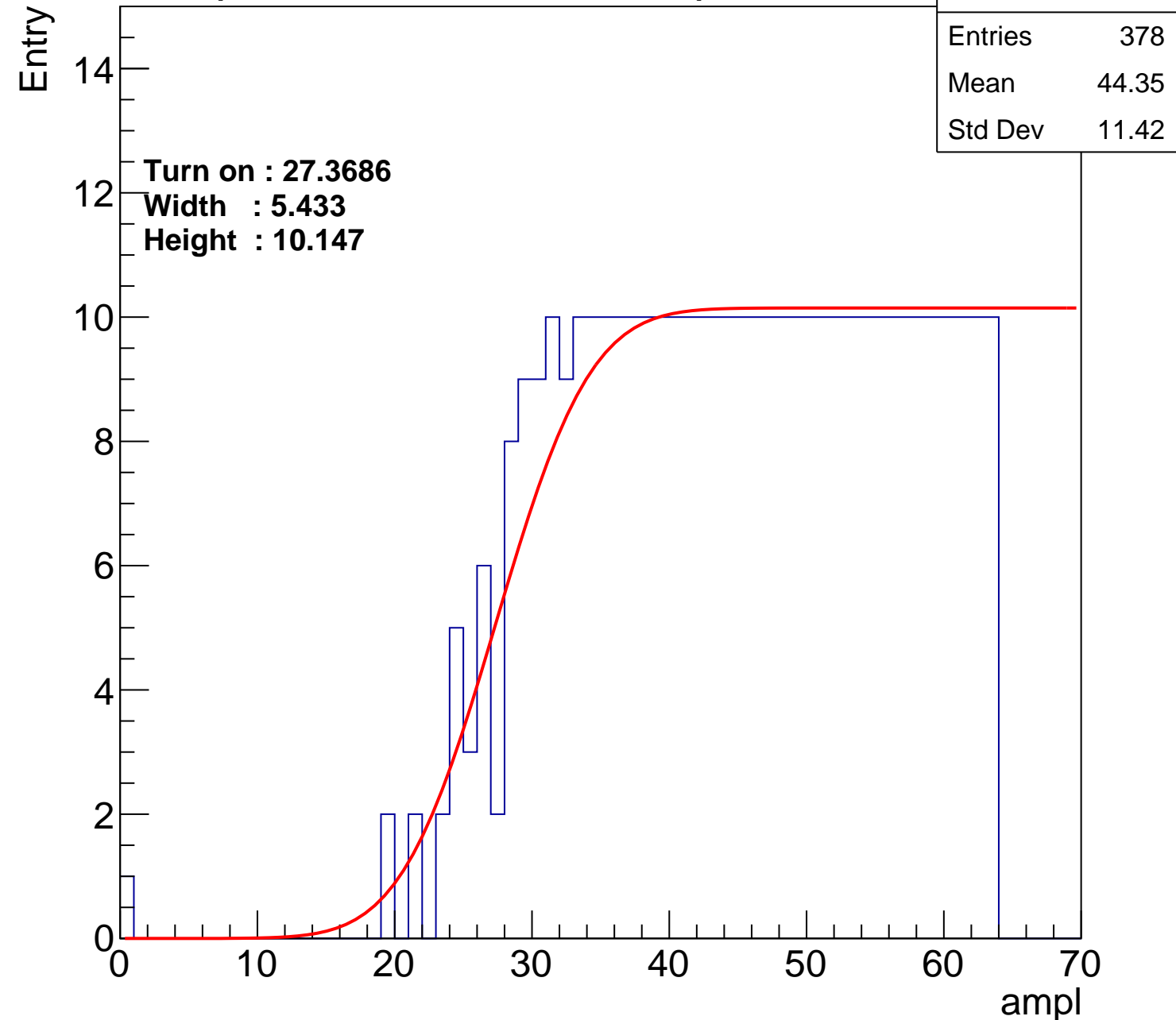
Width : 5.433

Height : 10.147

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch102

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.52
Std Dev	11.55

Turn on : 27.1258

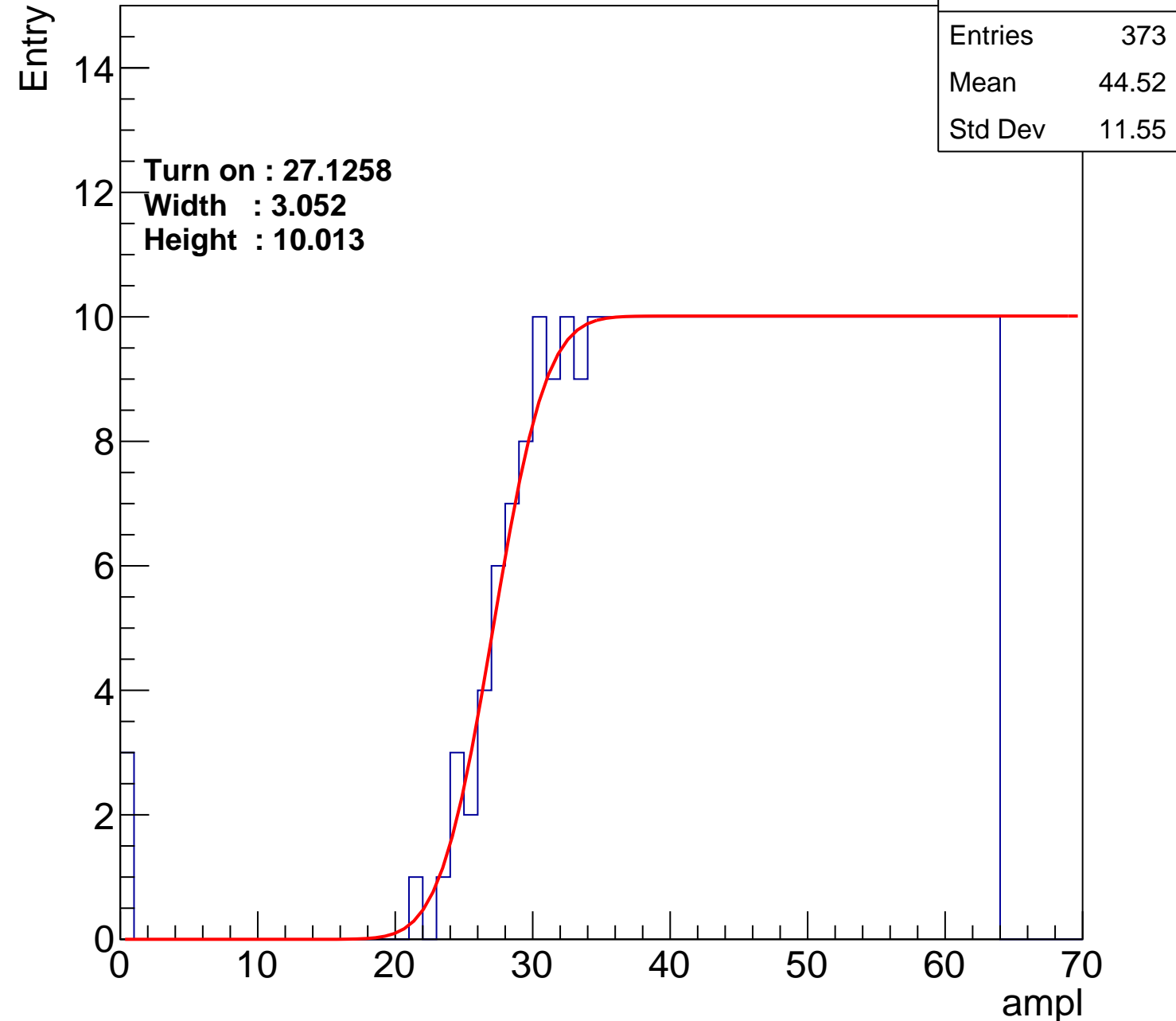
Width : 3.052

Height : 10.013

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch103

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.28
Std Dev	11.49

**Turn on : 26.2670**

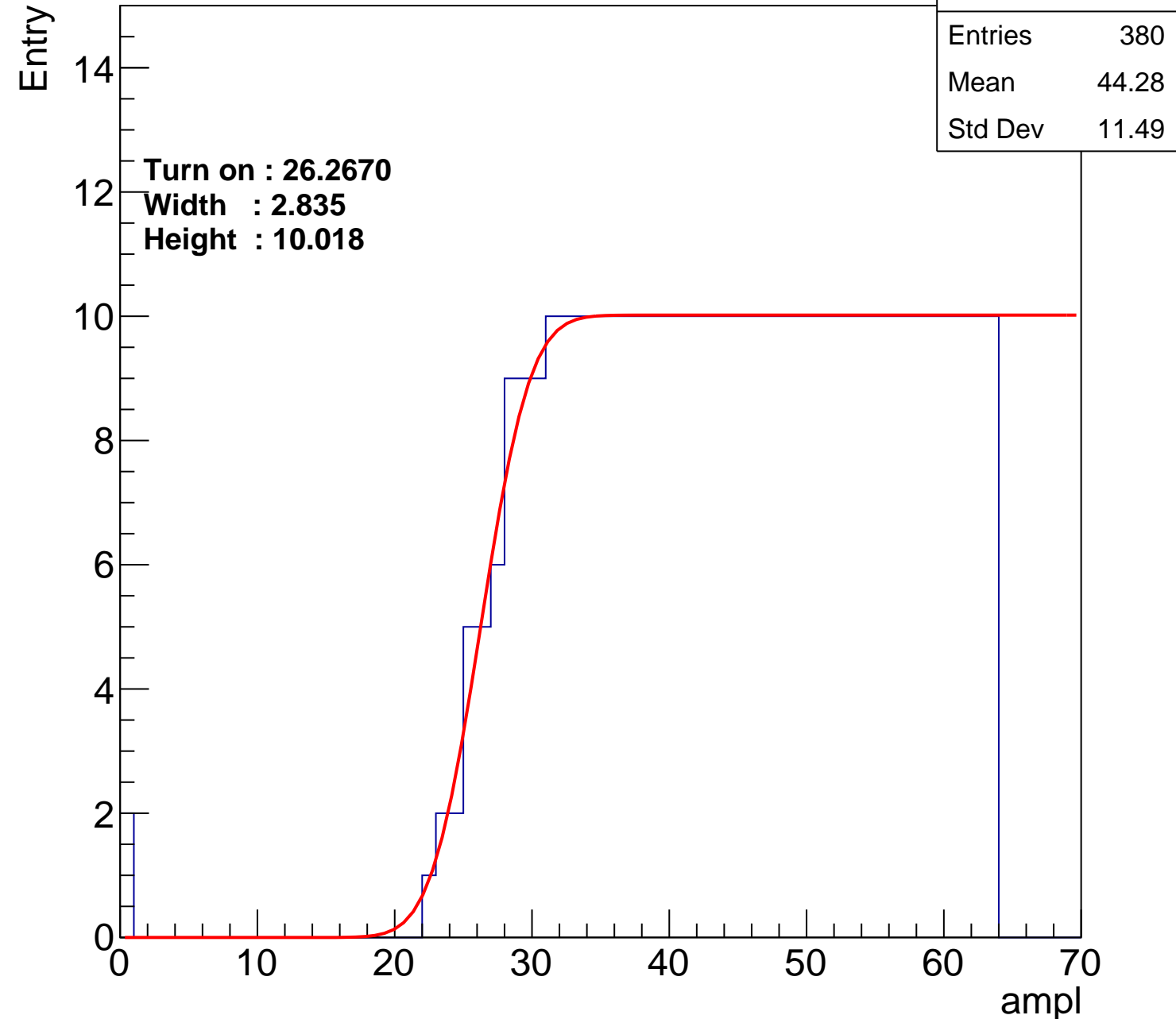
**Width : 2.835**

**Height : 10.018**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch104

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.66
Std Dev	10.83

Turn on : 29.1462

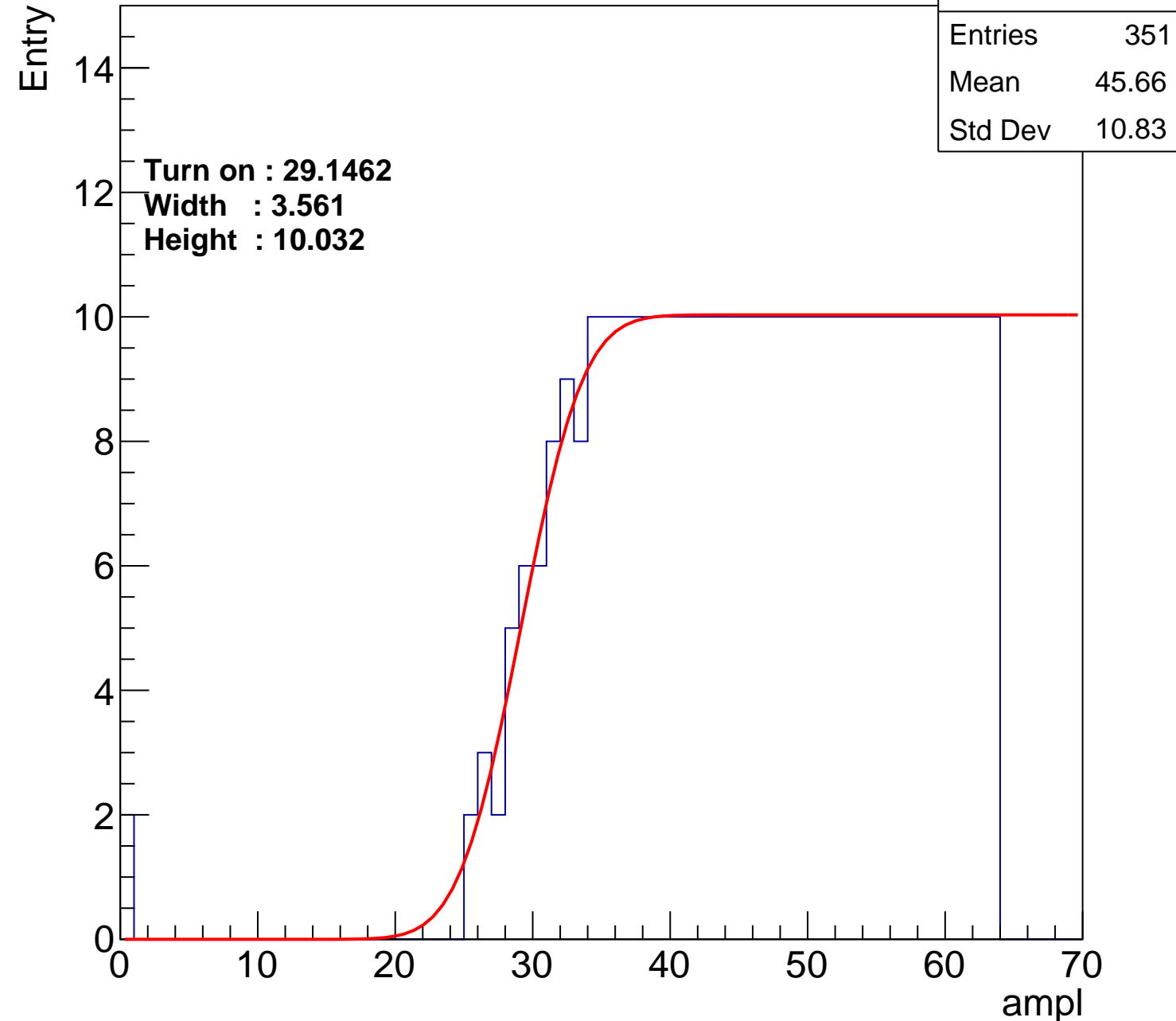
Width : 3.561

Height : 10.032

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch105

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	361
Mean	45.13
Std Dev	11.23

**Turn on : 28.3176**

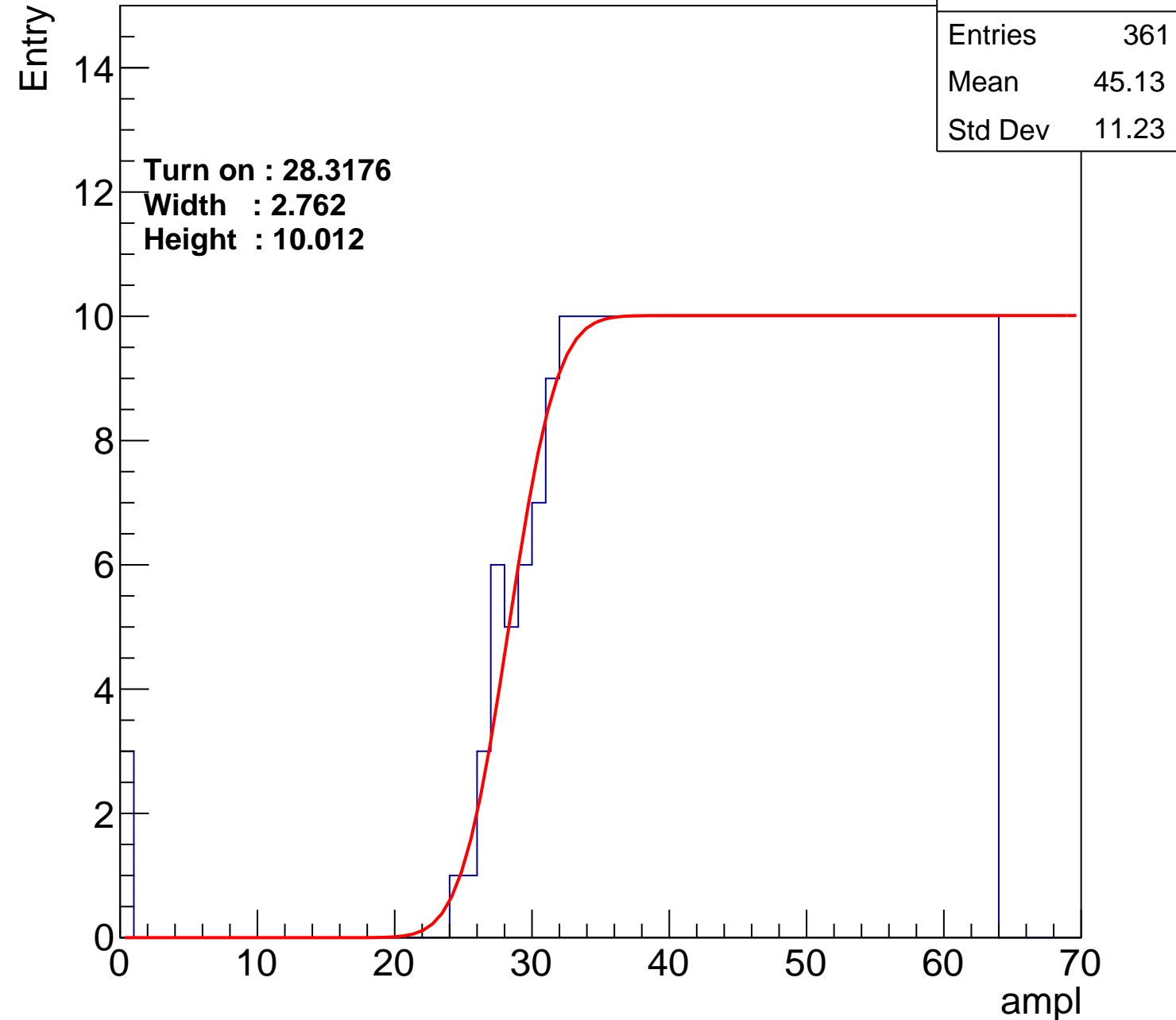
**Width : 2.762**

**Height : 10.012**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch106

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	364
Mean	45.02
Std Dev	11.15

**Turn on : 28.0643**

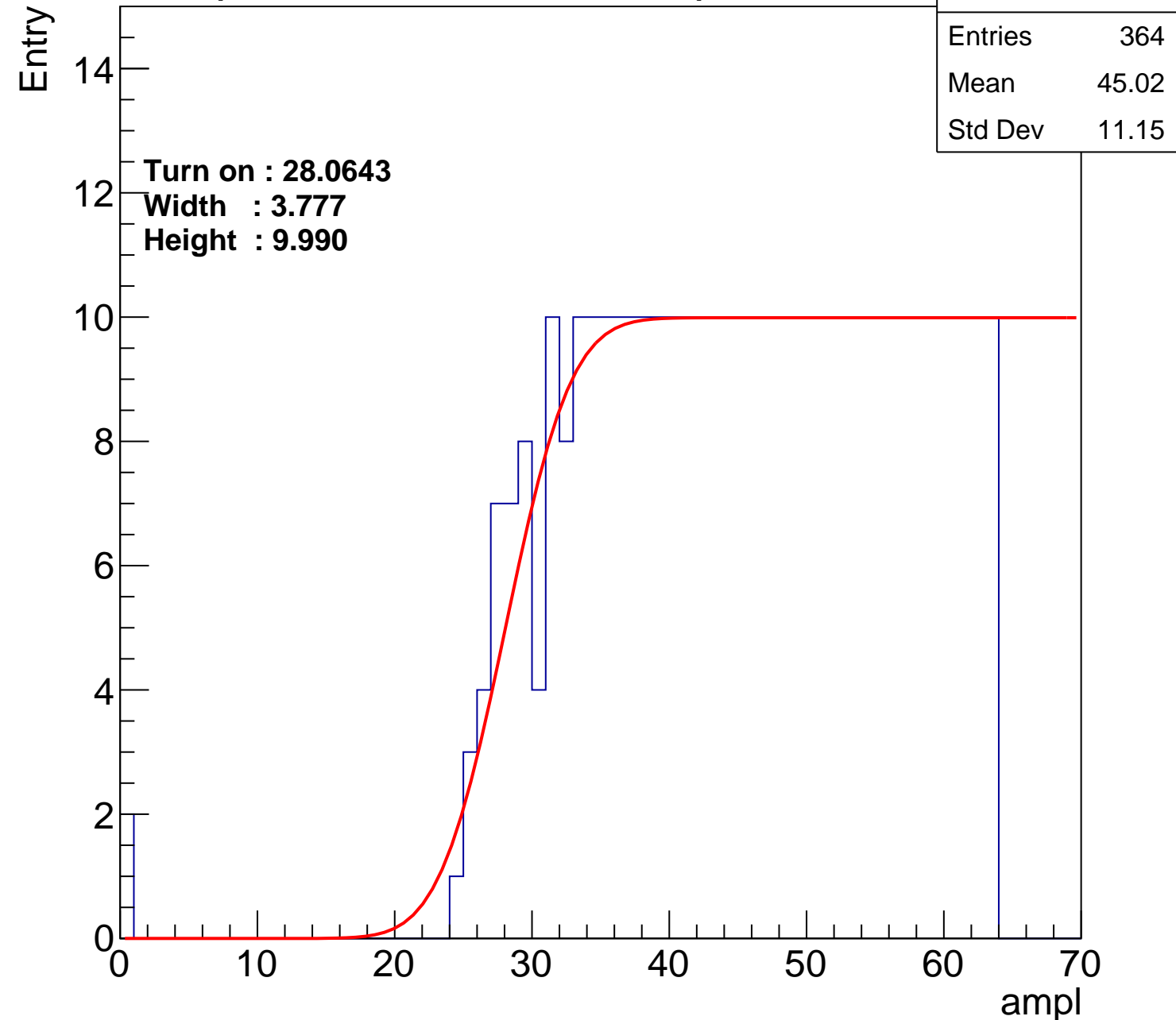
**Width : 3.777**

**Height : 9.990**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch107

**calib\_packv5\_042523\_0143.root, FC#8, port C1**

Entries	360
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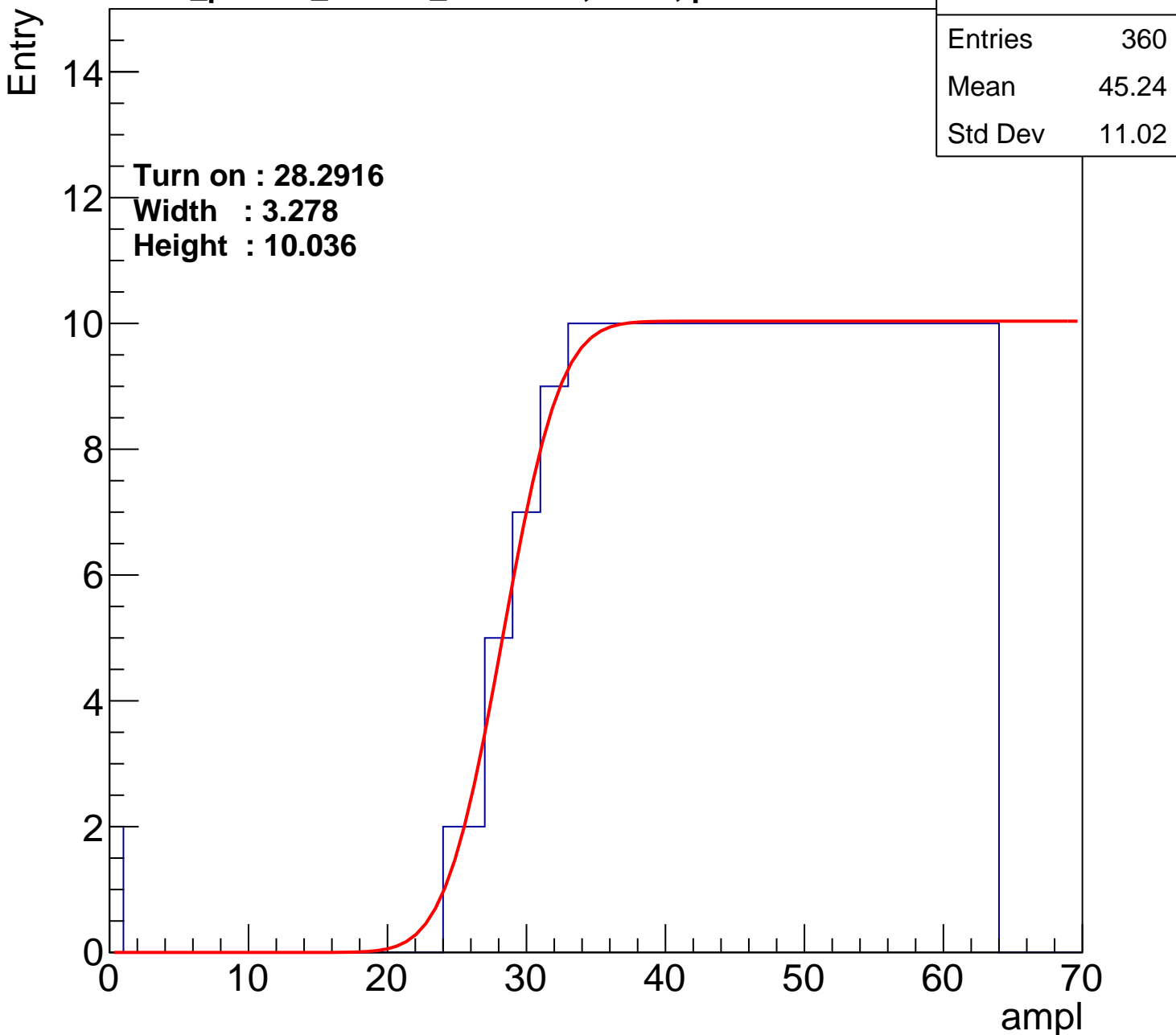
Mean	45.24
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Std Dev	11.02
---------	-------

**Turn on : 28.2916**

**Width : 3.278**

**Height : 10.036**



# B0L002S, U19-ch108

calib\_packv5\_042523\_0143.root, FC#8, port C1

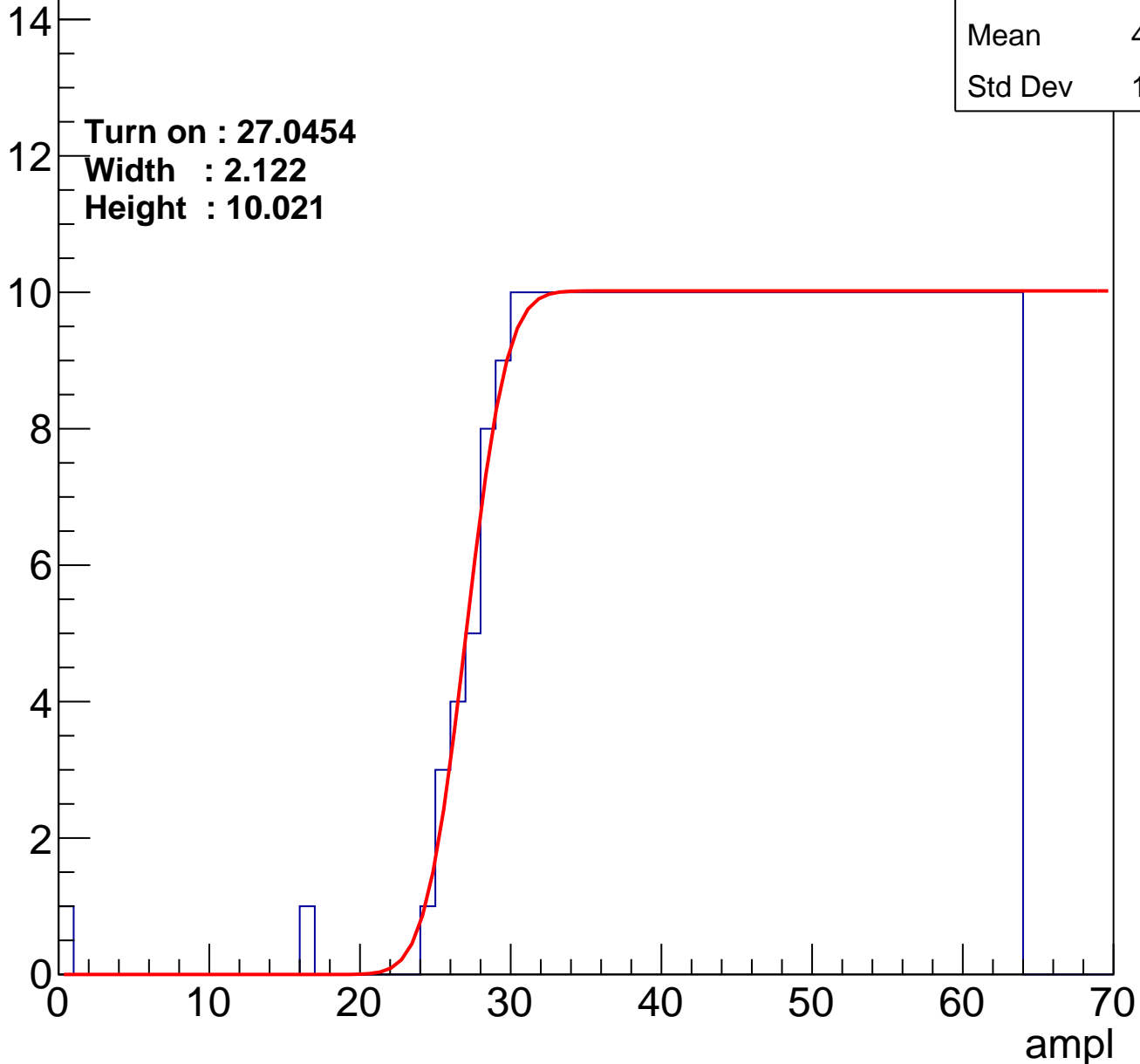
Entries	372
Mean	44.76
Std Dev	11.08

Turn on : 27.0454

Width : 2.122

Height : 10.021

Entry



# B0L002S, U19-ch109

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.21
Std Dev	11.68

Turn on : 26.3240

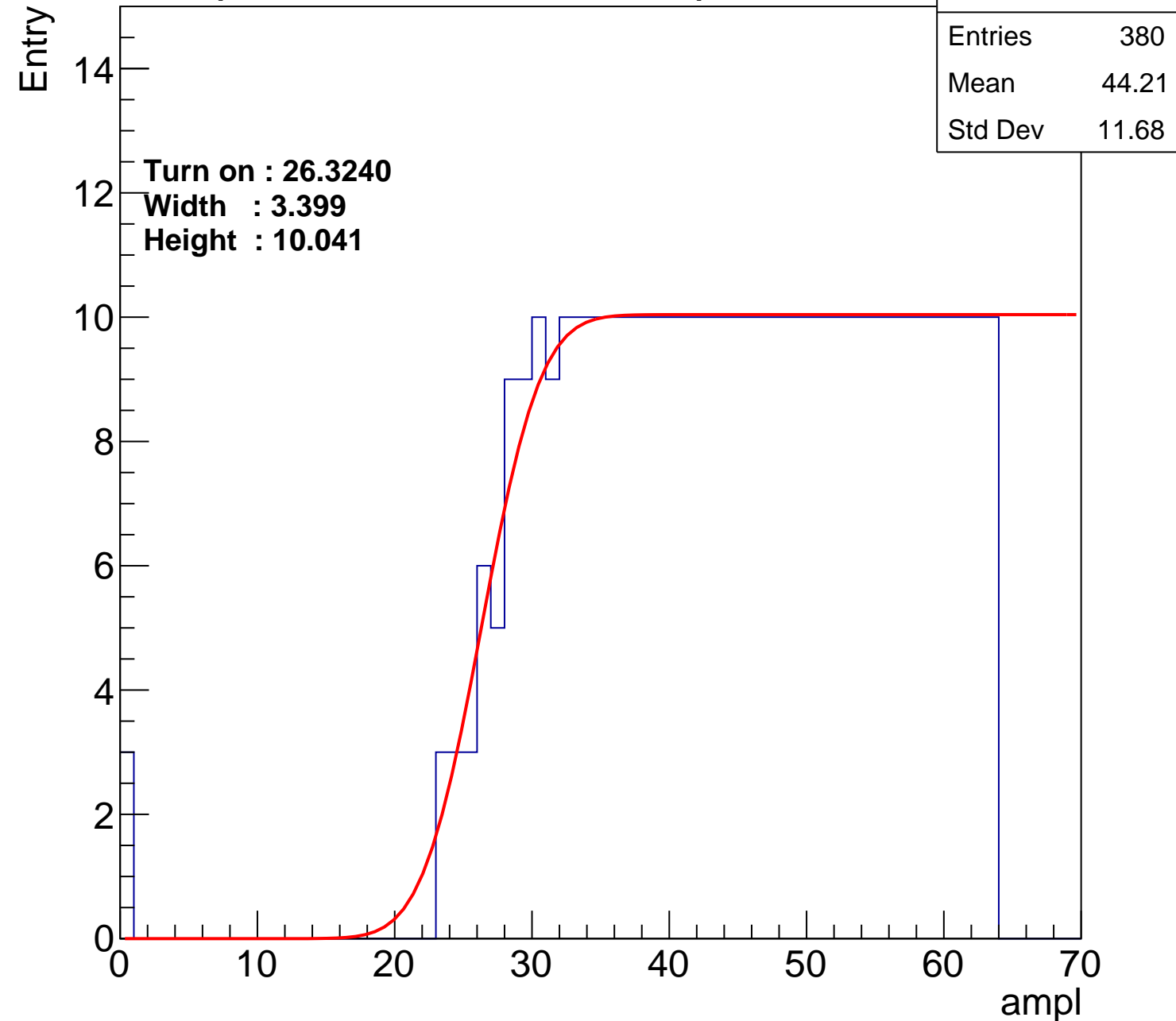
Width : 3.399

Height : 10.041

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch110

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	340
Mean	46.32
Std Dev	10.24

Turn on : 30.2118

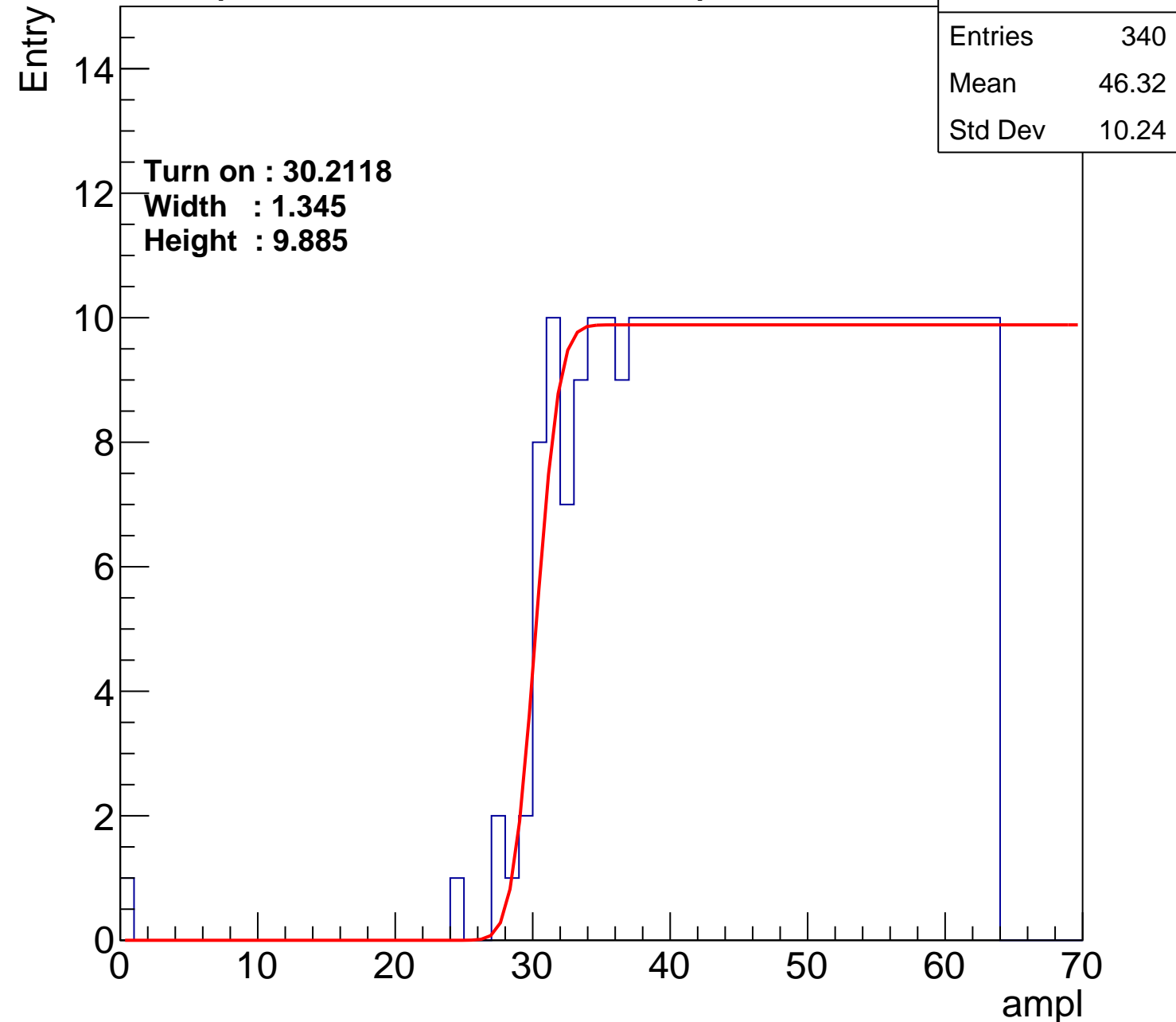
Width : 1.345

Height : 9.885

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch111

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	355
Mean	45.52
Std Dev	10.75

**Turn on : 29.9700**

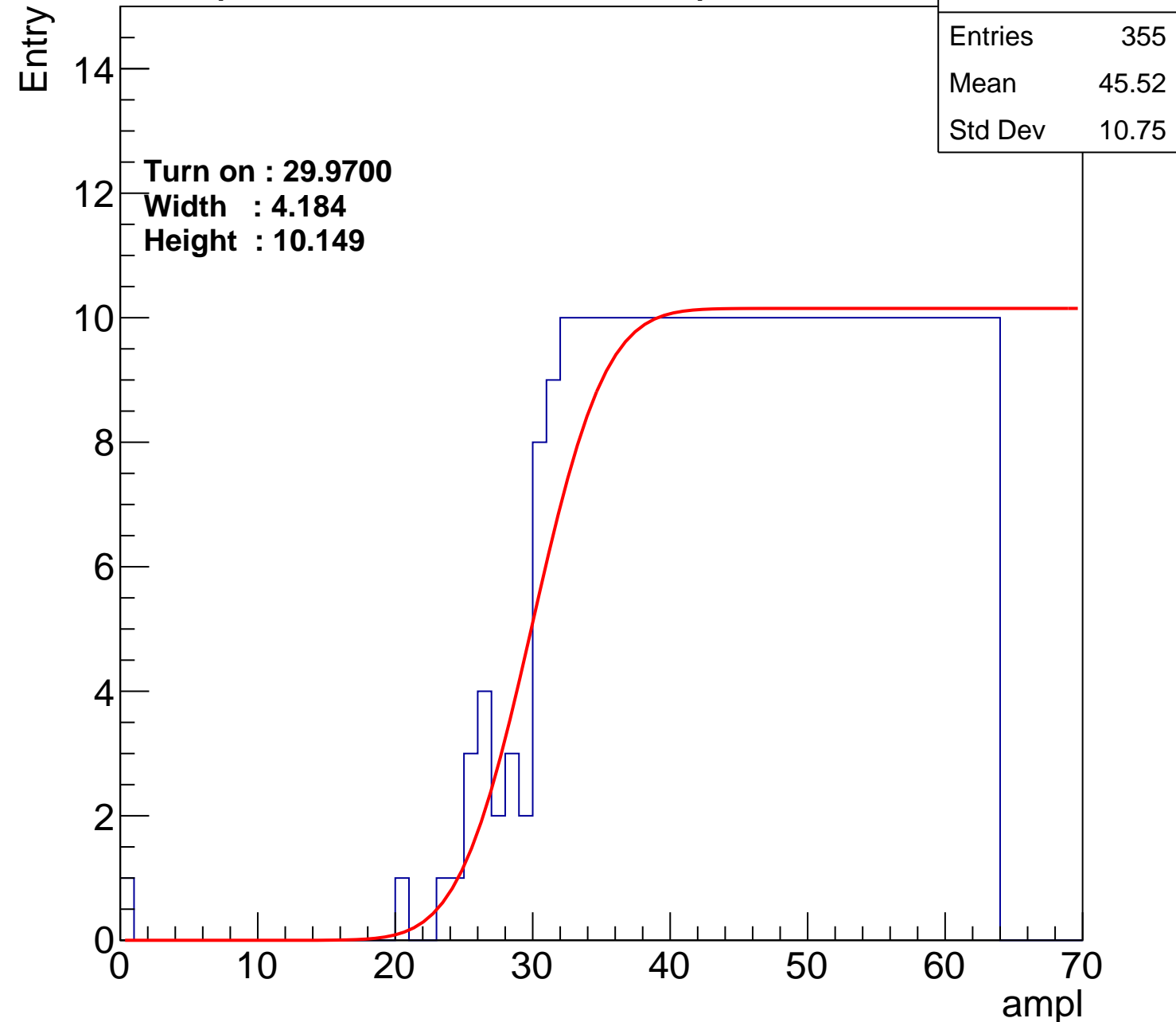
**Width : 4.184**

**Height : 10.149**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch112

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	363
Mean	45.11
Std Dev	11.07

**Turn on : 28.0607**

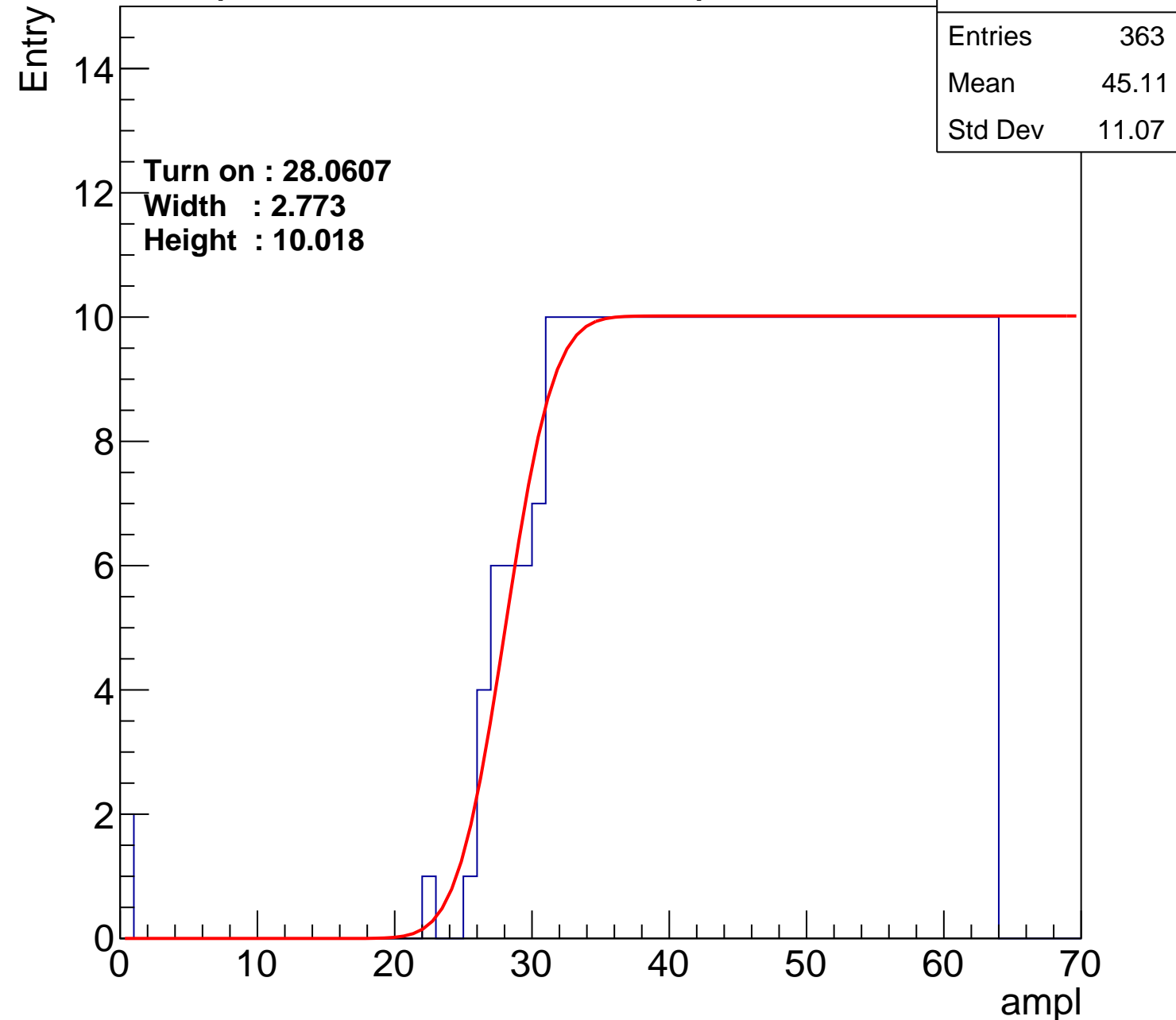
**Width : 2.773**

**Height : 10.018**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch113

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.75
Std Dev	11.26

**Turn on : 27.1388**

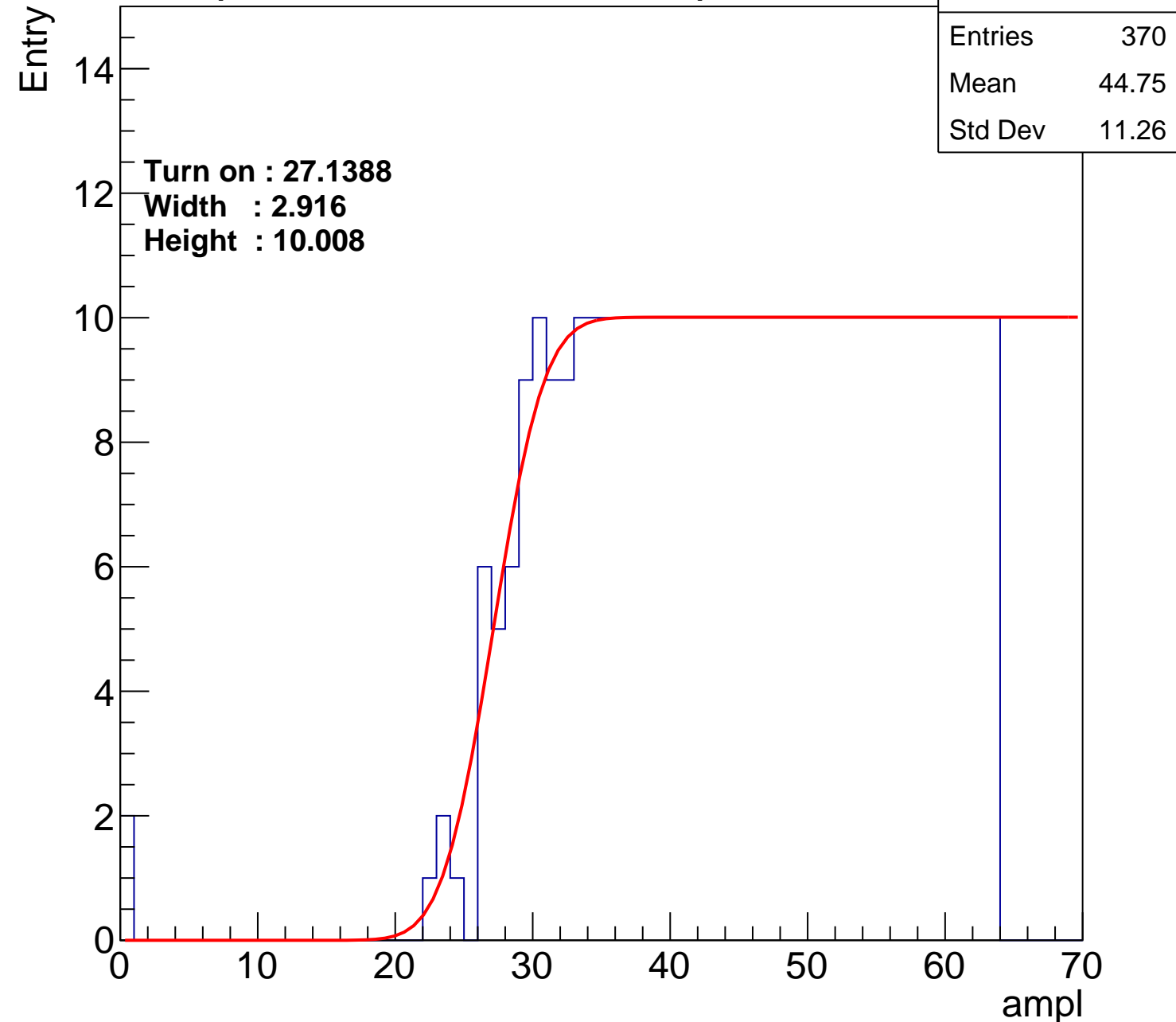
**Width : 2.916**

**Height : 10.008**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch114

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	378
Mean	44.44
Std Dev	11.26

Turn on : 26.2940

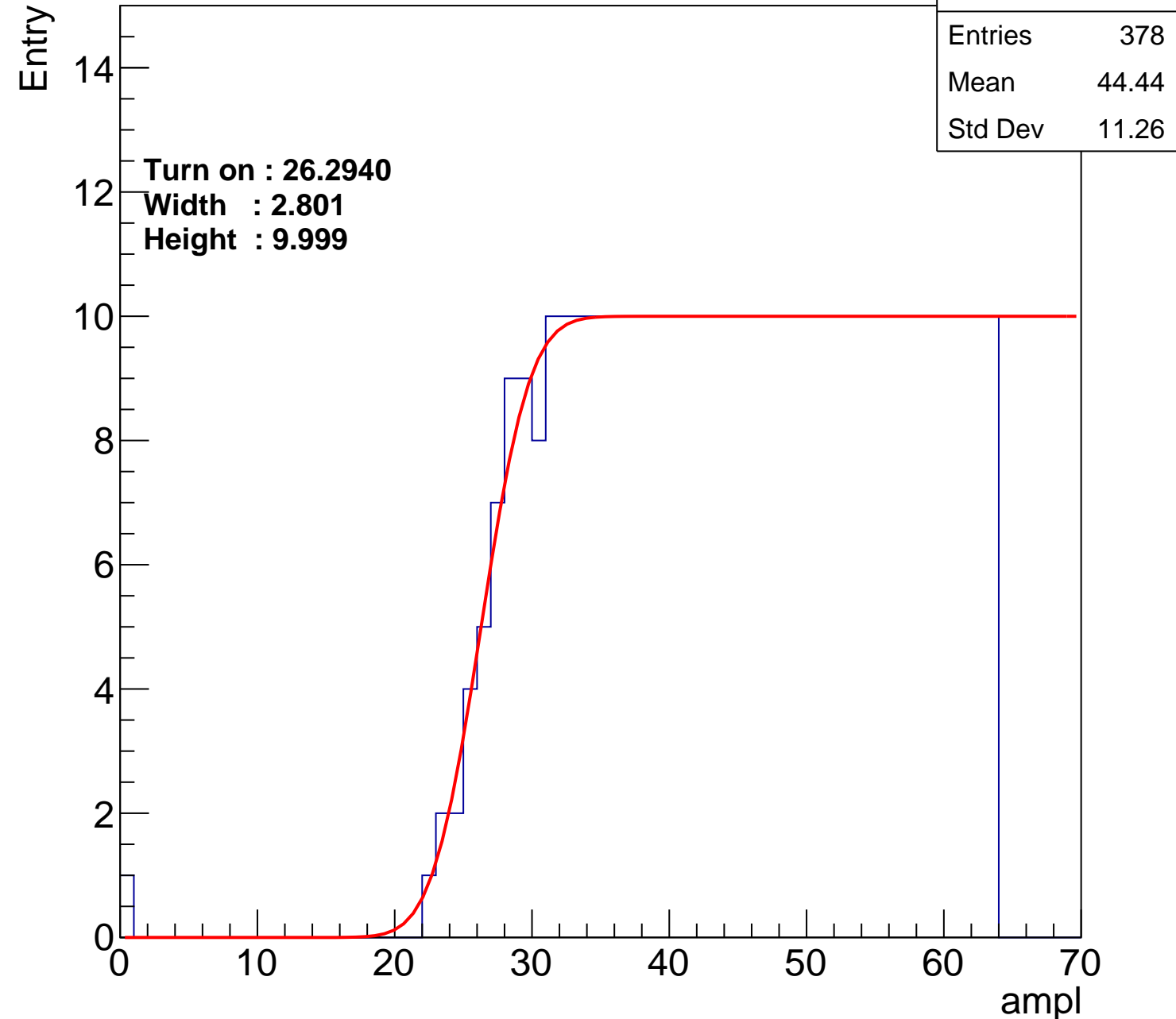
Width : 2.801

Height : 9.999

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch115

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.79
Std Dev	11.11

Turn on : 26.8937

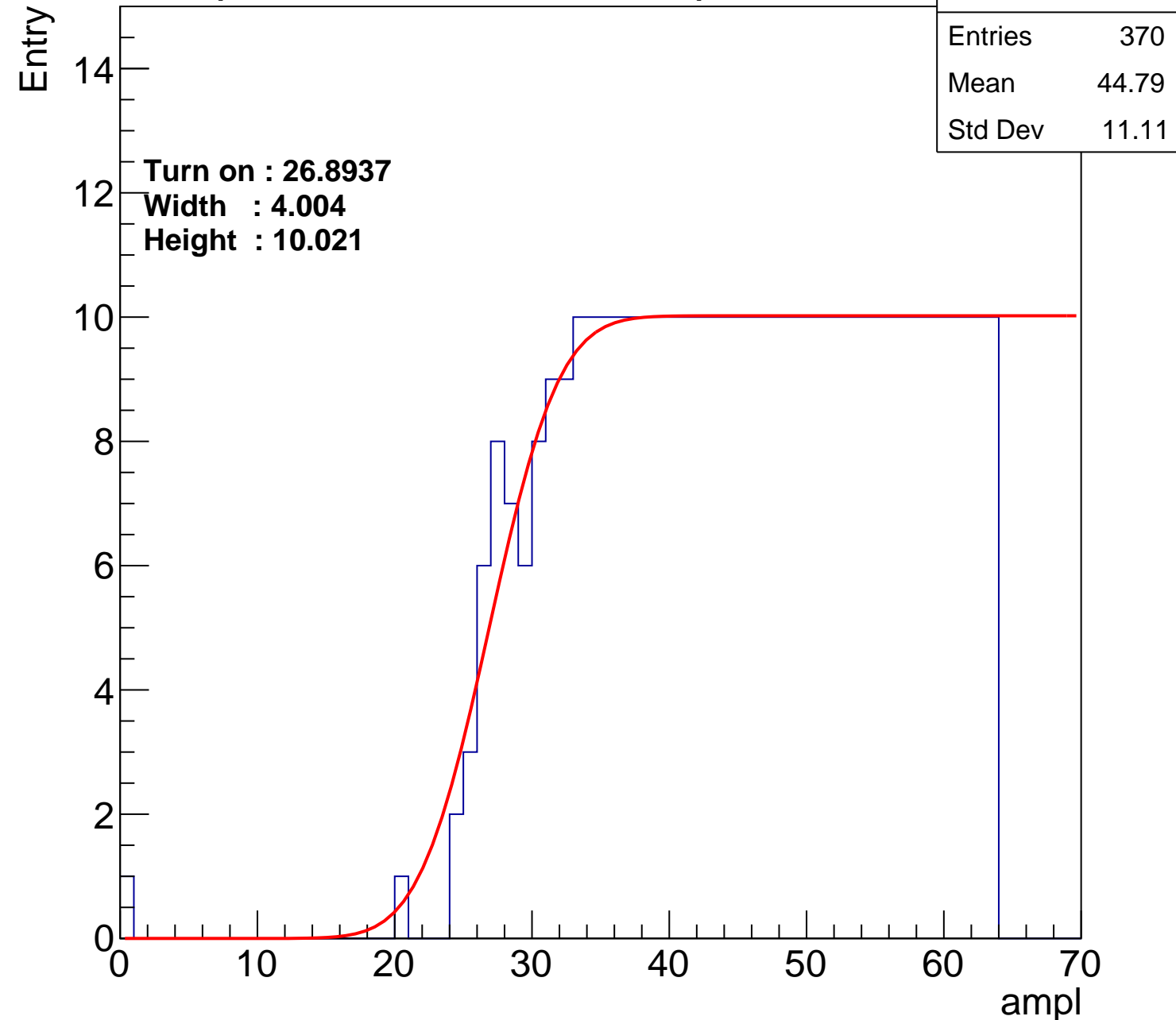
Width : 4.004

Height : 10.021

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch116

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	373
Mean	44.73
Std Dev	11.05

**Turn on : 26.7995**

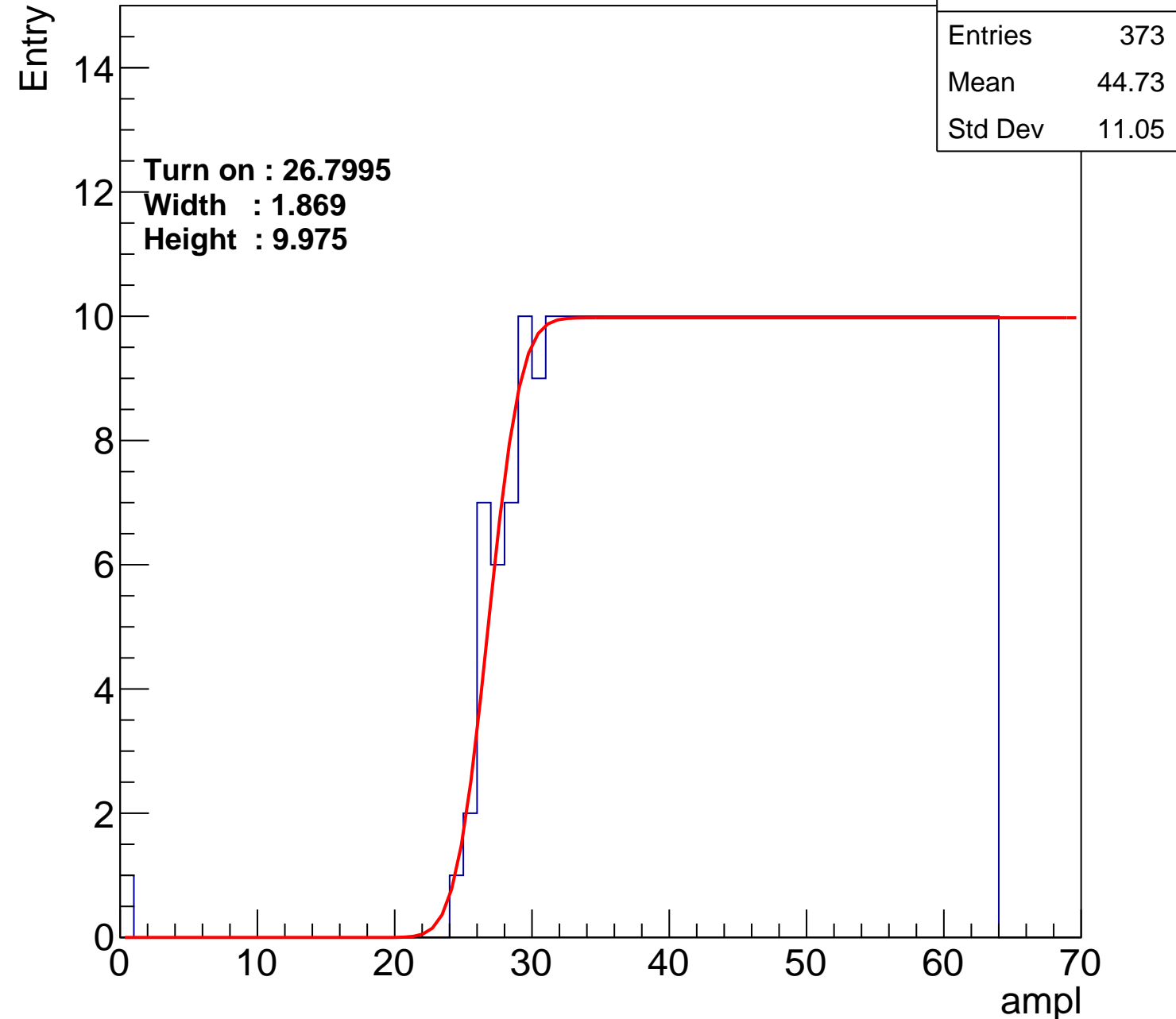
**Width : 1.869**

**Height : 9.975**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch117

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	366
Mean	45.05
Std Dev	10.91

Turn on : 27.0534

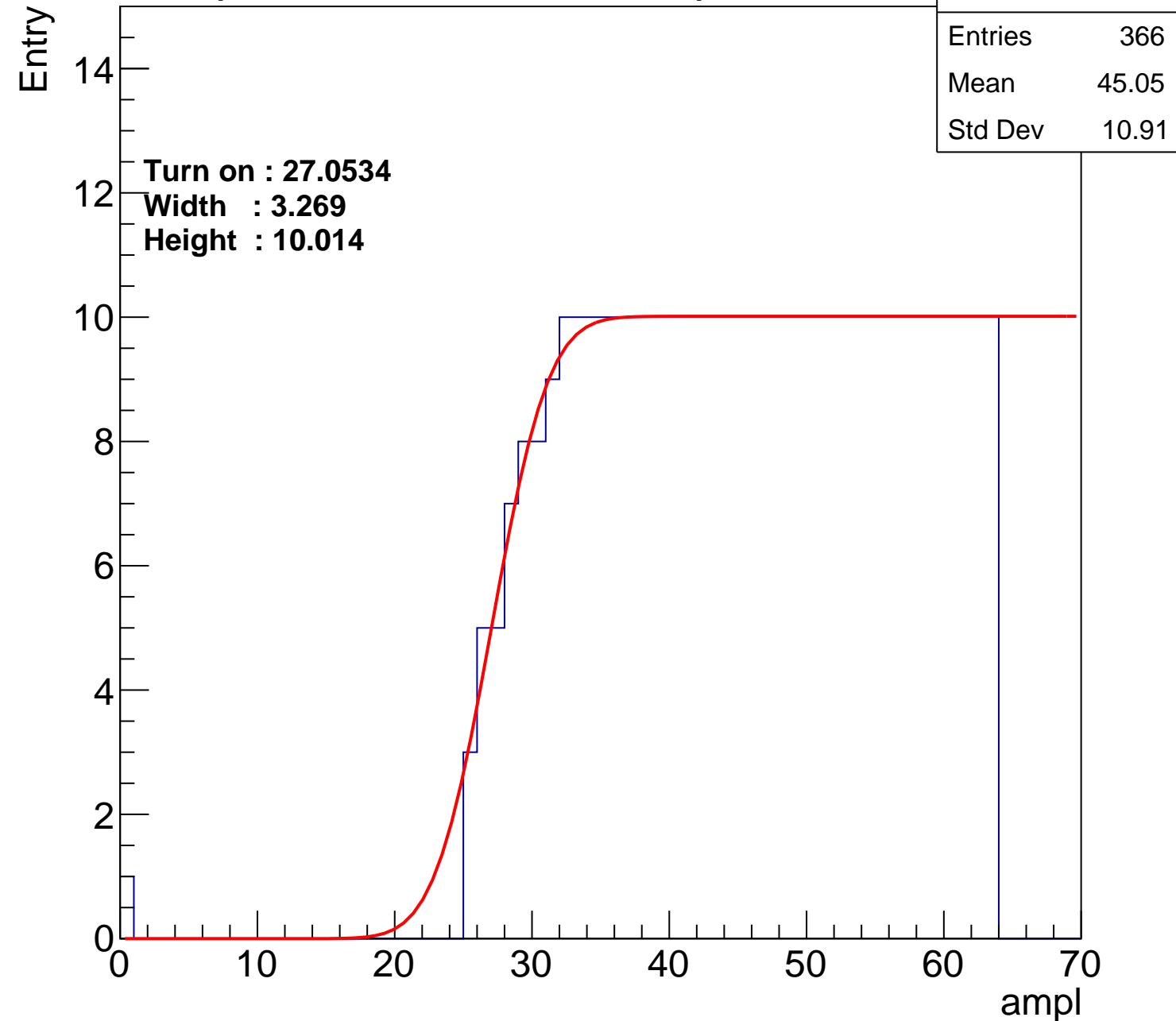
Width : 3.269

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch118

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	370
Mean	44.73
Std Dev	11.29

**Turn on : 27.0073**

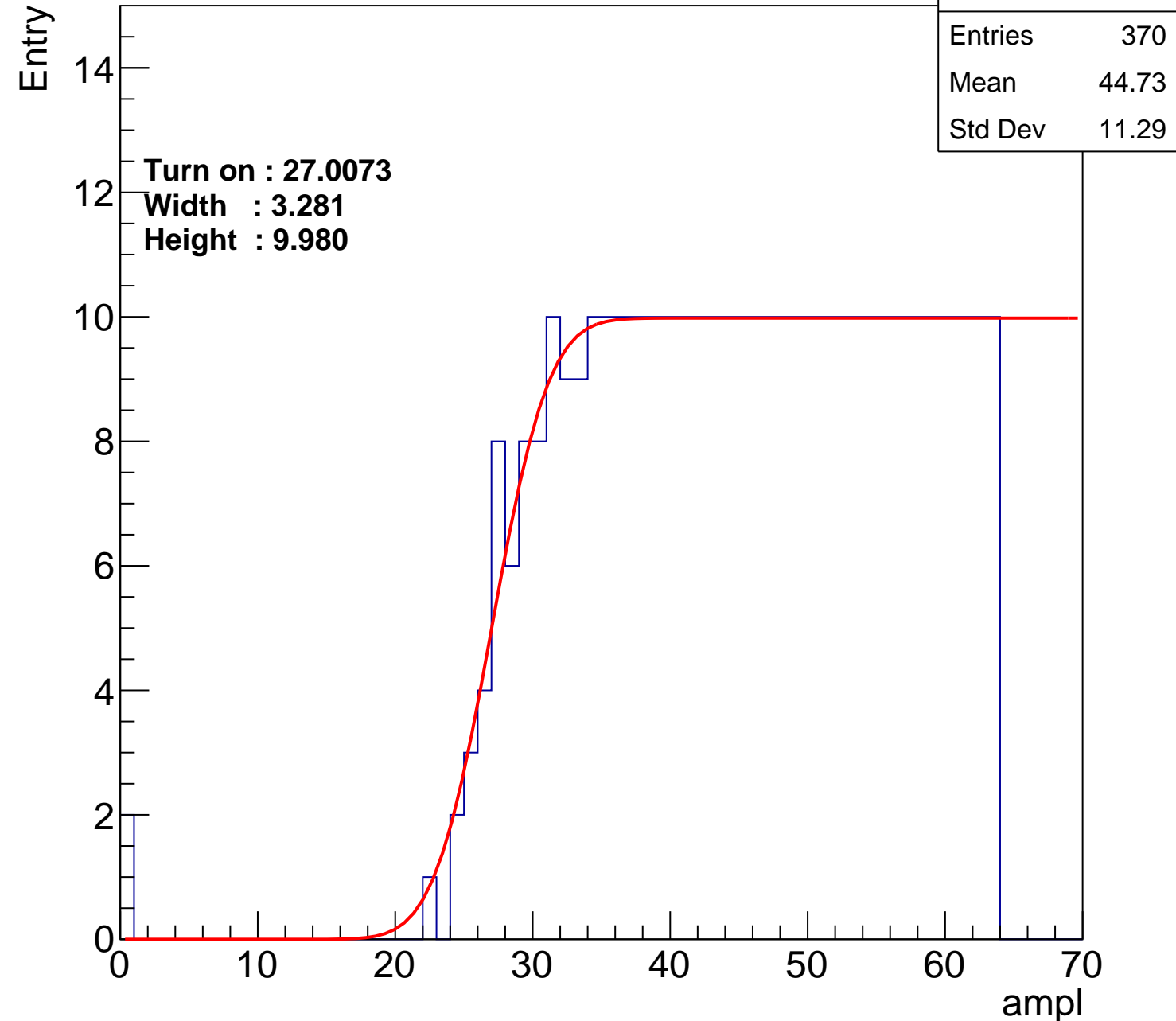
**Width : 3.281**

**Height : 9.980**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch119

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	353
Mean	45.66
Std Dev	10.61

**Turn on : 28.9977**

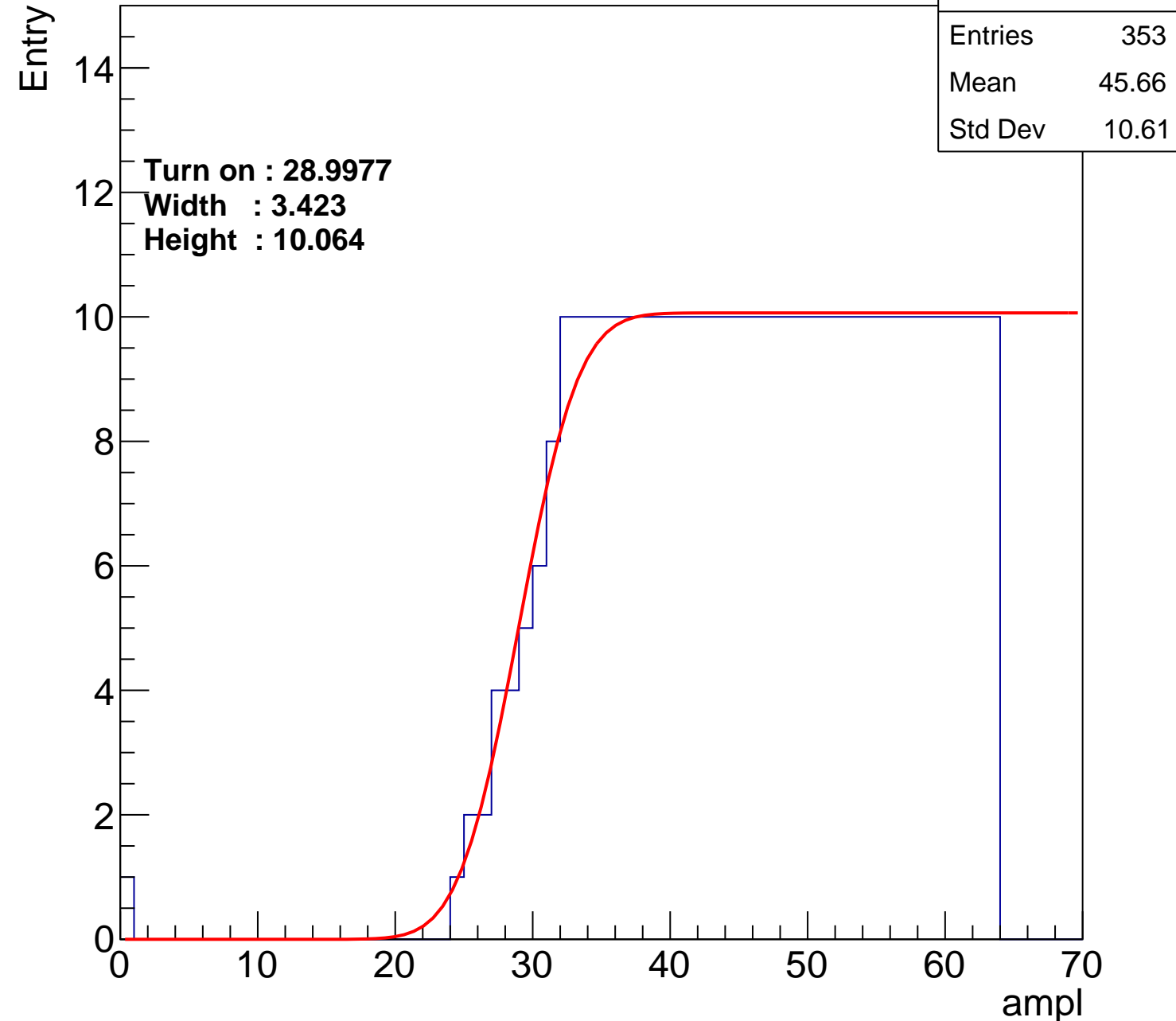
**Width : 3.423**

**Height : 10.064**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch120

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	375
Mean	44.34
Std Dev	11.82

Turn on : 27.1742

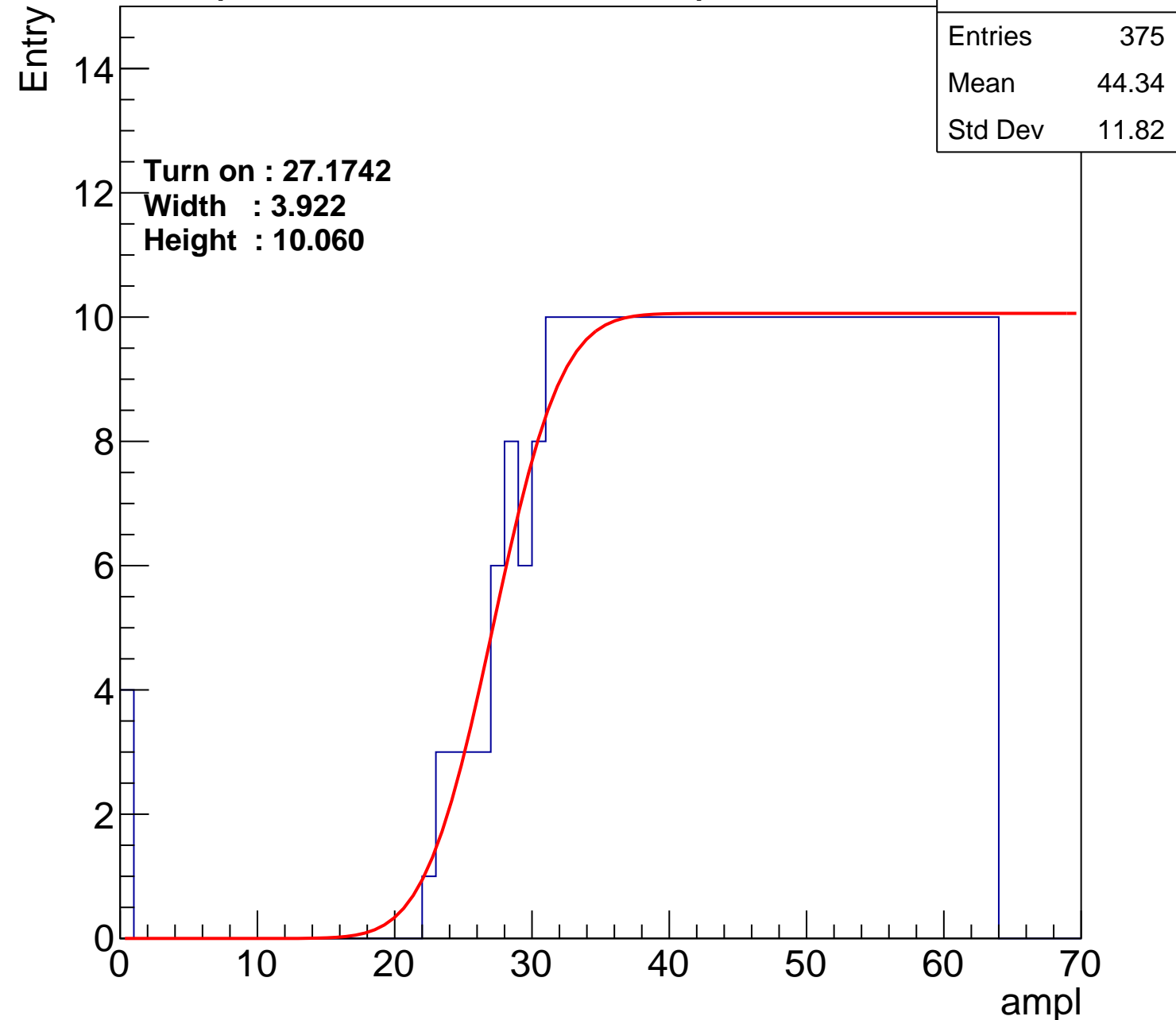
Width : 3.922

Height : 10.060

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch121

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	368
Mean	44.83
Std Dev	11.24

Turn on : 27.3301

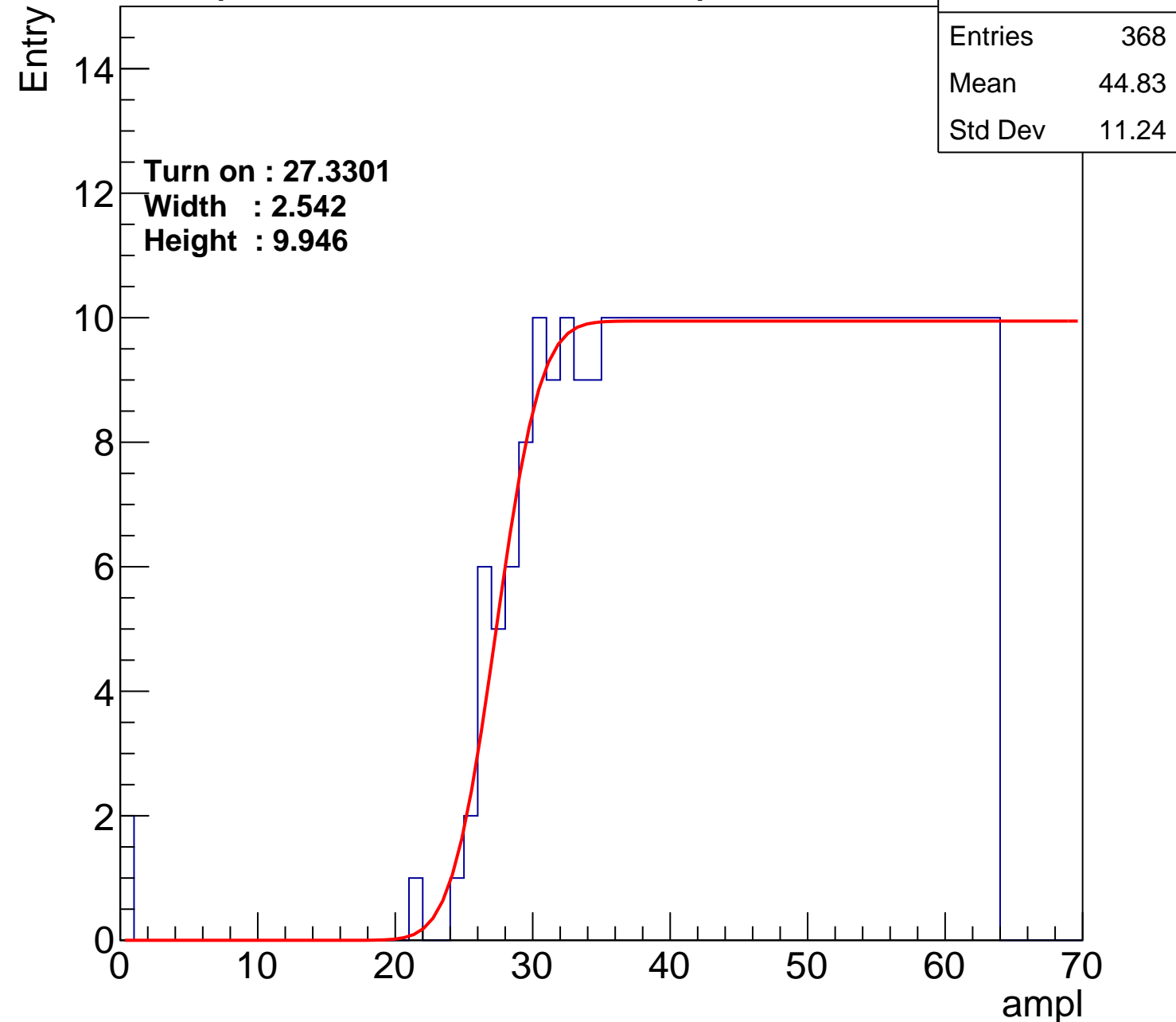
Width : 2.542

Height : 9.946

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch122

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	380
Mean	44.2
Std Dev	11.7

Turn on : 27.3036

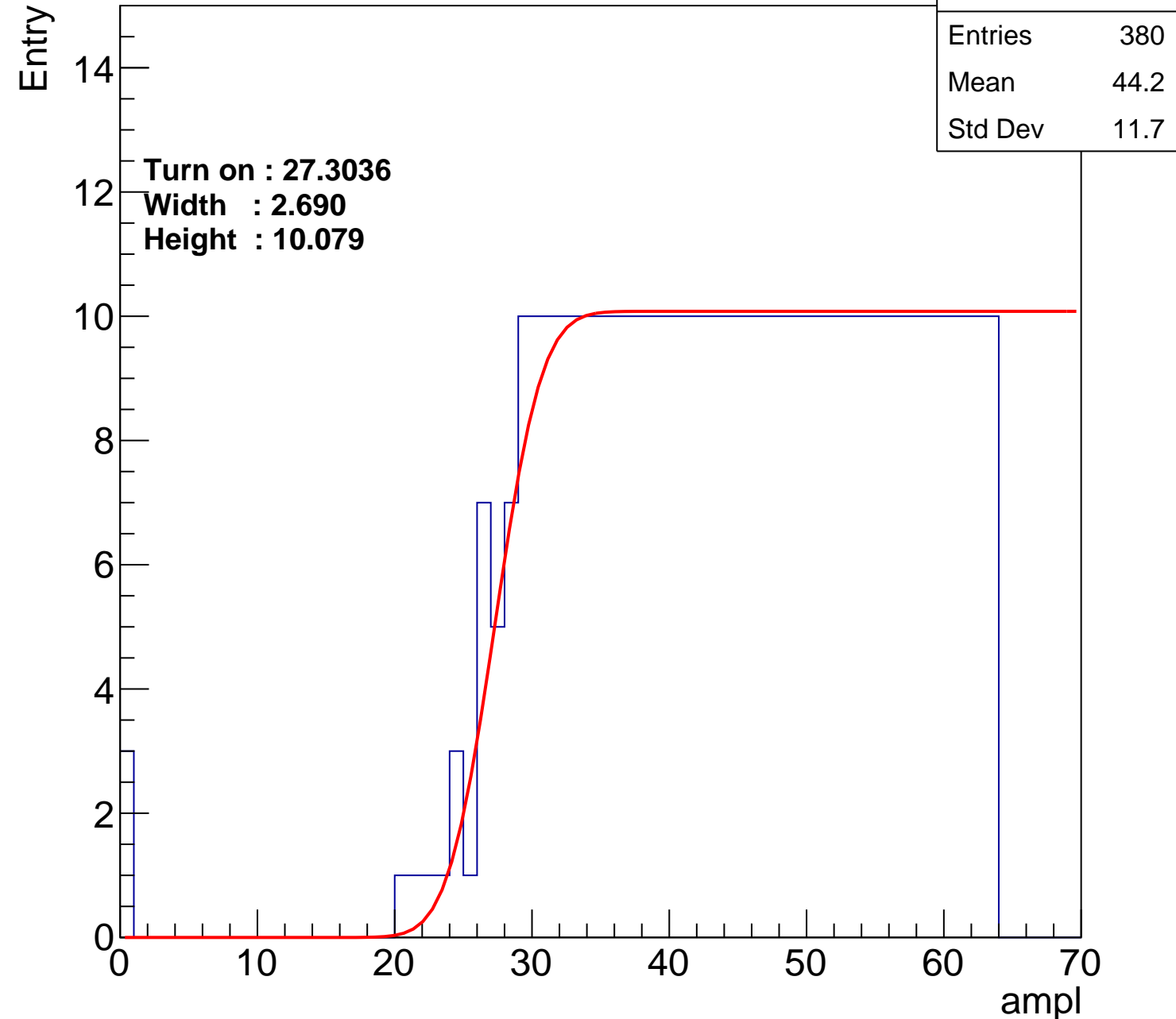
Width : 2.690

Height : 10.079

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch123

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	376
Mean	44.29
Std Dev	11.83

**Turn on : 26.9871**

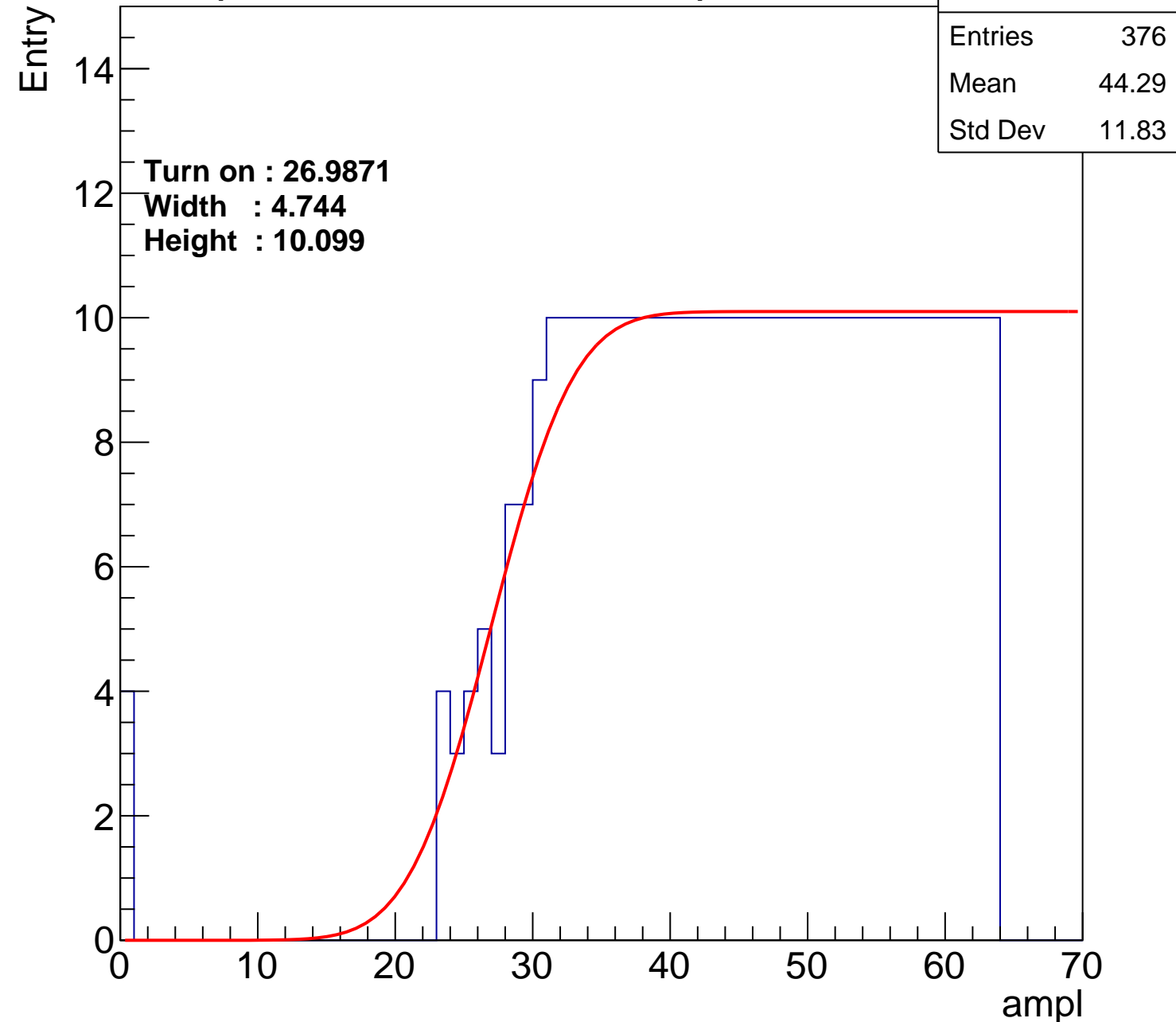
**Width : 4.744**

**Height : 10.099**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch124

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	387
Mean	43.86
Std Dev	11.86

**Turn on : 25.9909**

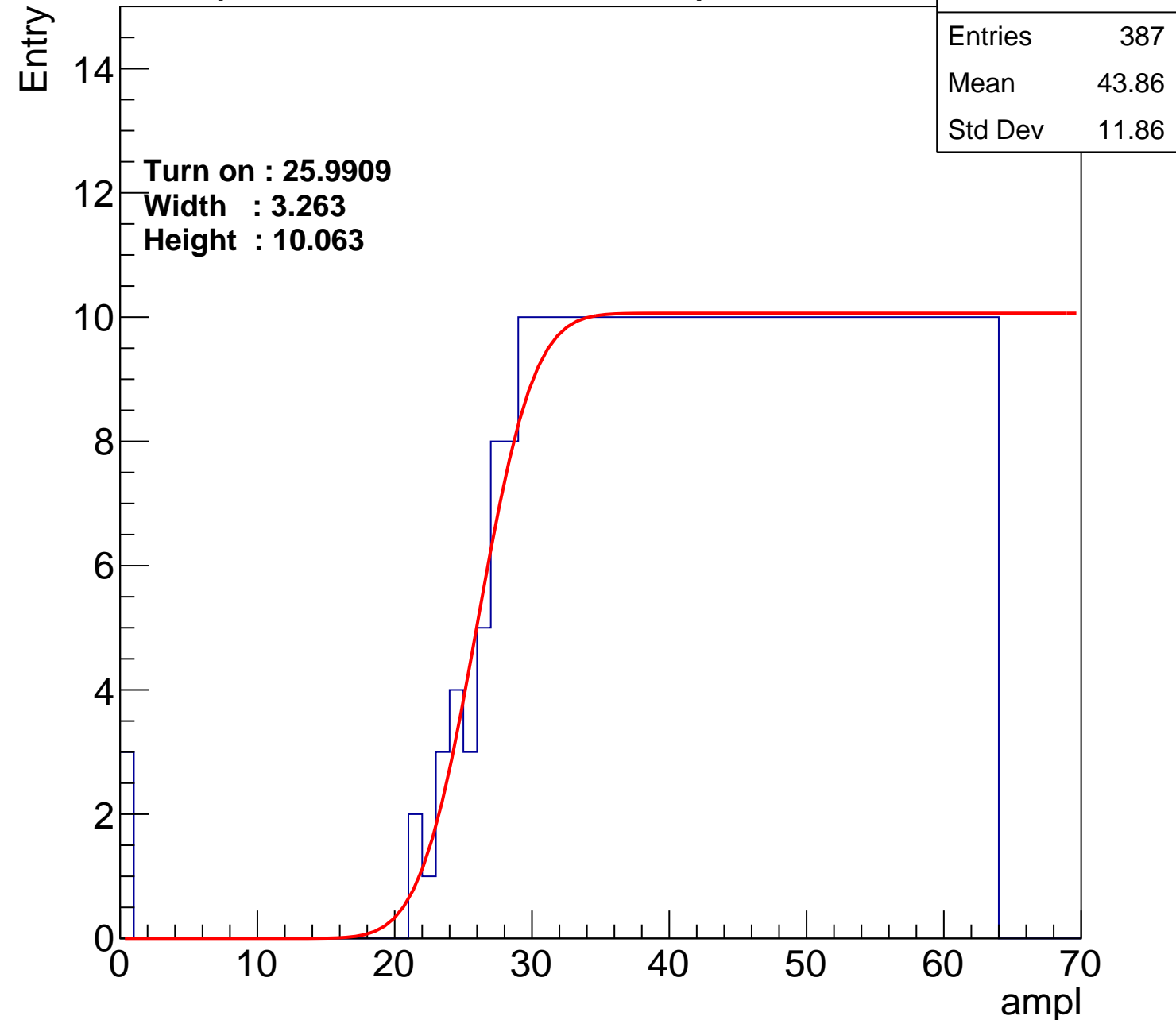
**Width : 3.263**

**Height : 10.063**

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch125

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	351
Mean	45.69
Std Dev	10.79

Turn on : 29.1182

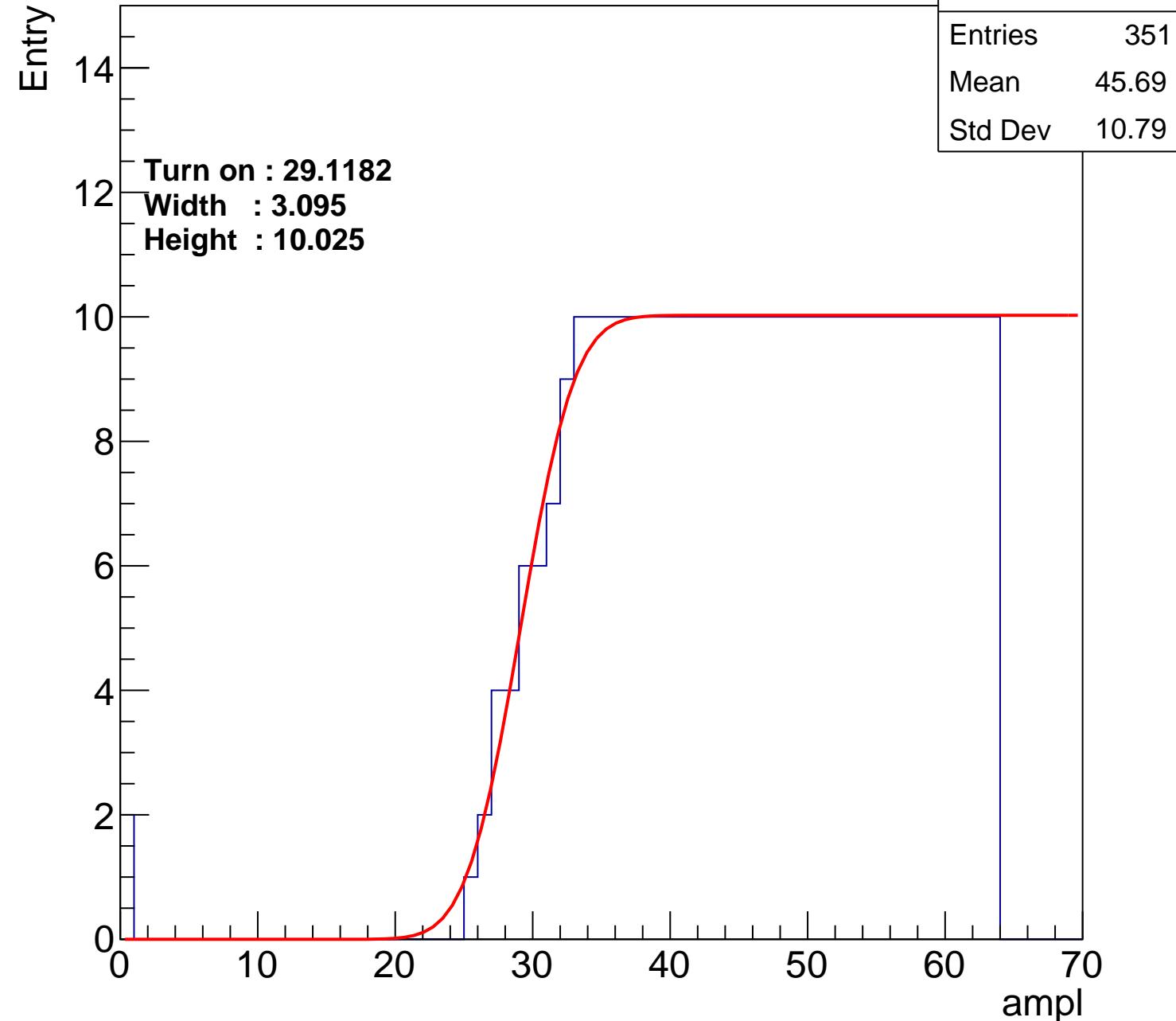
Width : 3.095

Height : 10.025

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl



# B0L002S, U19-ch126

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	344
Mean	46.02
Std Dev	10.62

Turn on : 30.1635

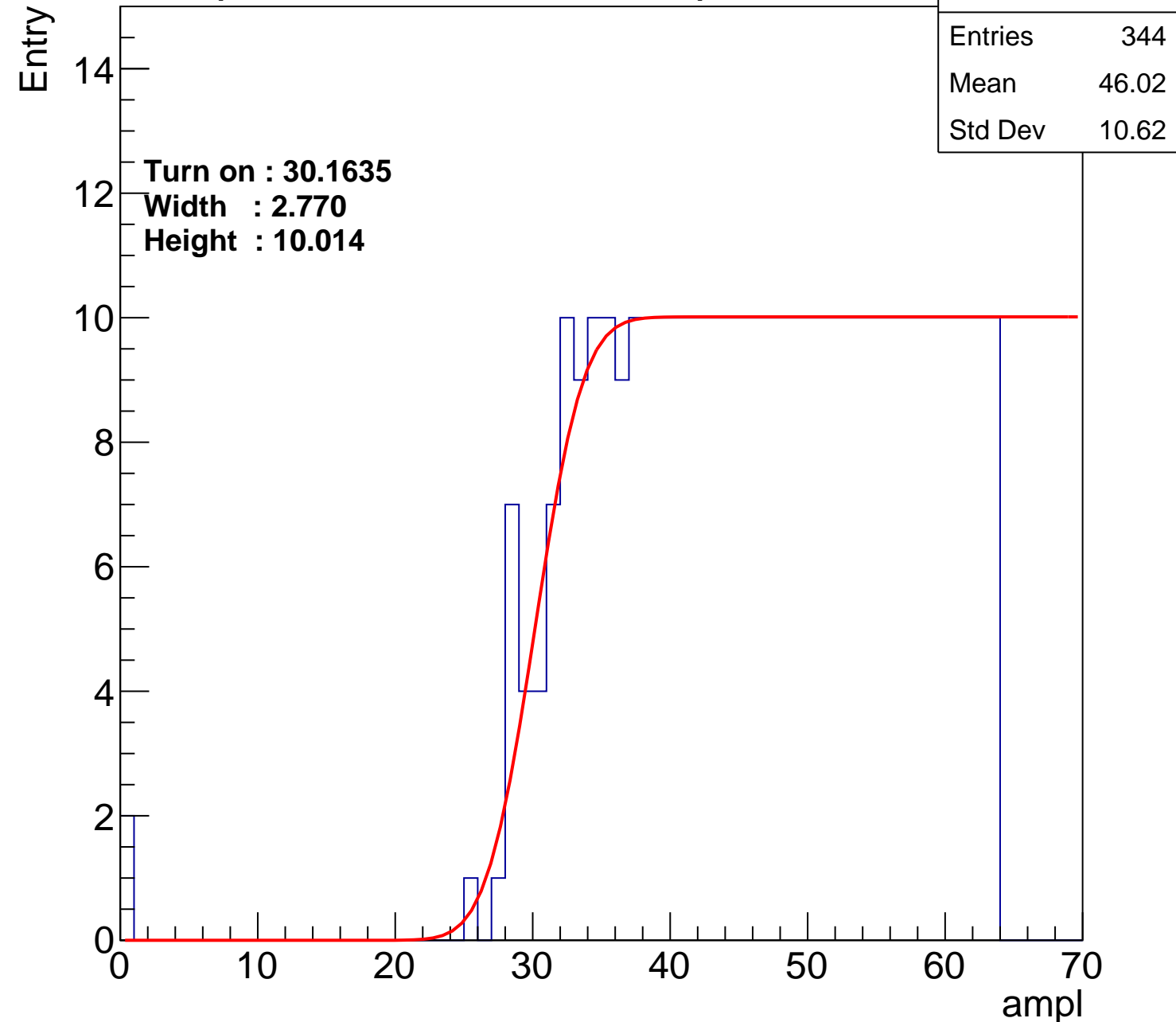
Width : 2.770

Height : 10.014

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl





# B0L002S, U19-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

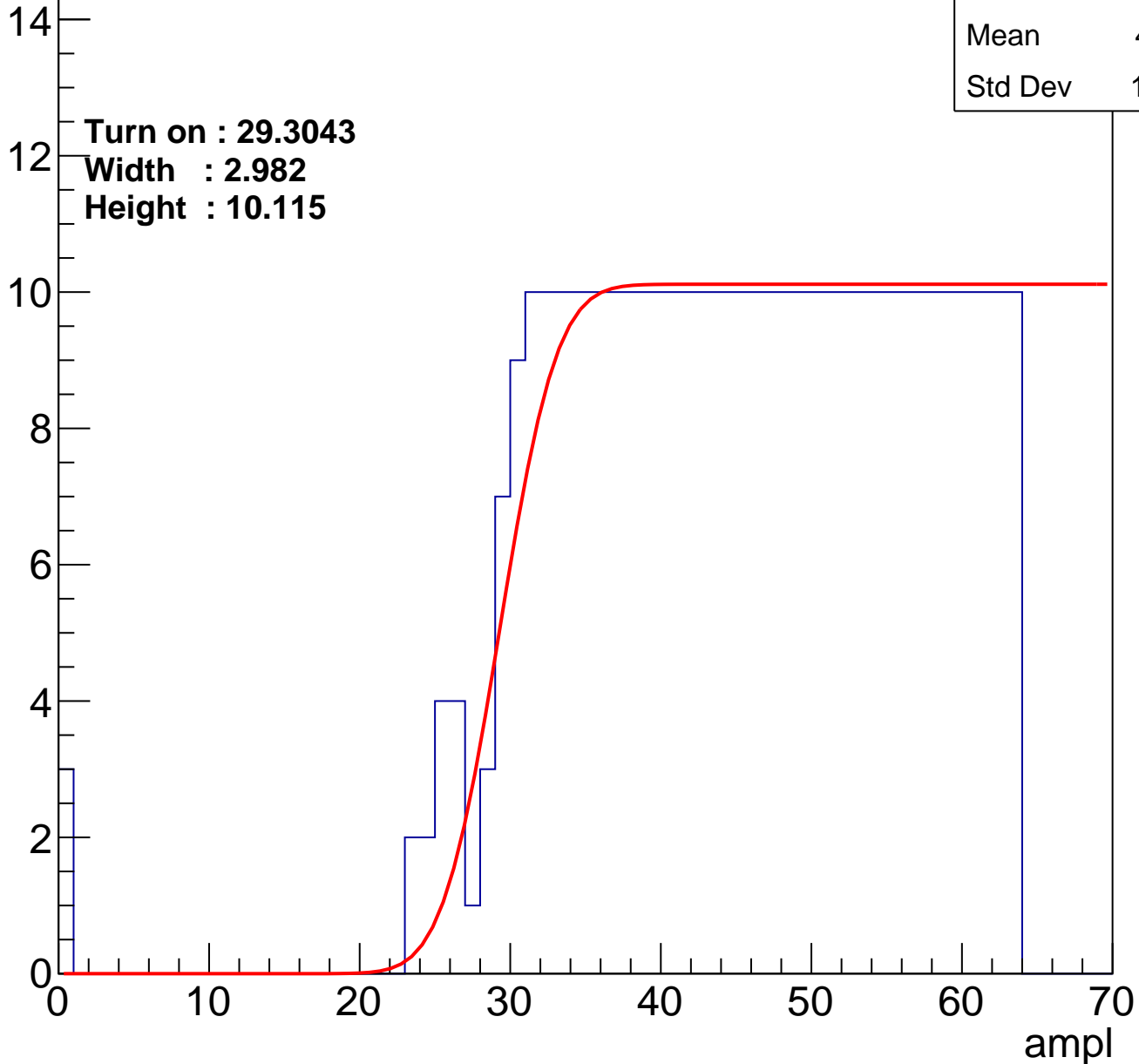
Entries	365
Mean	44.91
Std Dev	11.37

**Turn on : 29.3043**

**Width : 2.982**

**Height : 10.115**

Entry



# B0L002S, U19-ch127

calib\_packv5\_042523\_0143.root, FC#8, port C1

Entries	365
Mean	44.91
Std Dev	11.37

Turn on : 29.3043

Width : 2.982

Height : 10.115

Entry

14  
12  
10  
8  
6  
4  
2  
0

ampl

