

B0L002S, U18-ch0, adc0

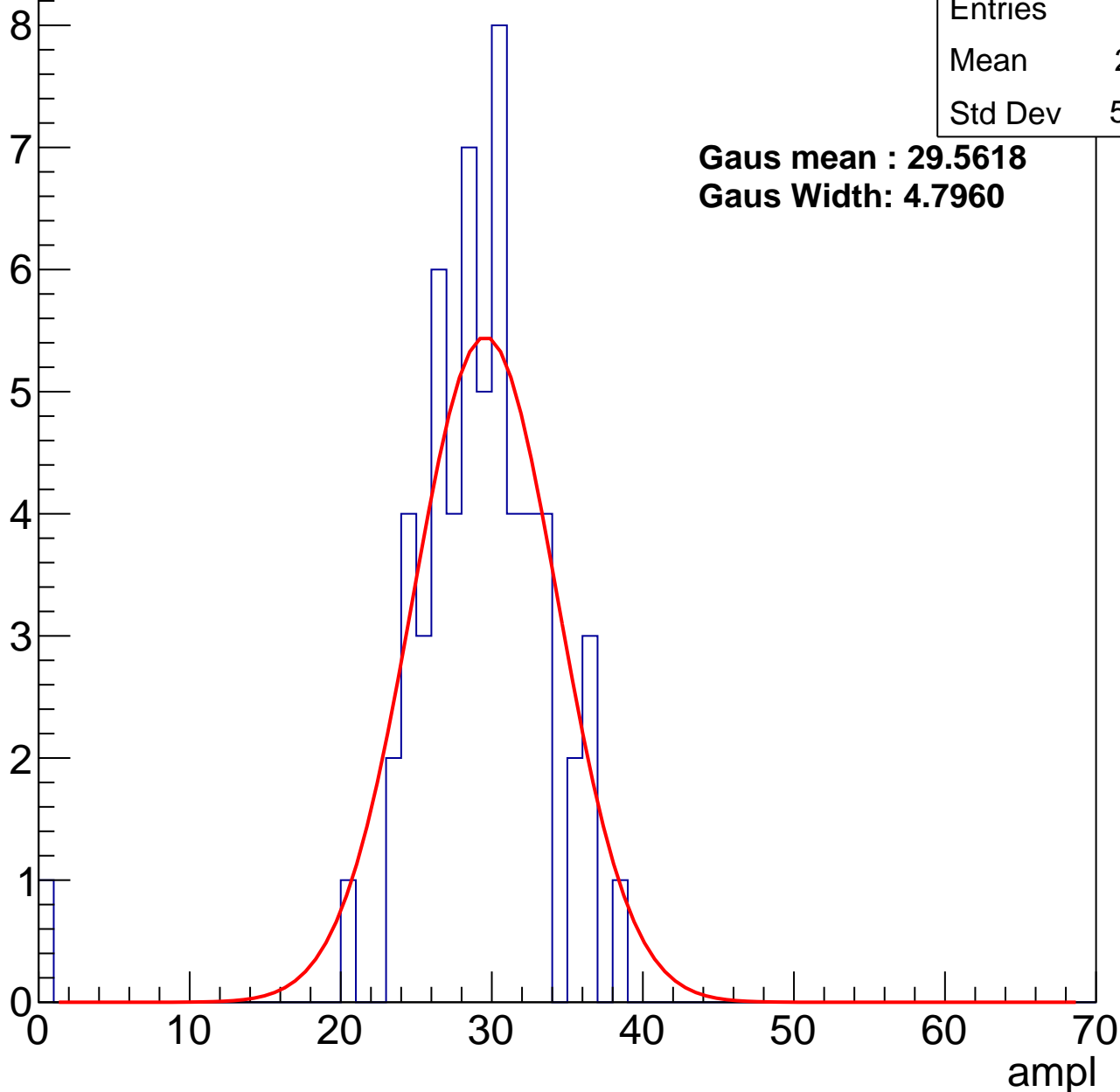
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	28.51
Std Dev	5.254

Gaus mean : 29.5618

Gaus Width: 4.7960



B0L002S, U18-ch0, adc1

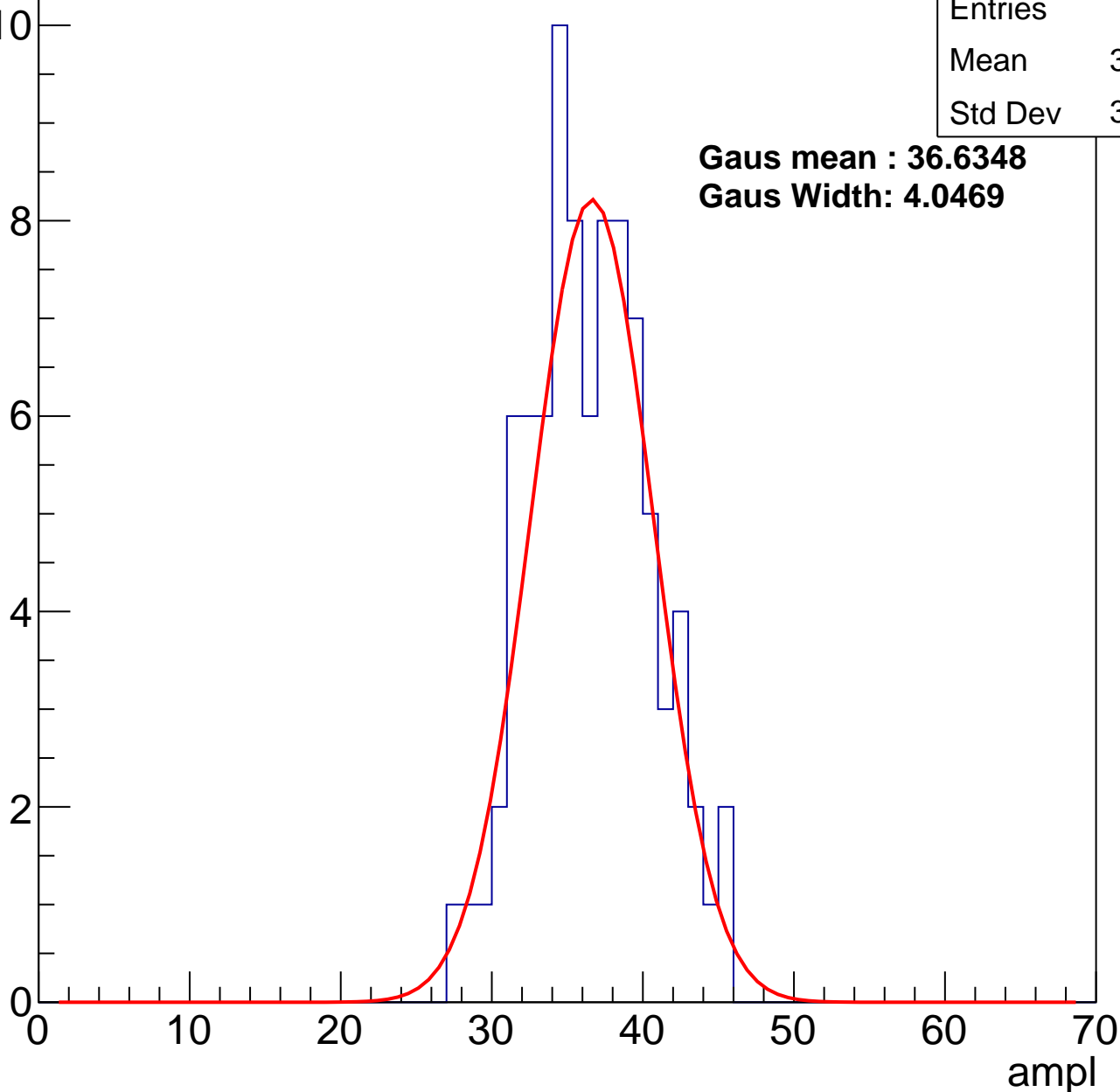
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	36.09
Std Dev	3.923

Gaus mean : 36.6348

Gaus Width: 4.0469



B0L002S, U18-ch0, adc2

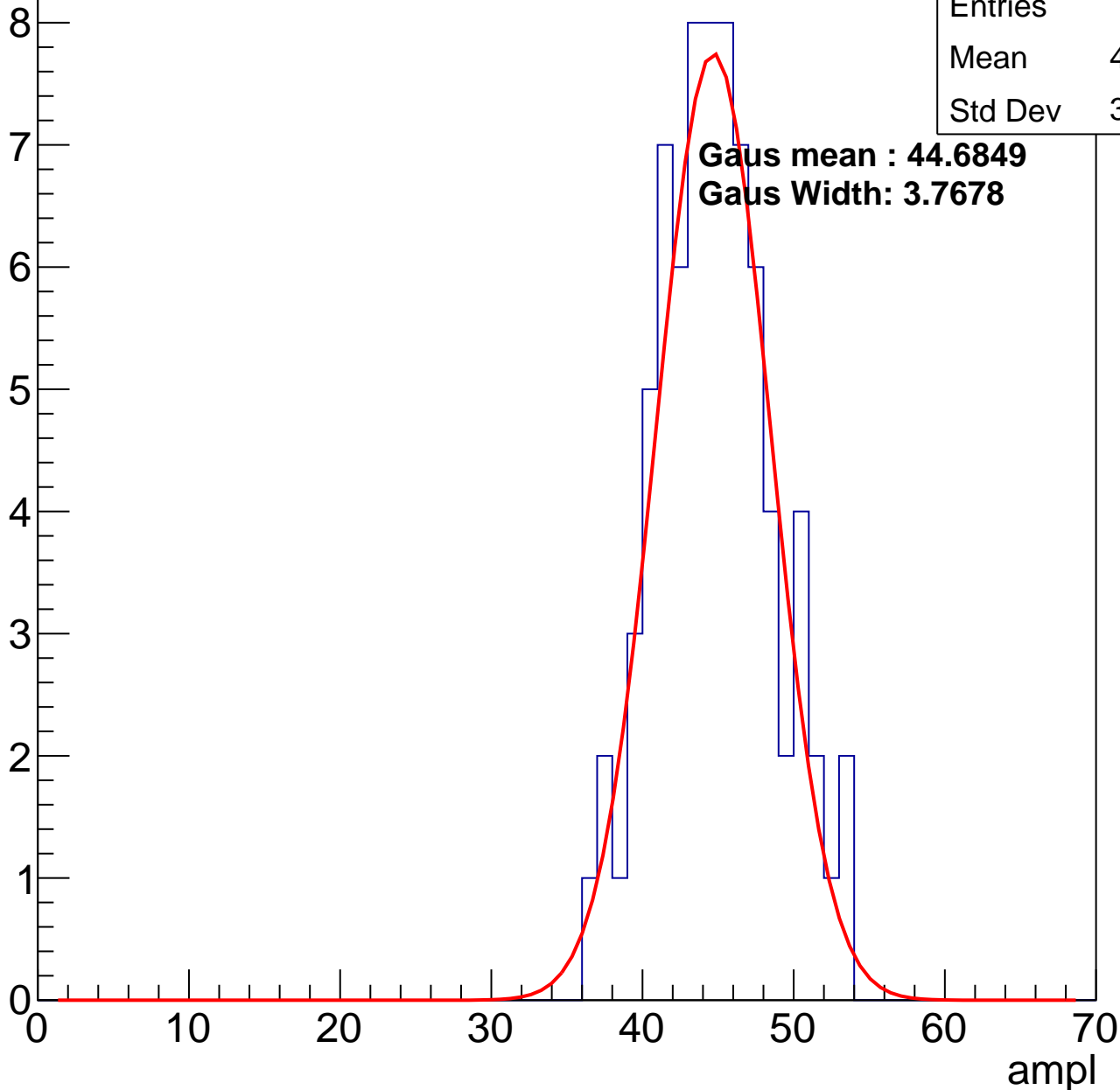
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.34
Std Dev	3.799

Gaus mean : 44.6849

Gaus Width: 3.7678

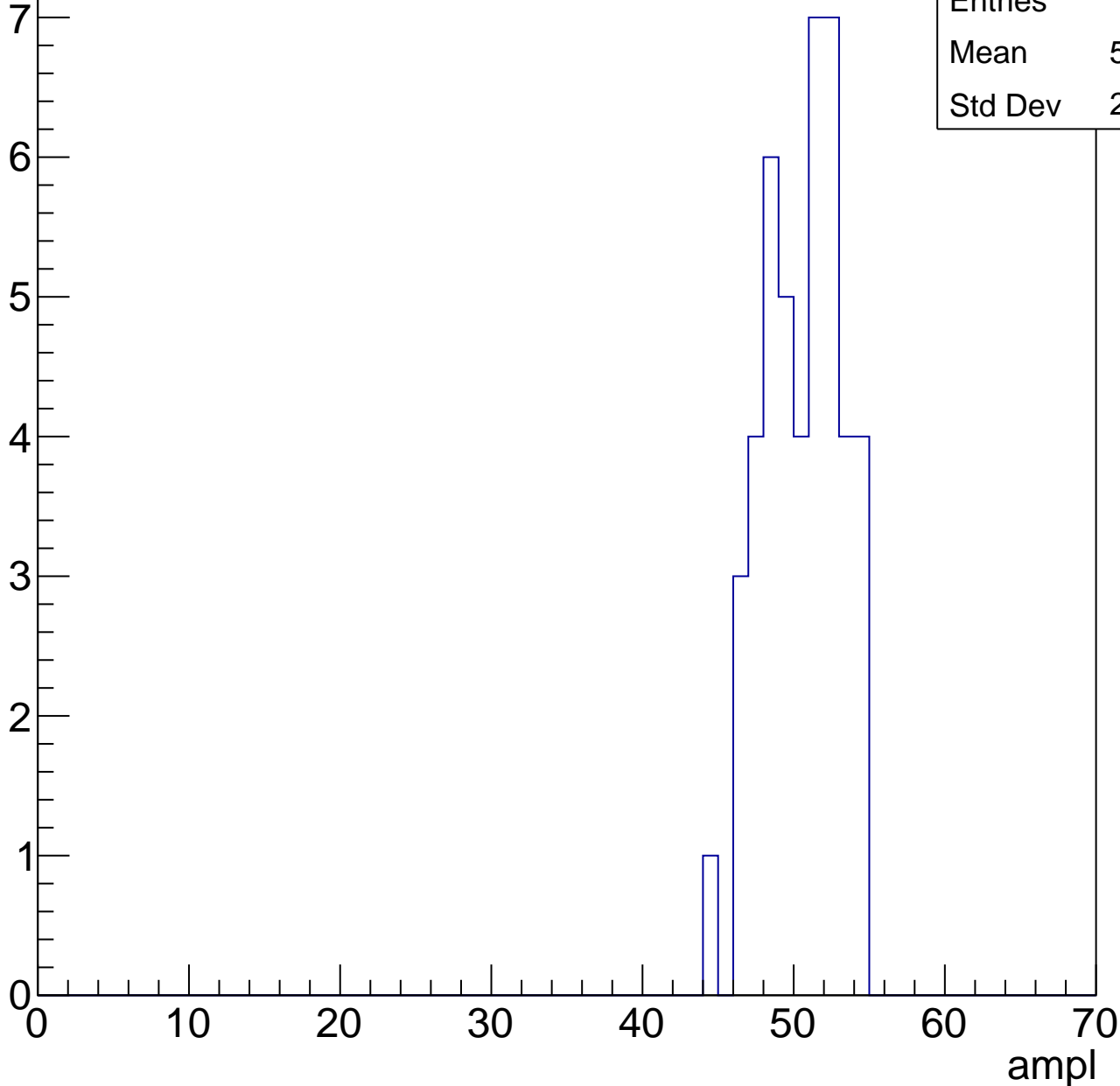


B0L002S, U18-ch0, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	50.04
Std Dev	2.512

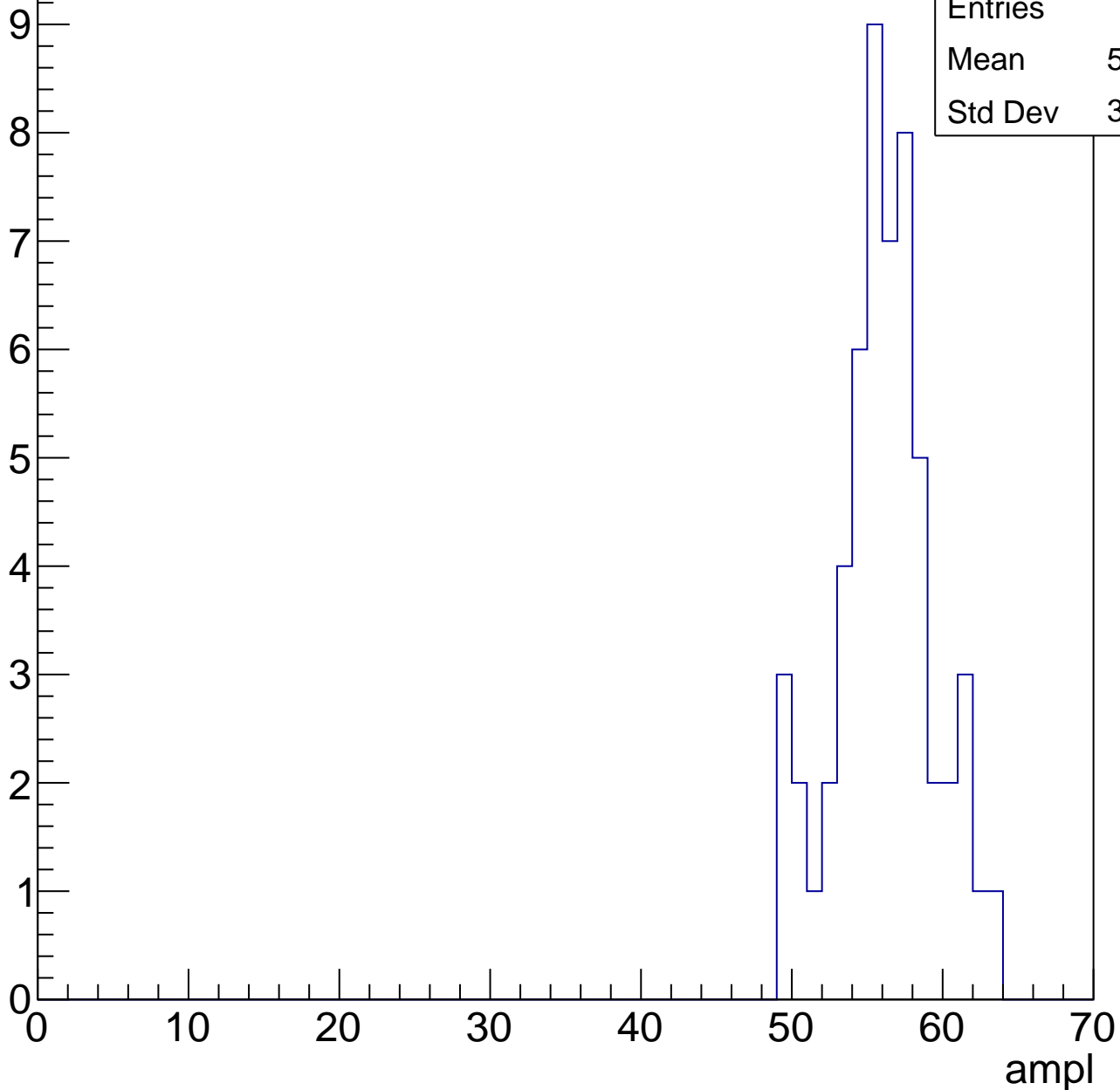


B0L002S, U18-ch0, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.66
Std Dev	3.209

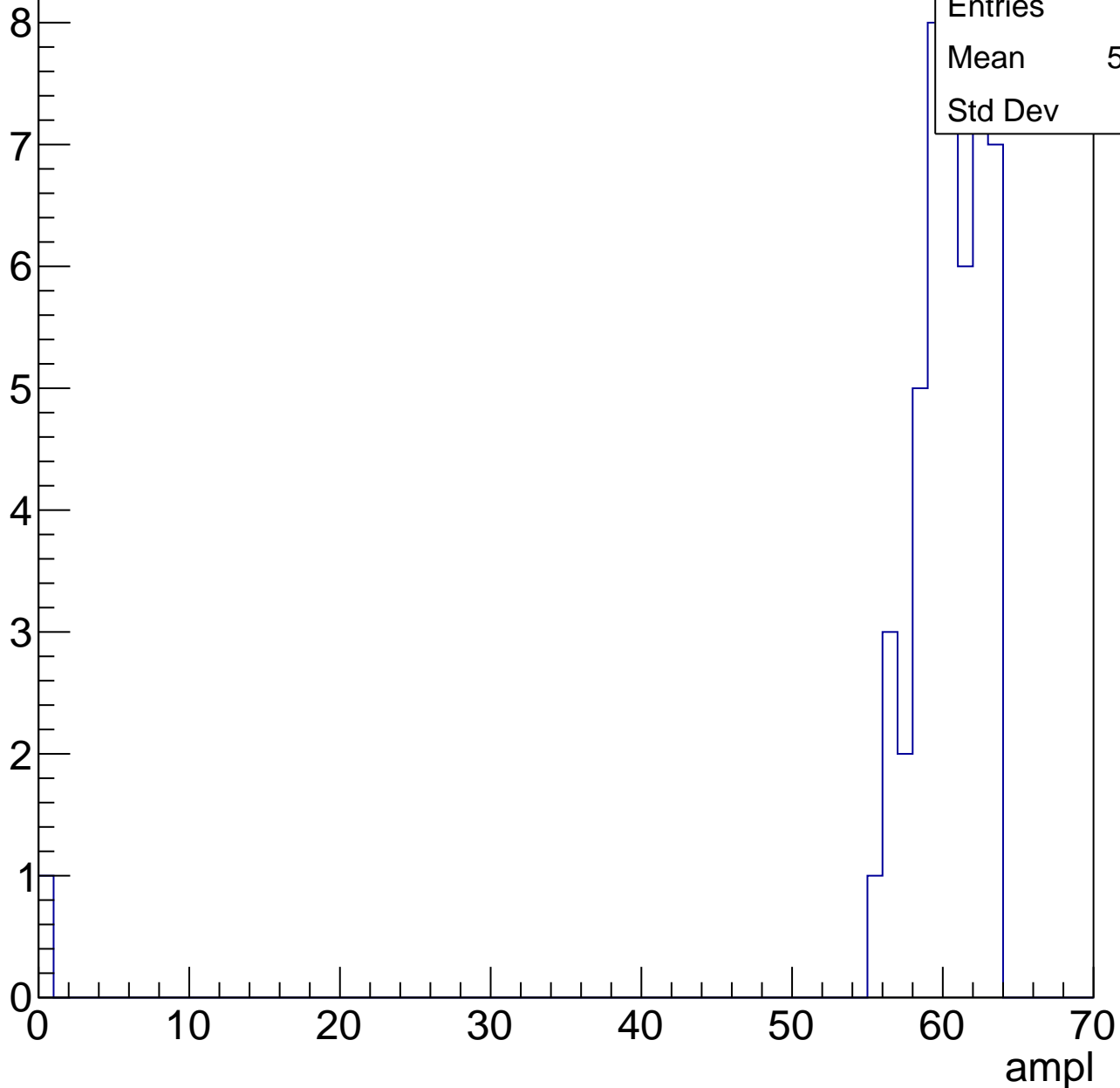


B0L002S, U18-ch0, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.82
Std Dev	8.75



B0L002S, U18-ch0, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch0, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch1, adc0

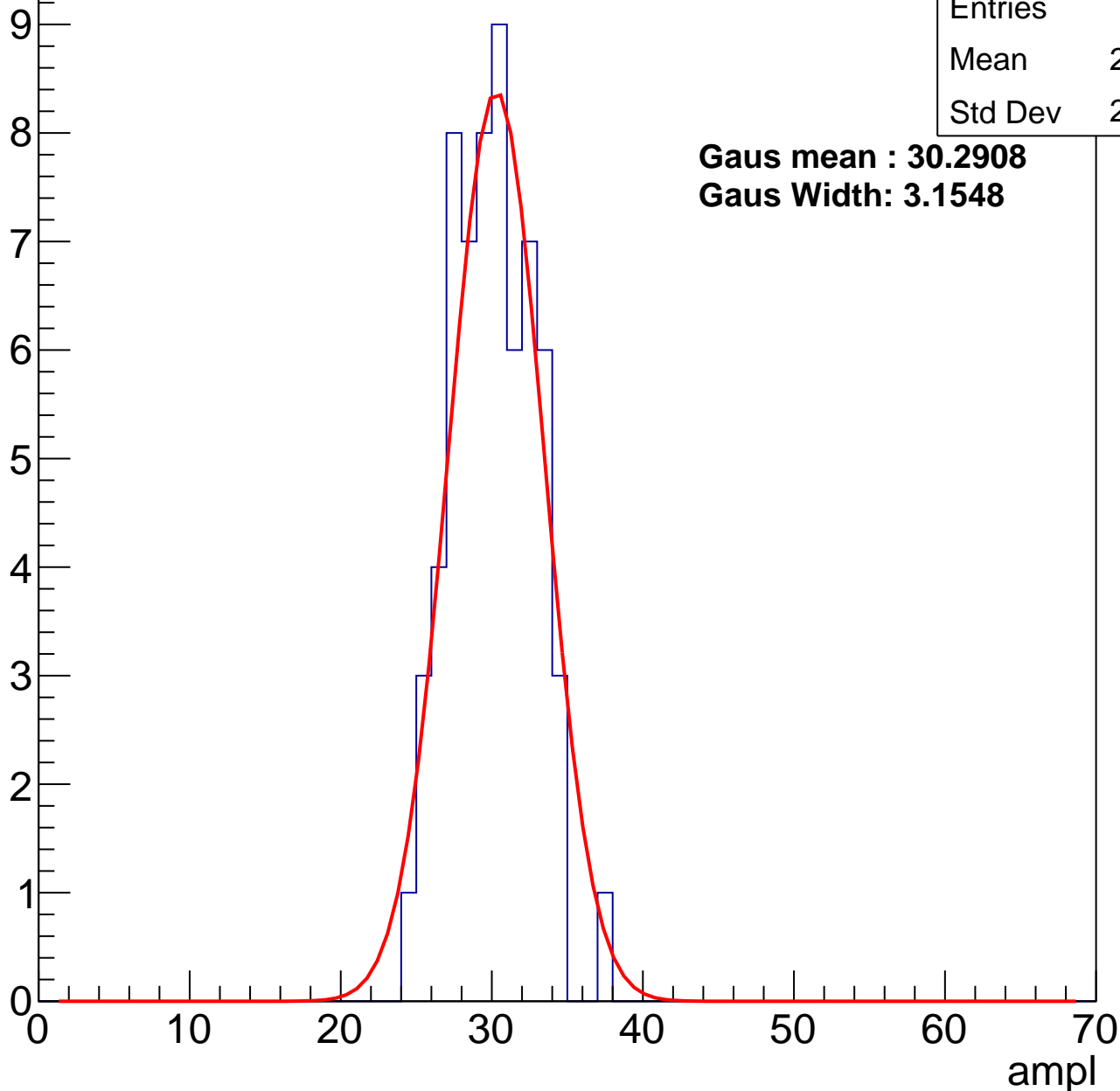
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.59
Std Dev	2.694

Gaus mean : 30.2908

Gaus Width: 3.1548



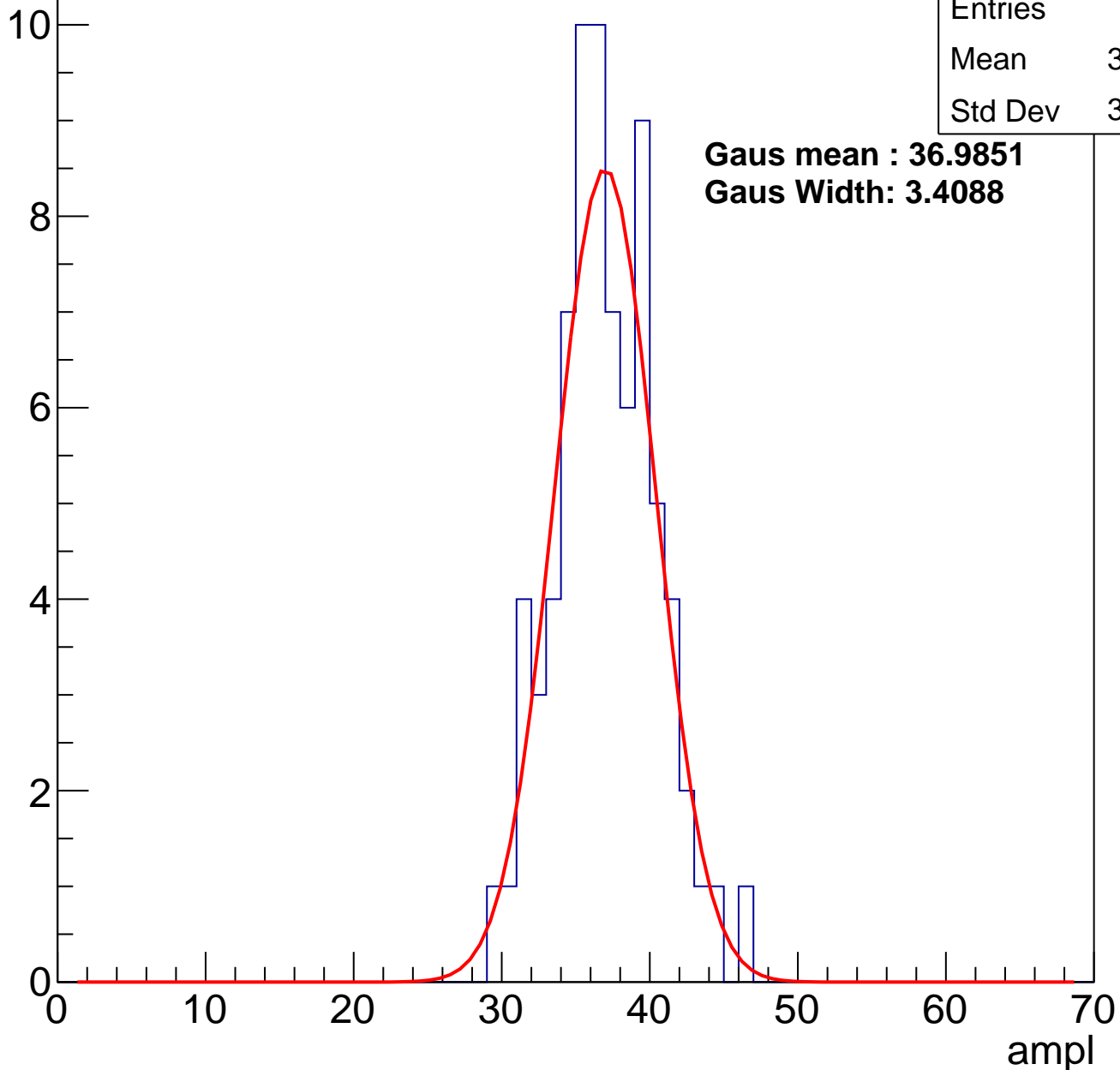
B0L002S, U18-ch1, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	36.55
Std Dev	3.365

Gaus mean : 36.9851
Gaus Width: 3.4088

Entry



B0L002S, U18-ch1, adc2

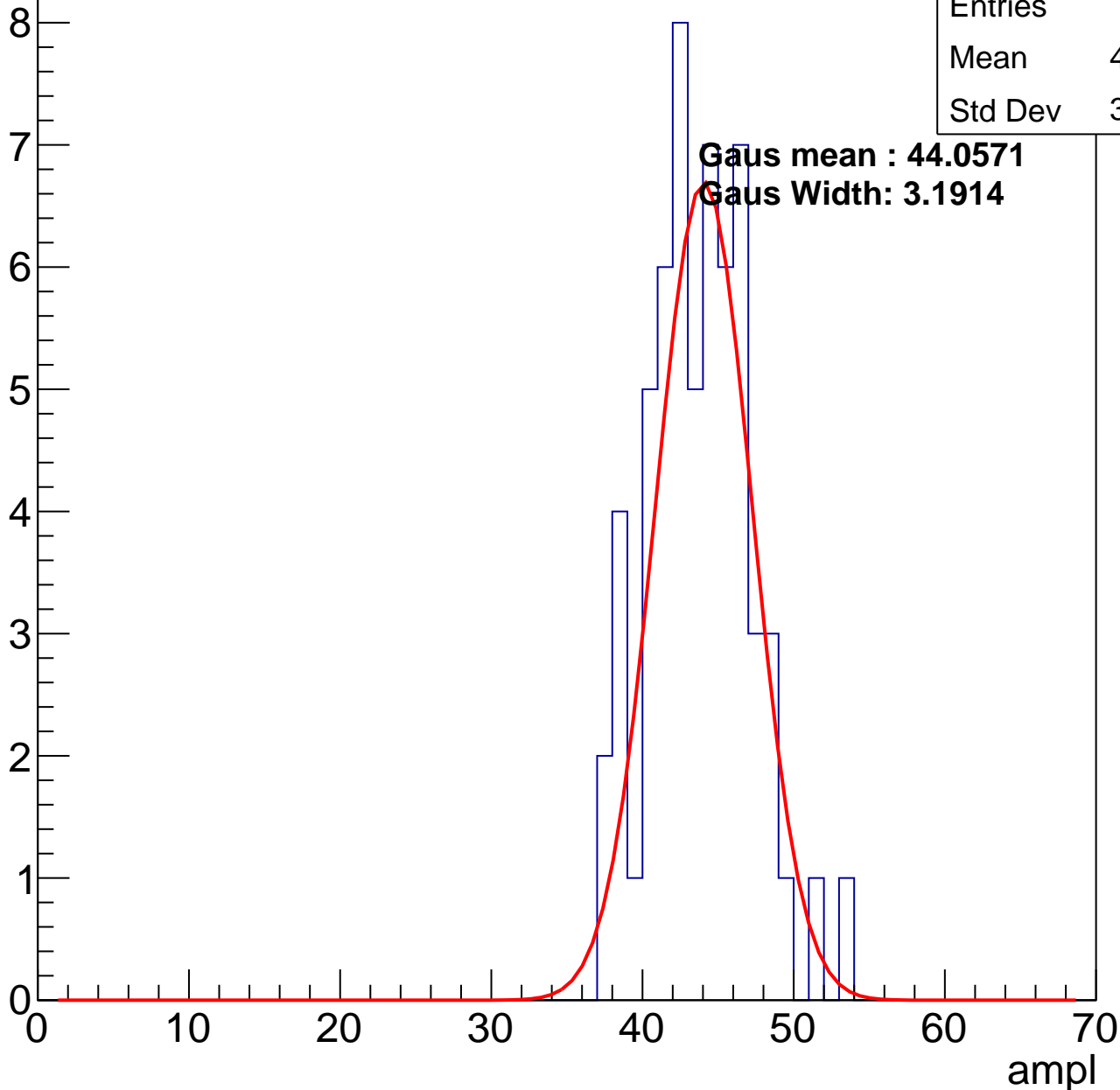
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.33
Std Dev	3.355

Gaus mean : 44.0571

Gaus Width: 3.1914

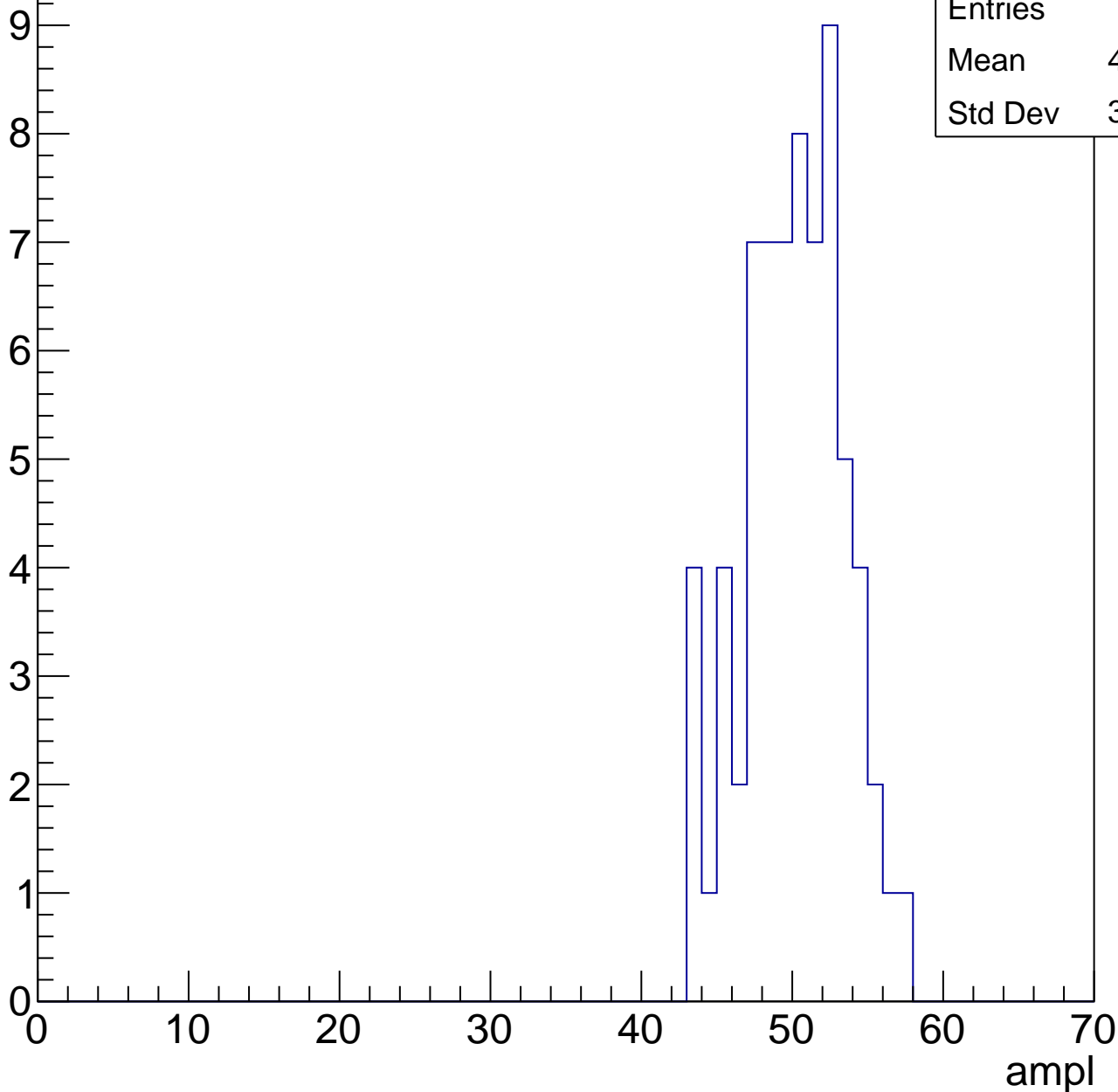


B0L002S, U18-ch1, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.64
Std Dev	3.275

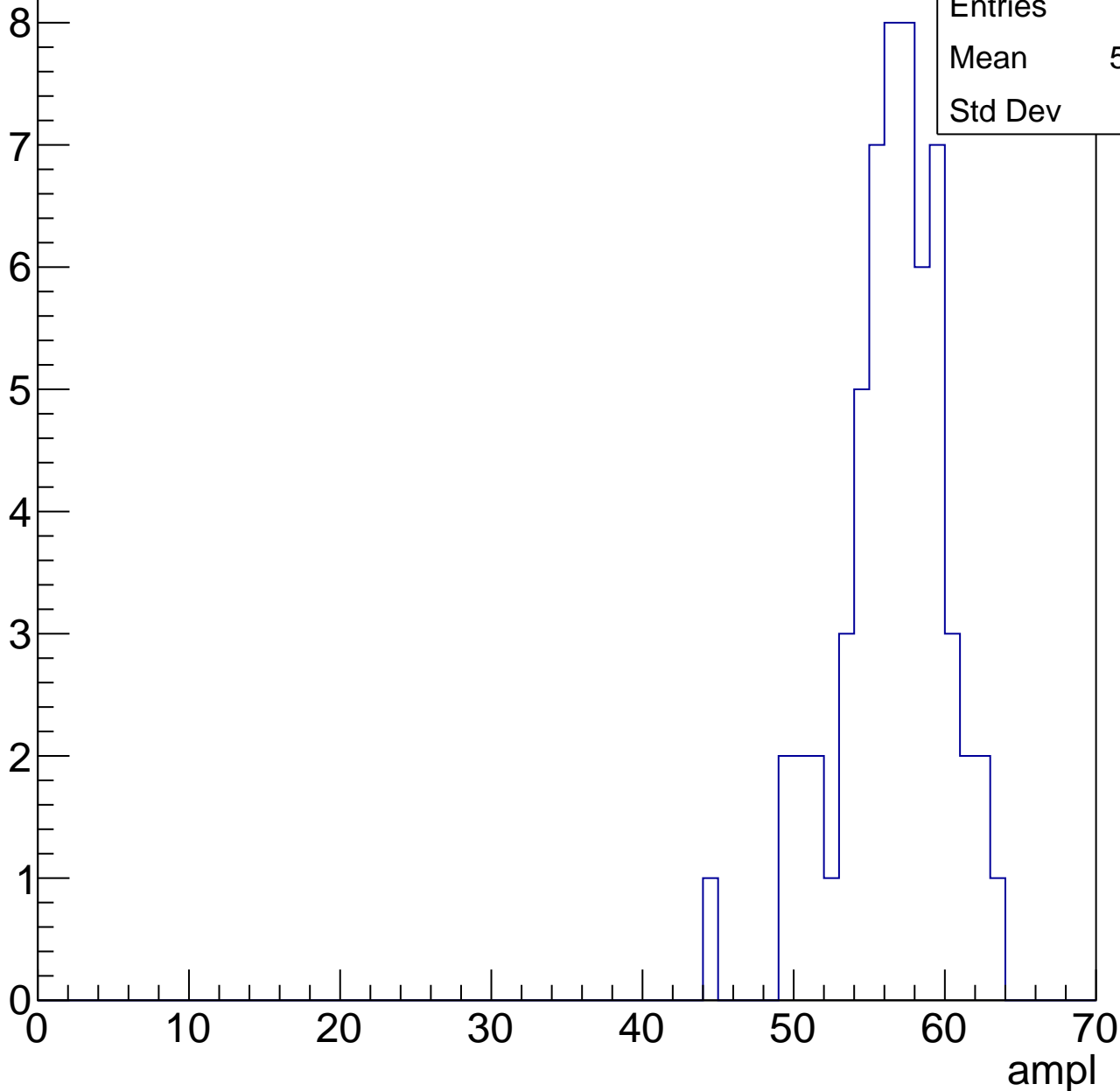


B0L002S, U18-ch1, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.07
Std Dev	3.53

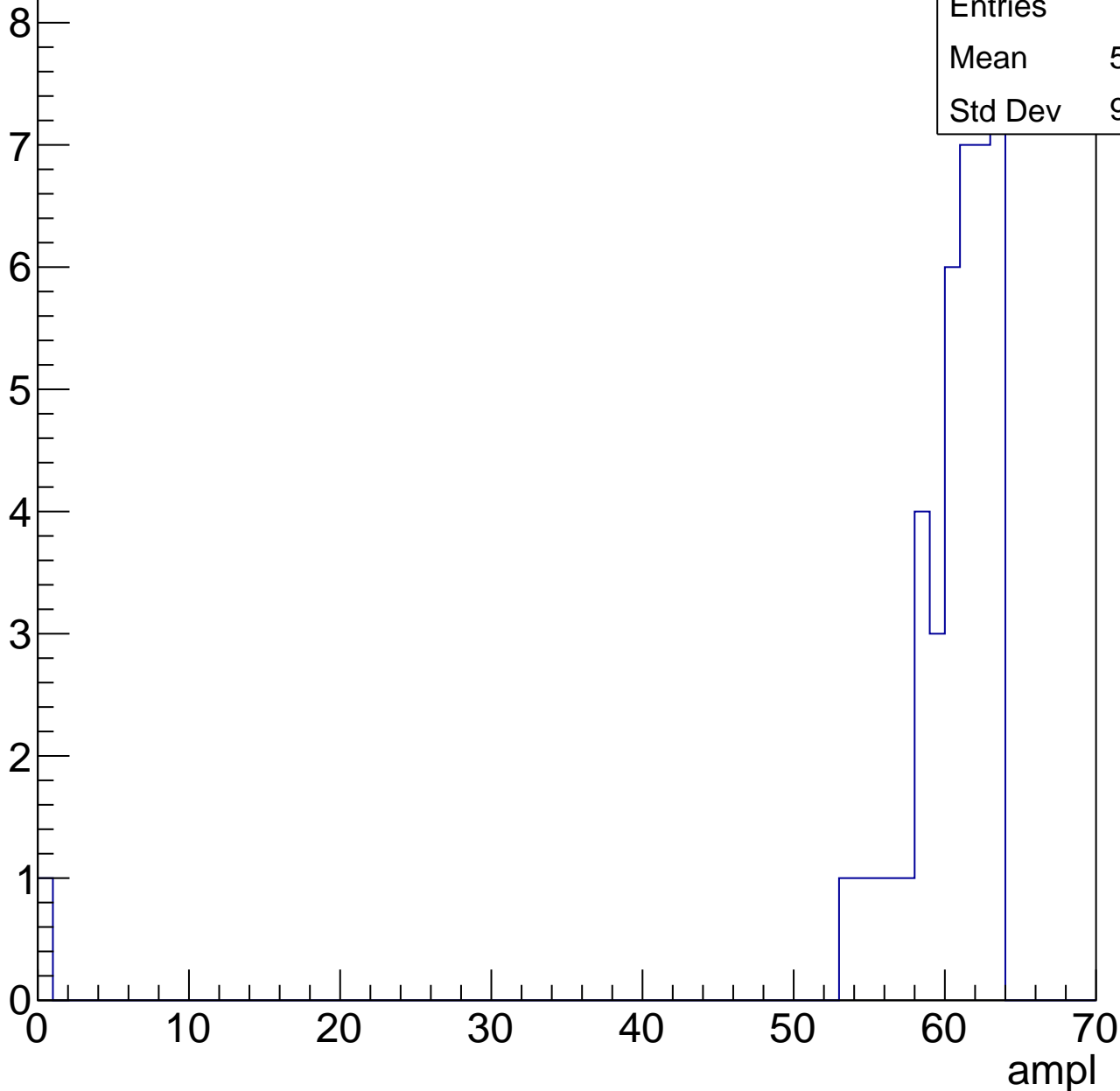


B0L002S, U18-ch1, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

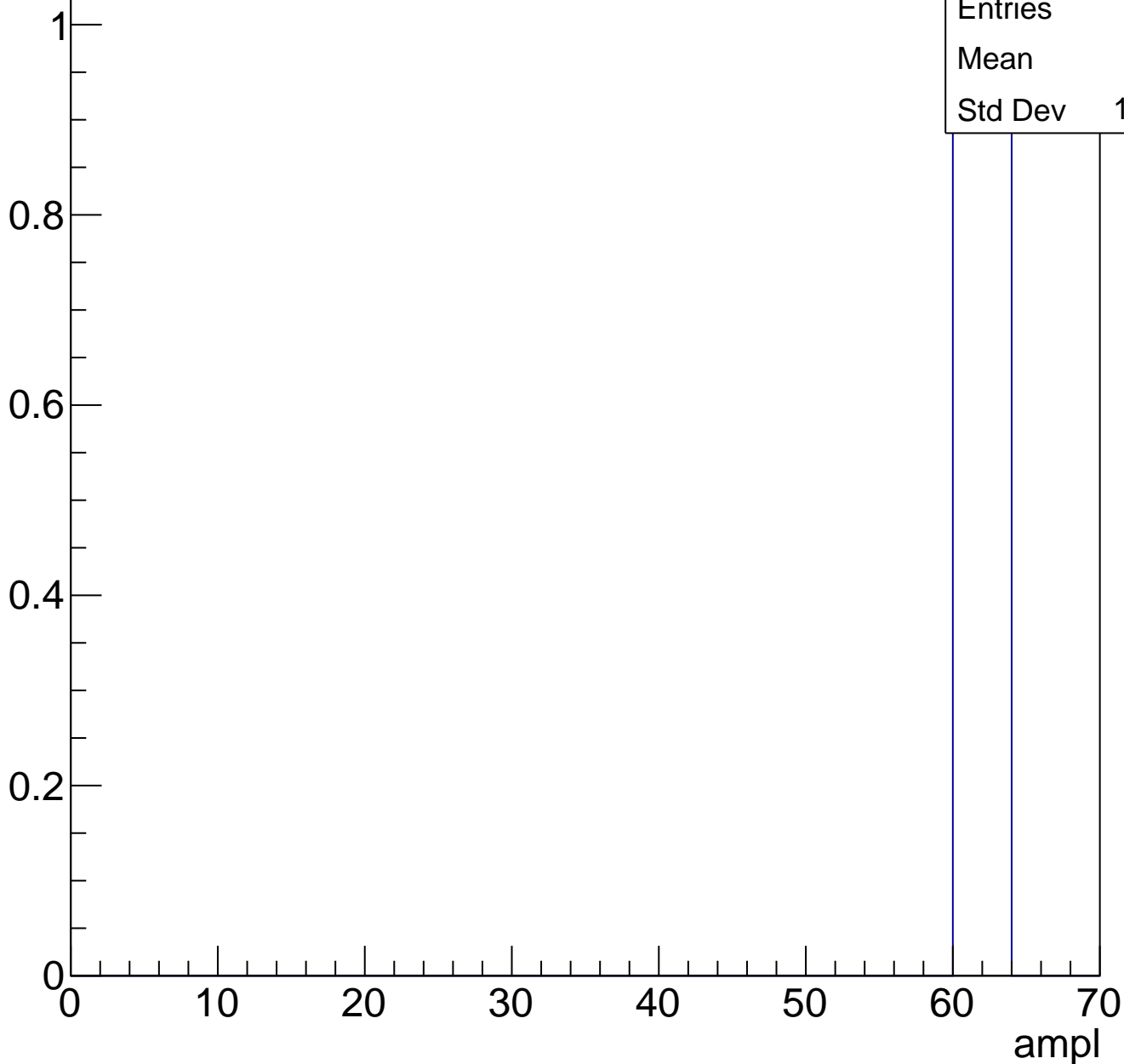
Entries	41
Mean	58.76
Std Dev	9.624



B0L002S, U18-ch1, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch1, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	8
Std Dev	11.31

B0L002S, U18-ch2, adc0

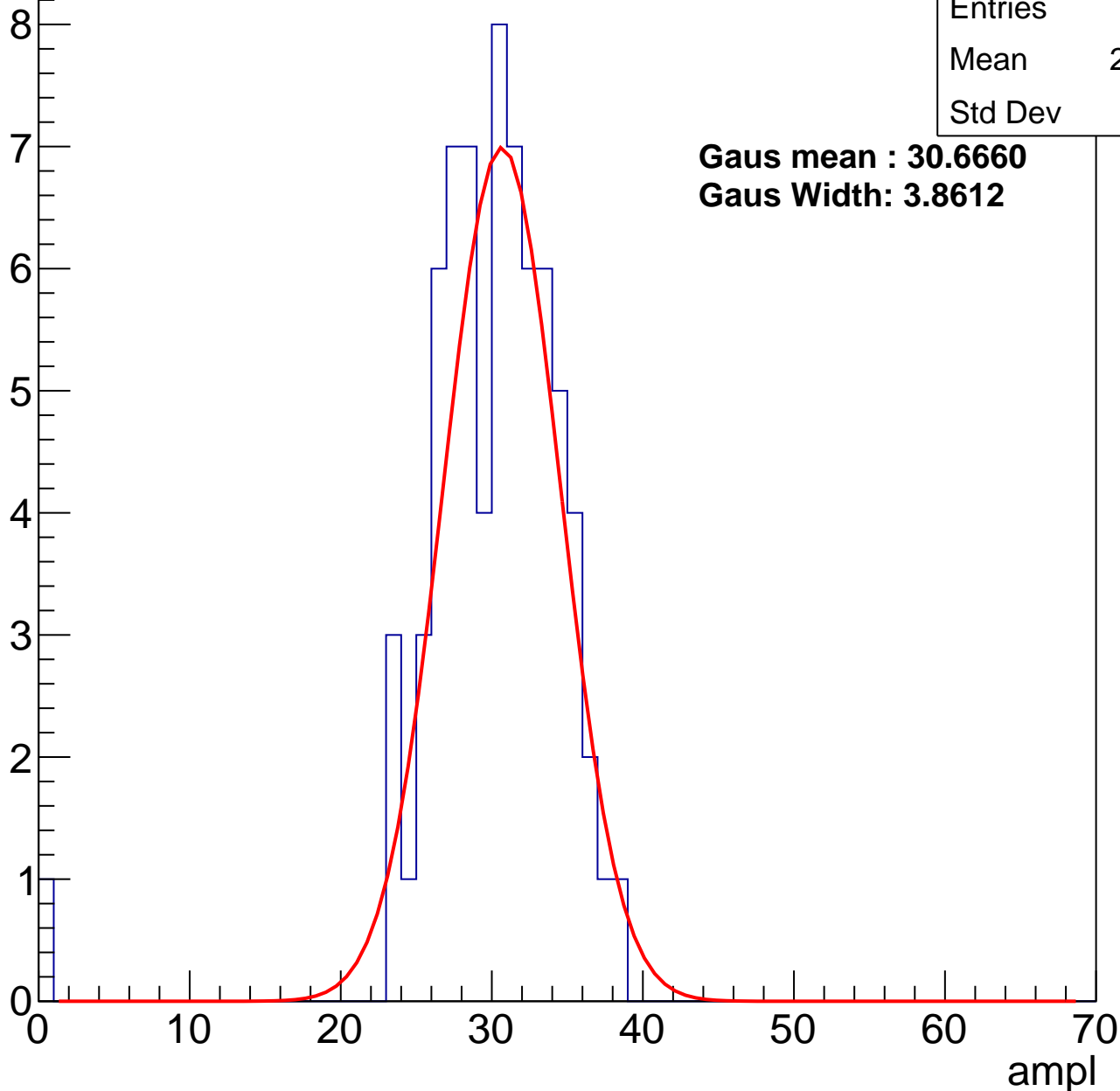
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	29.57
Std Dev	4.98

Gaus mean : 30.6660

Gaus Width: 3.8612



B0L002S, U18-ch2, adc1

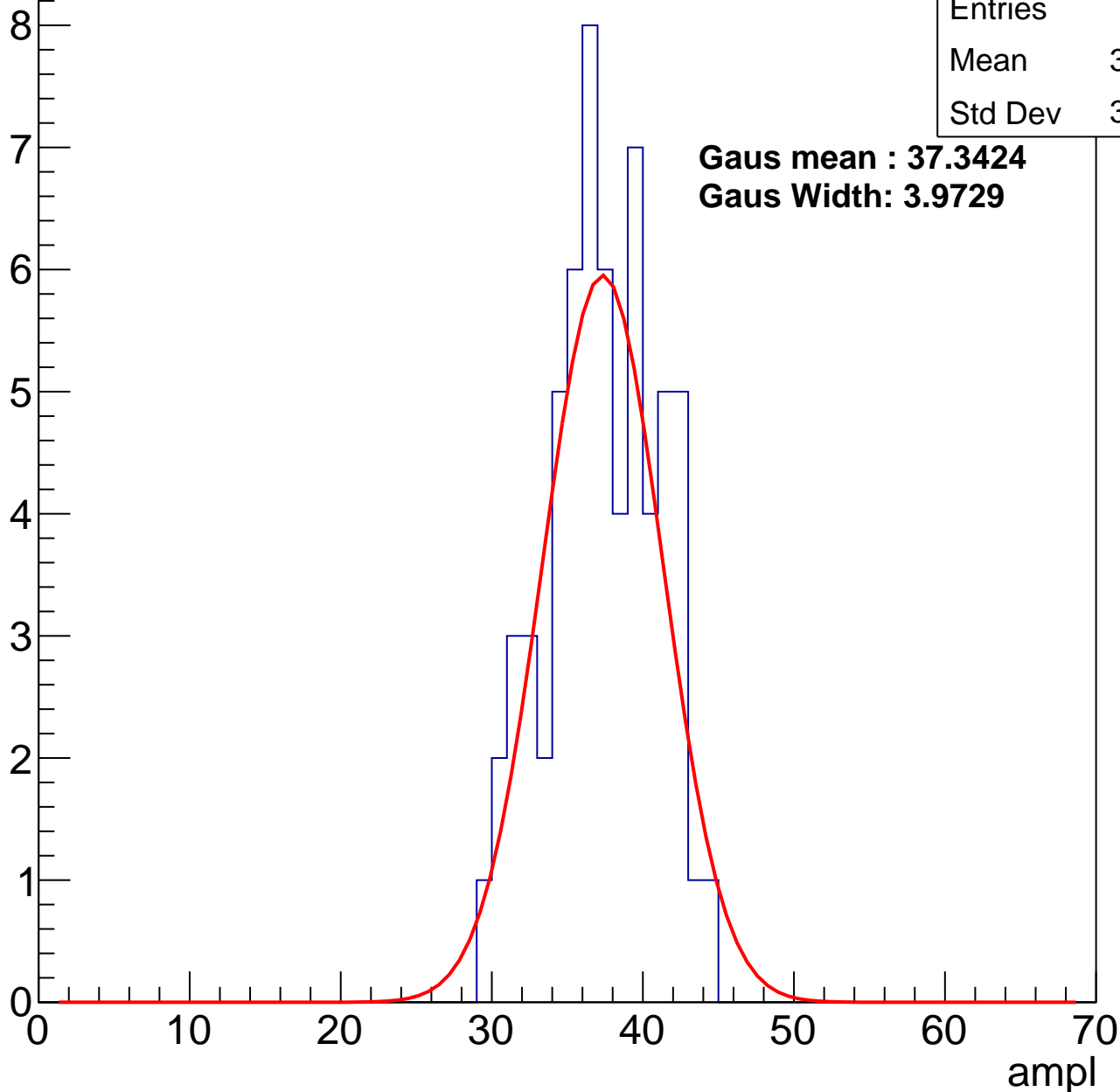
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.84
Std Dev	3.587

Gaus mean : 37.3424

Gaus Width: 3.9729



B0L002S, U18-ch2, adc2

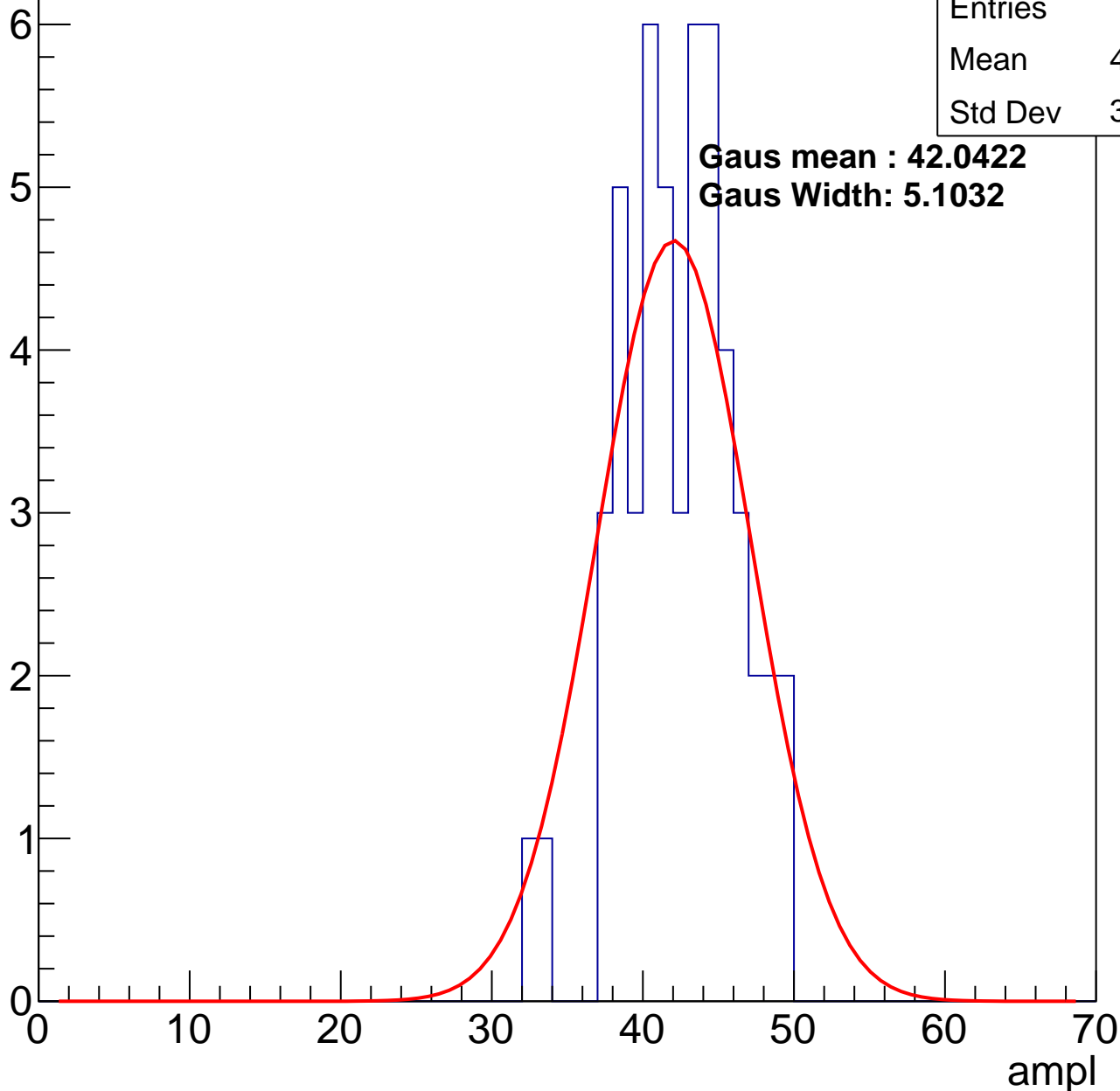
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	41.96
Std Dev	3.736

Gaus mean : 42.0422

Gaus Width: 5.1032

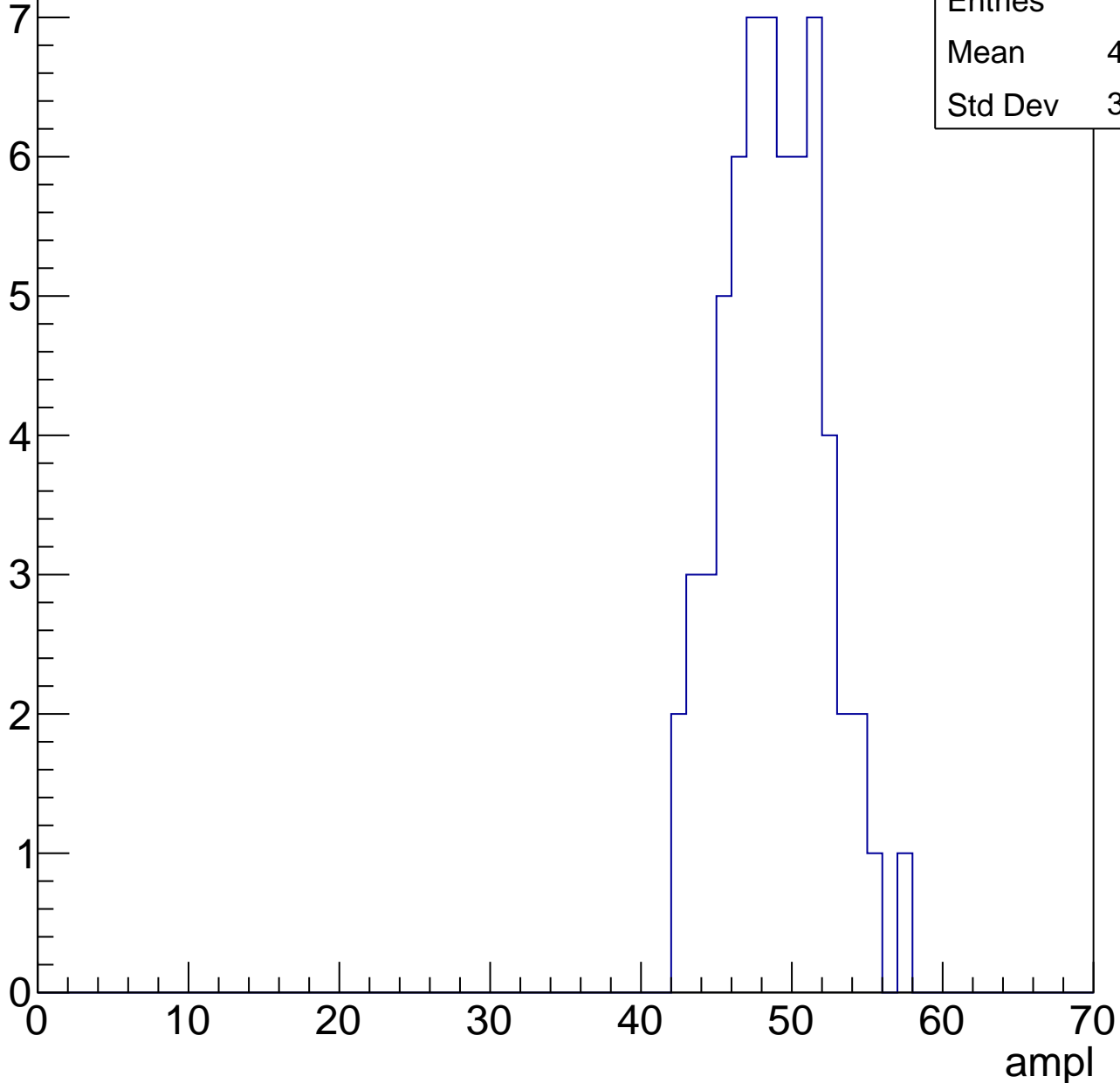


B0L002S, U18-ch2, adc3

calib_packv5_042523_0143.root, FC#8, port C1

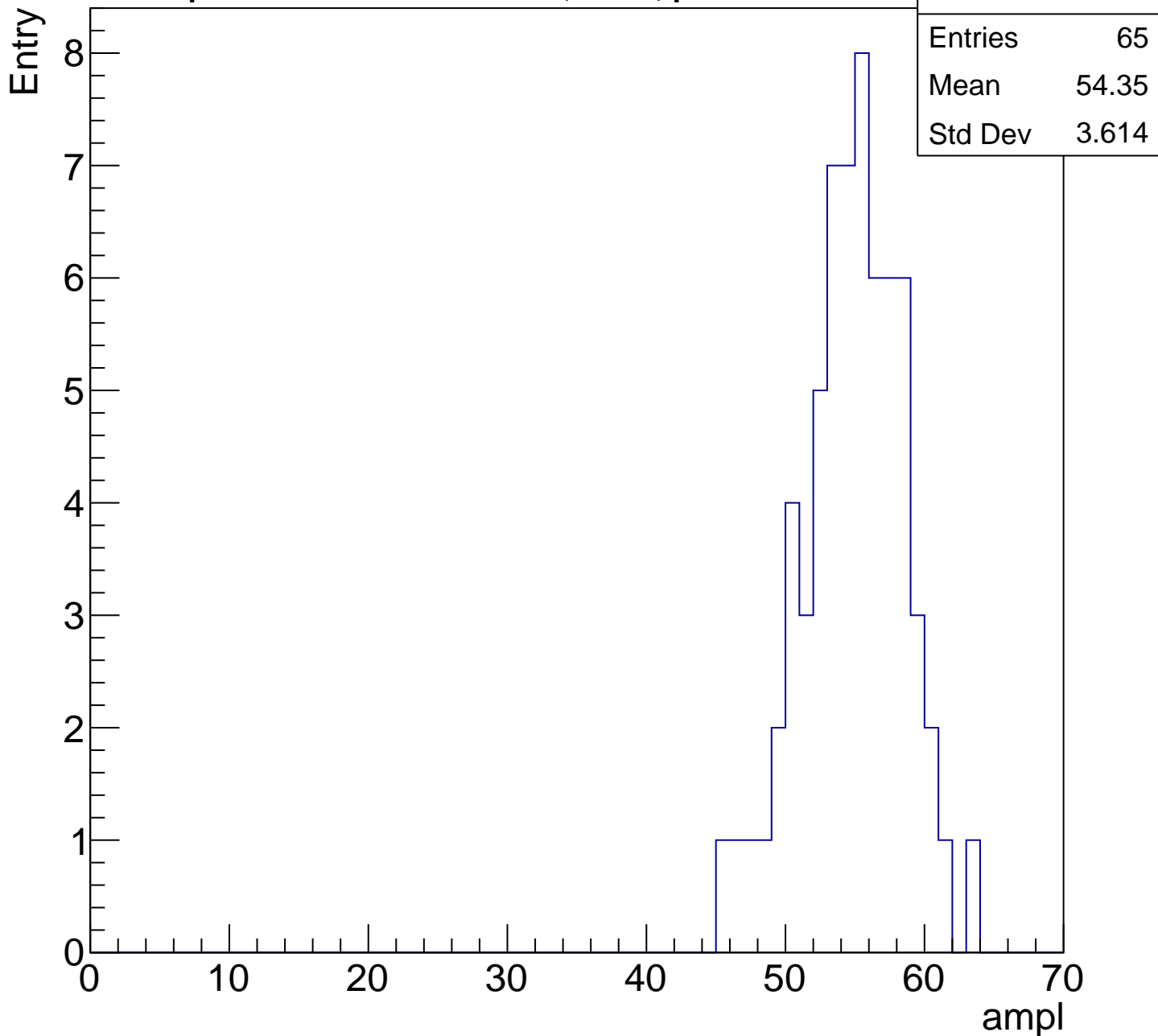
Entry

Entries	62
Mean	48.32
Std Dev	3.296



B0L002S, U18-ch2, adc4

calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U18-ch2, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries	47
Mean	59.43
Std Dev	2.773

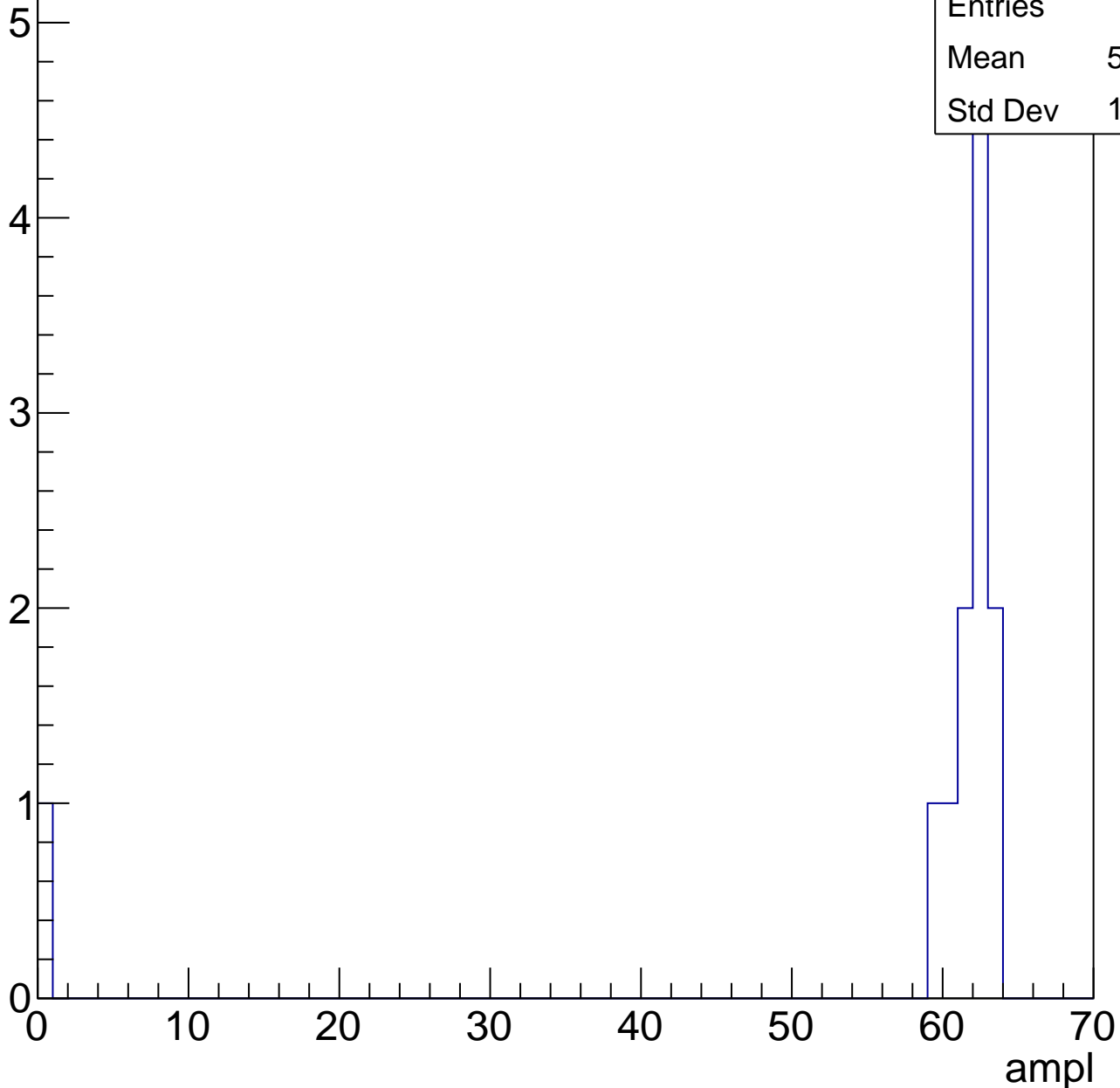
ampl

B0L002S, U18-ch2, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	56.42
Std Dev	17.05



B0L002S, U18-ch2, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch3, adc0

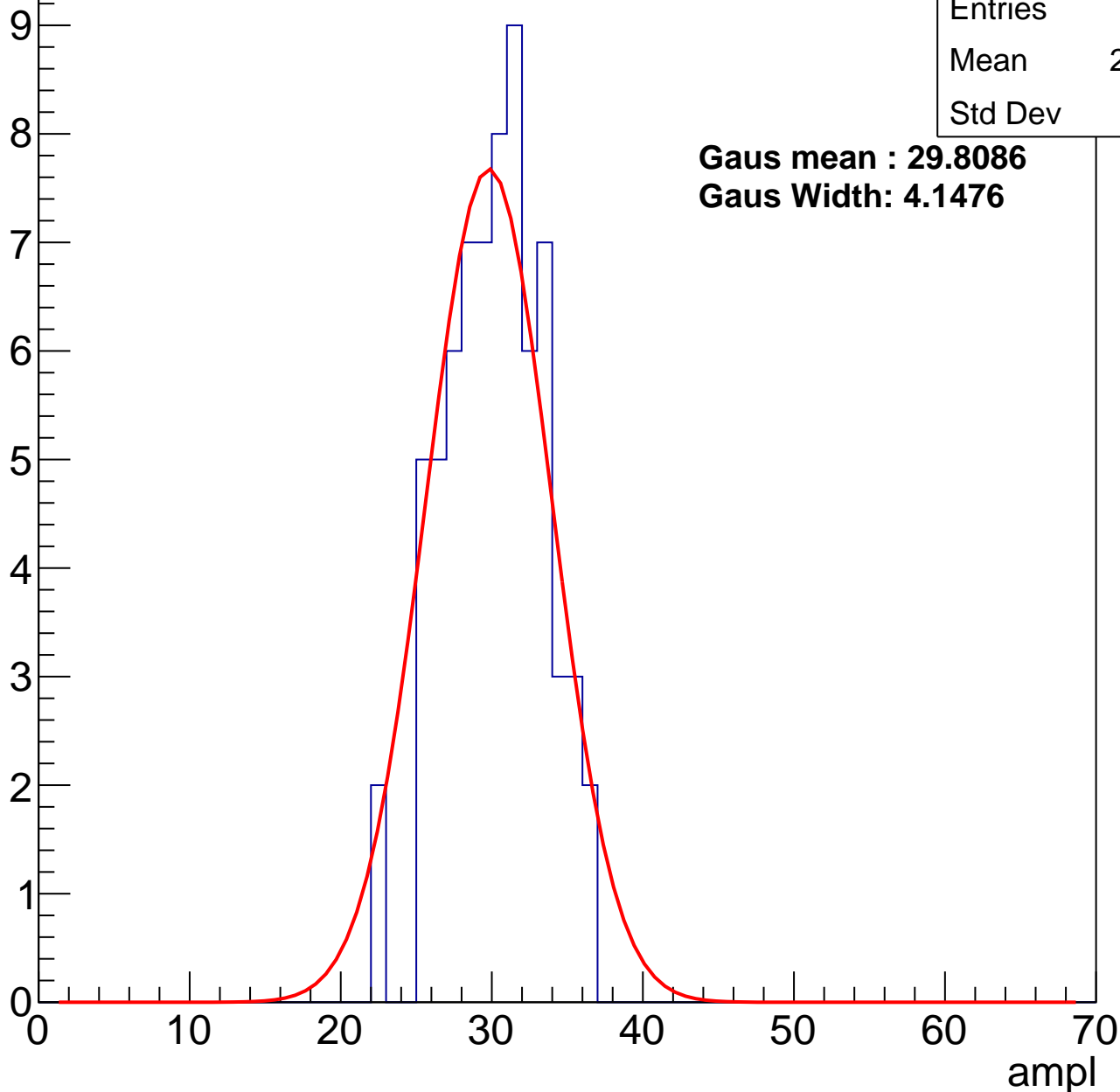
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.73
Std Dev	3.18

Gaus mean : 29.8086

Gaus Width: 4.1476



B0L002S, U18-ch3, adc1

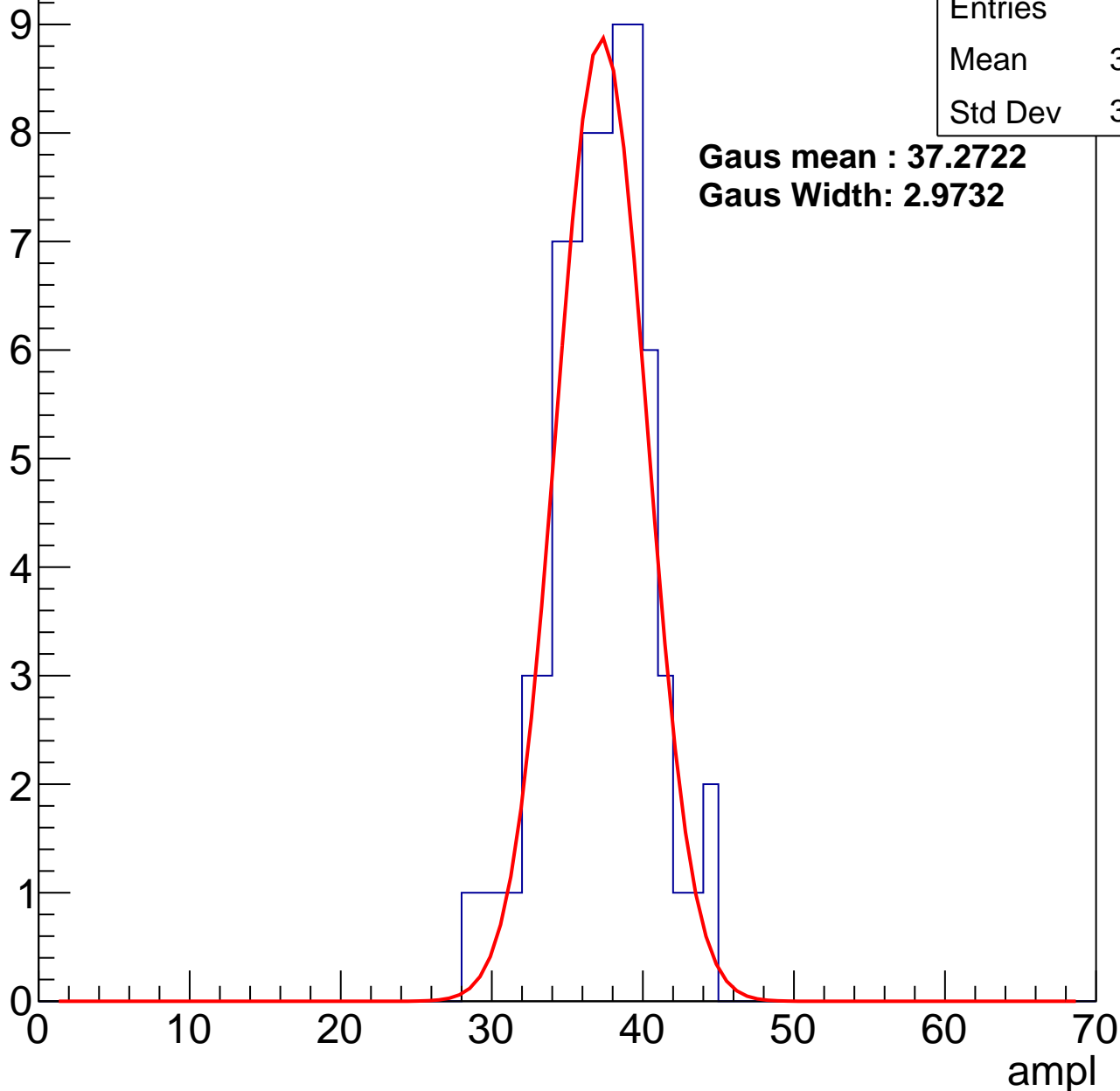
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.75
Std Dev	3.258

Gaus mean : 37.2722

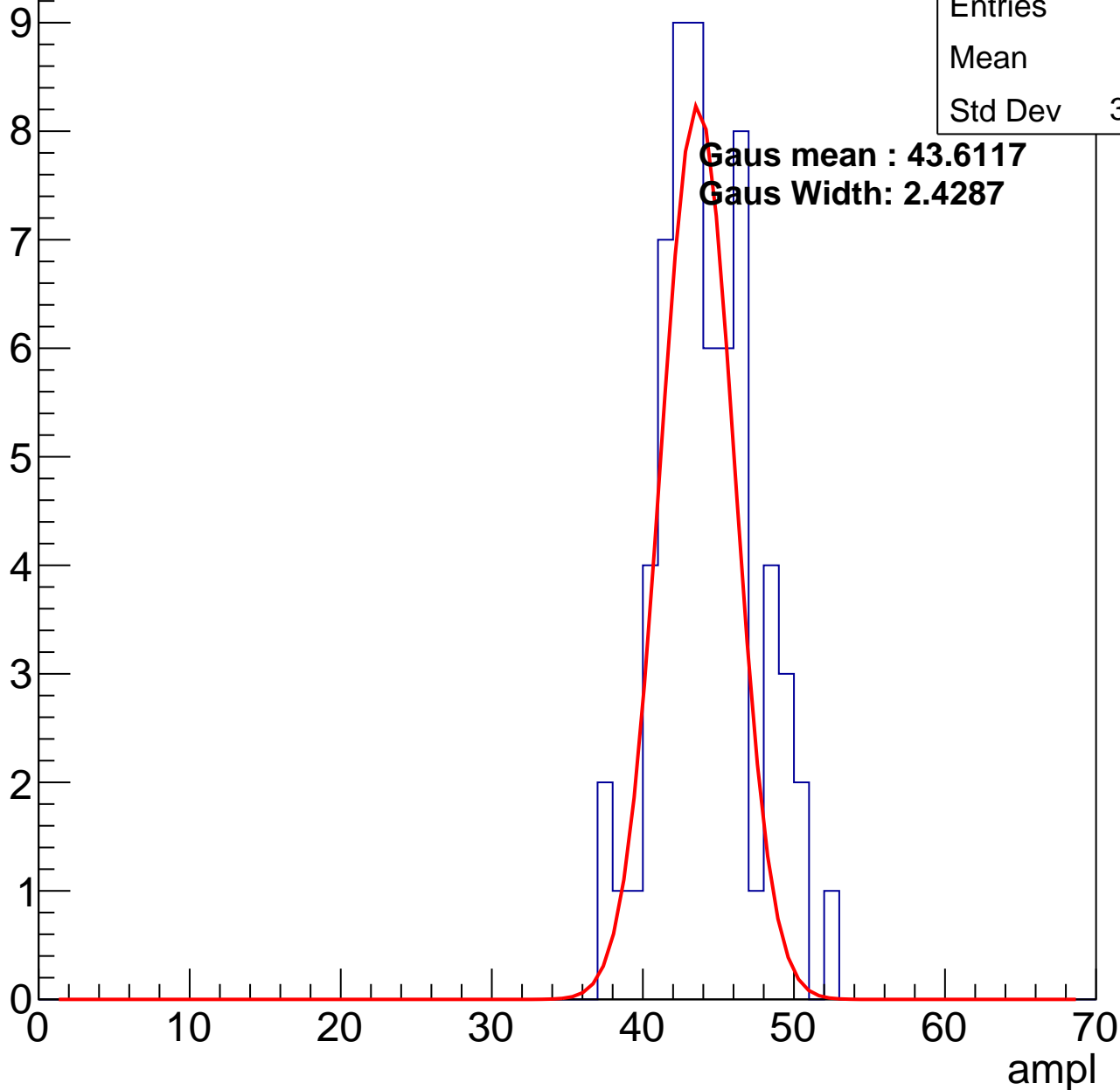
Gaus Width: 2.9732



B0L002S, U18-ch3, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

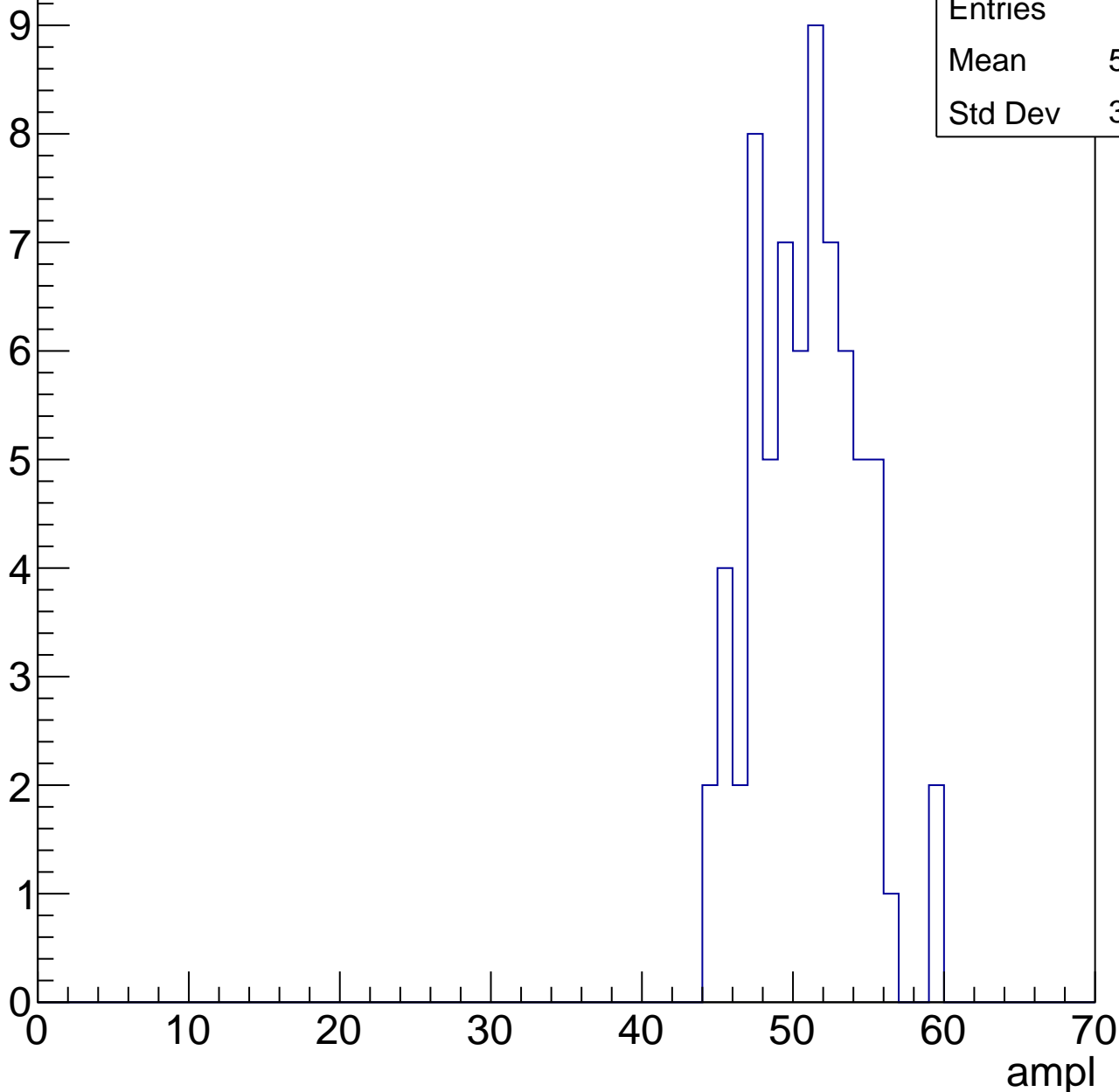


B0L002S, U18-ch3, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

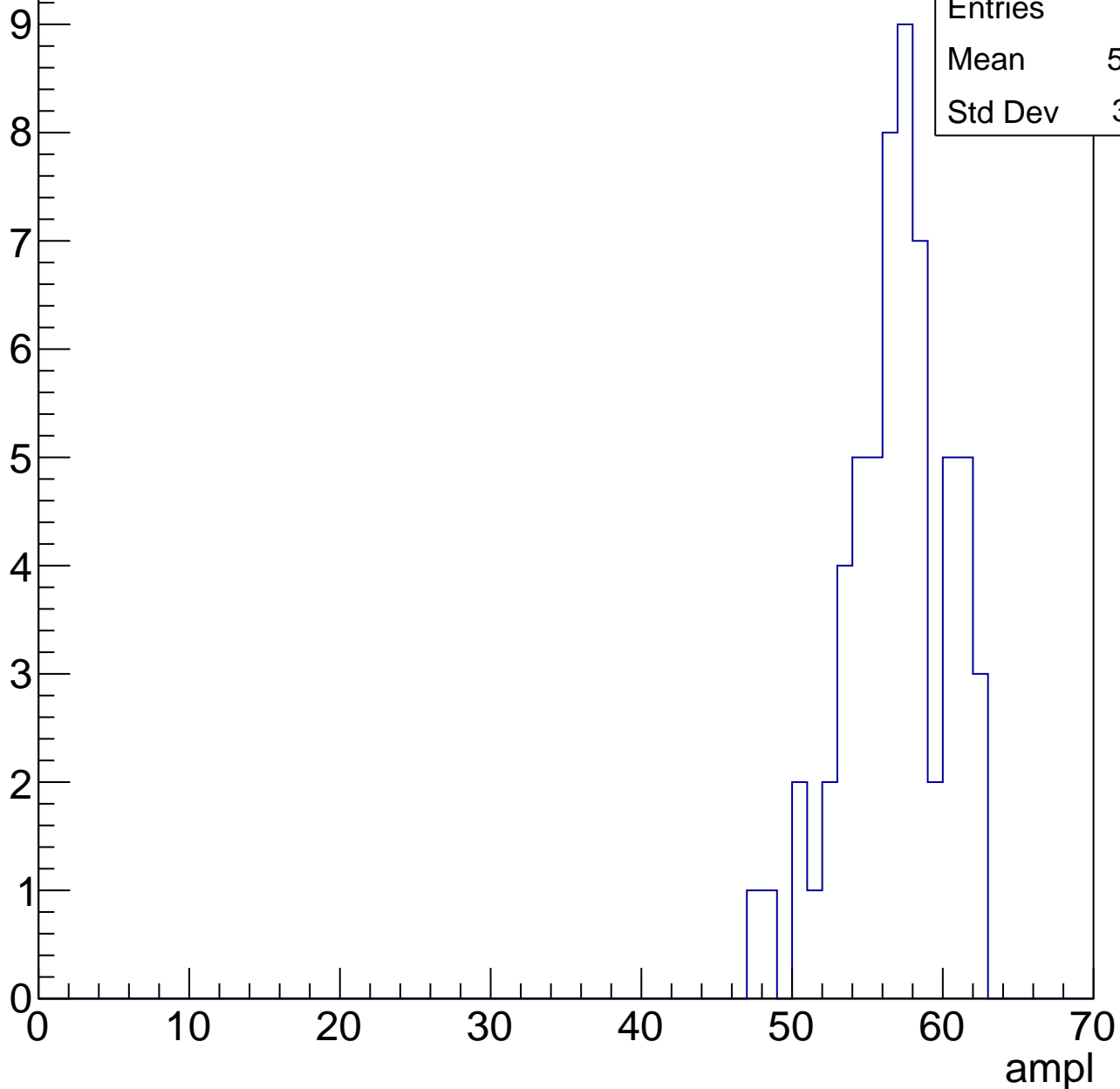
Entries	69
Mean	50.42
Std Dev	3.373



B0L002S, U18-ch3, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

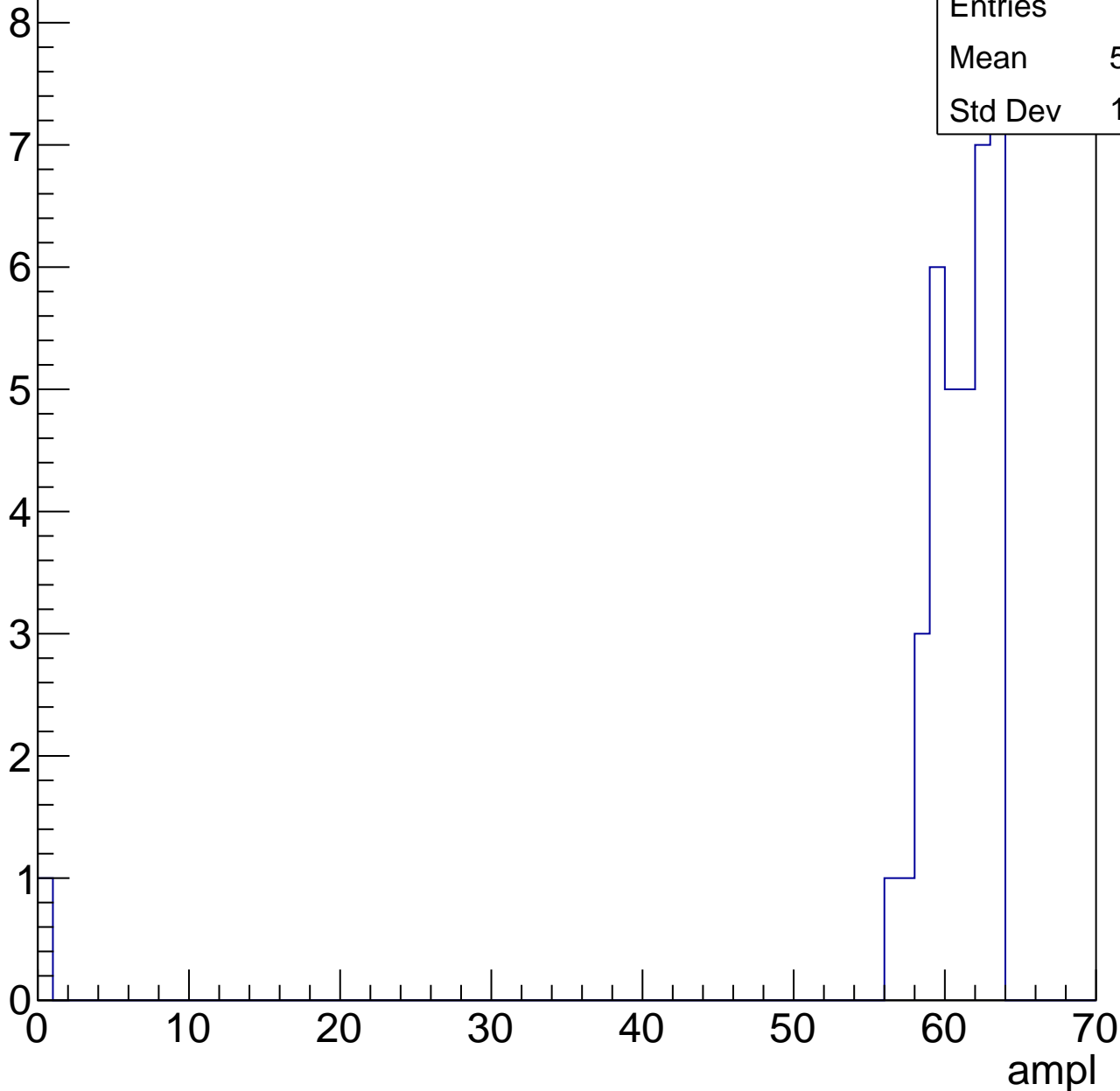


B0L002S, U18-ch3, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	59.03
Std Dev	10.02



B0L002S, U18-ch3, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	63
Std Dev	0

B0L002S, U18-ch3, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L002S, U18-ch4, adc0

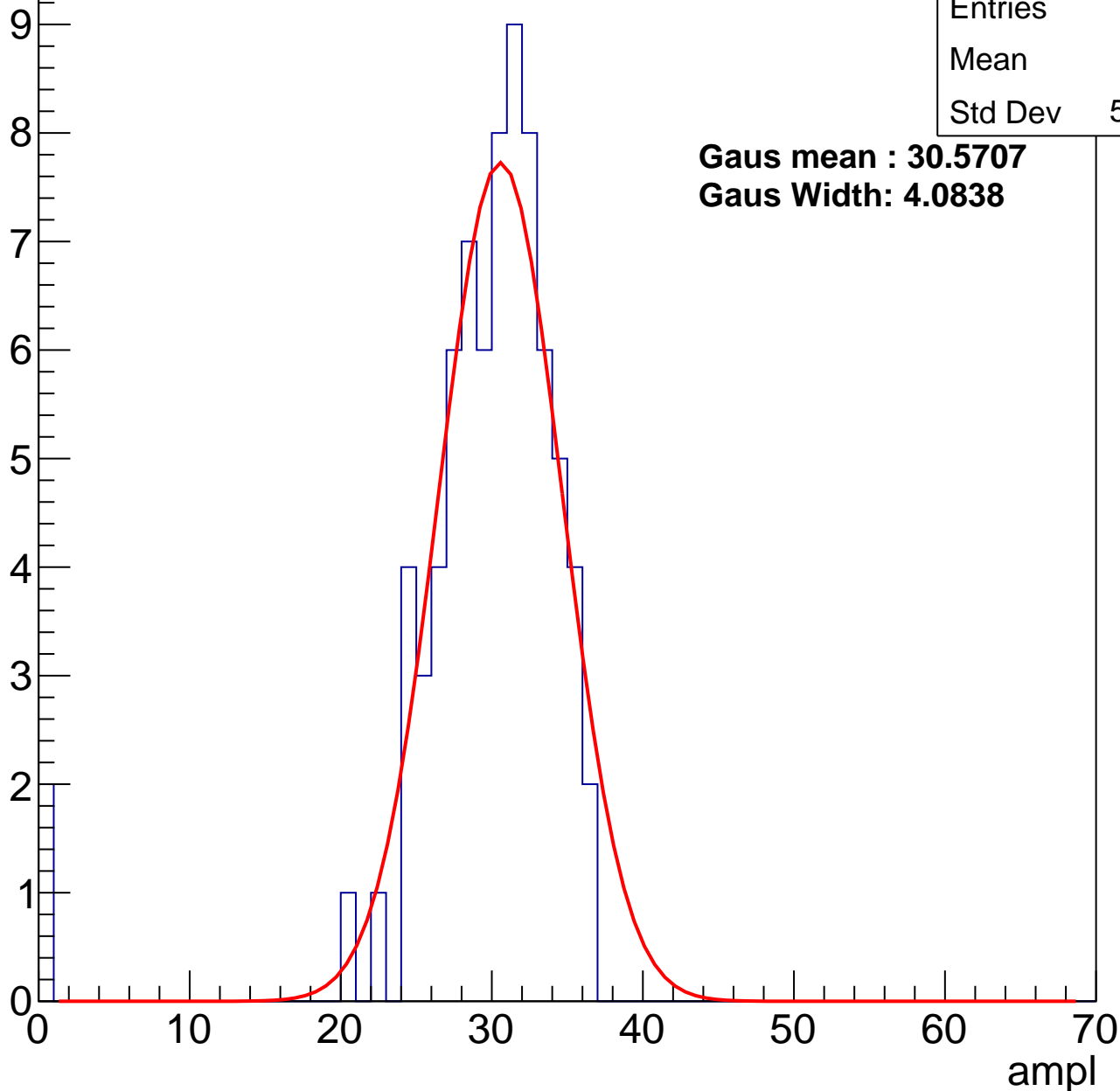
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	29
Std Dev	5.856

Gaus mean : 30.5707

Gaus Width: 4.0838



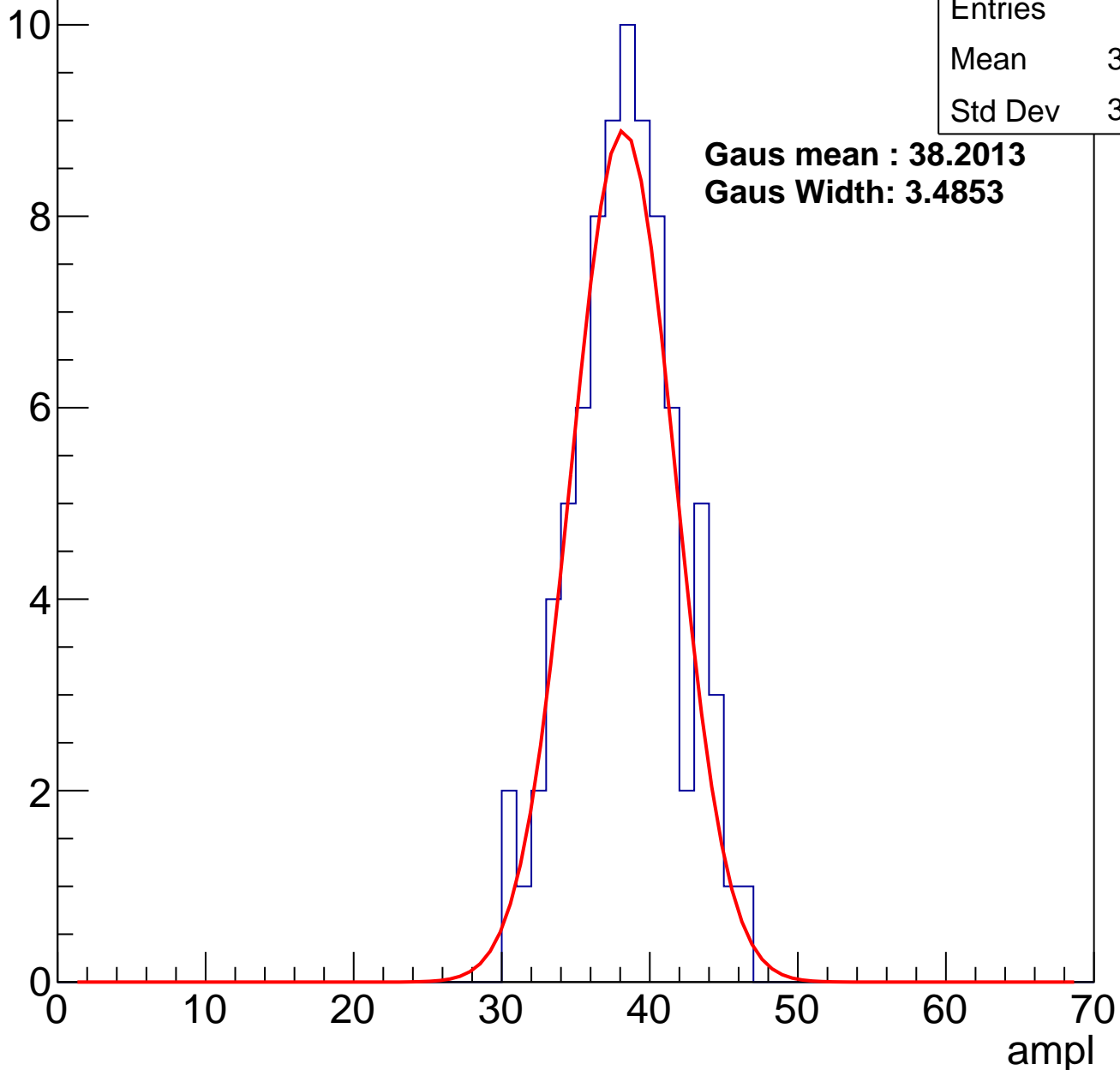
B0L002S, U18-ch4, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	37.89
Std Dev	3.485

Gaus mean : 38.2013
Gaus Width: 3.4853

Entry



B0L002S, U18-ch4, adc2

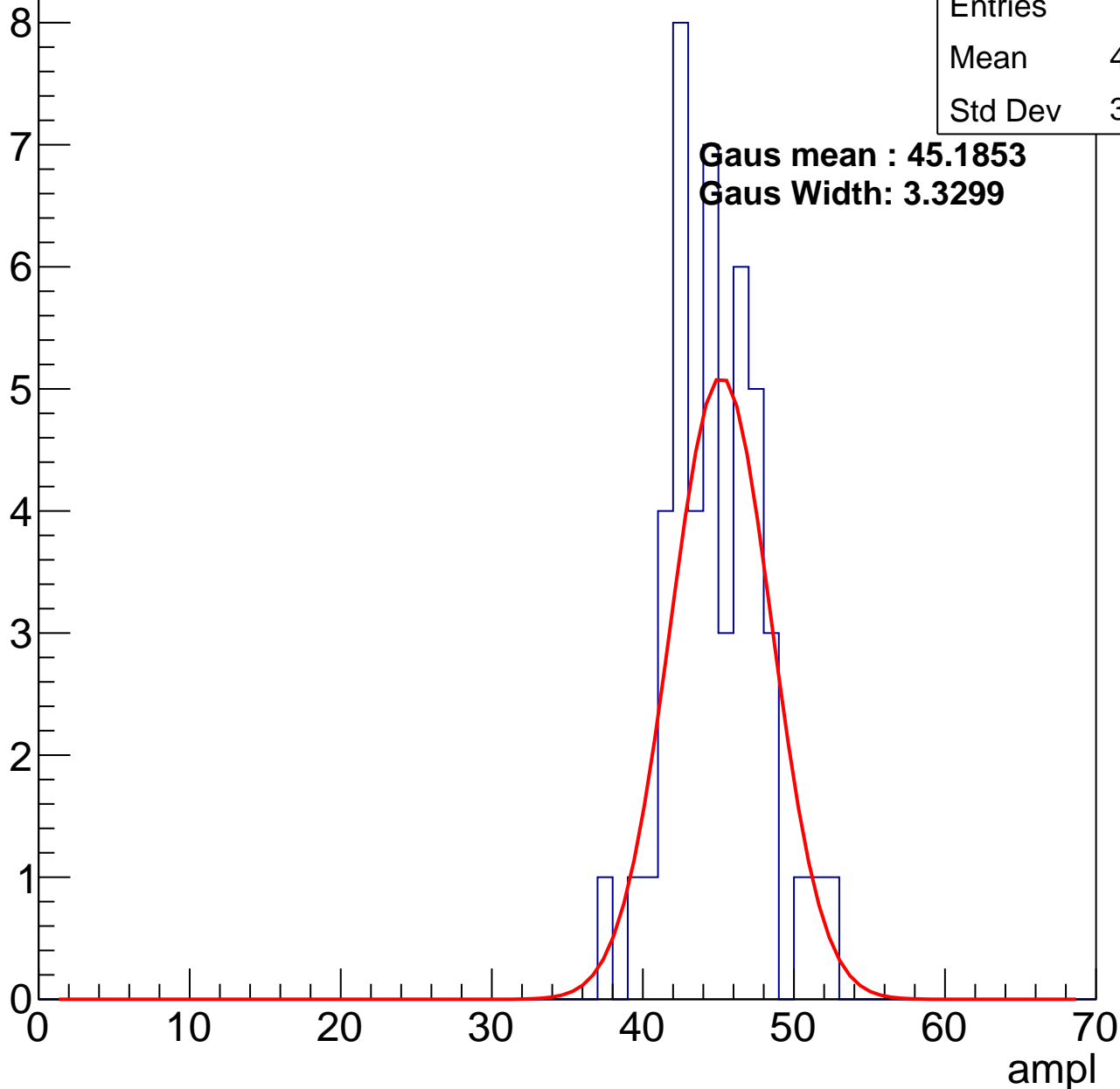
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	44.33
Std Dev	3.044

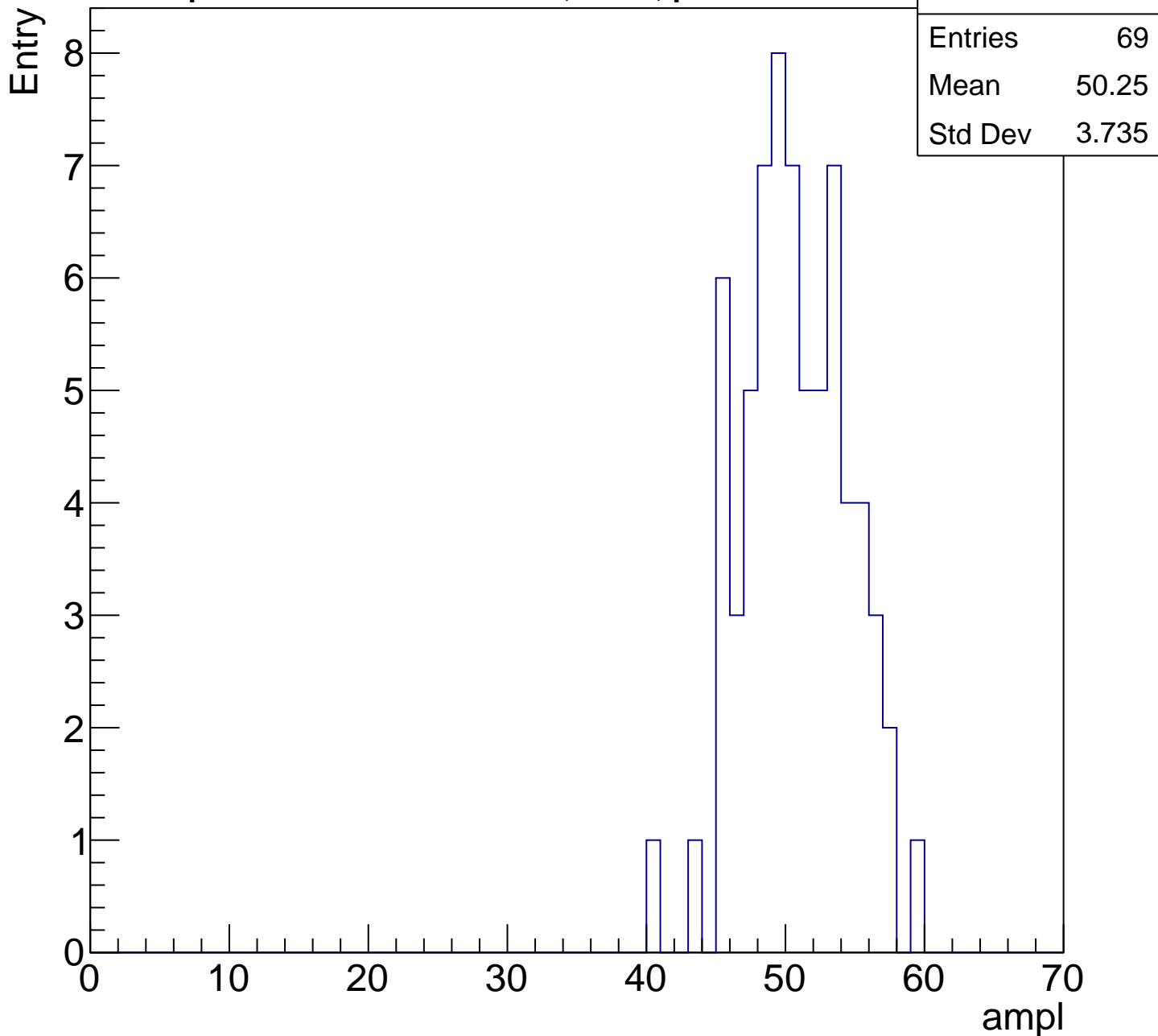
Gaus mean : 45.1853

Gaus Width: 3.3299



B0L002S, U18-ch4, adc3

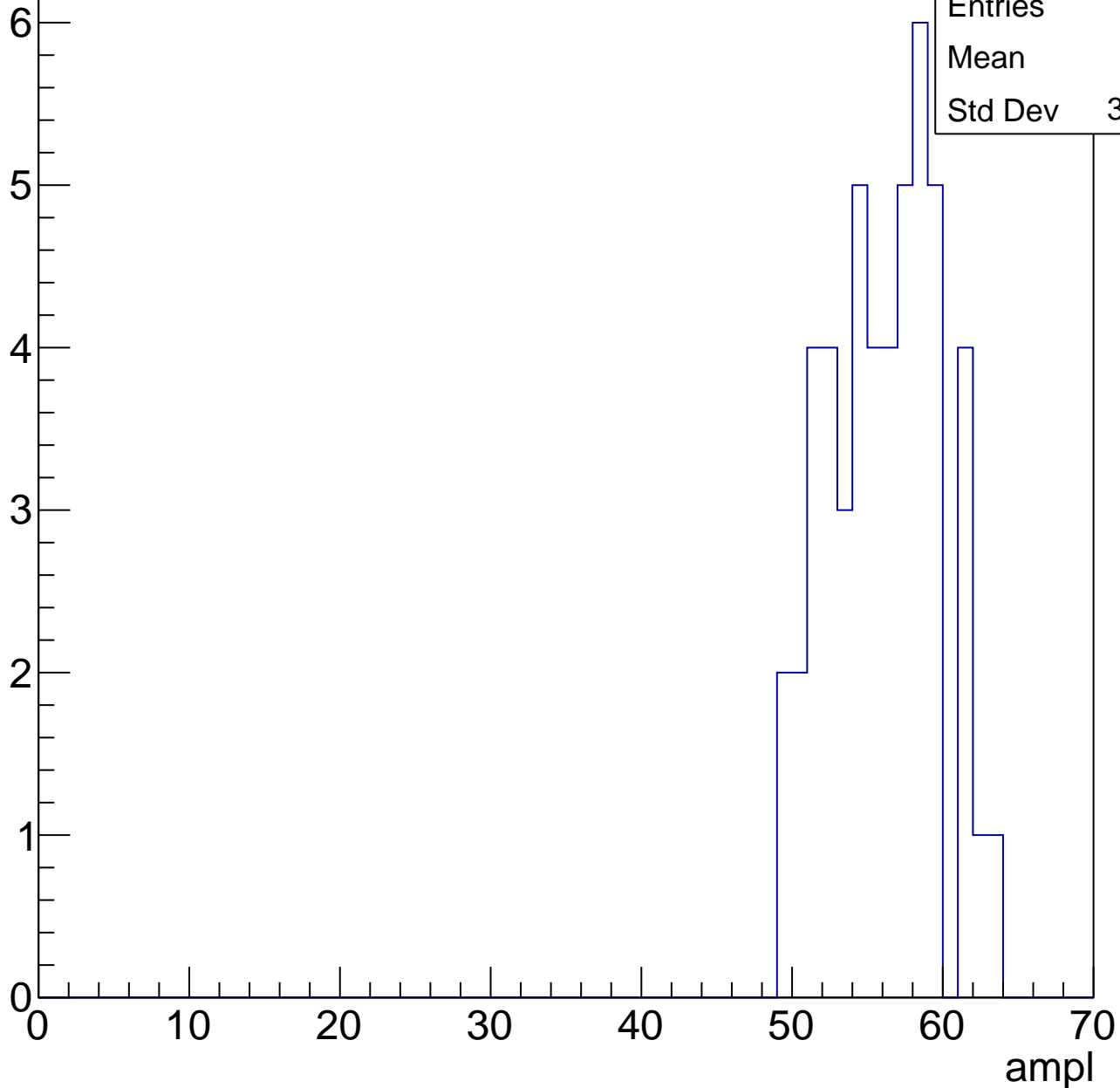
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U18-ch4, adc4

calib_packv5_042523_0143.root, FC#8, port C1

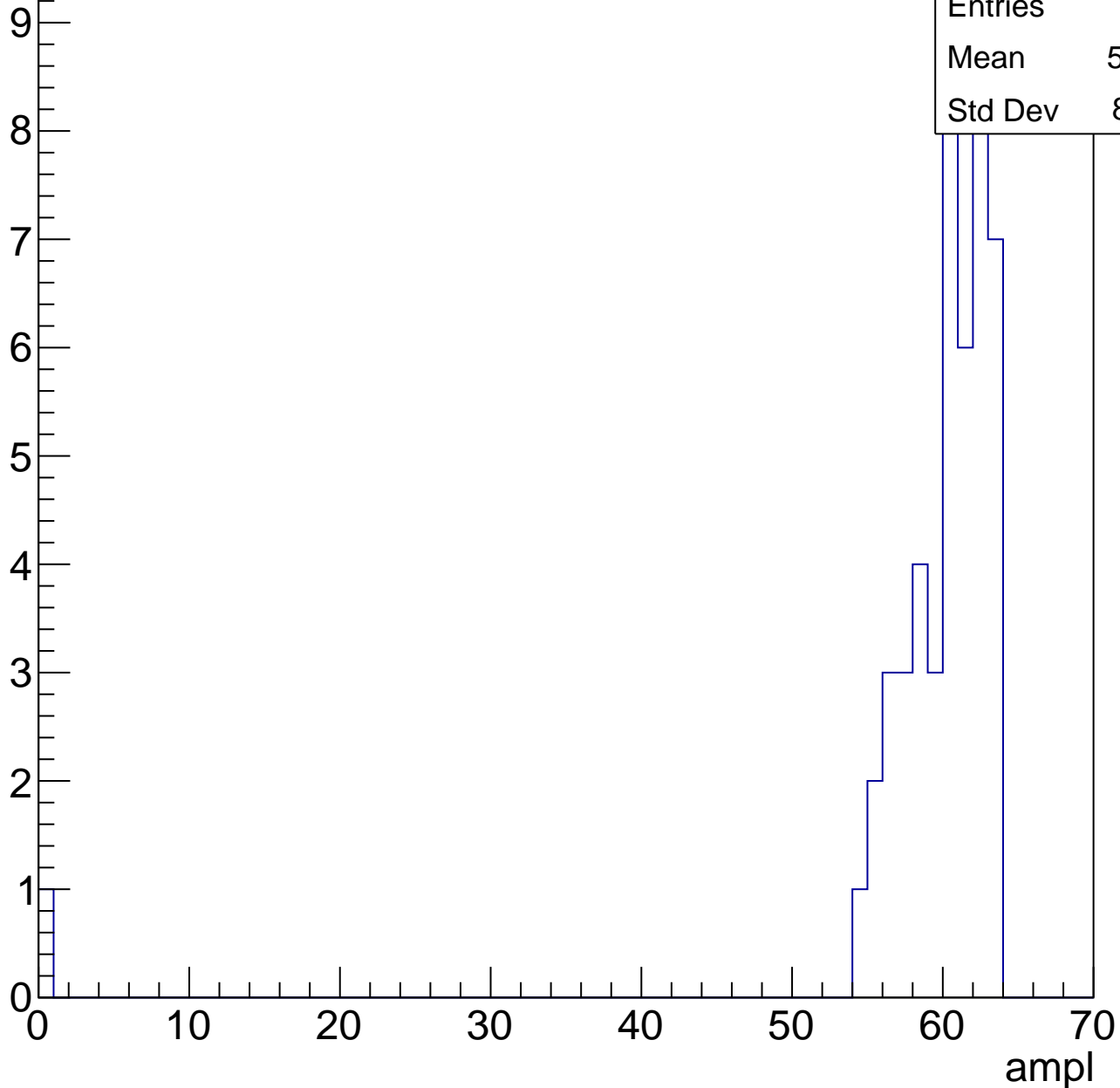
Entry



B0L002S, U18-ch4, adc5

calib_packv5_042523_0143.root, FC#8, port C1

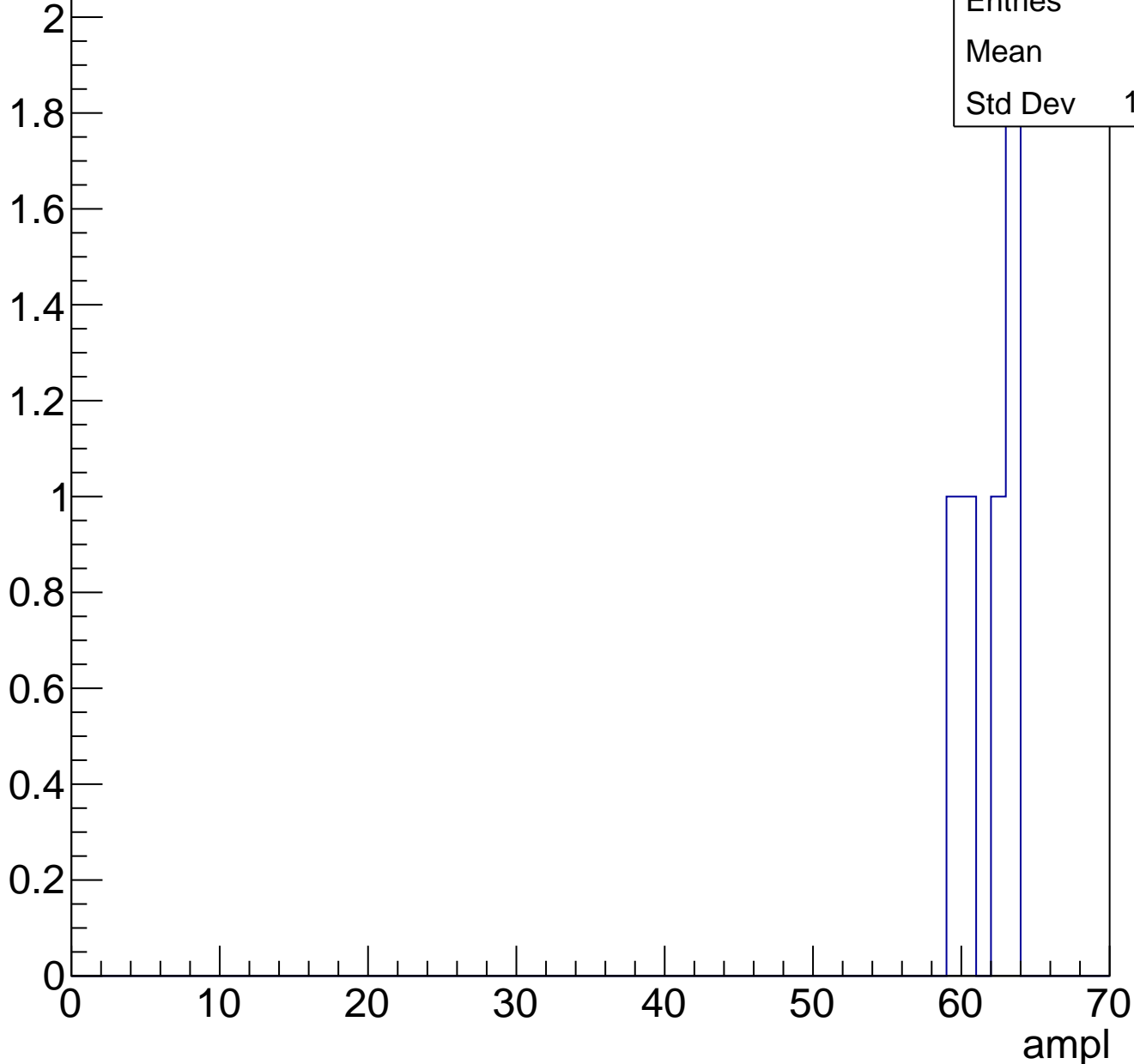
Entry



B0L002S, U18-ch4, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch4, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch5, adc0

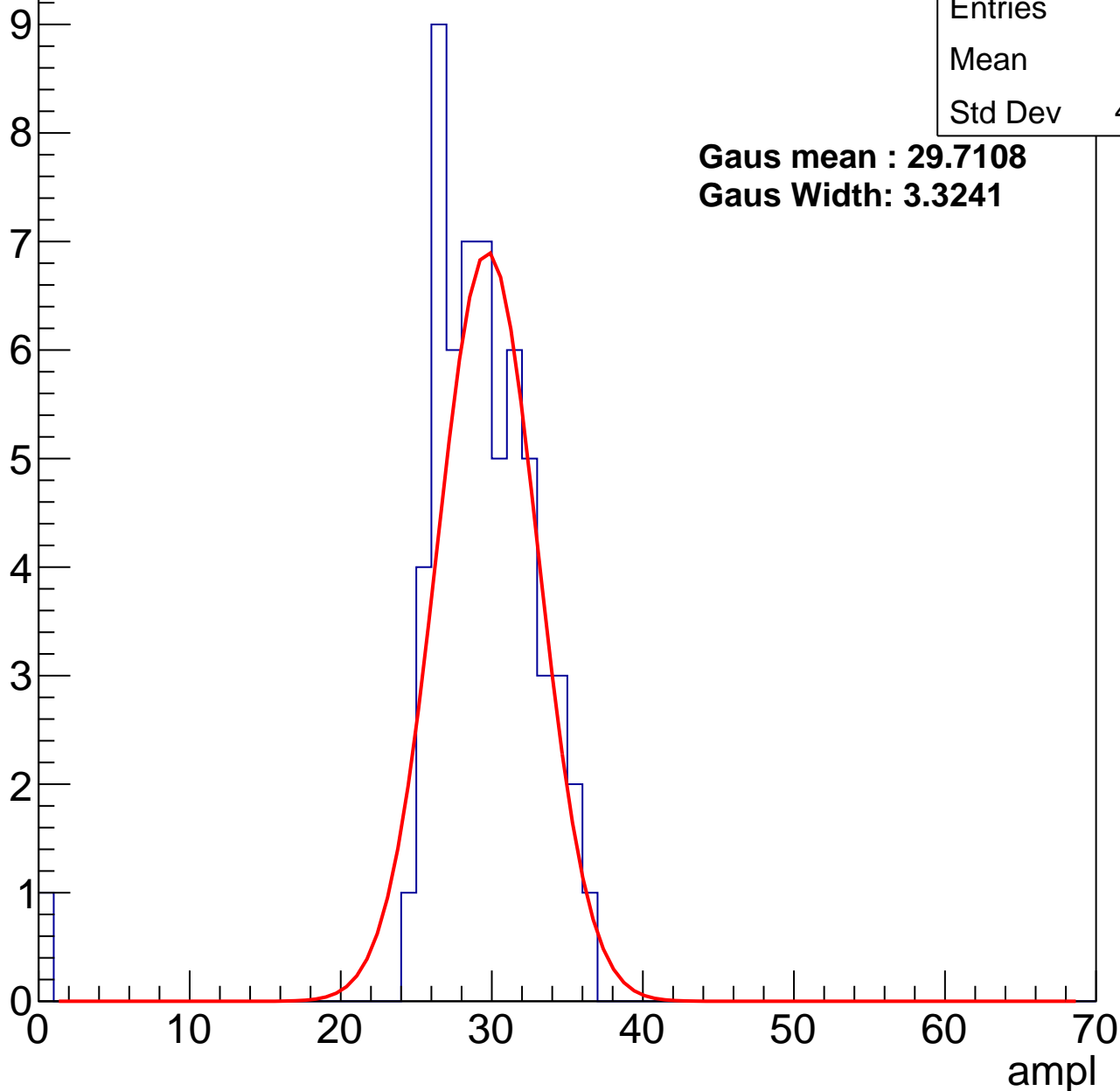
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28.7
Std Dev	4.751

Gaus mean : 29.7108

Gaus Width: 3.3241



B0L002S, U18-ch5, adc1

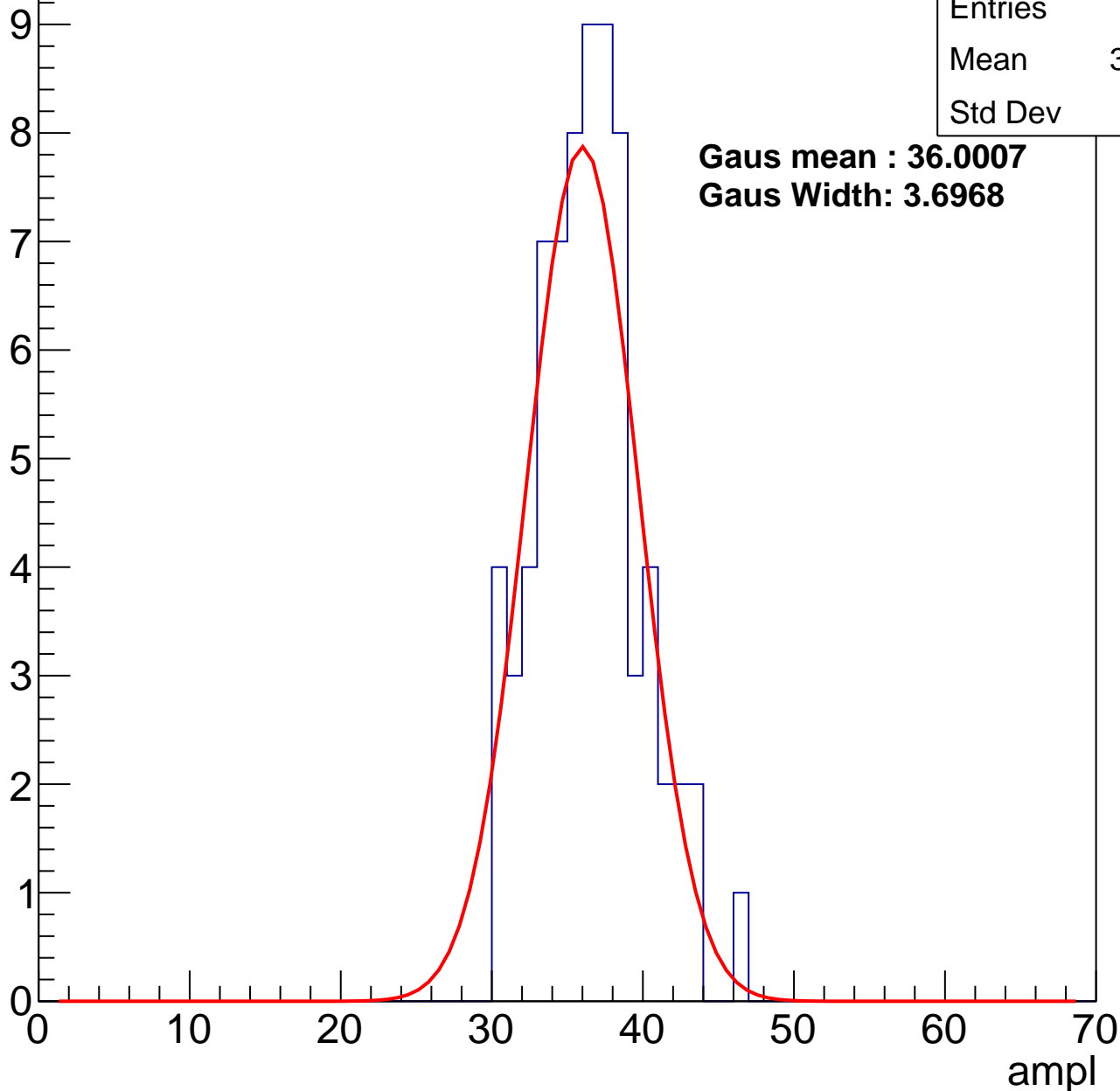
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	35.97
Std Dev	3.38

Gaus mean : 36.0007

Gaus Width: 3.6968



B0L002S, U18-ch5, adc2

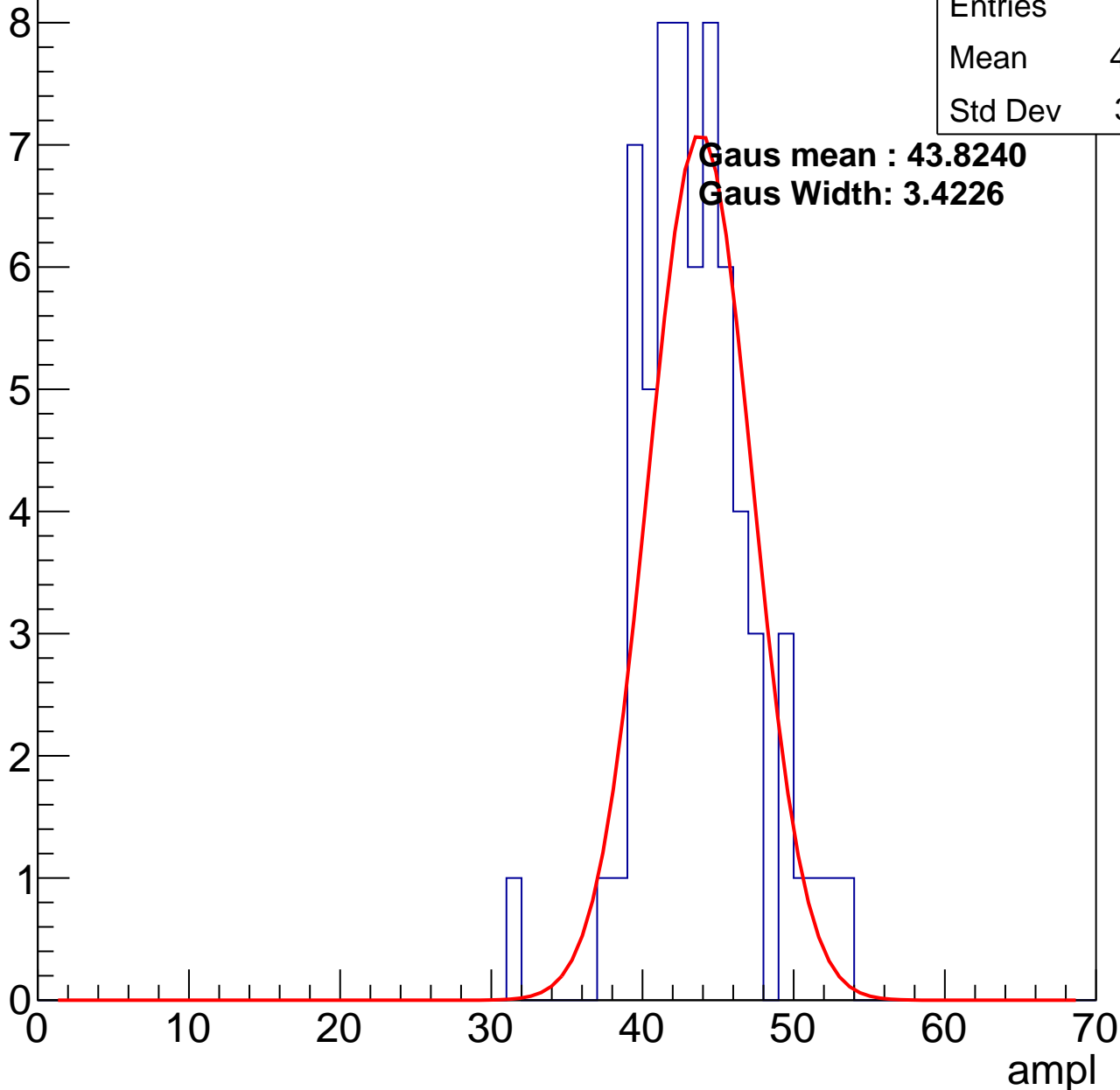
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	43.09
Std Dev	3.761

Gaus mean : 43.8240

Gaus Width: 3.4226

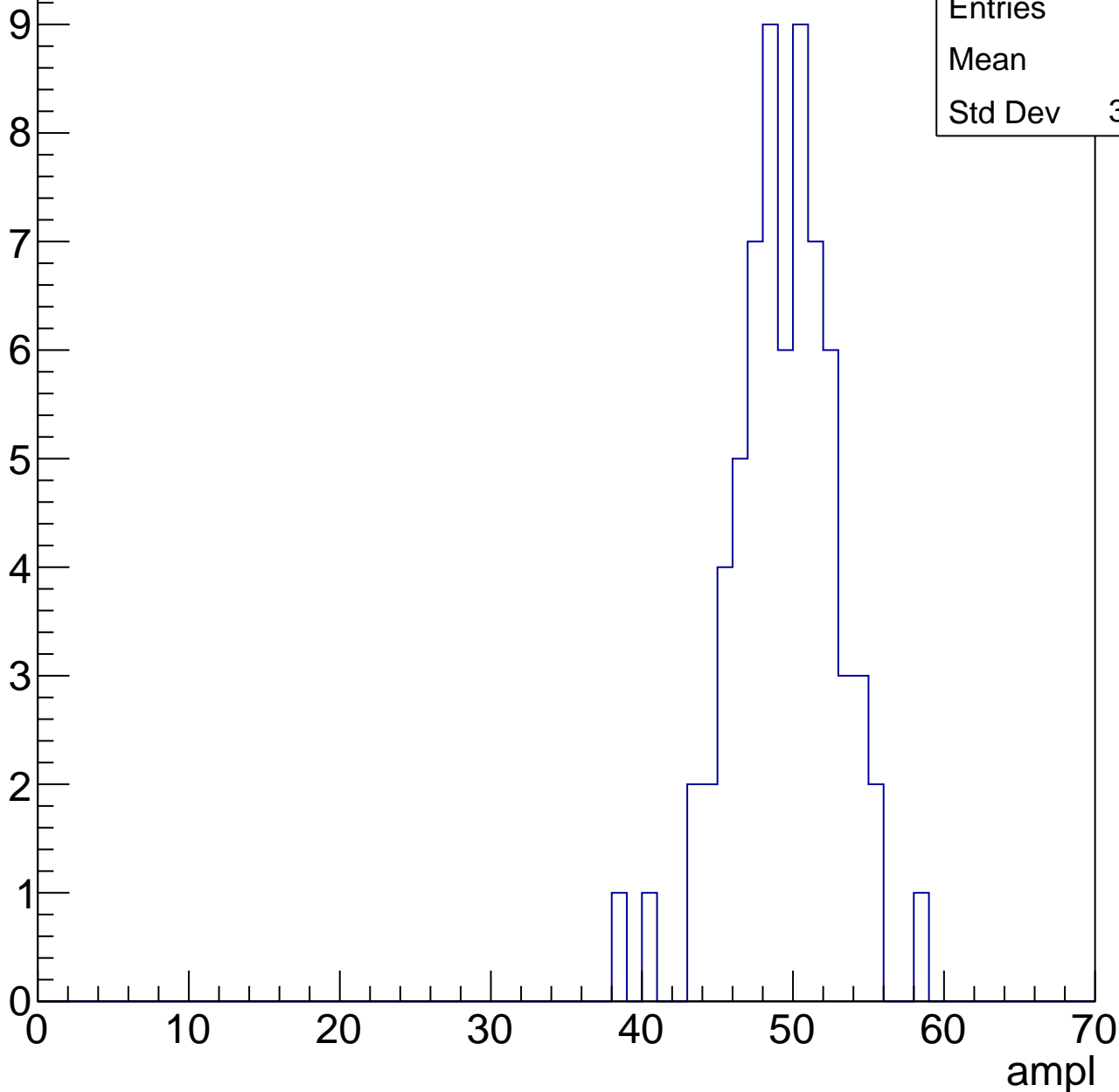


B0L002S, U18-ch5, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

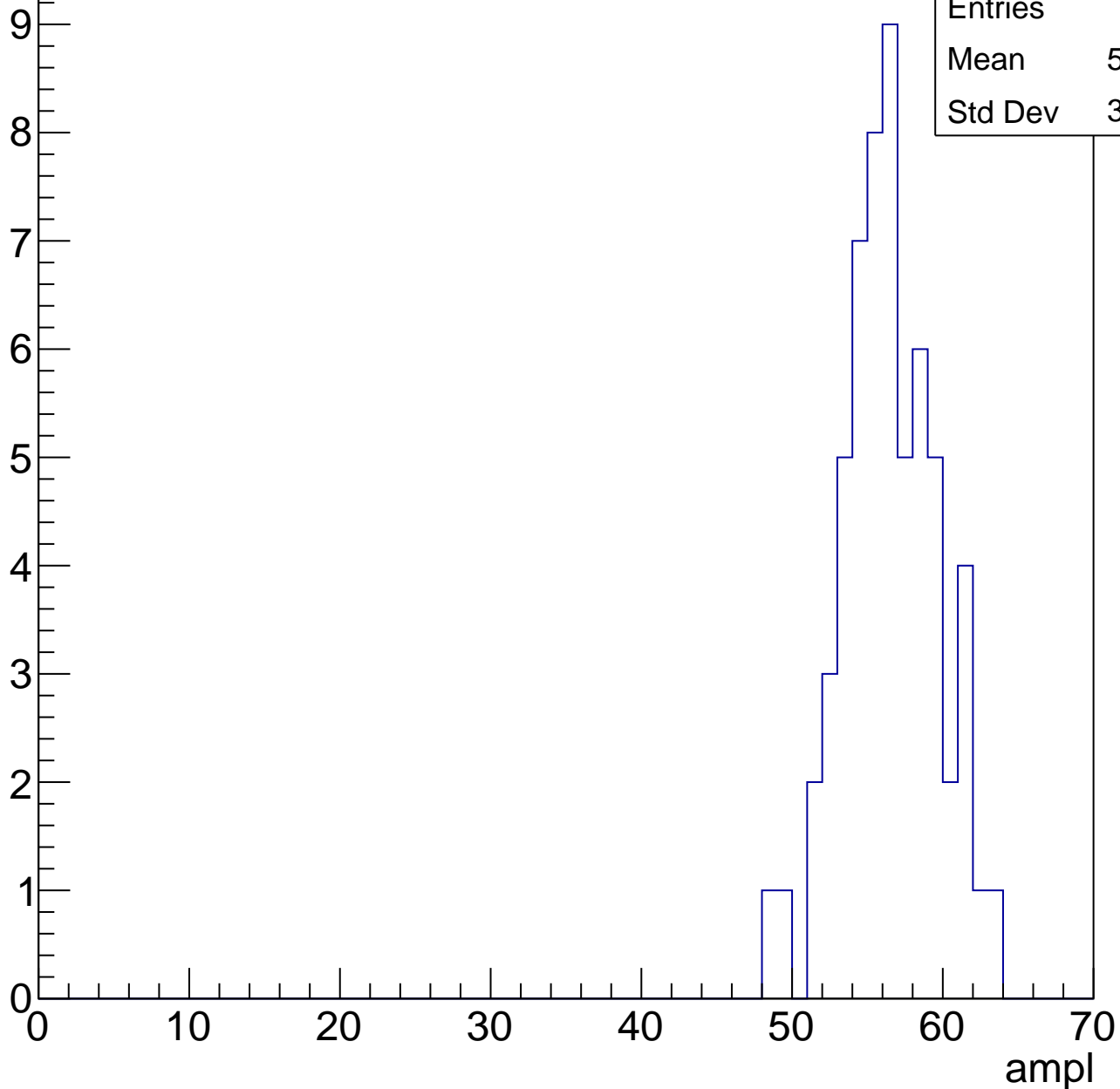
Entries	68
Mean	48.9
Std Dev	3.507



B0L002S, U18-ch5, adc4

calib_packv5_042523_0143.root, FC#8, port C1

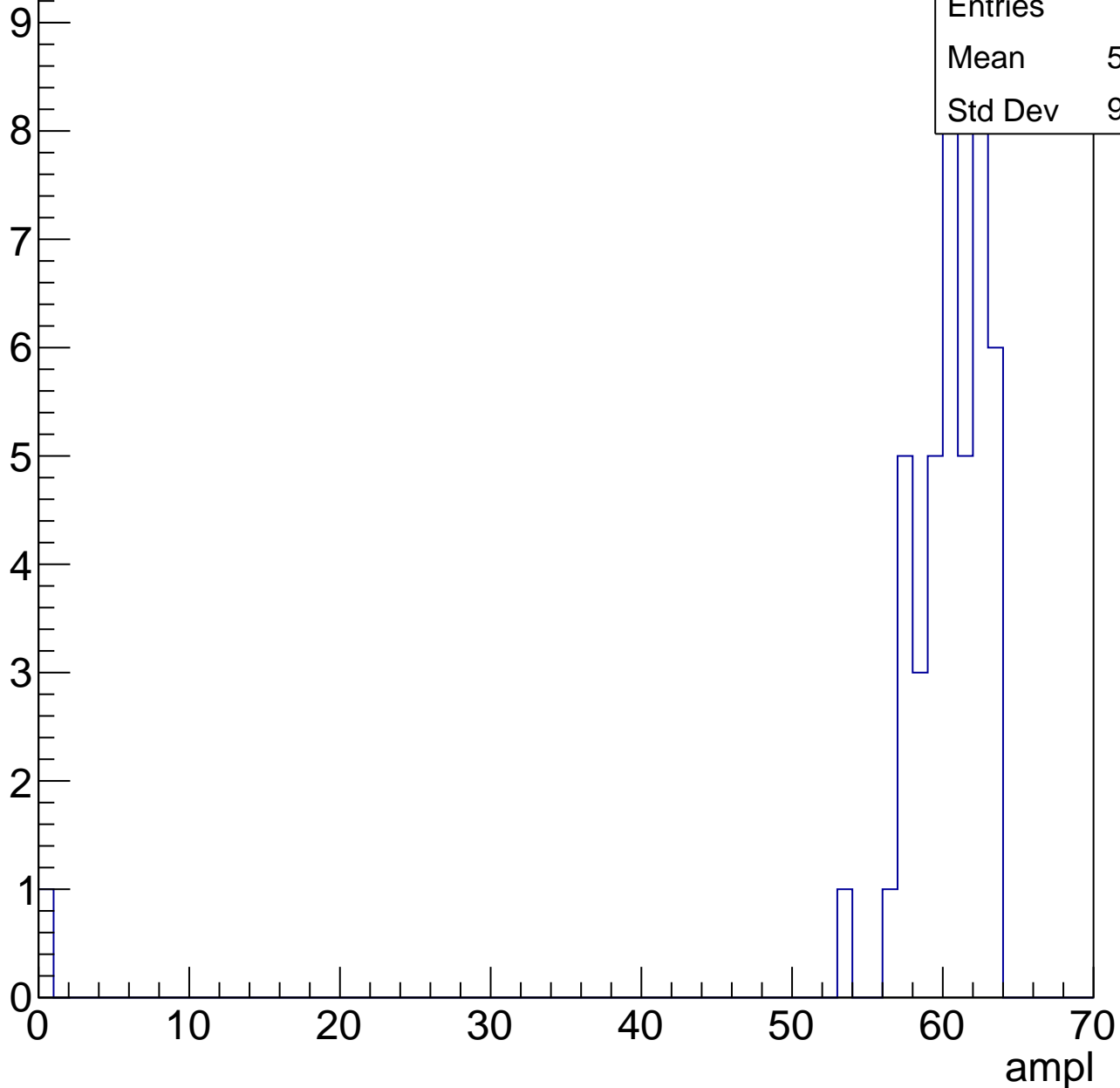
Entry



B0L002S, U18-ch5, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch5, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch5, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch6, adc0

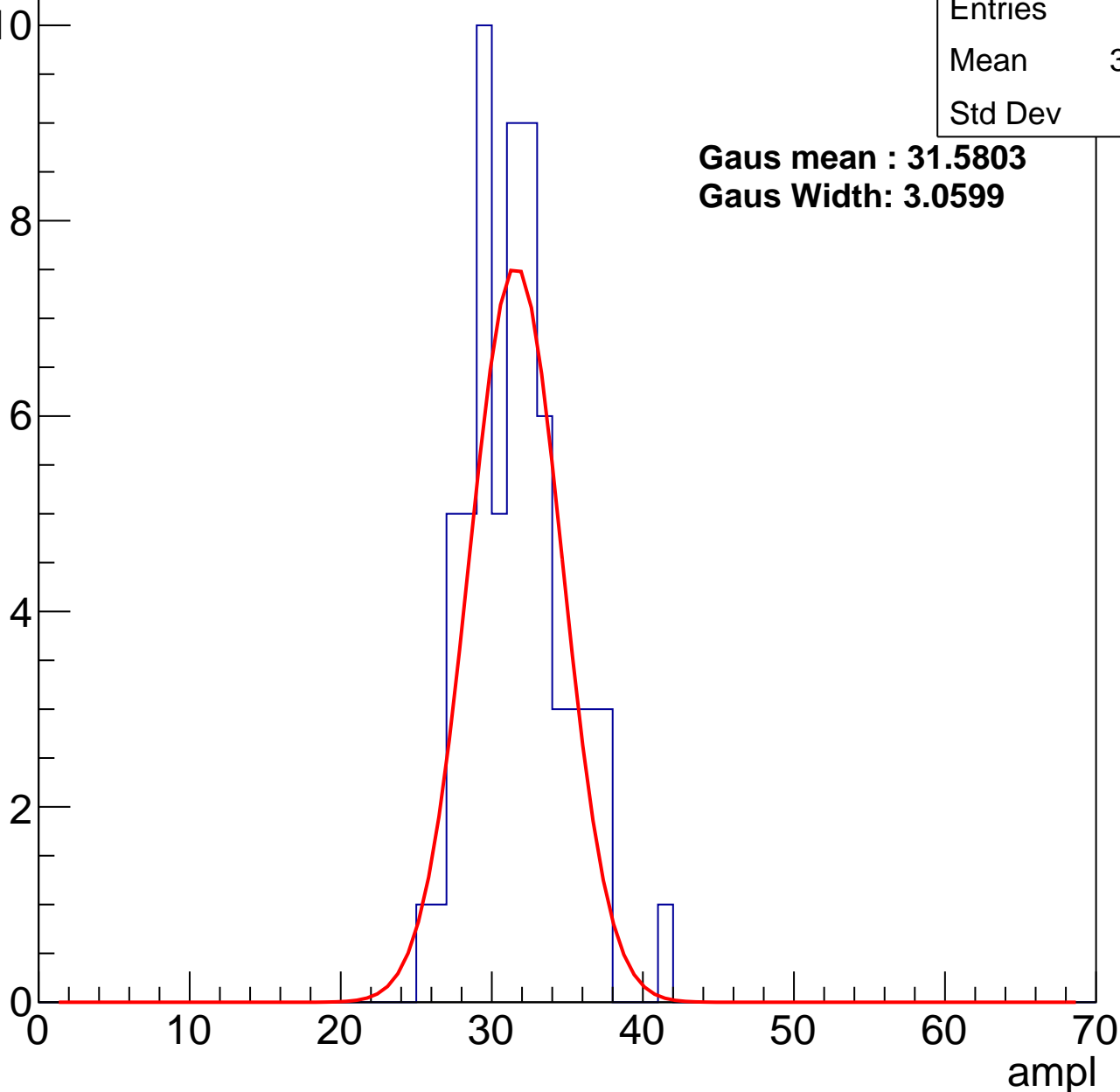
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	31.22
Std Dev	3.11

Gaus mean : 31.5803

Gaus Width: 3.0599



B0L002S, U18-ch6, adc1

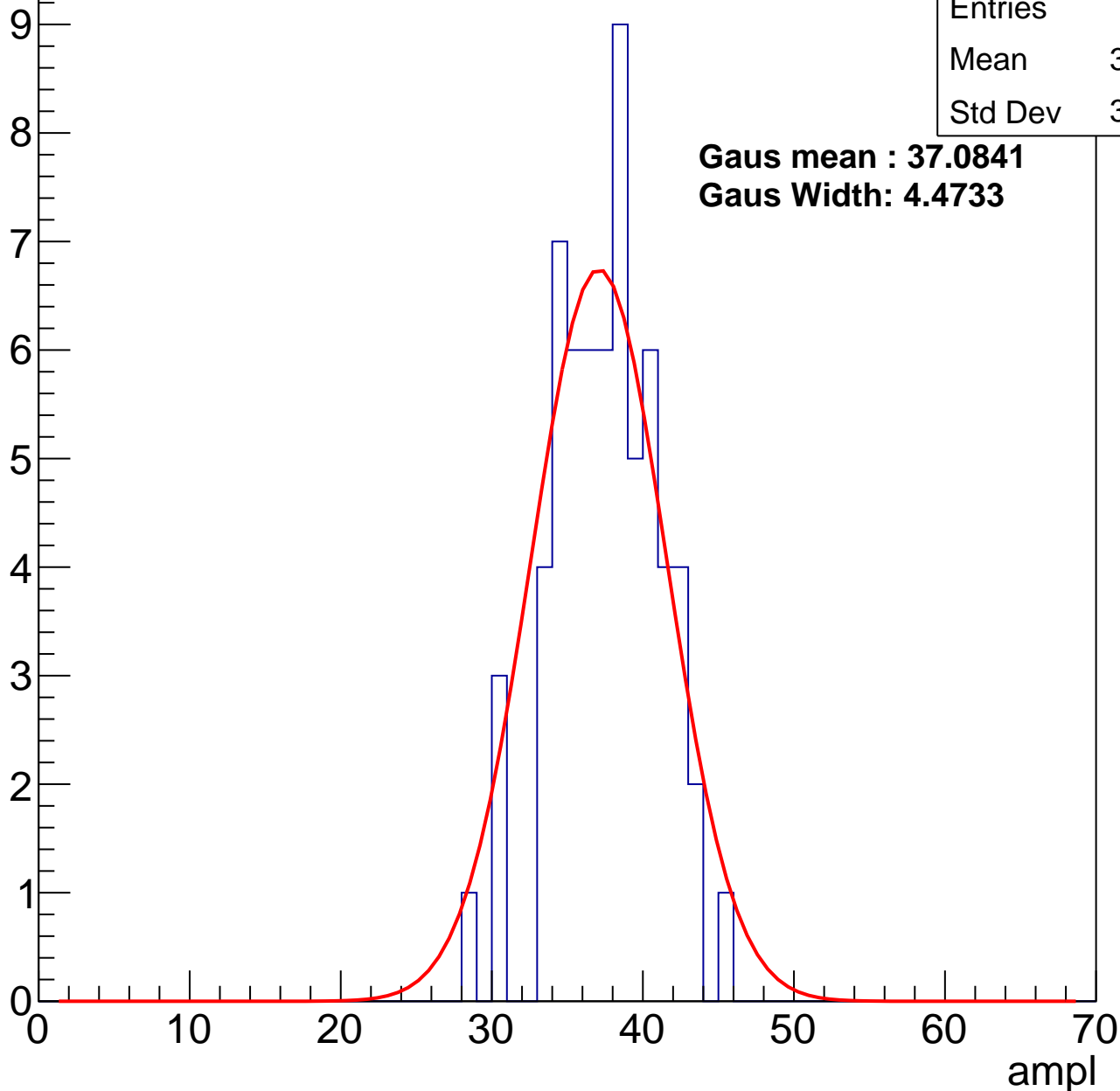
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	37.12
Std Dev	3.462

Gaus mean : 37.0841

Gaus Width: 4.4733



B0L002S, U18-ch6, adc2

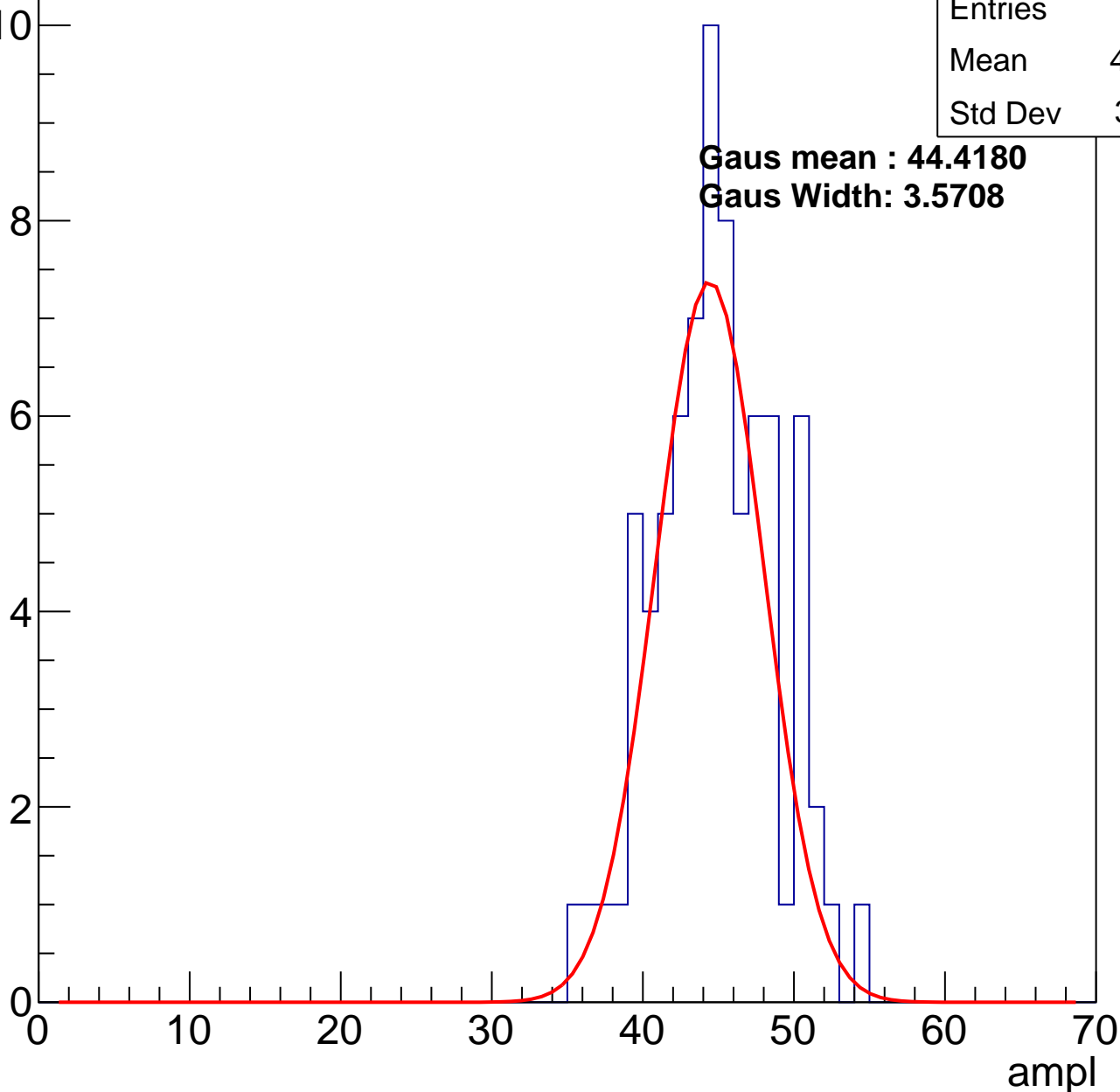
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	44.36
Std Dev	3.891

Gaus mean : 44.4180

Gaus Width: 3.5708

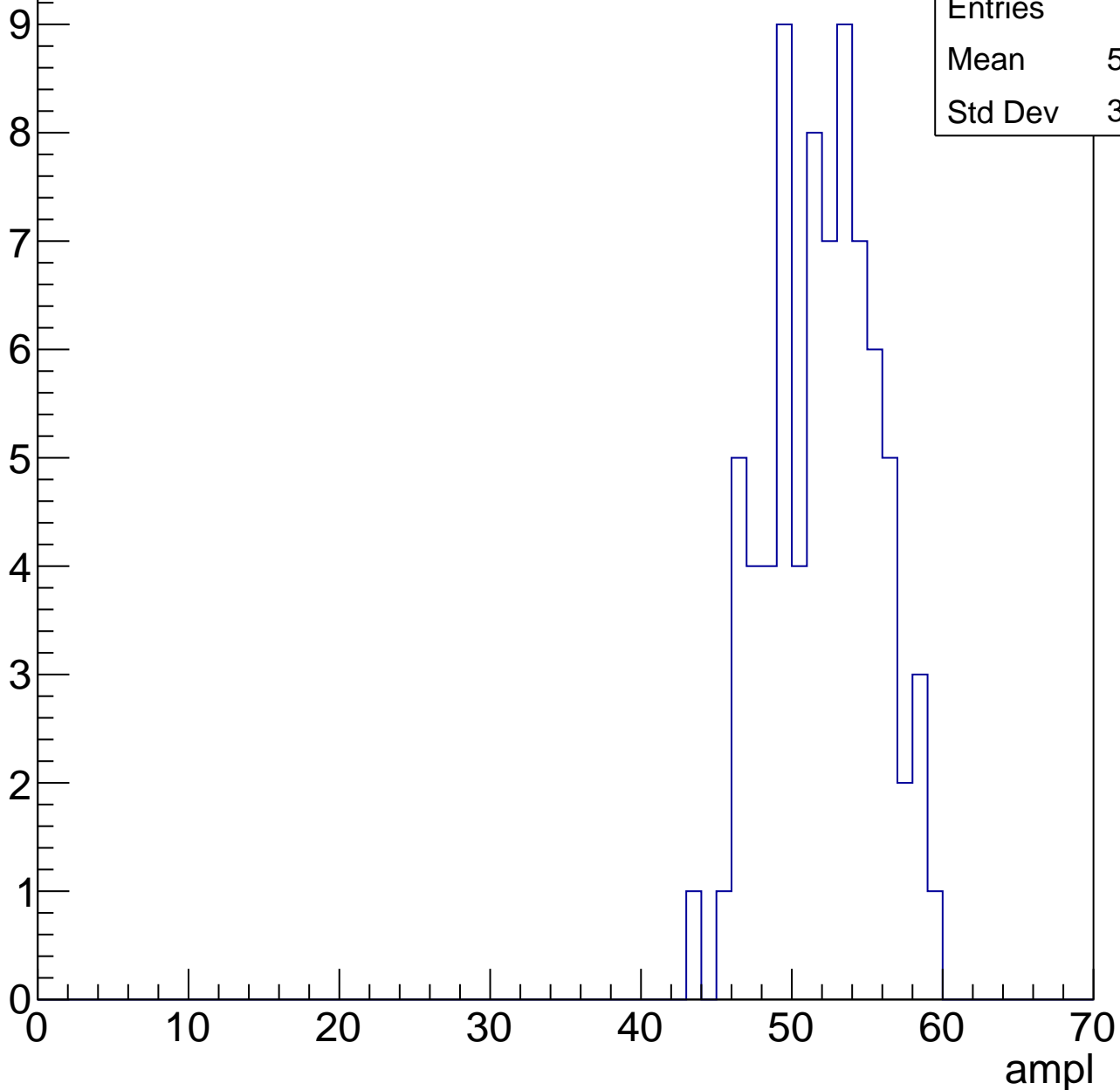


B0L002S, U18-ch6, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	51.62
Std Dev	3.528

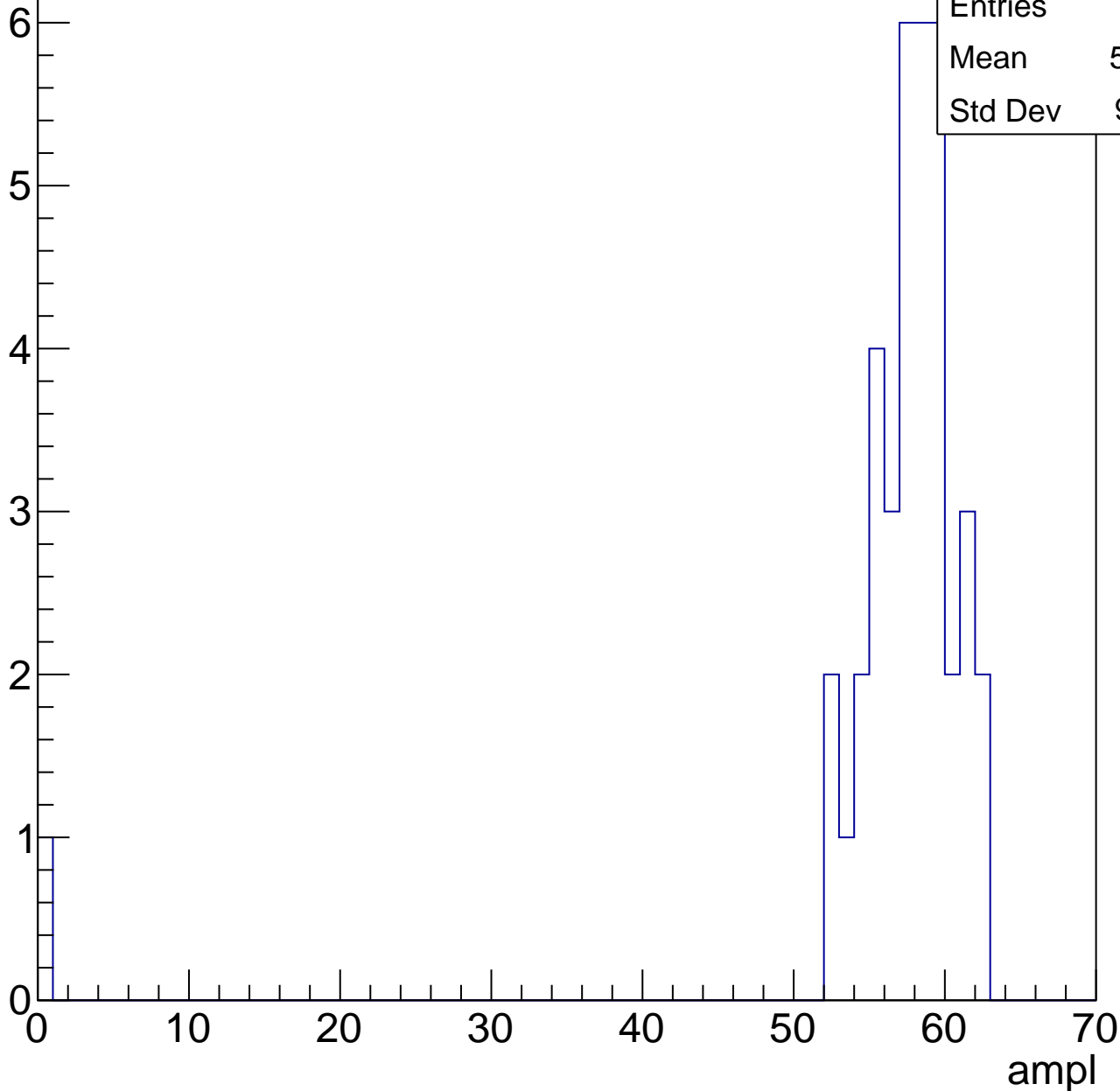


B0L002S, U18-ch6, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

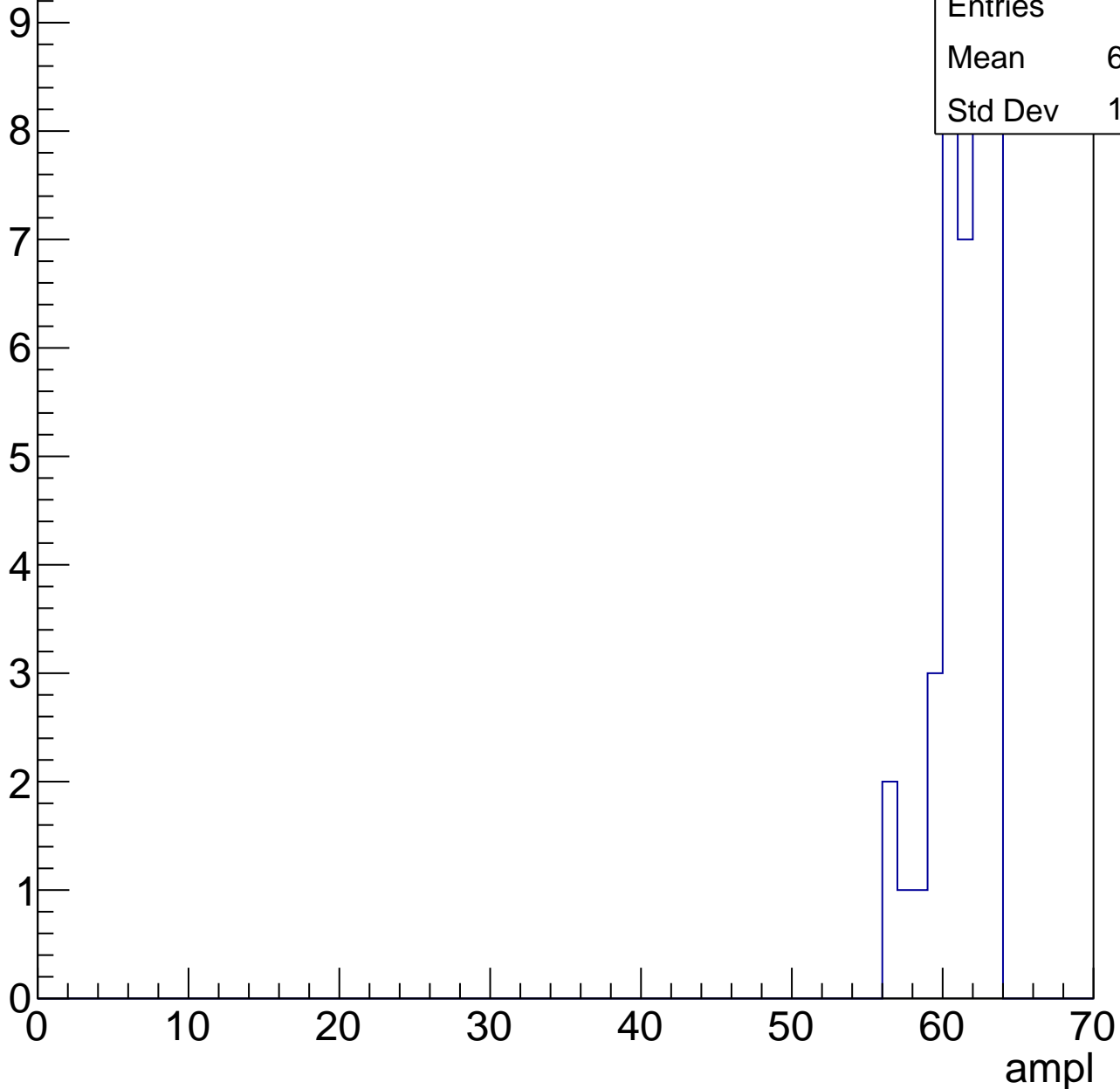
Entries	38
Mean	55.89
Std Dev	9.531



B0L002S, U18-ch6, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch6, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	60
Std Dev	3

ampl

B0L002S, U18-ch6, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch7, adc0

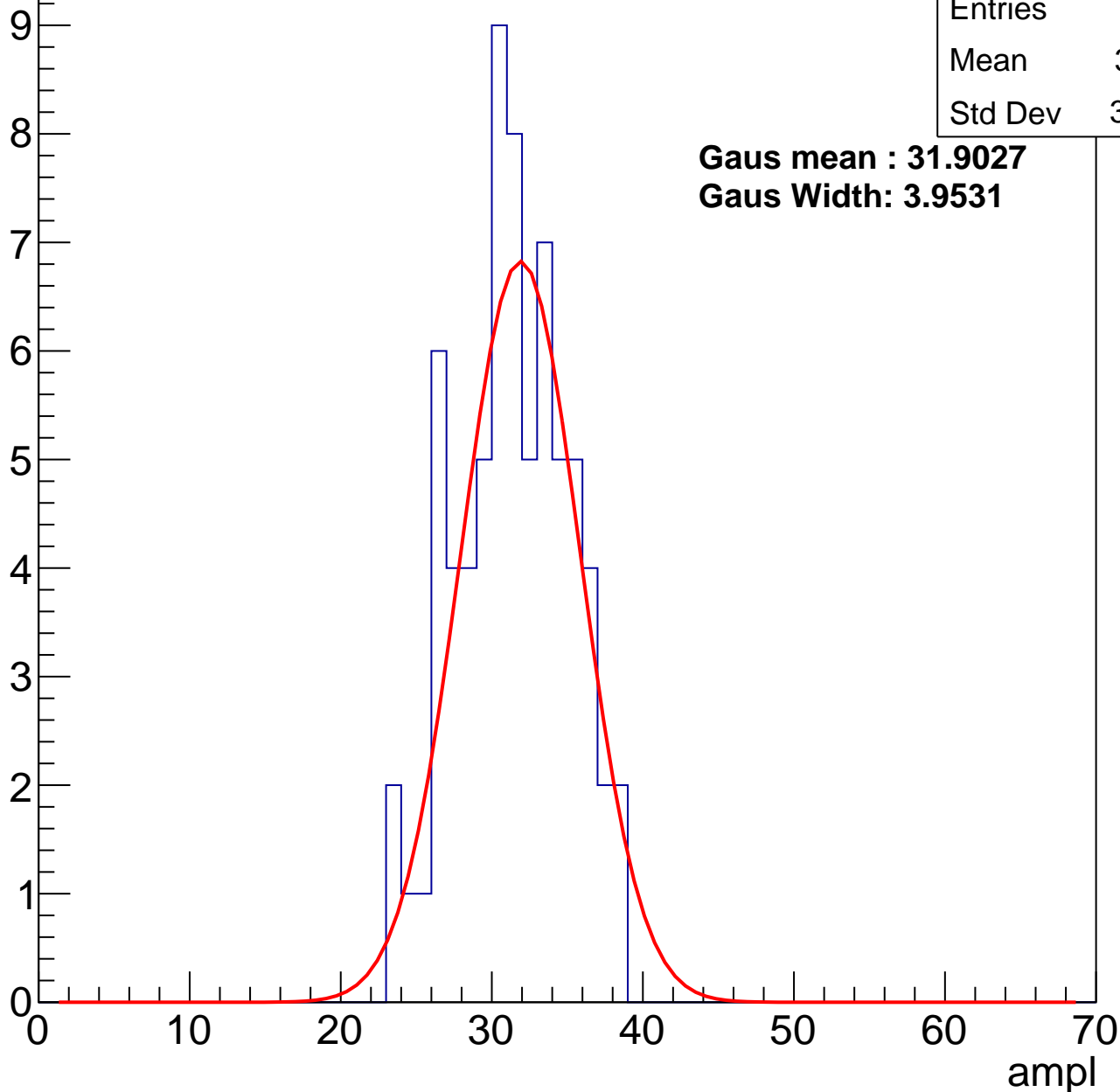
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	30.91
Std Dev	3.624

Gaus mean : 31.9027

Gaus Width: 3.9531



B0L002S, U18-ch7, adc1

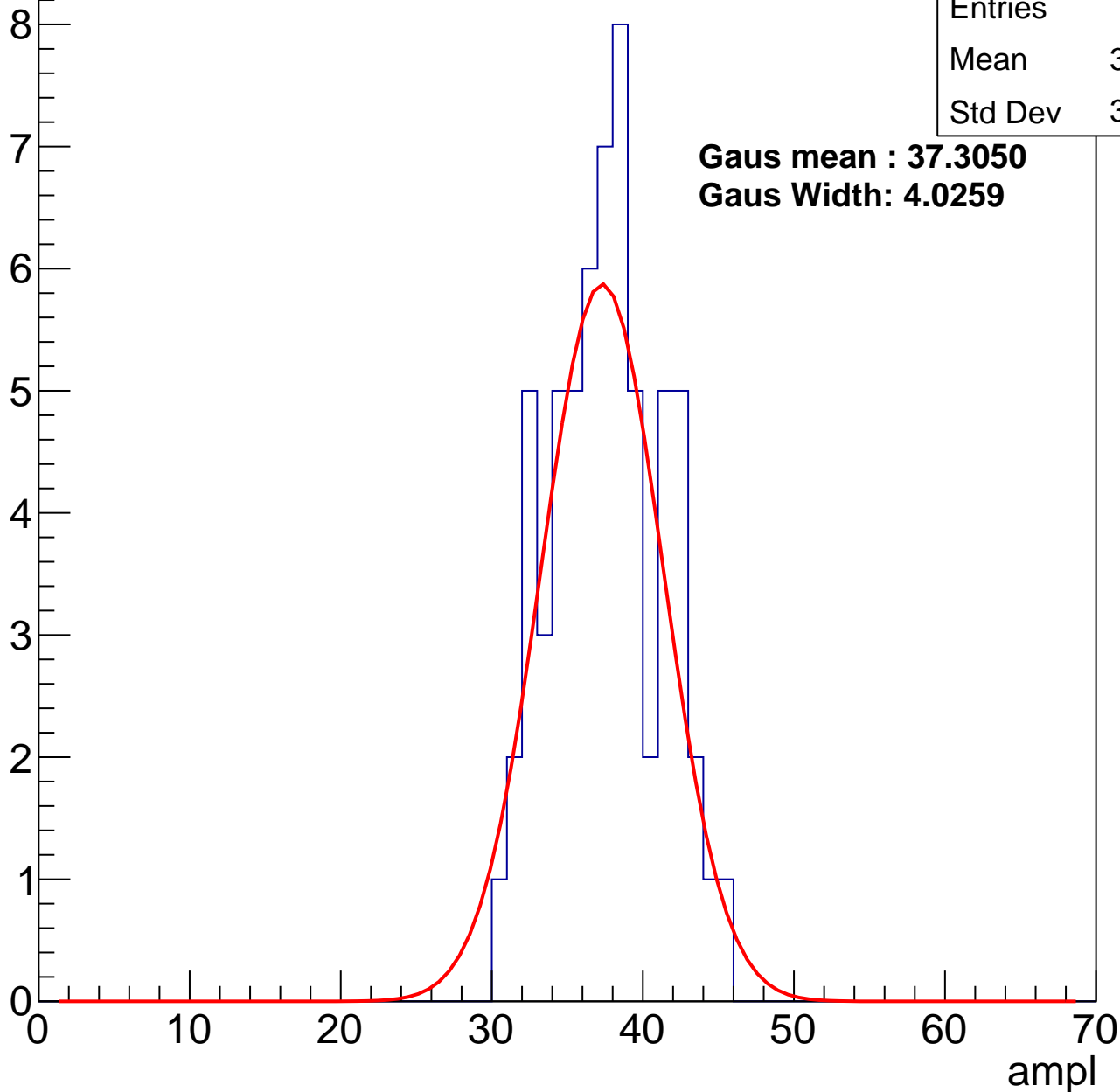
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.14
Std Dev	3.563

Gaus mean : 37.3050

Gaus Width: 4.0259



B0L002S, U18-ch7, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	44.15
Std Dev	3.506

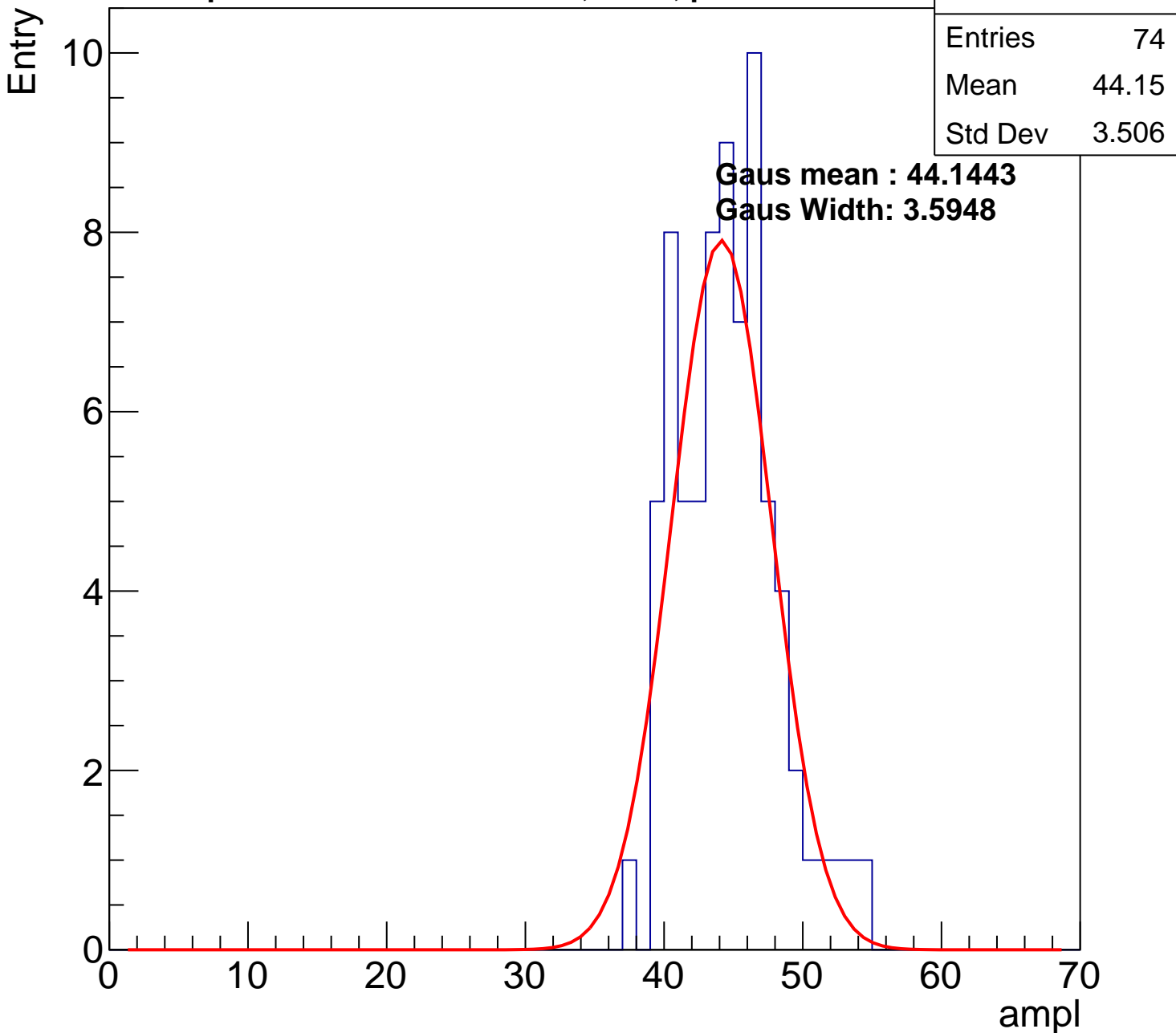
Gaus mean : 44.1443

Gaus Width: 3.5948

10
8
6
4
2
0

ampl

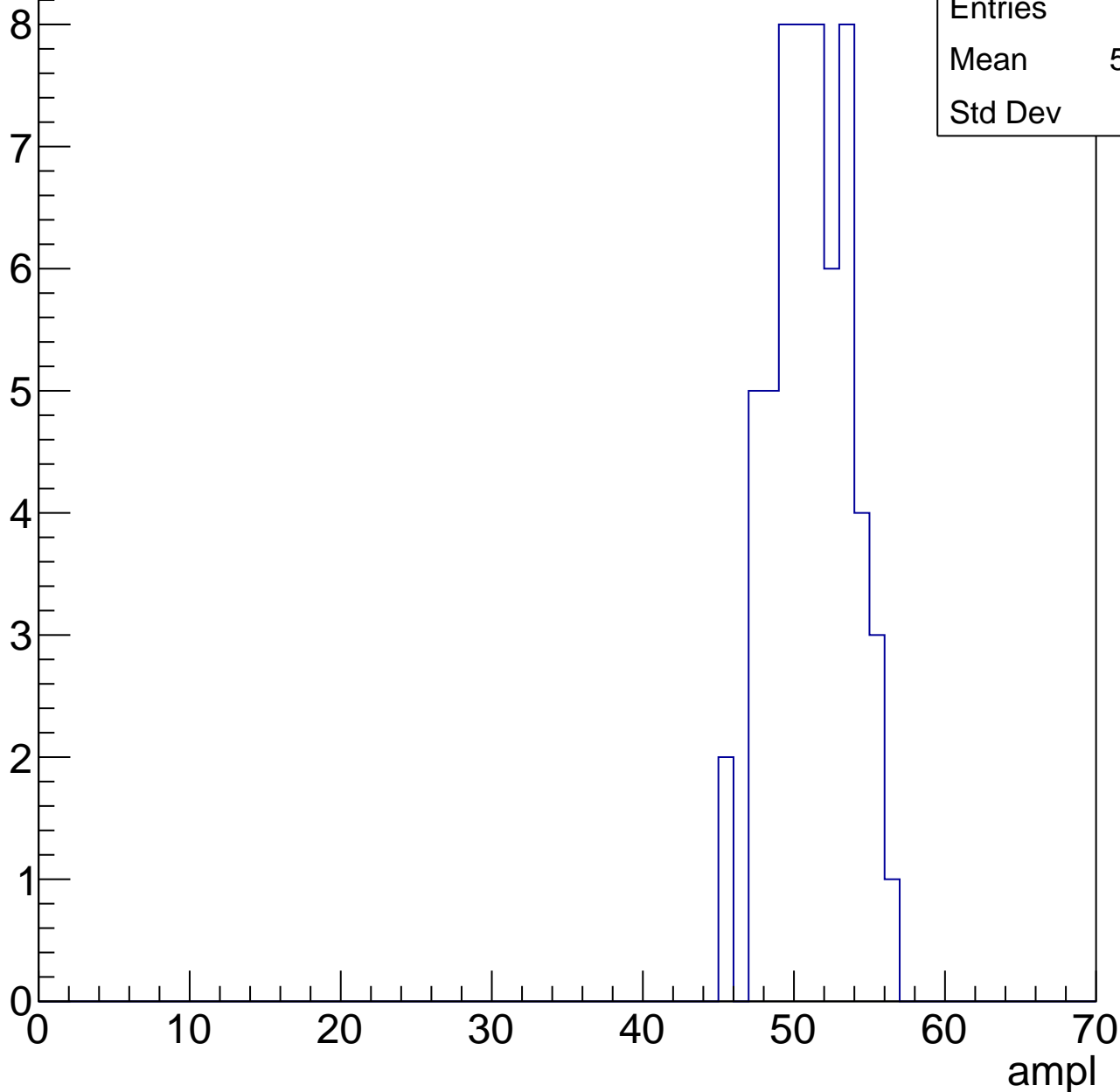
0 10 20 30 40 50 60 70



B0L002S, U18-ch7, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

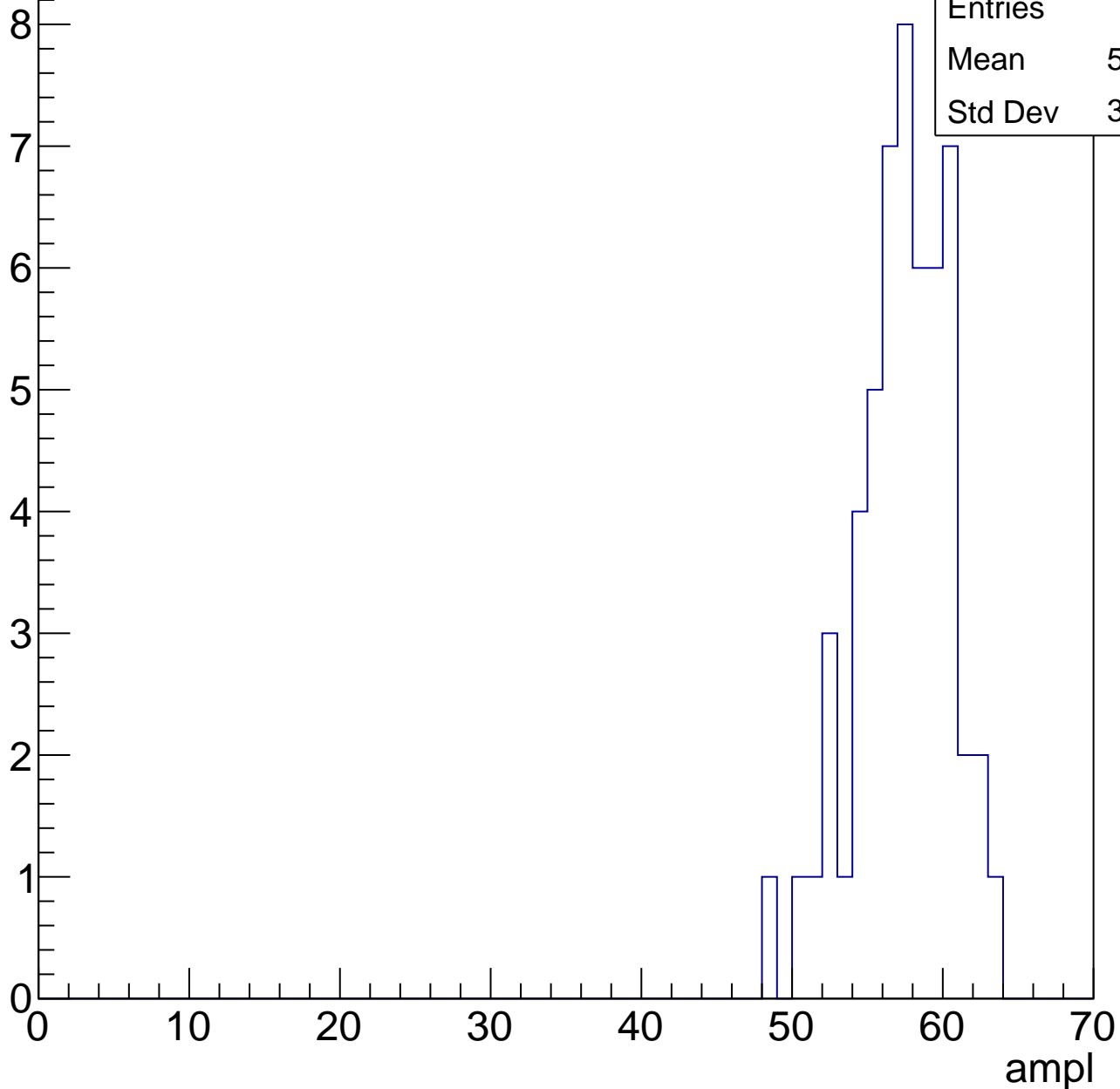


Entries	58
Mean	50.66
Std Dev	2.55

B0L002S, U18-ch7, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

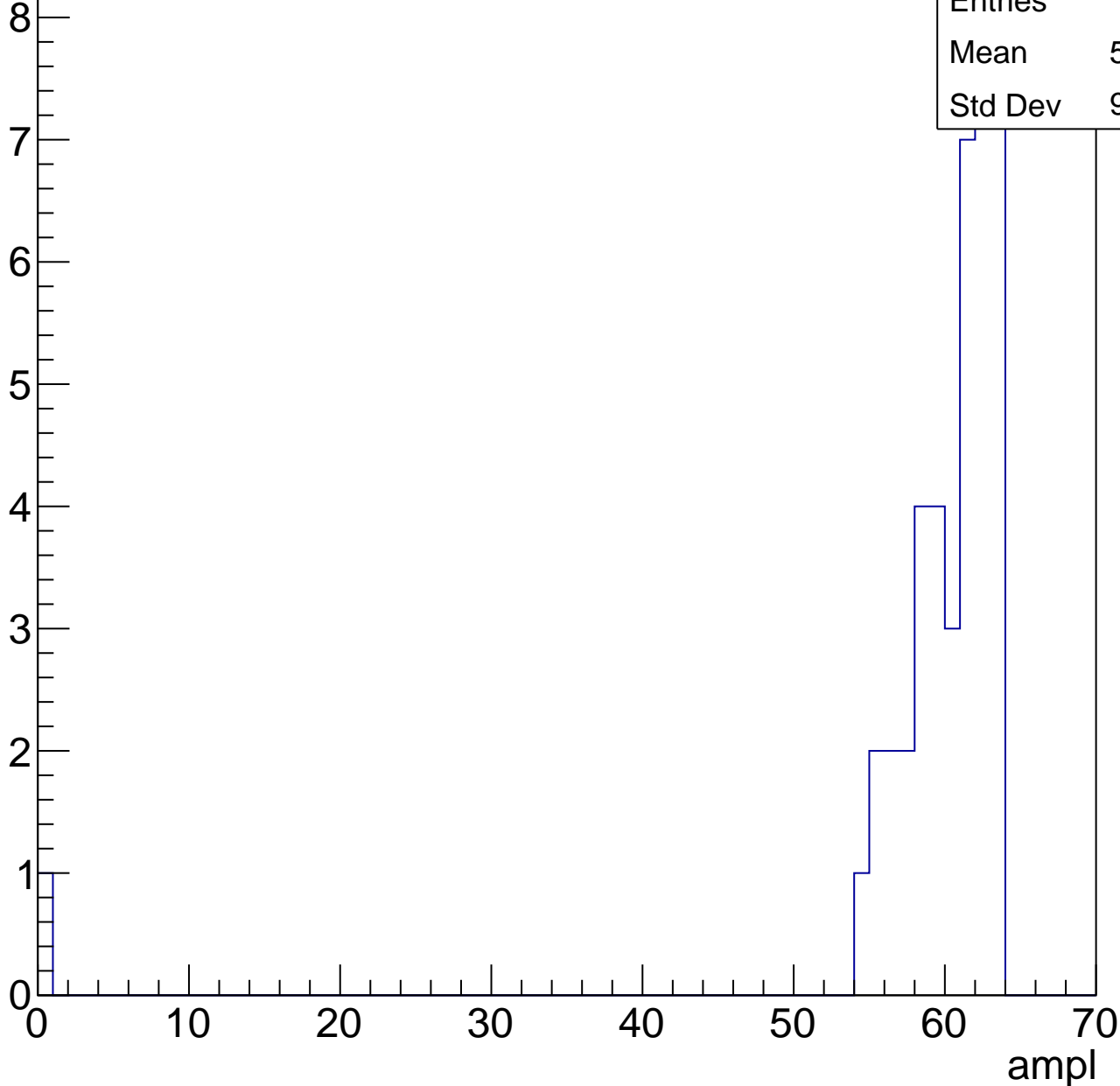


B0L002S, U18-ch7, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	58.69
Std Dev	9.506



B0L002S, U18-ch7, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch7, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U18-ch8, adc0

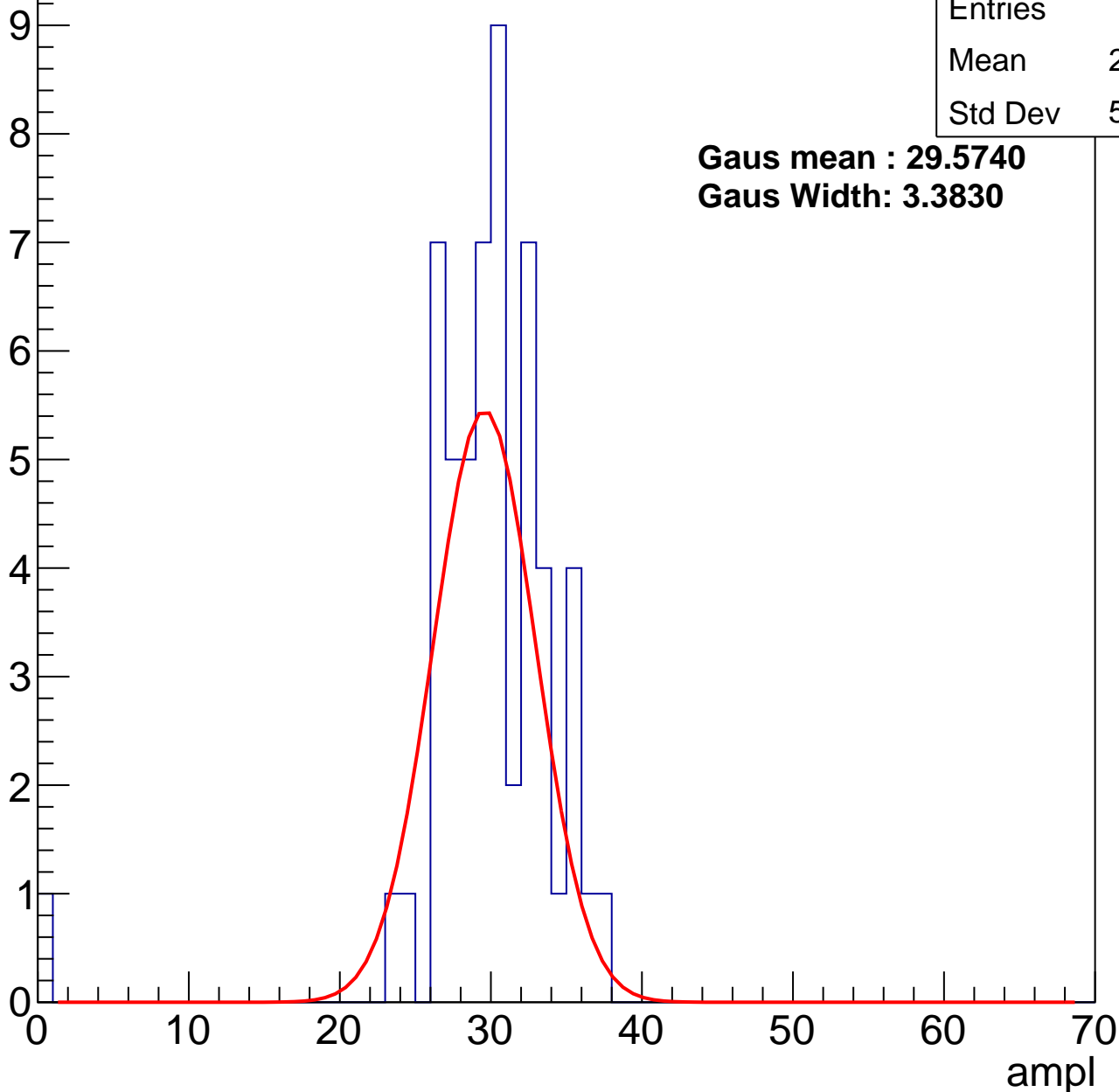
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	29.32
Std Dev	5.018

Gaus mean : 29.5740

Gaus Width: 3.3830



B0L002S, U18-ch8, adc1

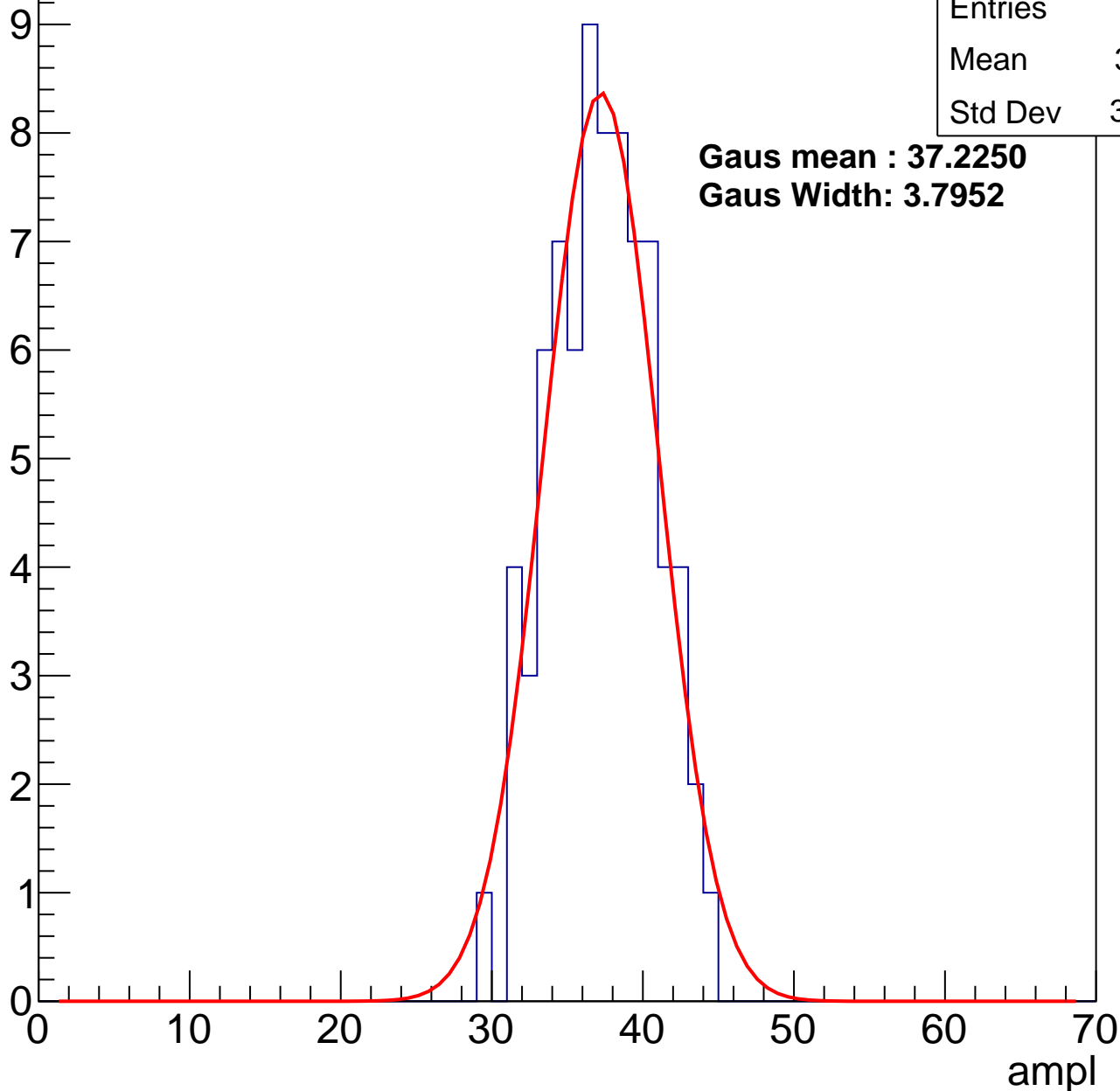
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.81
Std Dev	3.334

Gaus mean : 37.2250

Gaus Width: 3.7952



B0L002S, U18-ch8, adc2

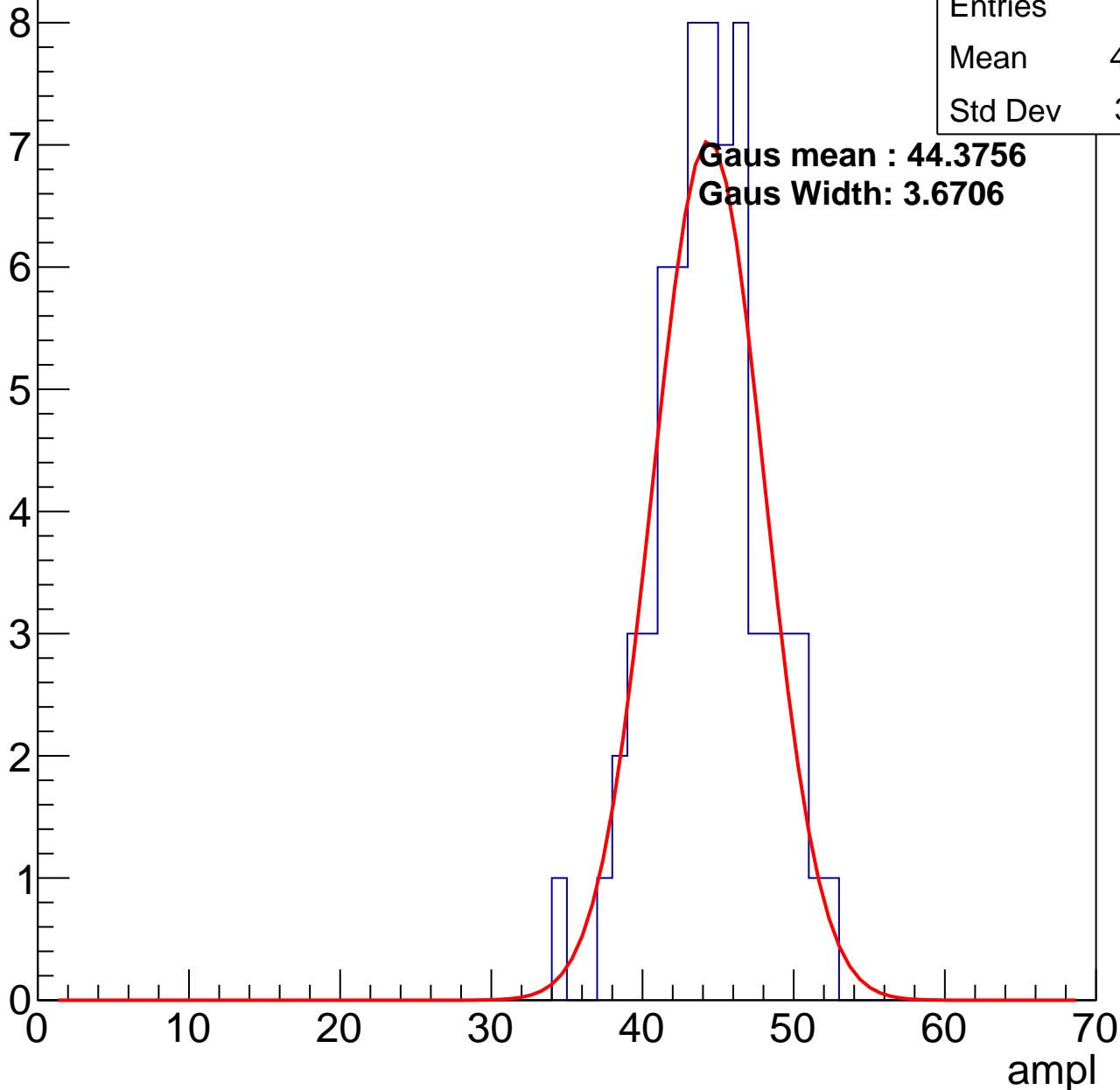
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.97
Std Dev	3.541

Gaus mean : 44.3756

Gaus Width: 3.6706

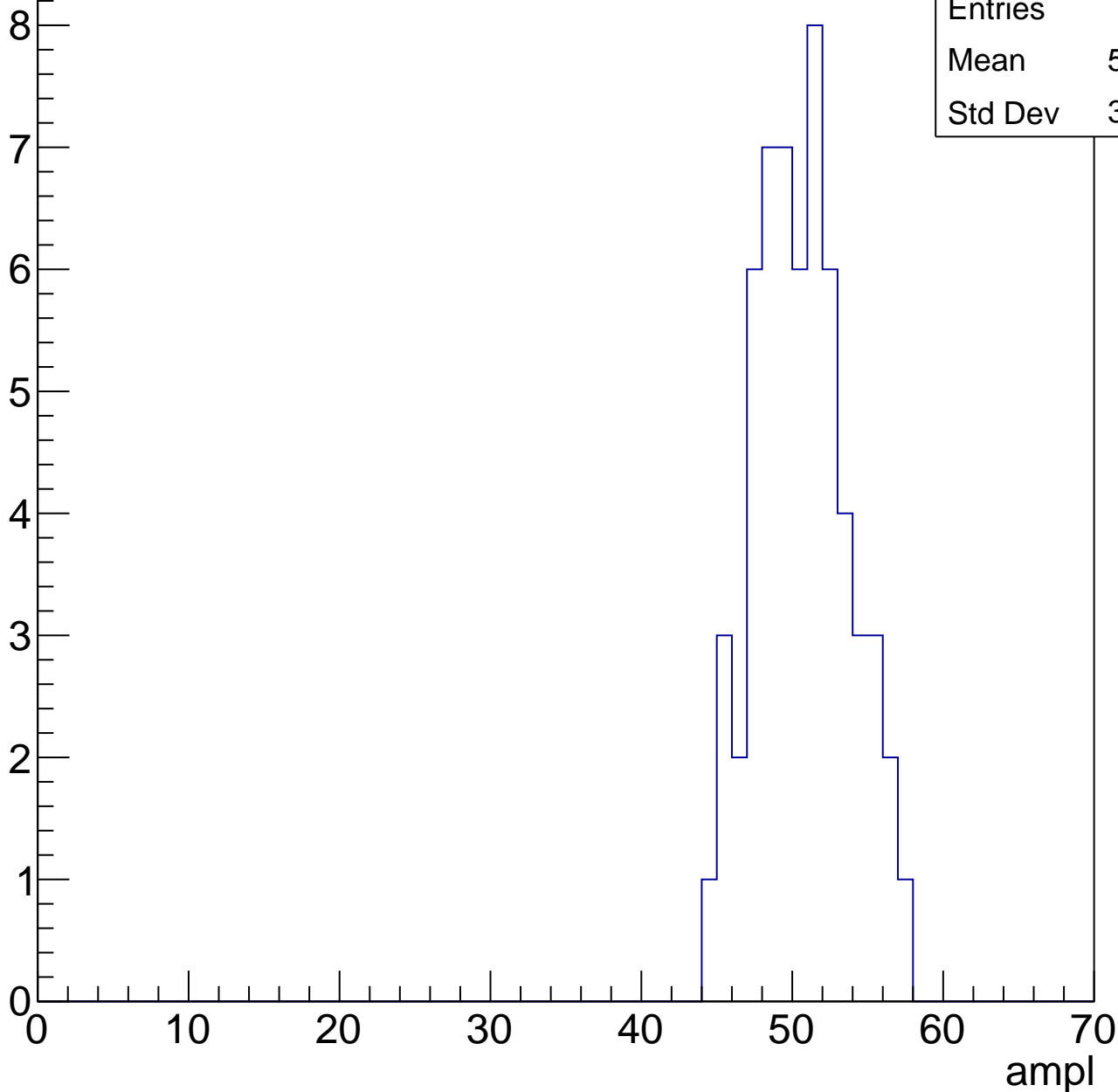


B0L002S, U18-ch8, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.17
Std Dev	3.032

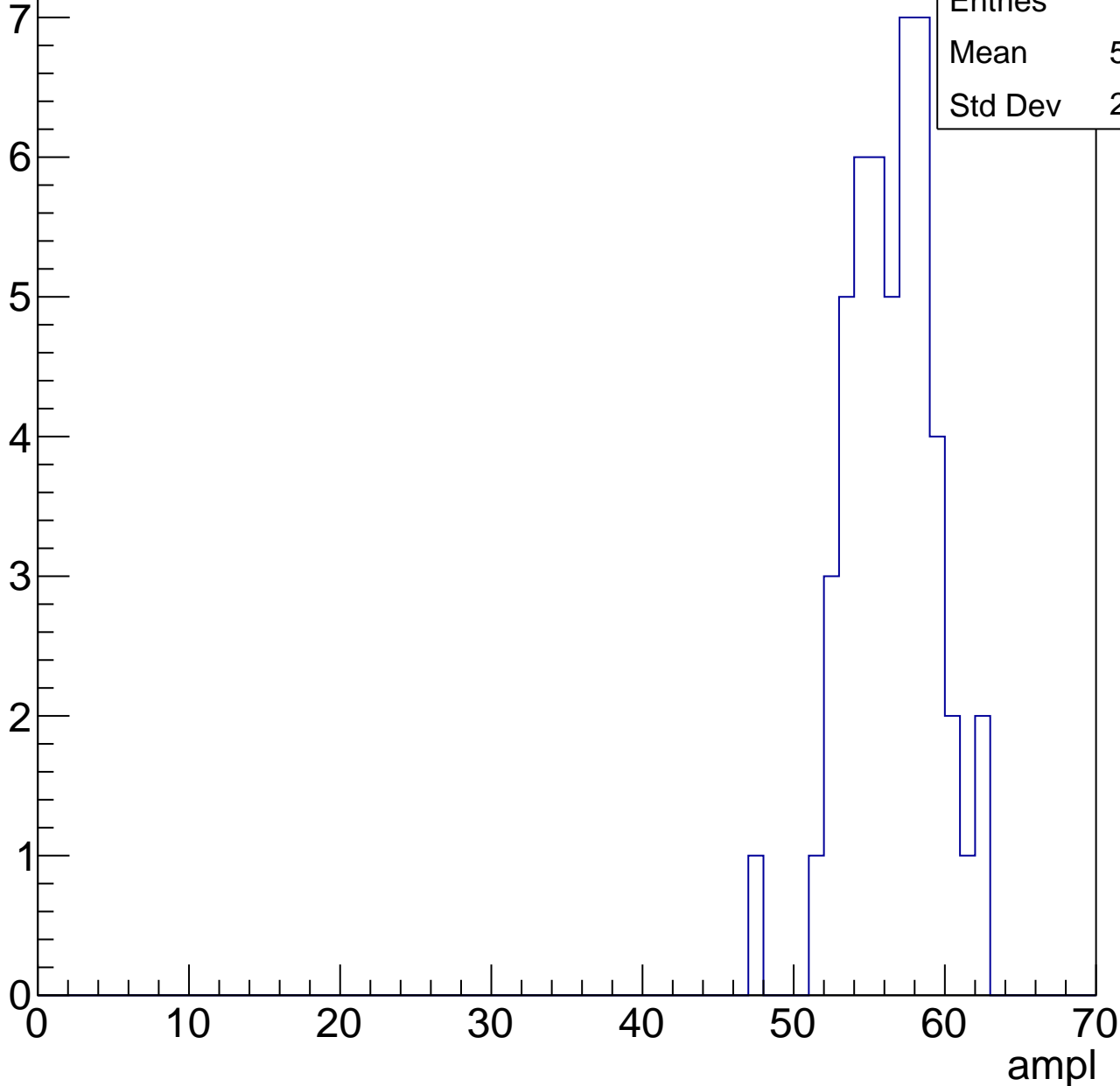


B0L002S, U18-ch8, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

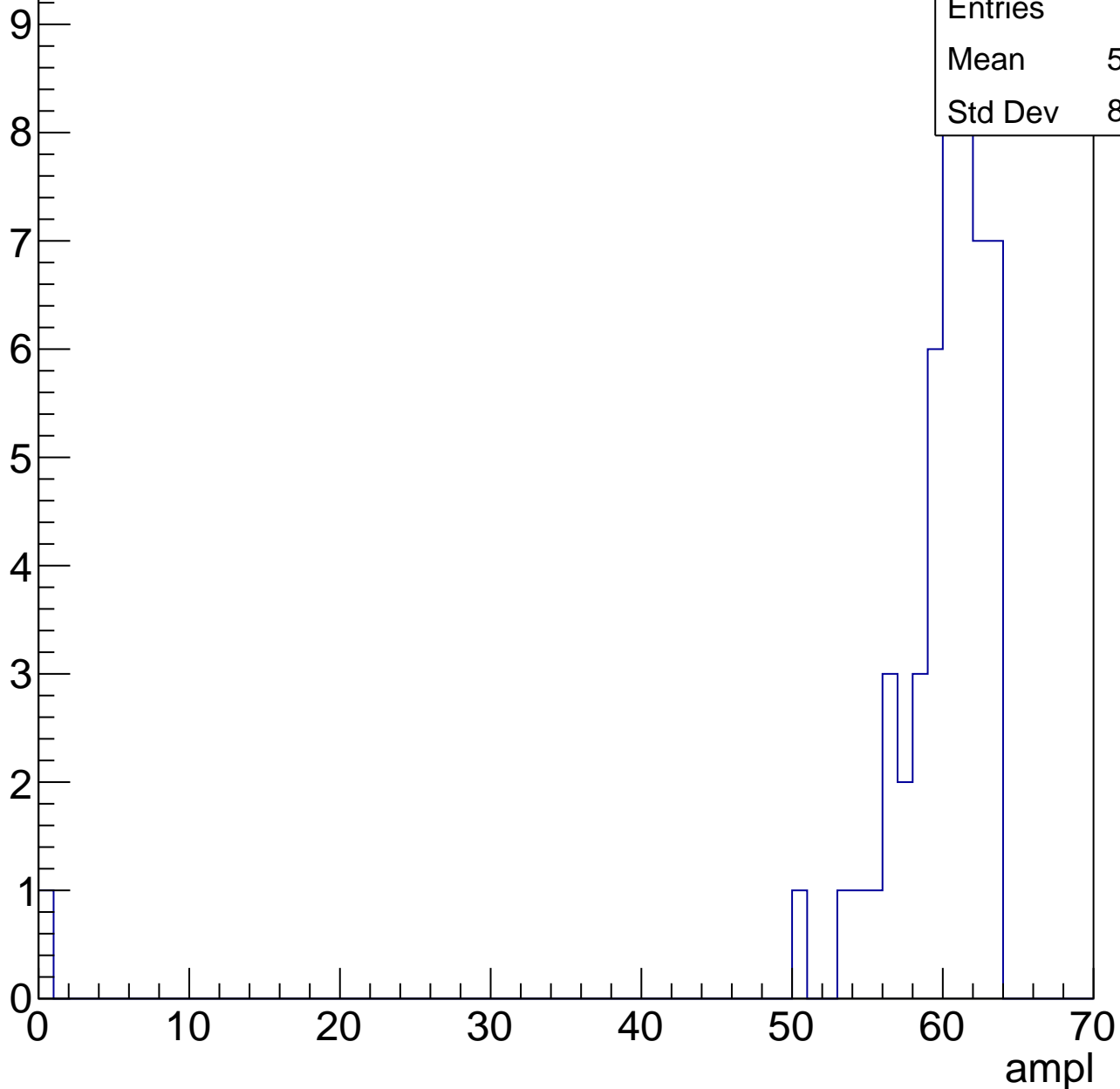
Entries	50
Mean	55.98
Std Dev	2.929



B0L002S, U18-ch8, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch8, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch8, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch9, adc0

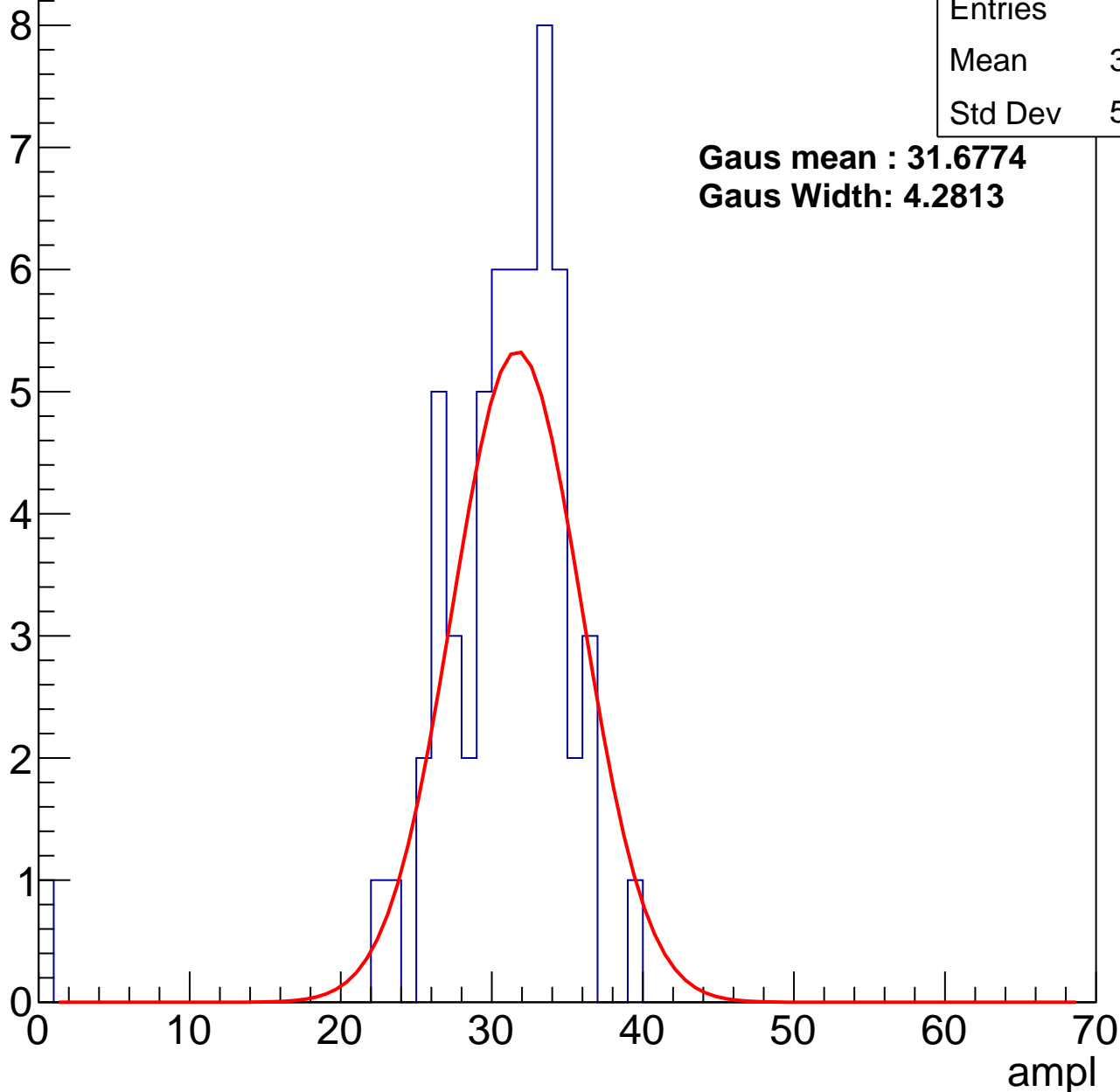
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.17
Std Dev	5.282

Gaus mean : 31.6774

Gaus Width: 4.2813



B0L002S, U18-ch9, adc1

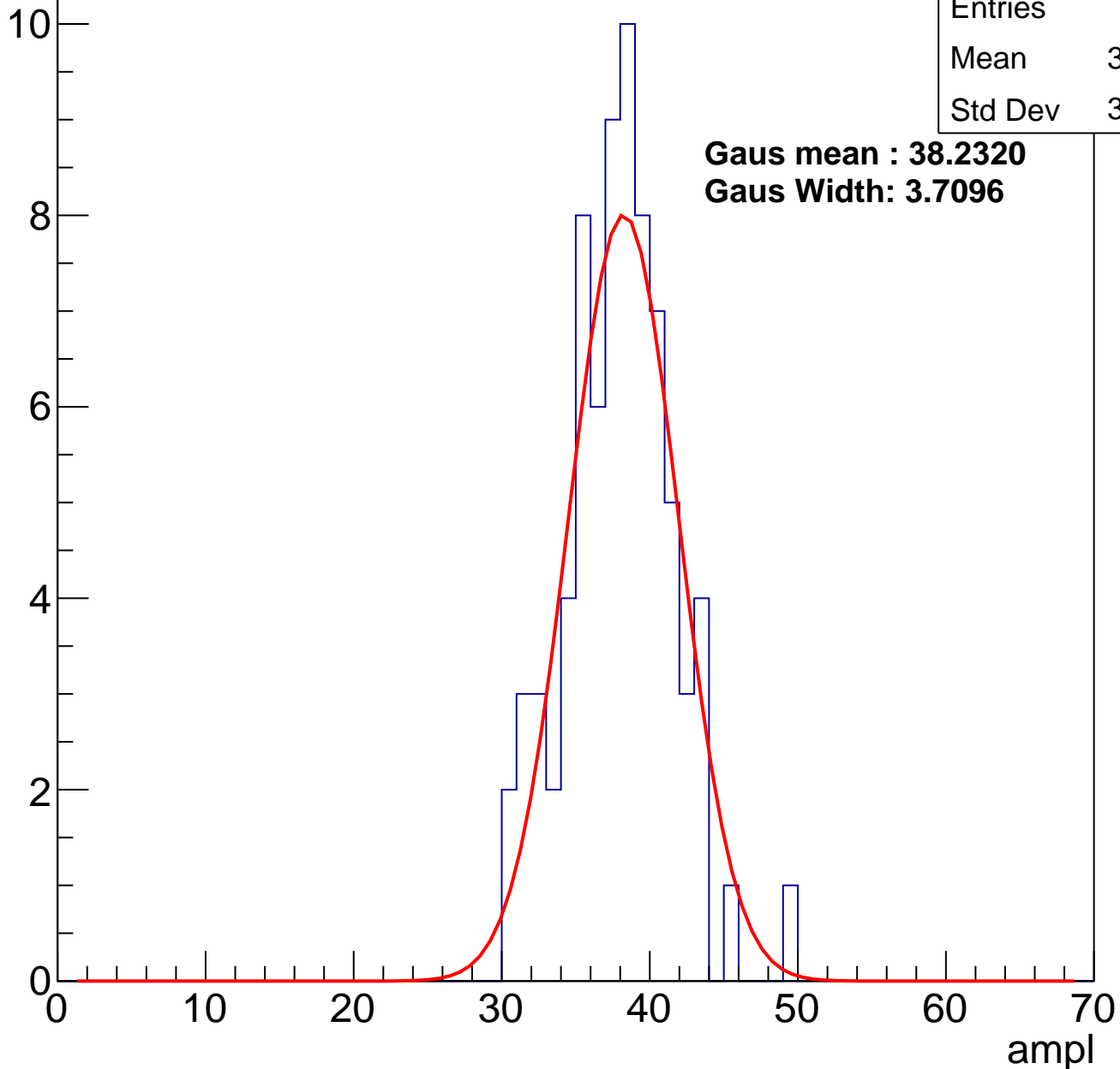
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	37.49
Std Dev	3.593

Gaus mean : 38.2320

Gaus Width: 3.7096

Entry



B0L002S, U18-ch9, adc2

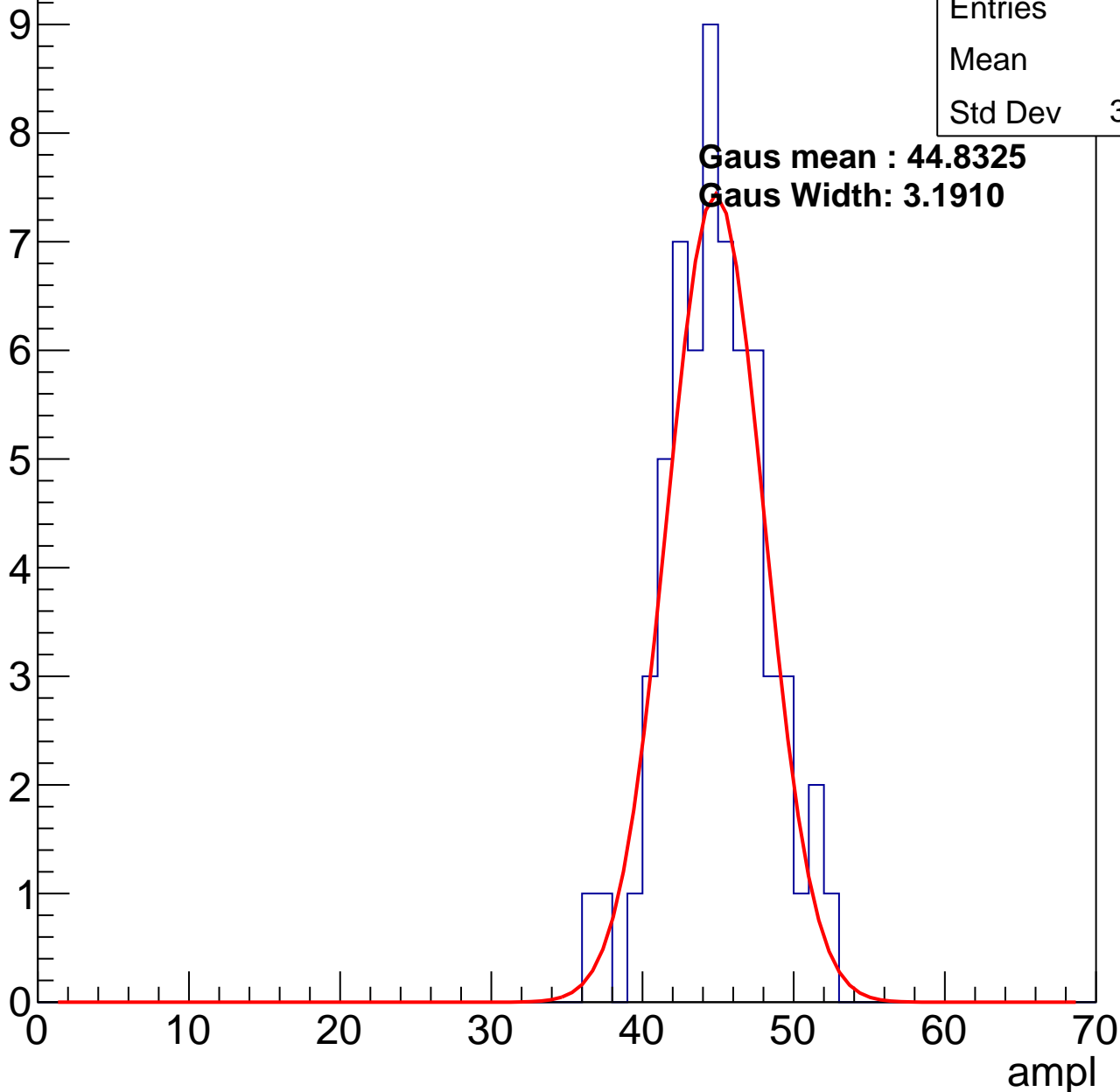
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.4
Std Dev	3.265

Gaus mean : 44.8325

Gaus Width: 3.1910

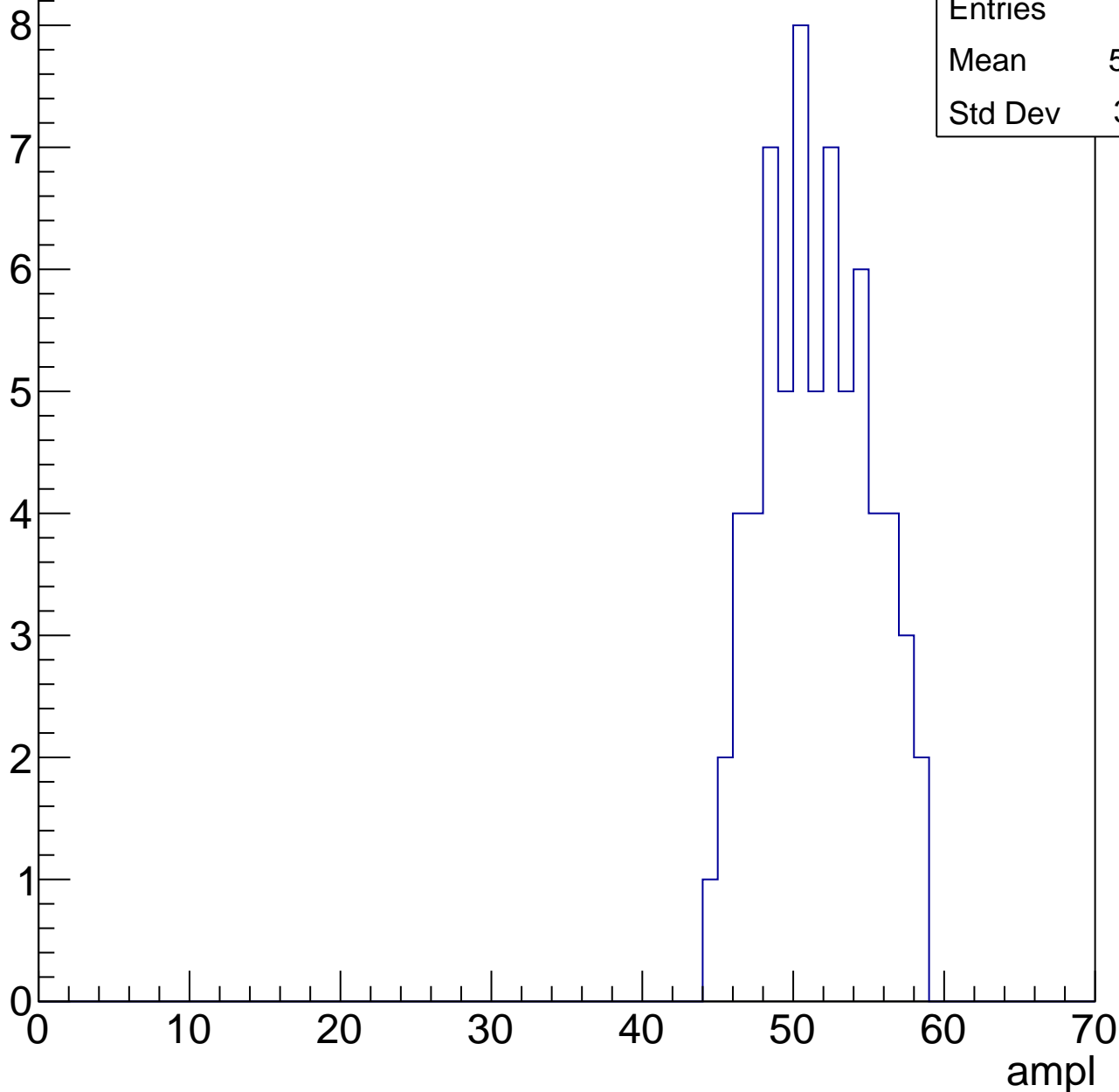


B0L002S, U18-ch9, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	51.13
Std Dev	3.511



B0L002S, U18-ch9, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

7

6

5

4

3

2

1

0

Entries 61

Mean 56.75

Std Dev 3.434

0

0

10

20

30

40

50

60

70

ampl

0

1

2

3

4

5

6

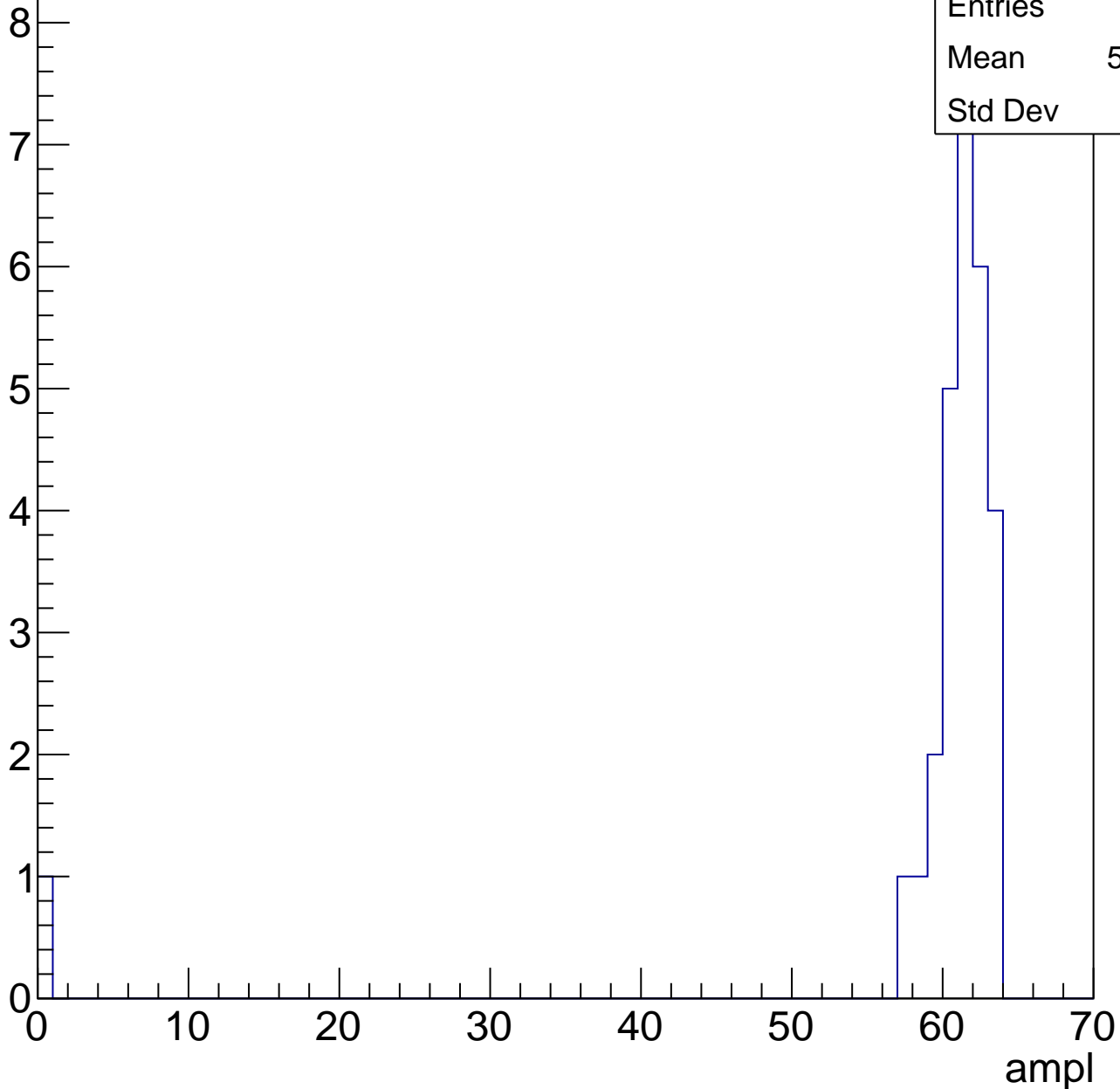
7

B0L002S, U18-ch9, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.75
Std Dev	11.4



B0L002S, U18-ch9, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	5
Mean	61.8
Std Dev	1.47

B0L002S, U18-ch9, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch10, adc0

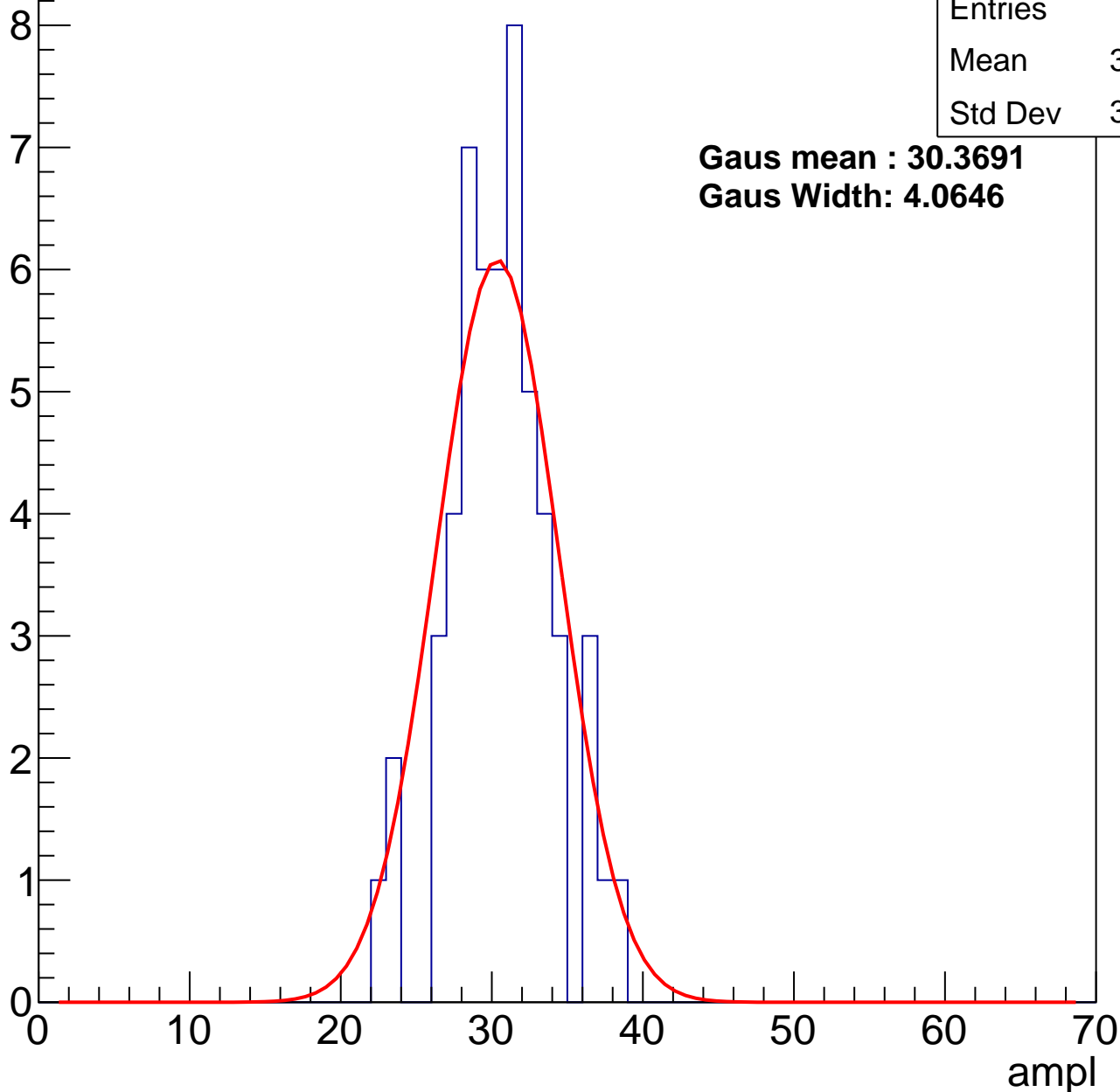
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	30.17
Std Dev	3.365

Gaus mean : 30.3691

Gaus Width: 4.0646



B0L002S, U18-ch10, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.2
Std Dev	3.973

Gaus mean : 36.9395

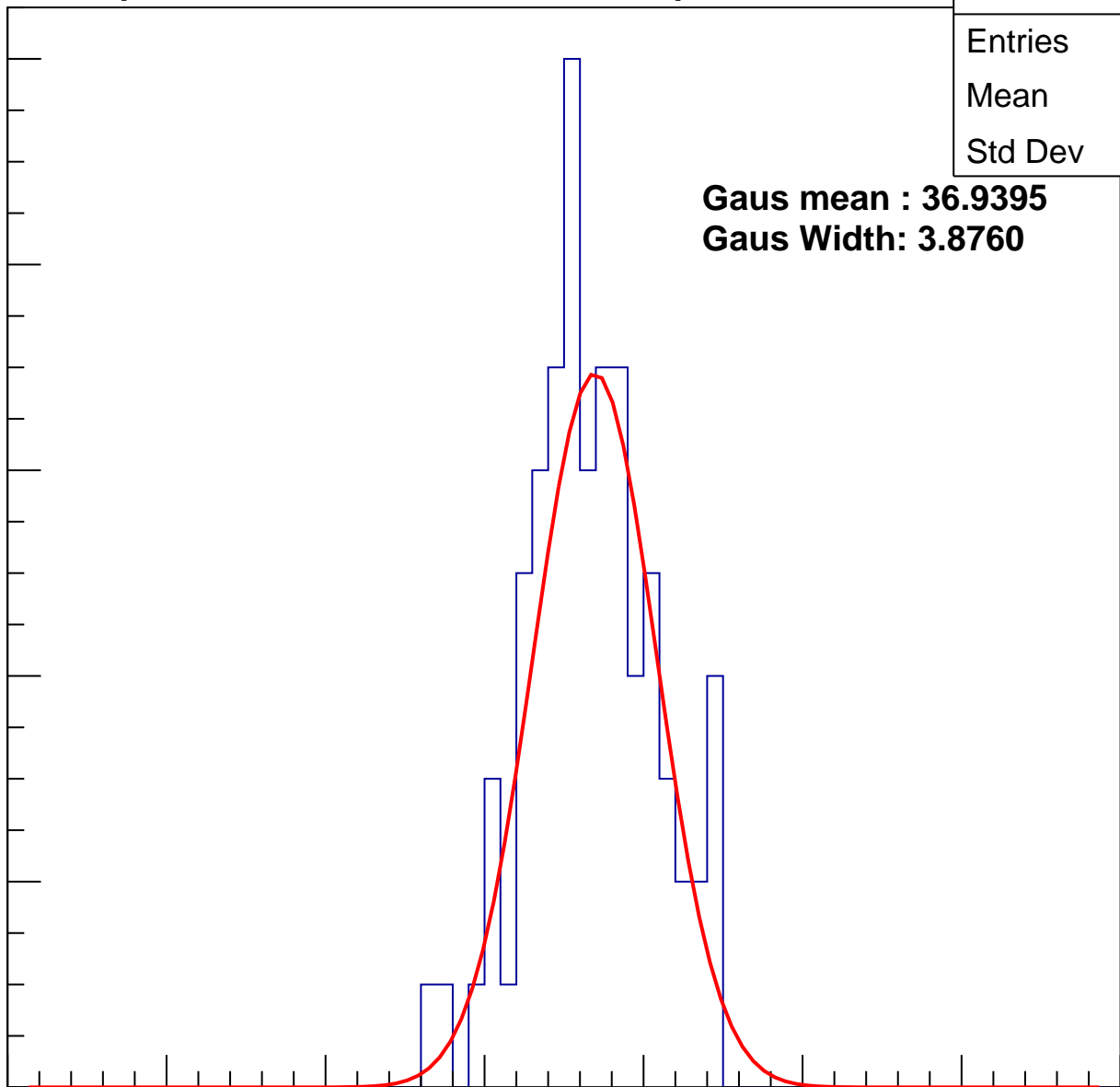
Gaus Width: 3.8760

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch10, adc2

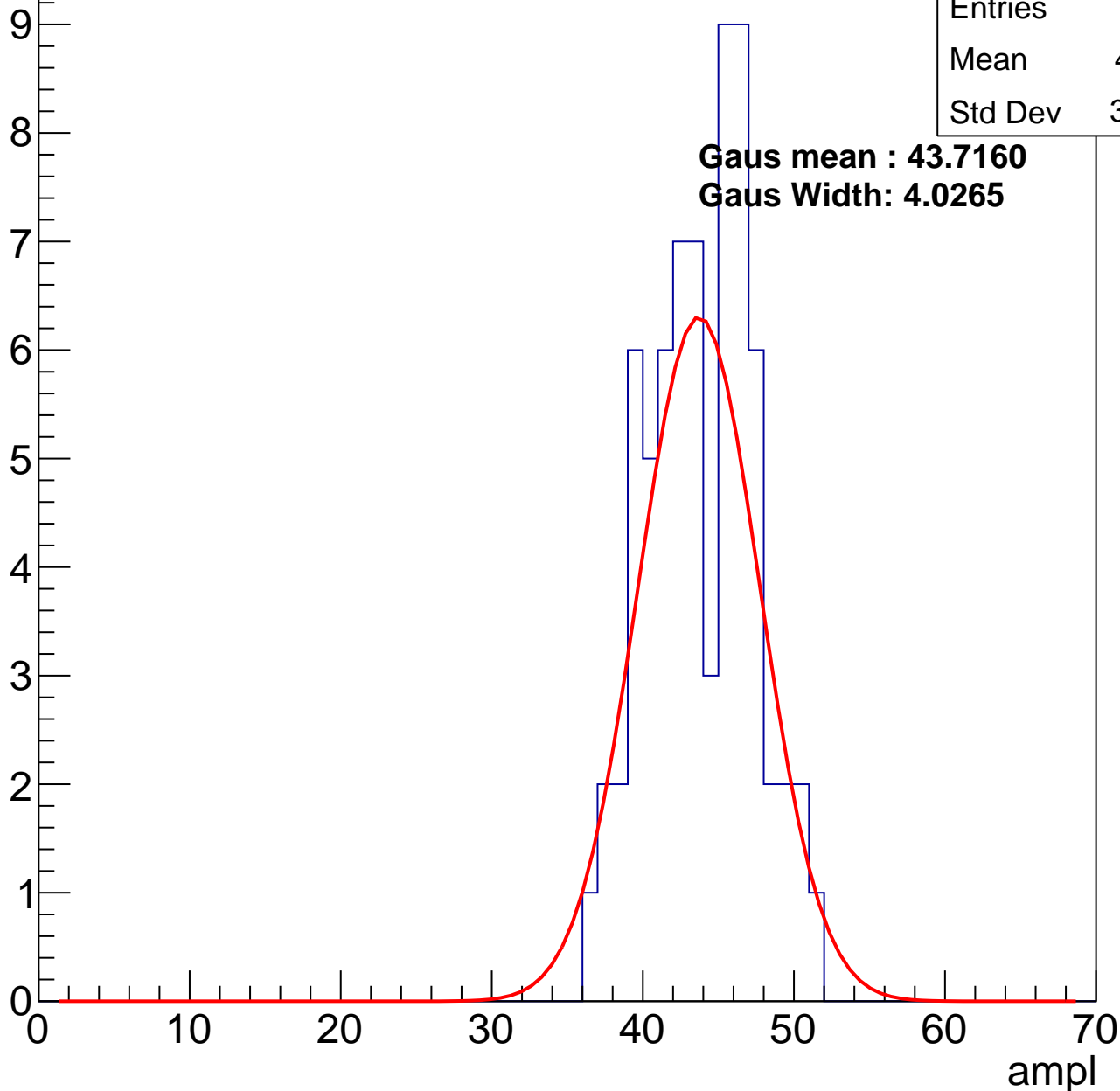
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.41
Std Dev	3.445

Gaus mean : 43.7160

Gaus Width: 4.0265

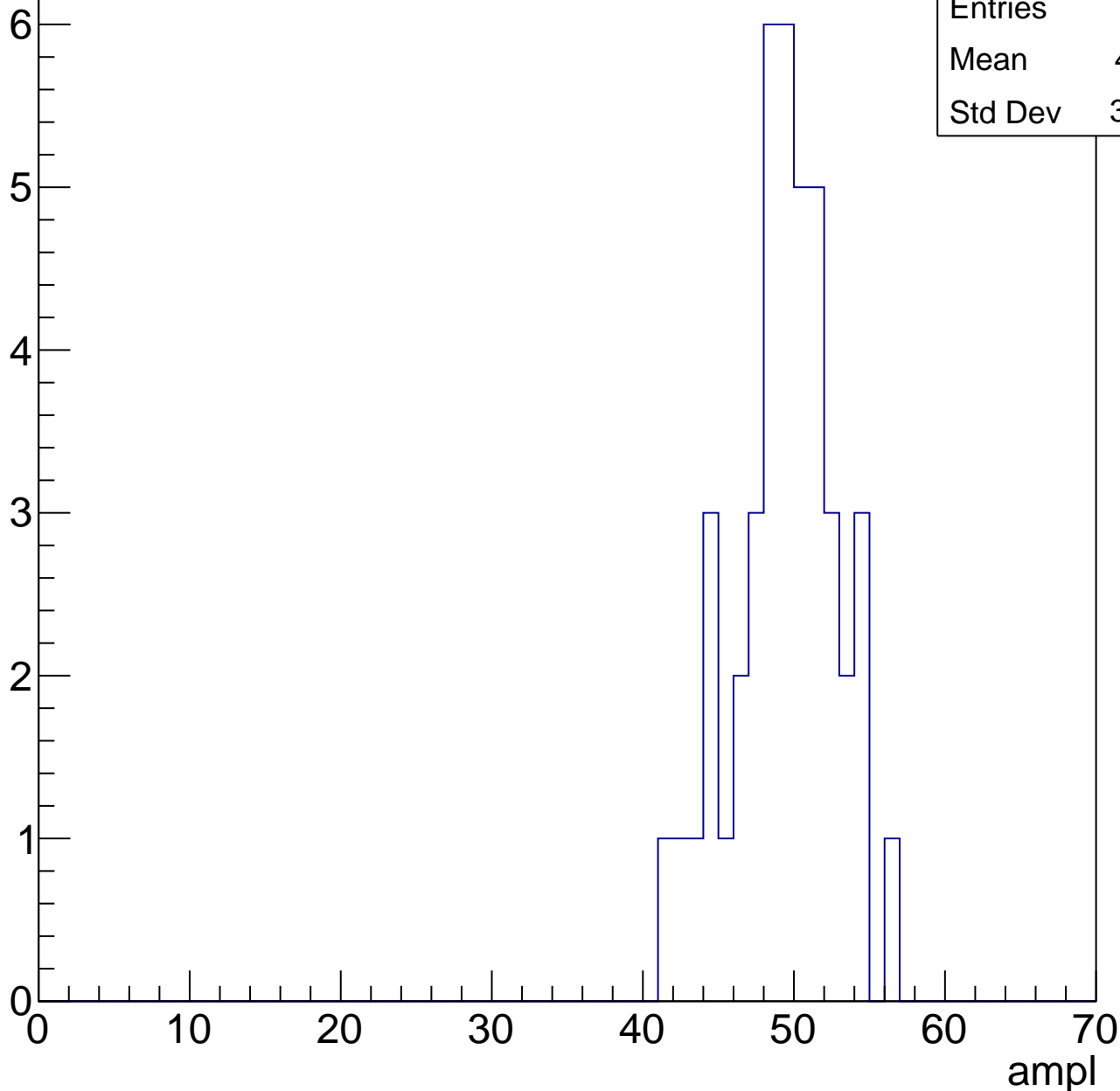


B0L002S, U18-ch10, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	48.91
Std Dev	3.354

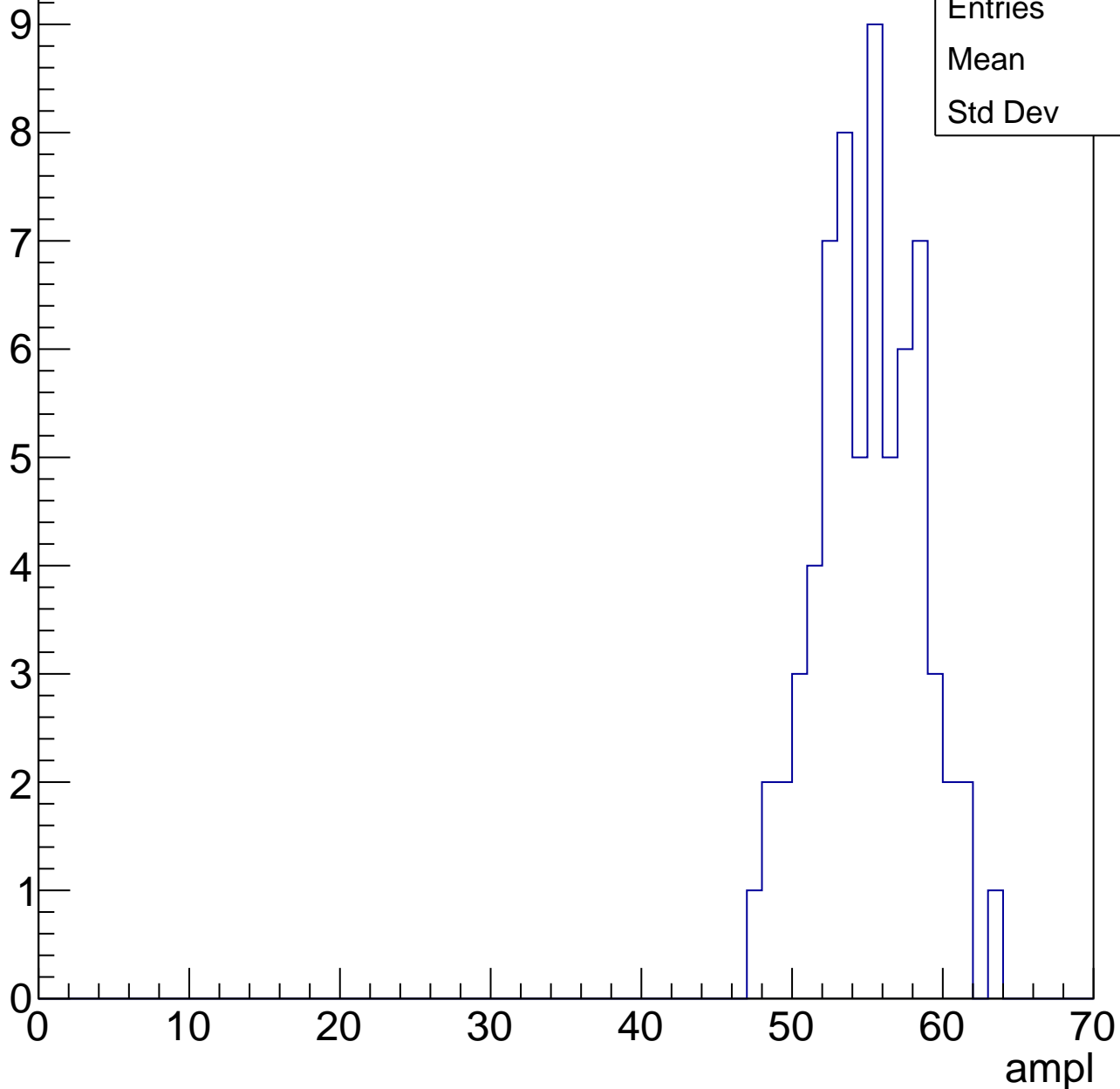


B0L002S, U18-ch10, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	54.6
Std Dev	3.43

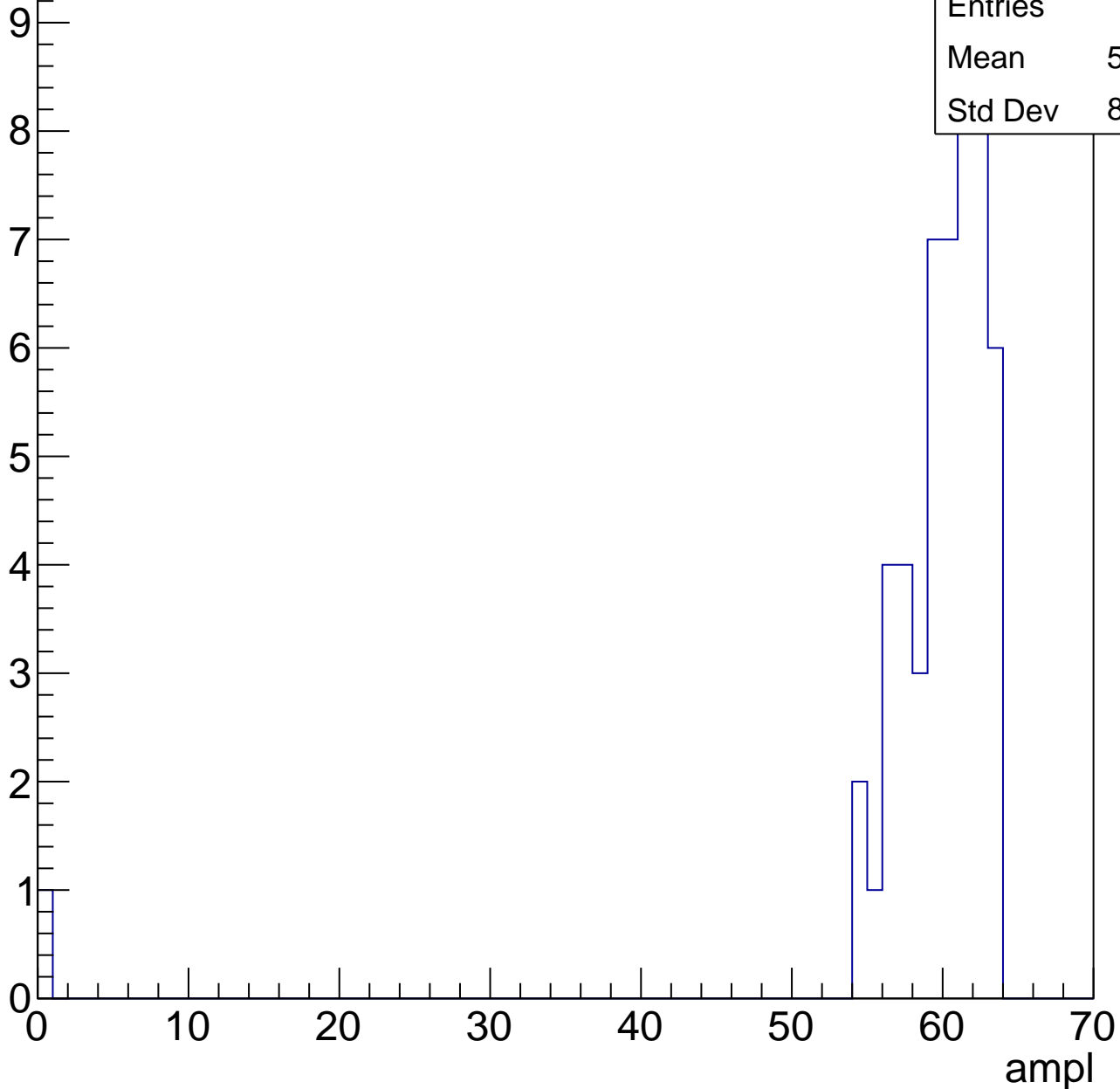


B0L002S, U18-ch10, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	58.58
Std Dev	8.558



B0L002S, U18-ch10, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch10, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch11, adc0

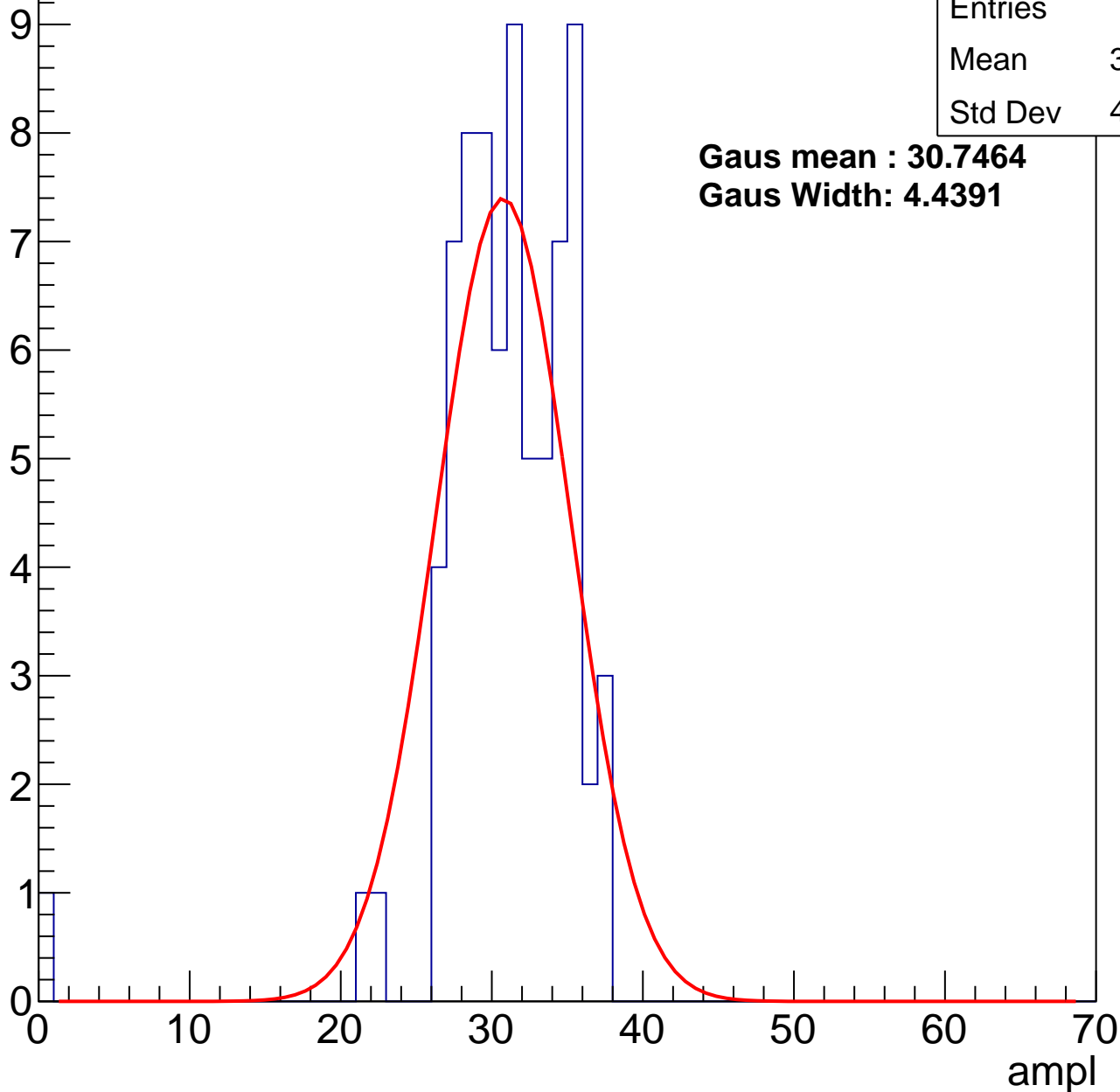
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	30.42
Std Dev	4.905

Gaus mean : 30.7464

Gaus Width: 4.4391



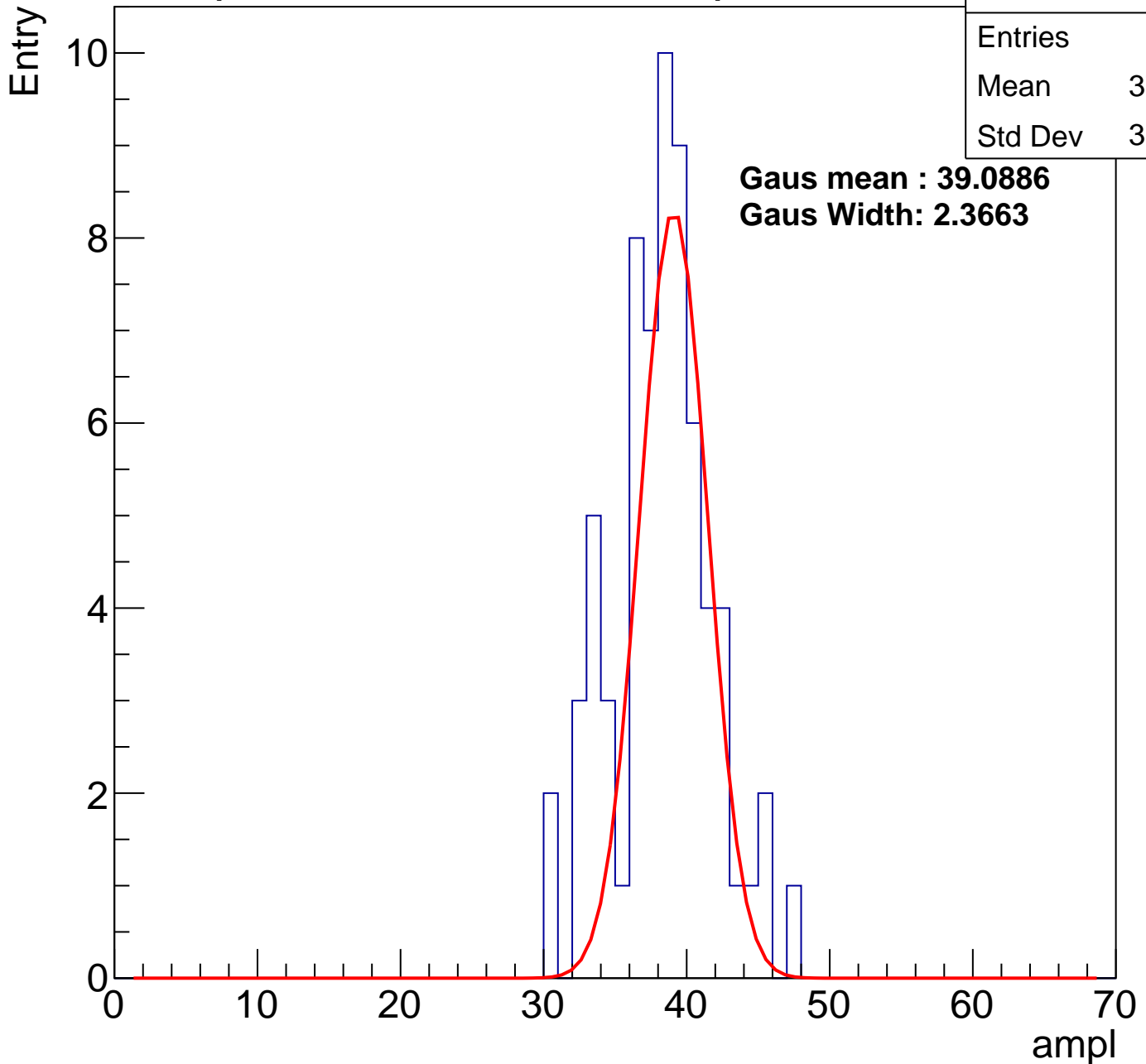
B0L002S, U18-ch11, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	37.79
Std Dev	3.513

Gaus mean : 39.0886

Gaus Width: 2.3663



B0L002S, U18-ch11, adc2

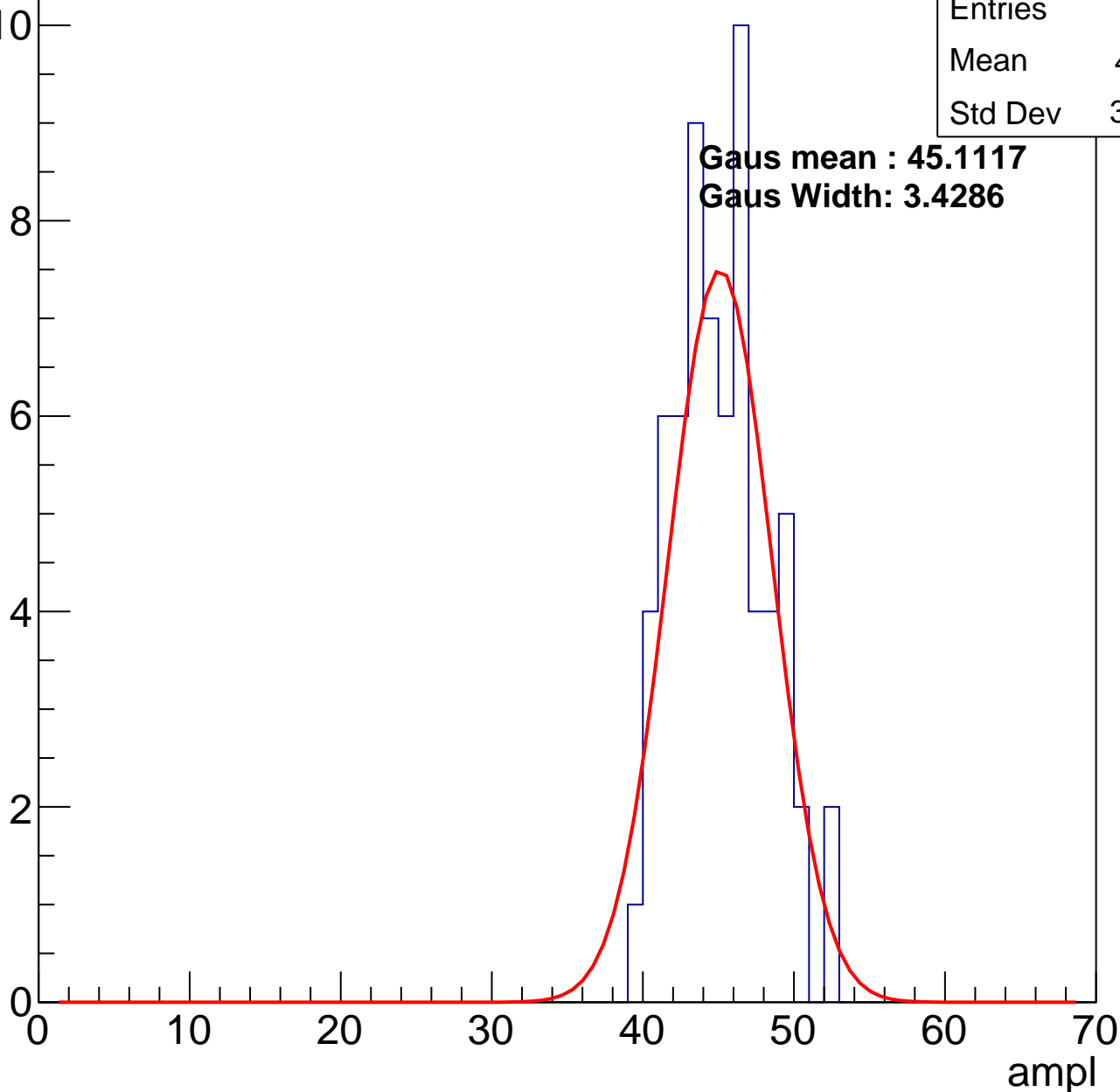
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.71
Std Dev	3.049

Gaus mean : 45.1117

Gaus Width: 3.4286

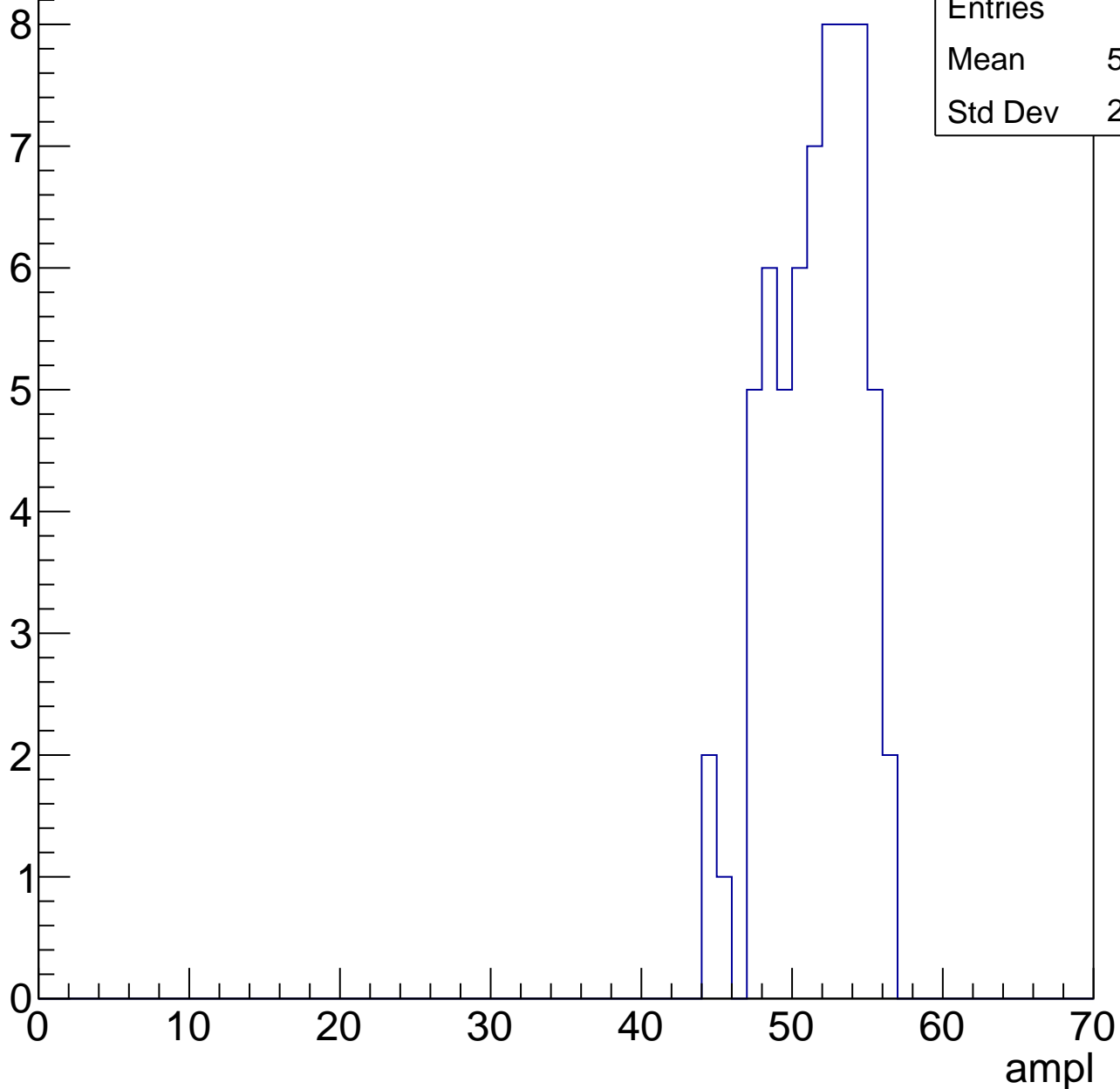


B0L002S, U18-ch11, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	51.06
Std Dev	2.916



B0L002S, U18-ch11, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	62
Mean	56.19
Std Dev	7.957

Entry

10

8

6

4

2

0

0

10

20

30

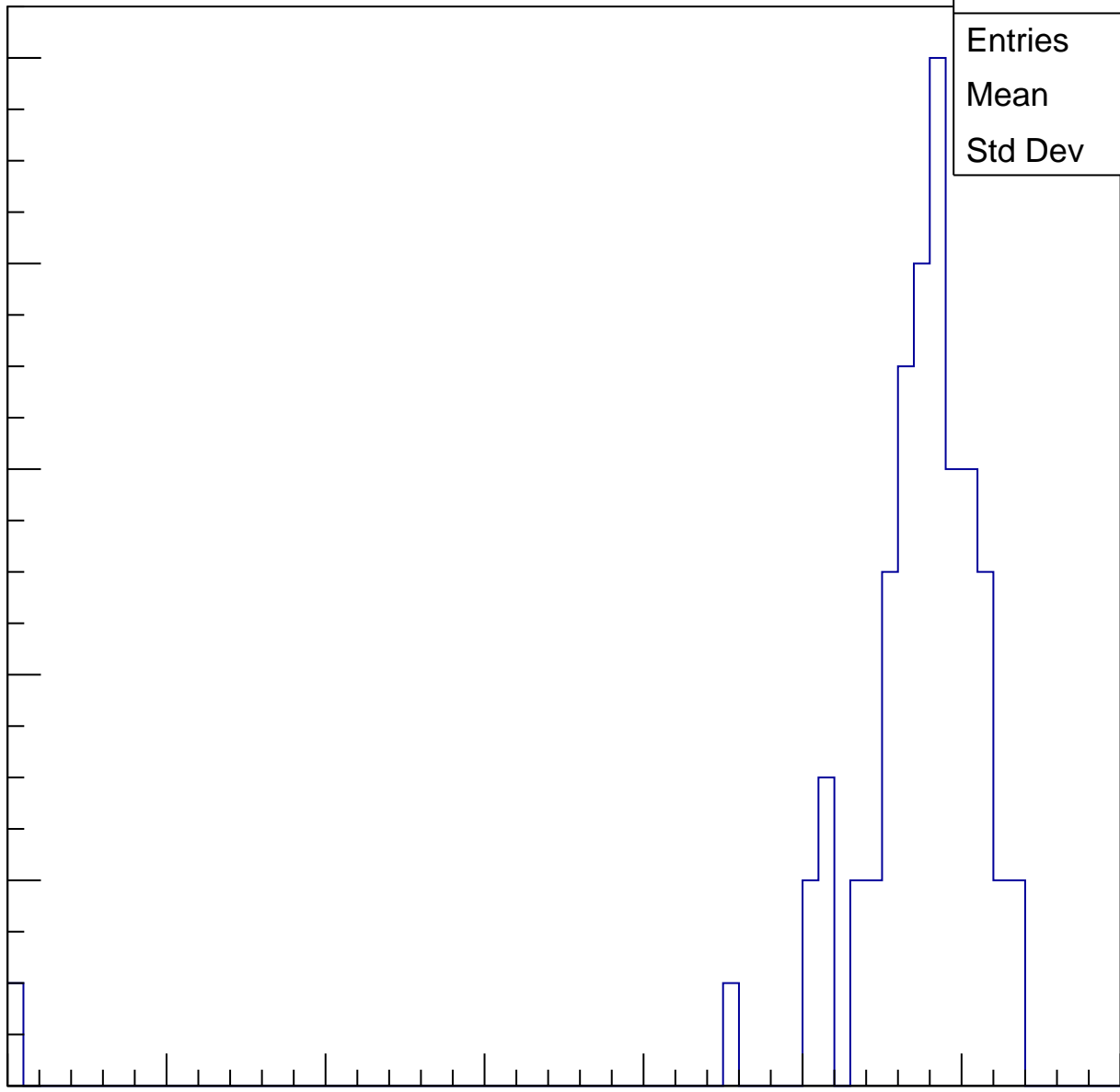
40

50

60

70

ampl

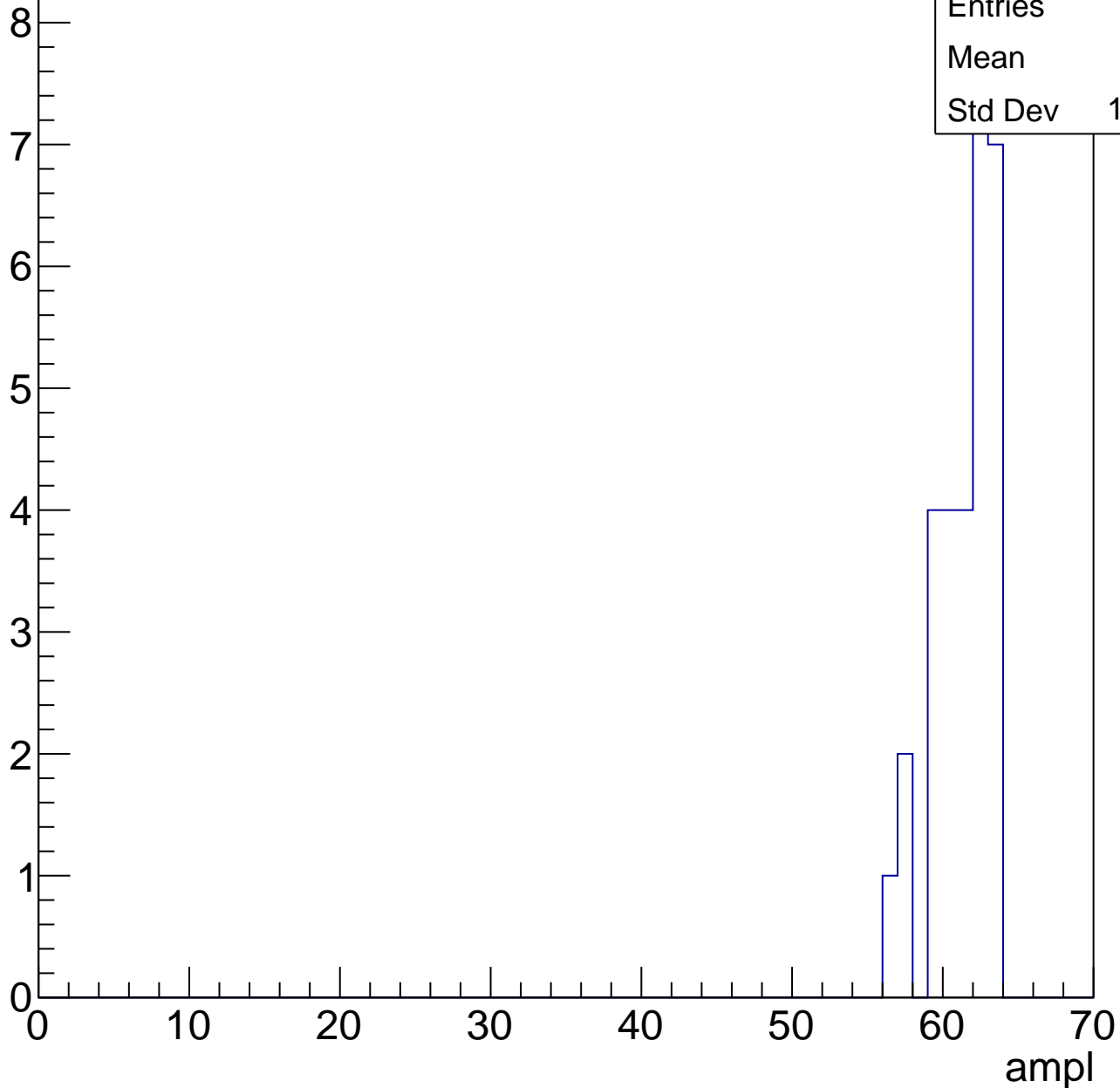


B0L002S, U18-ch11, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	60.9
Std Dev	1.938



B0L002S, U18-ch11, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch11, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10
Std Dev	10

B0L002S, U18-ch12, adc0

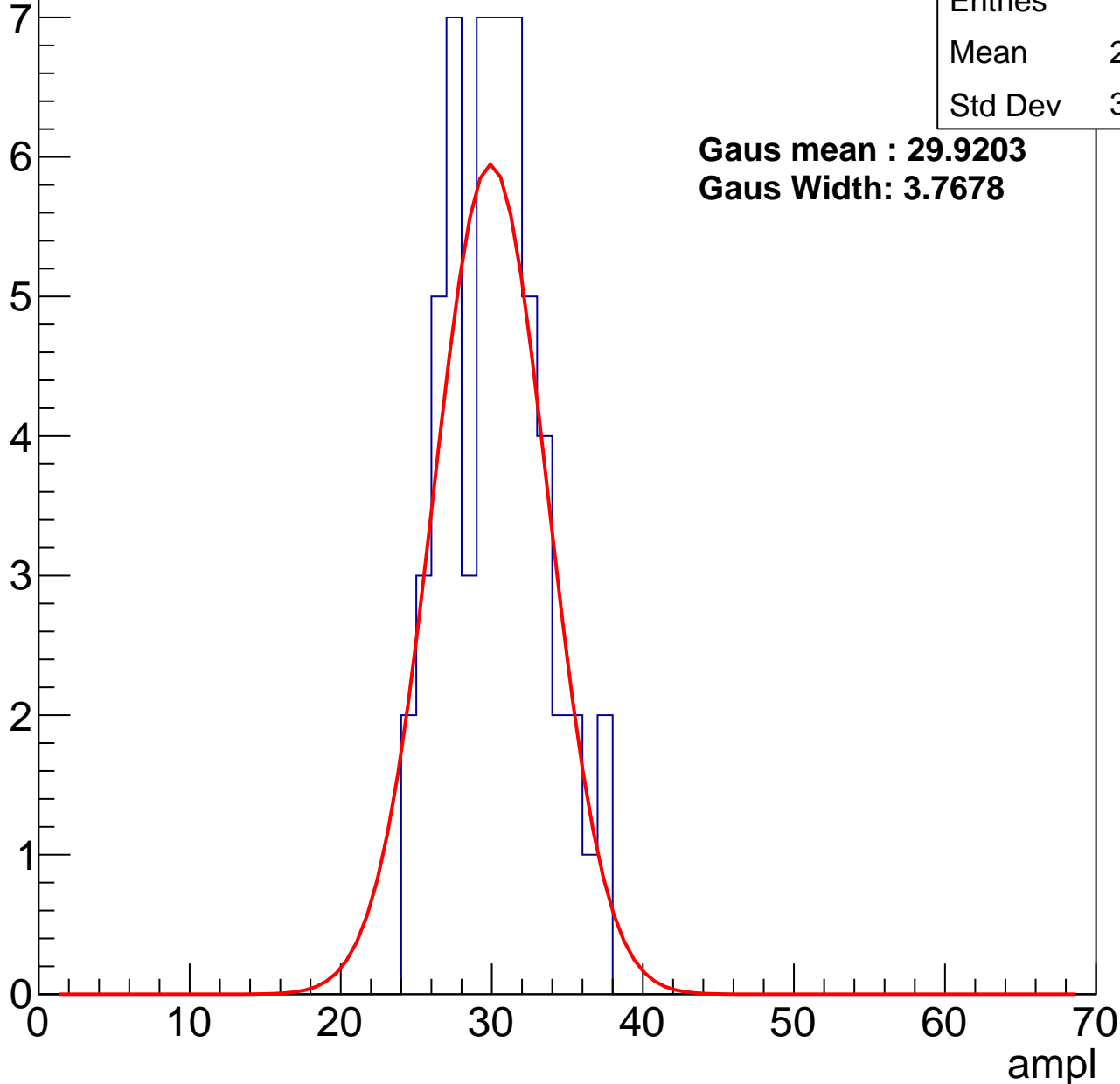
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.75
Std Dev	3.202

Gaus mean : 29.9203

Gaus Width: 3.7678



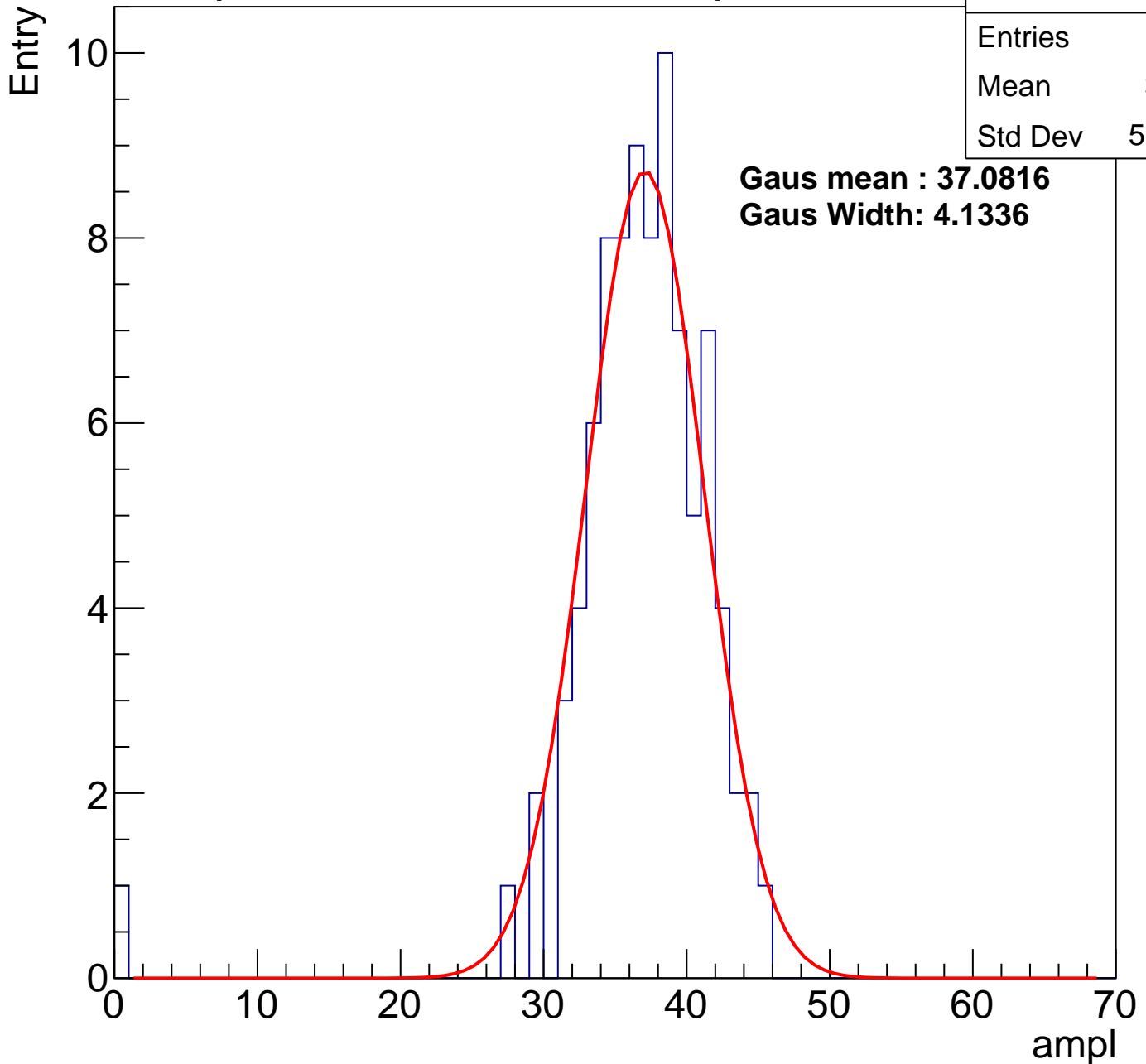
B0L002S, U18-ch12, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	88
Mean	36.4
Std Dev	5.342

Gaus mean : 37.0816

Gaus Width: 4.1336



B0L002S, U18-ch12, adc2

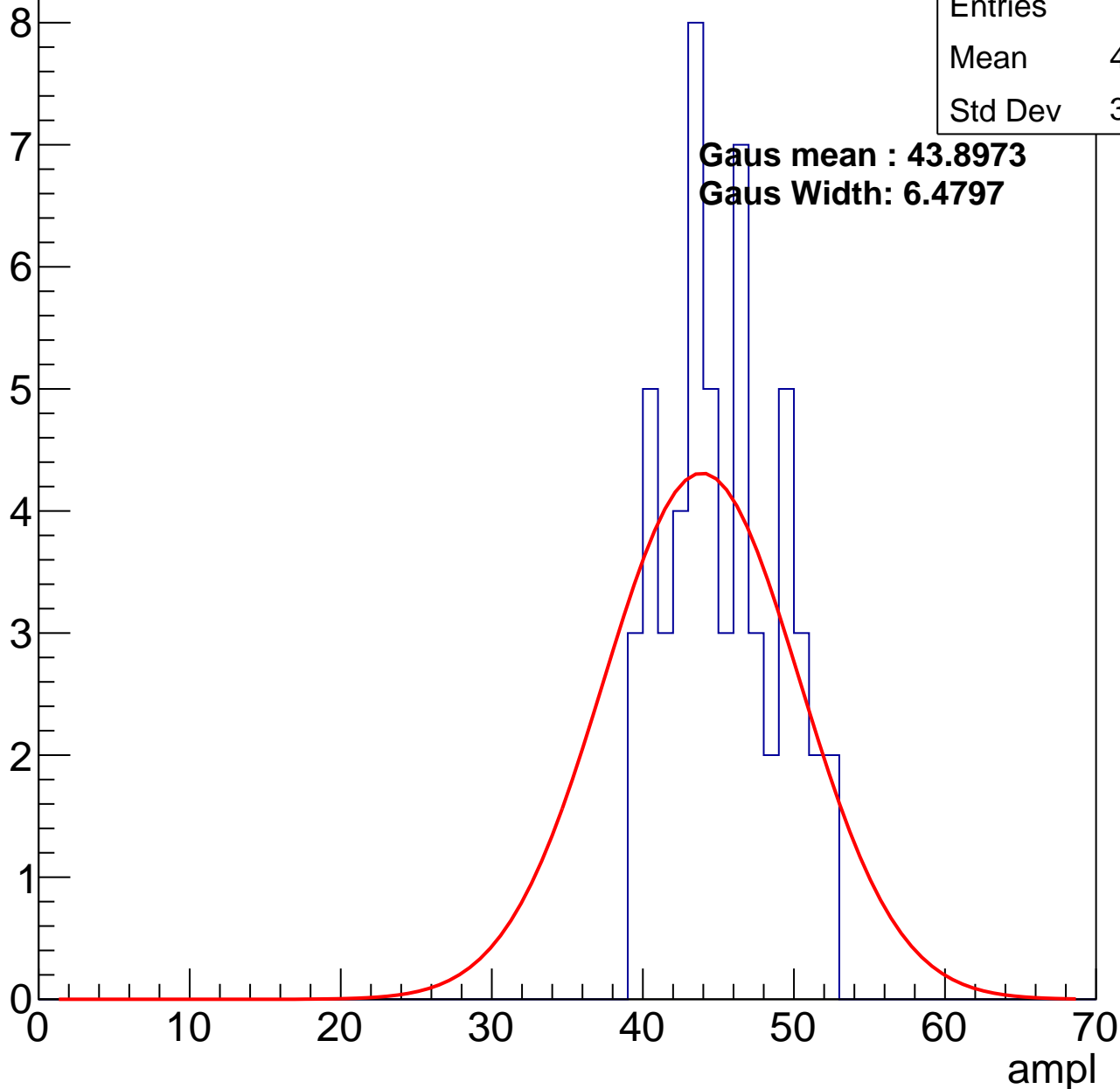
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.85
Std Dev	3.605

Gaus mean : 43.8973

Gaus Width: 6.4797

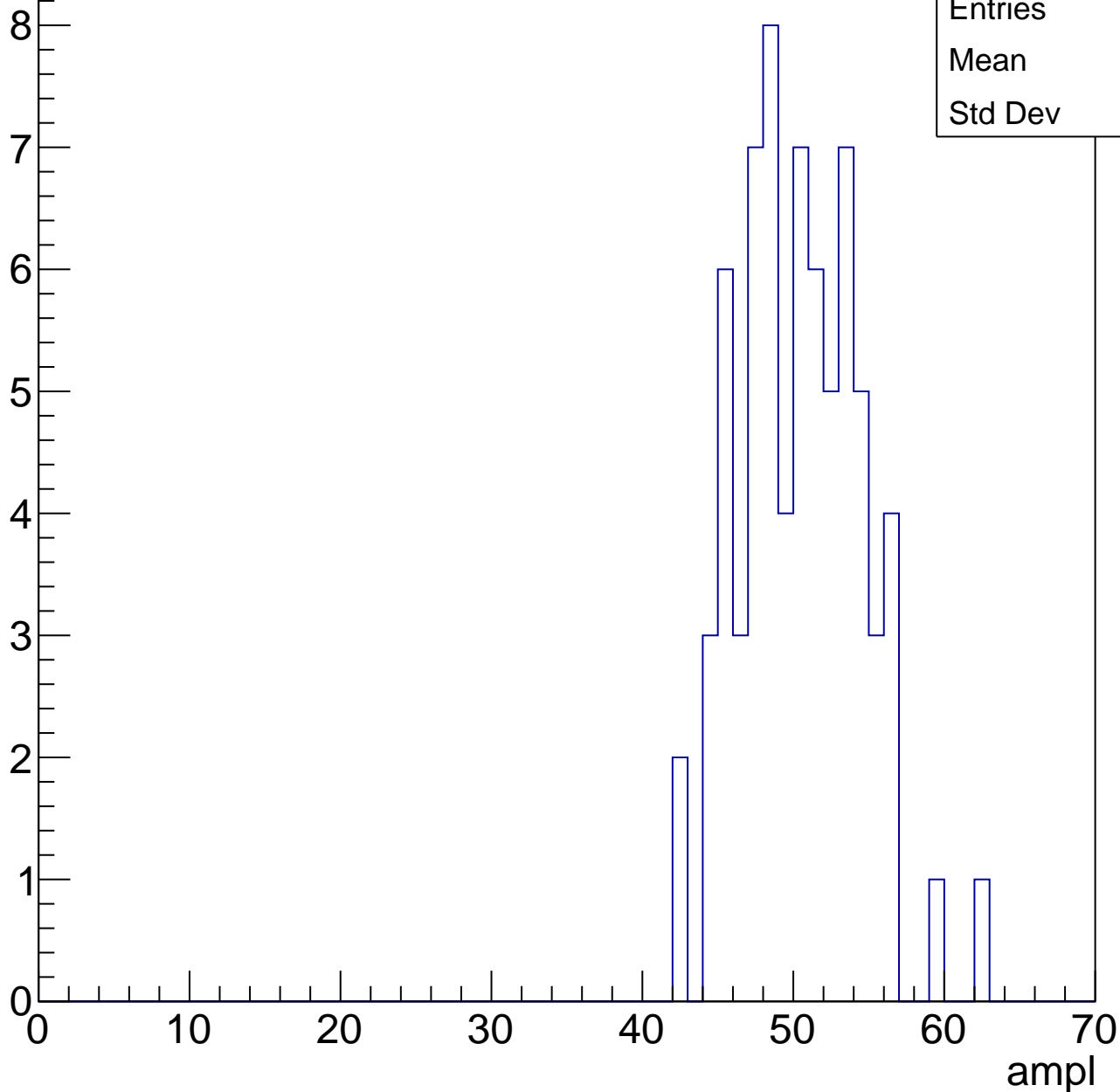


B0L002S, U18-ch12, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

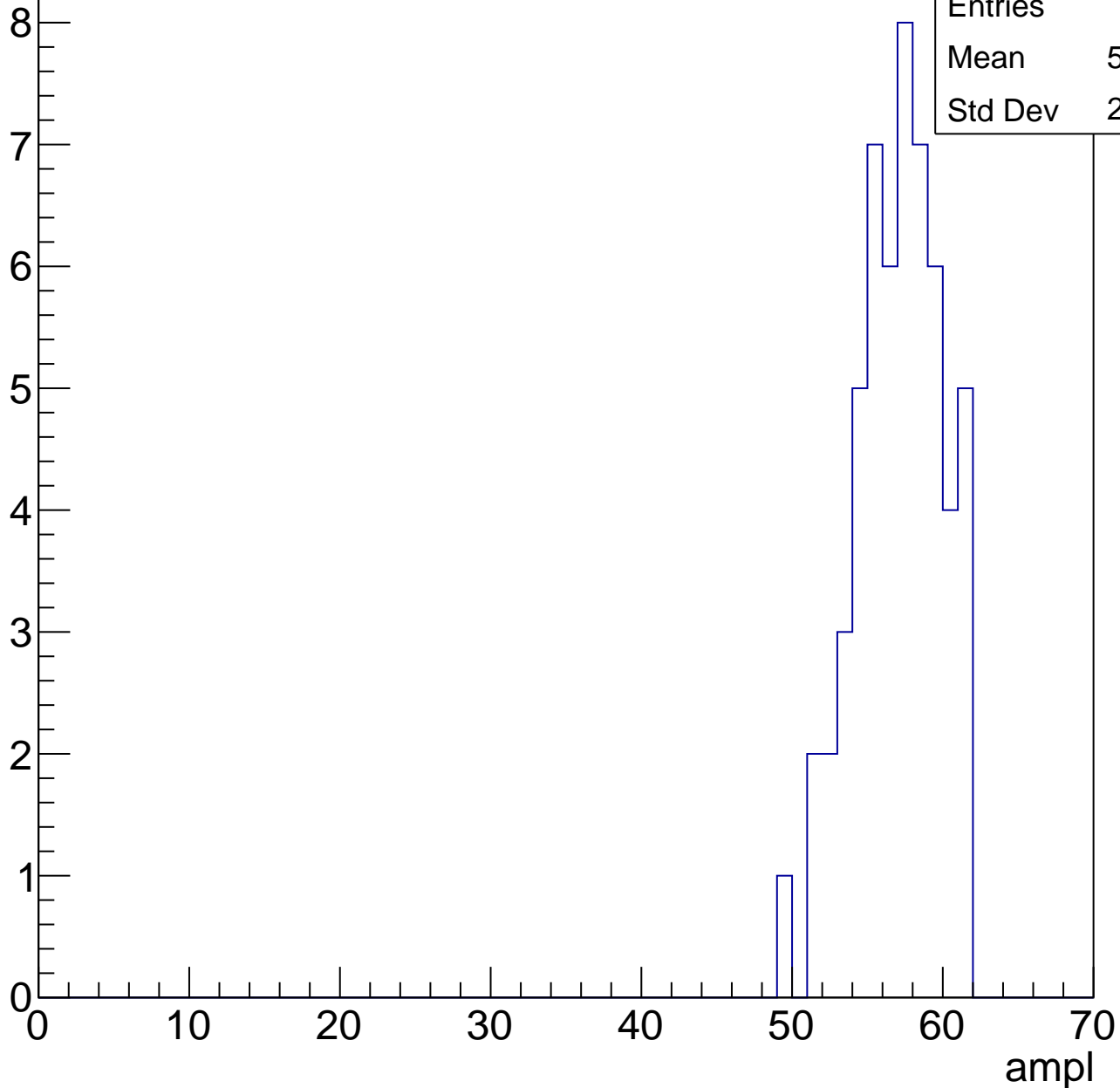
Entries	72
Mean	50
Std Dev	3.99



B0L002S, U18-ch12, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



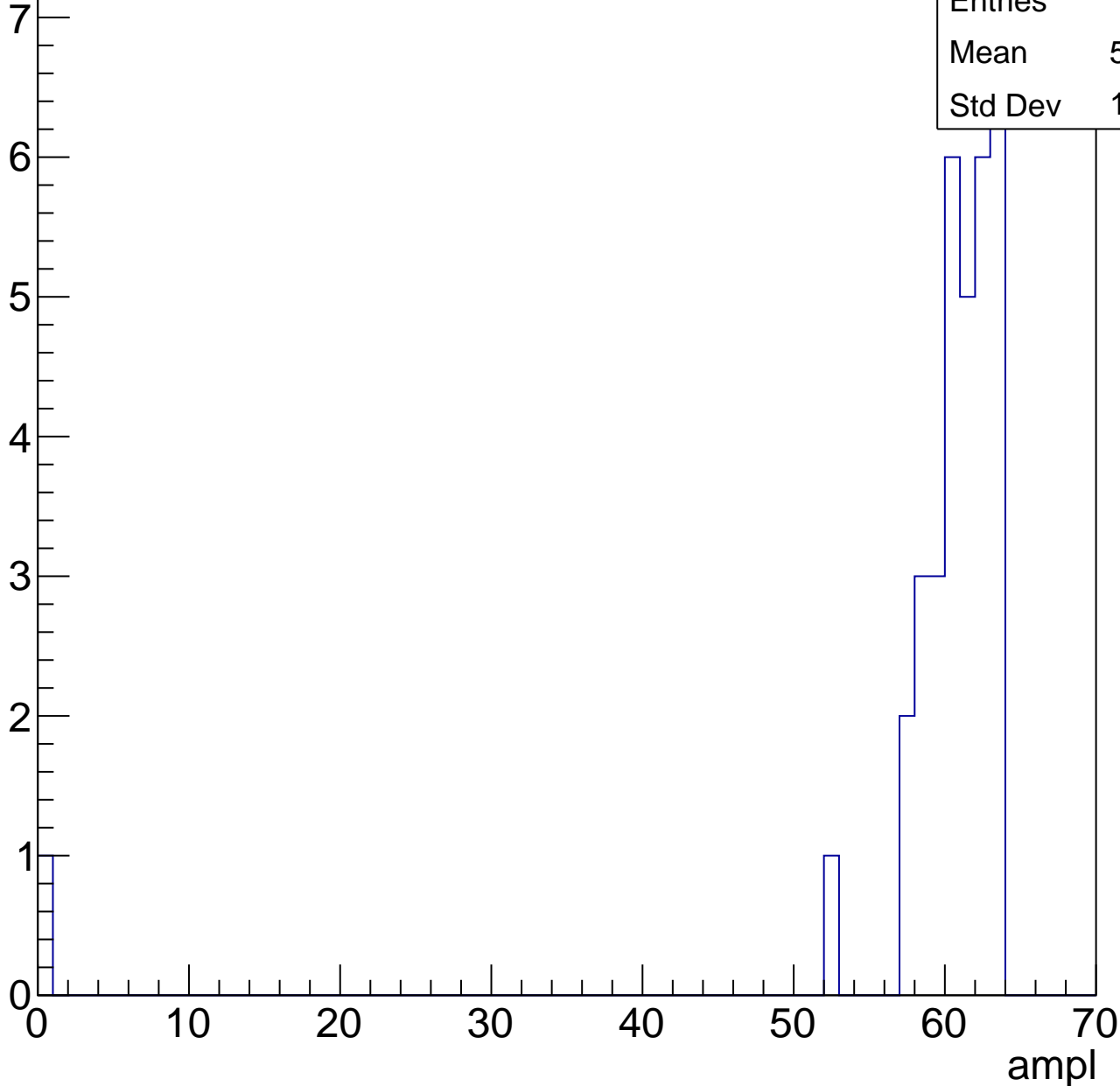
Entries	56
Mean	56.54
Std Dev	2.828

B0L002S, U18-ch12, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

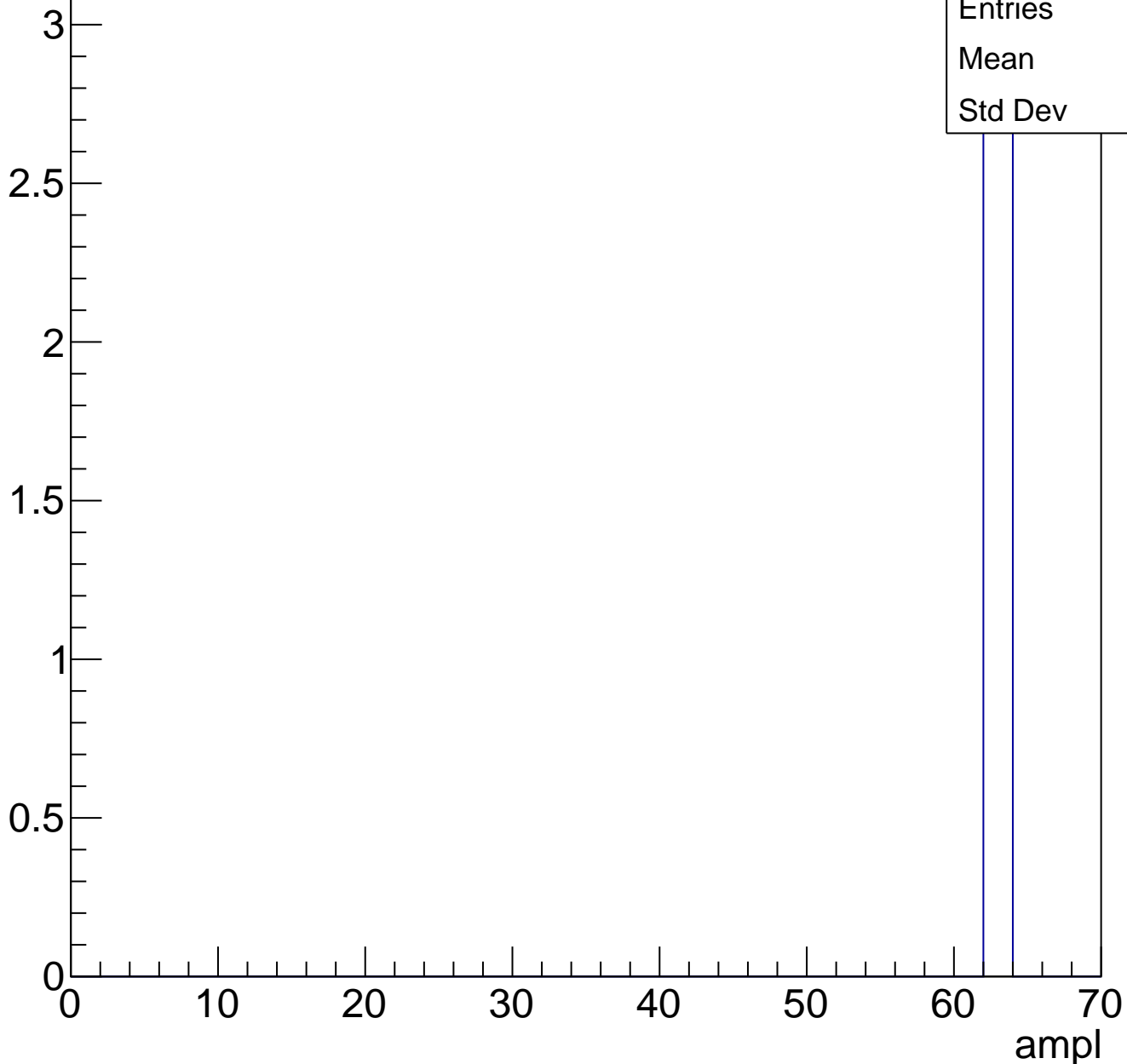
Entries	34
Mean	58.68
Std Dev	10.47



B0L002S, U18-ch12, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch12, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch13, adc0

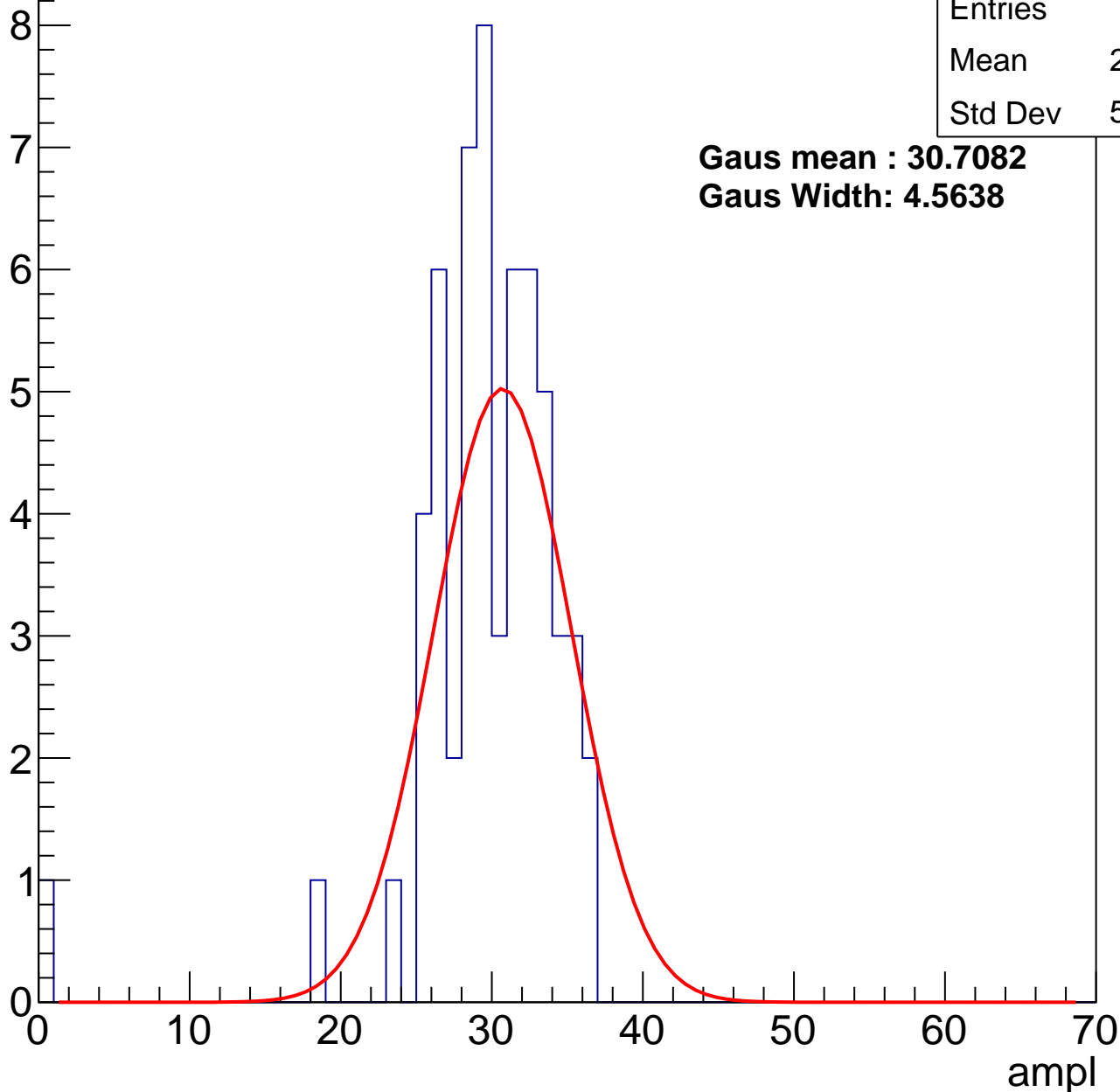
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	29.16
Std Dev	5.205

Gaus mean : 30.7082

Gaus Width: 4.5638



B0L002S, U18-ch13, adc1

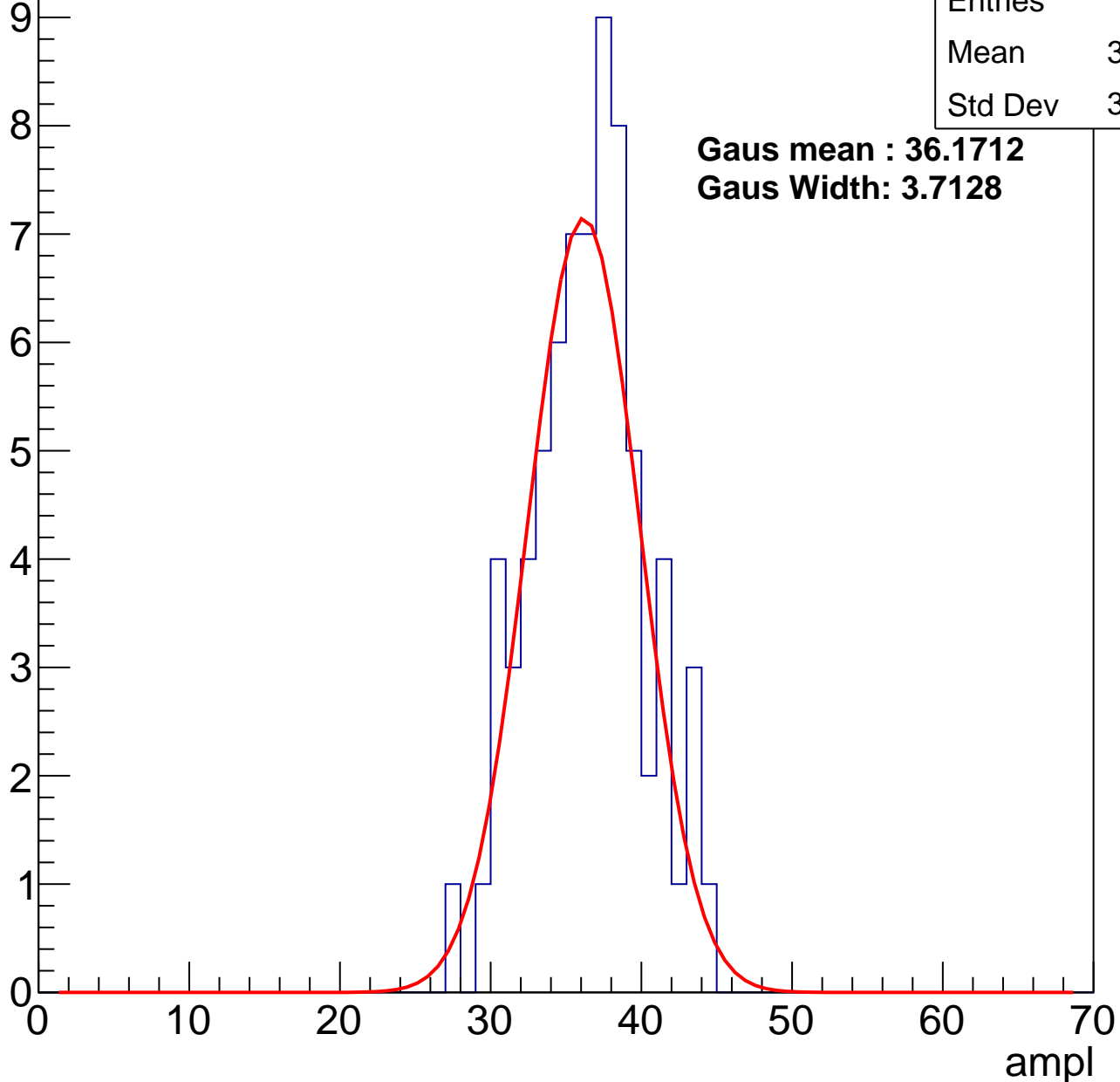
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	35.97
Std Dev	3.665

Gaus mean : 36.1712

Gaus Width: 3.7128



B0L002S, U18-ch13, adc2

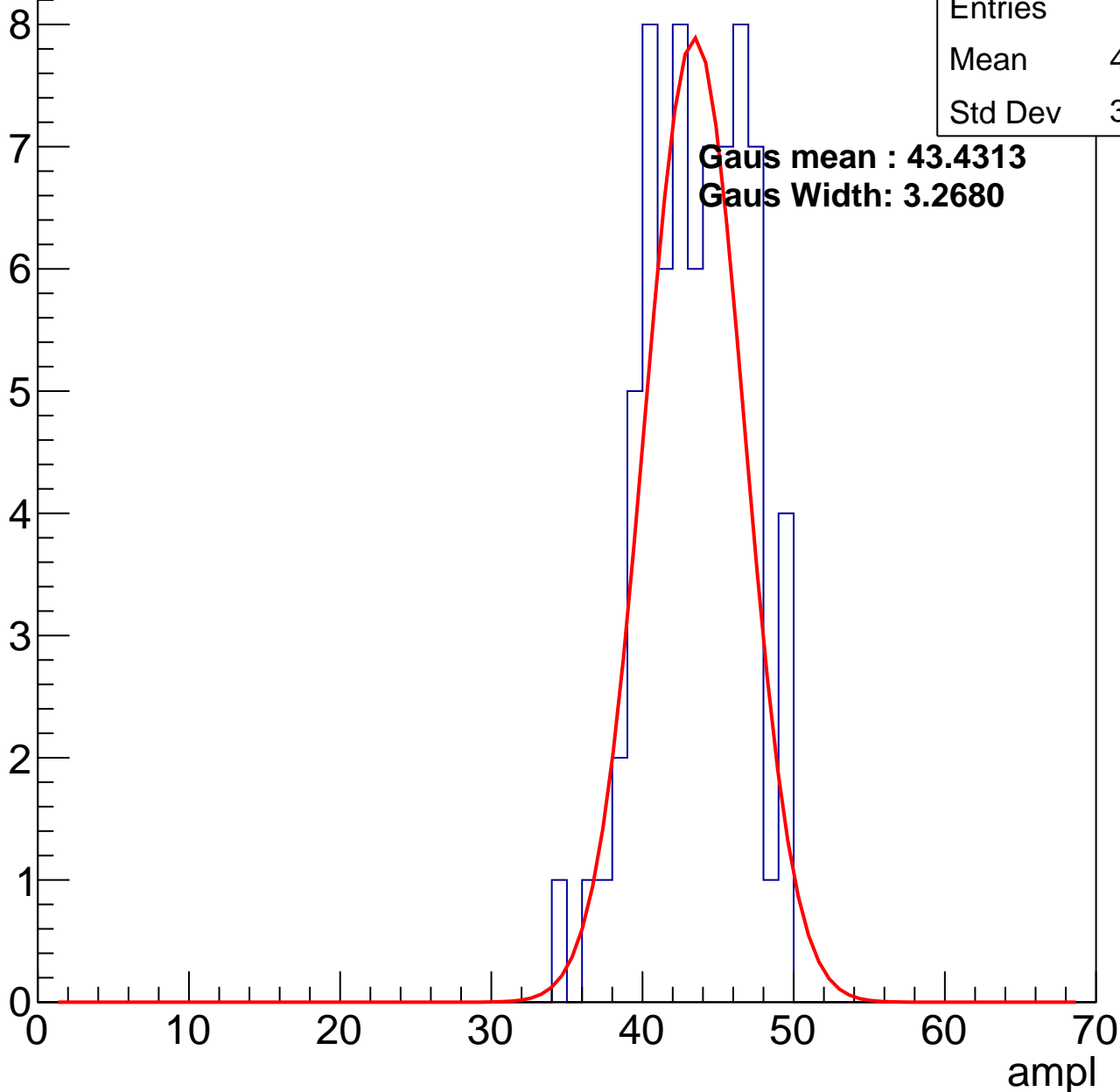
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	43.08
Std Dev	3.307

Gaus mean : 43.4313

Gaus Width: 3.2680

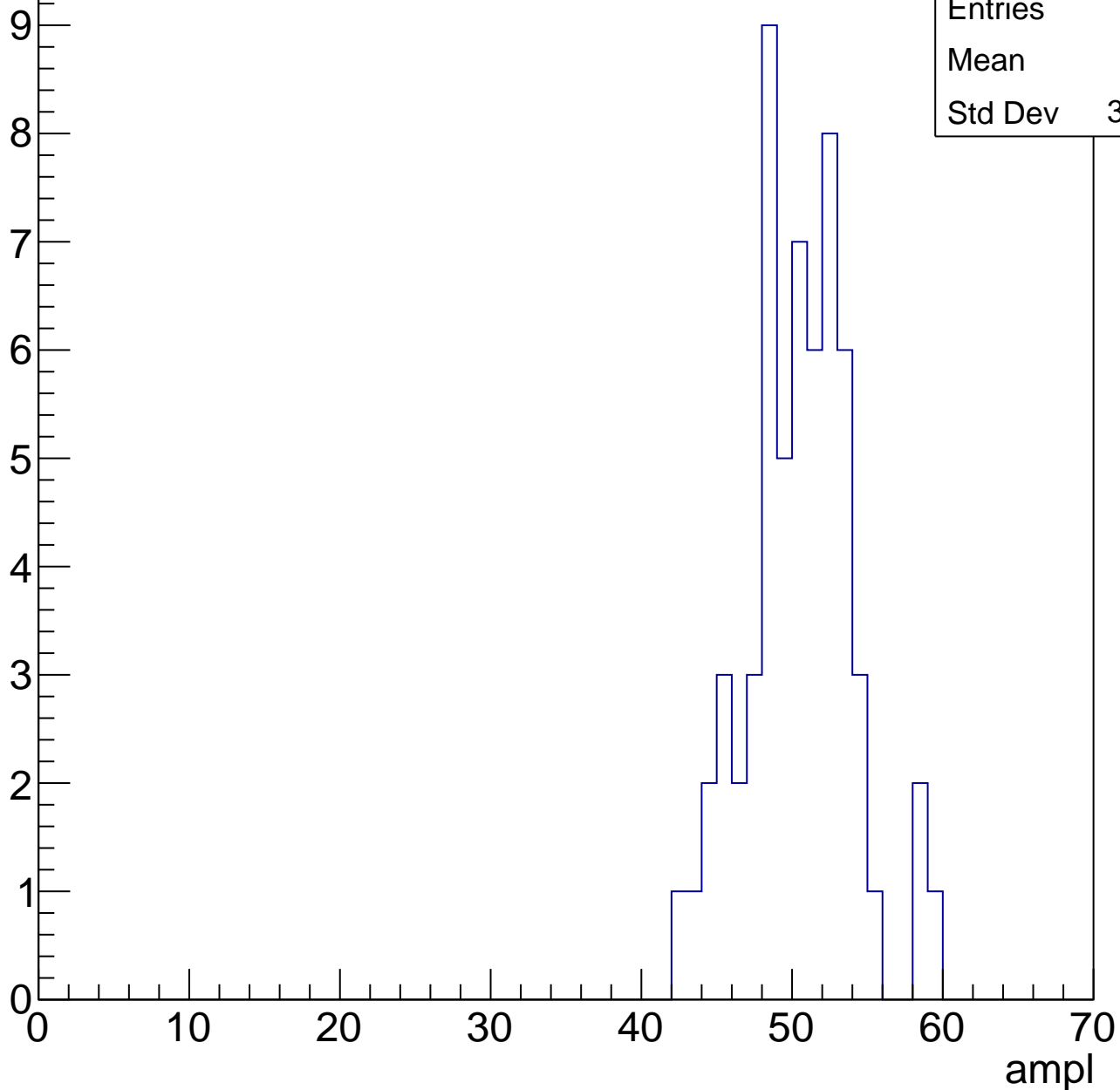


B0L002S, U18-ch13, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50
Std Dev	3.498

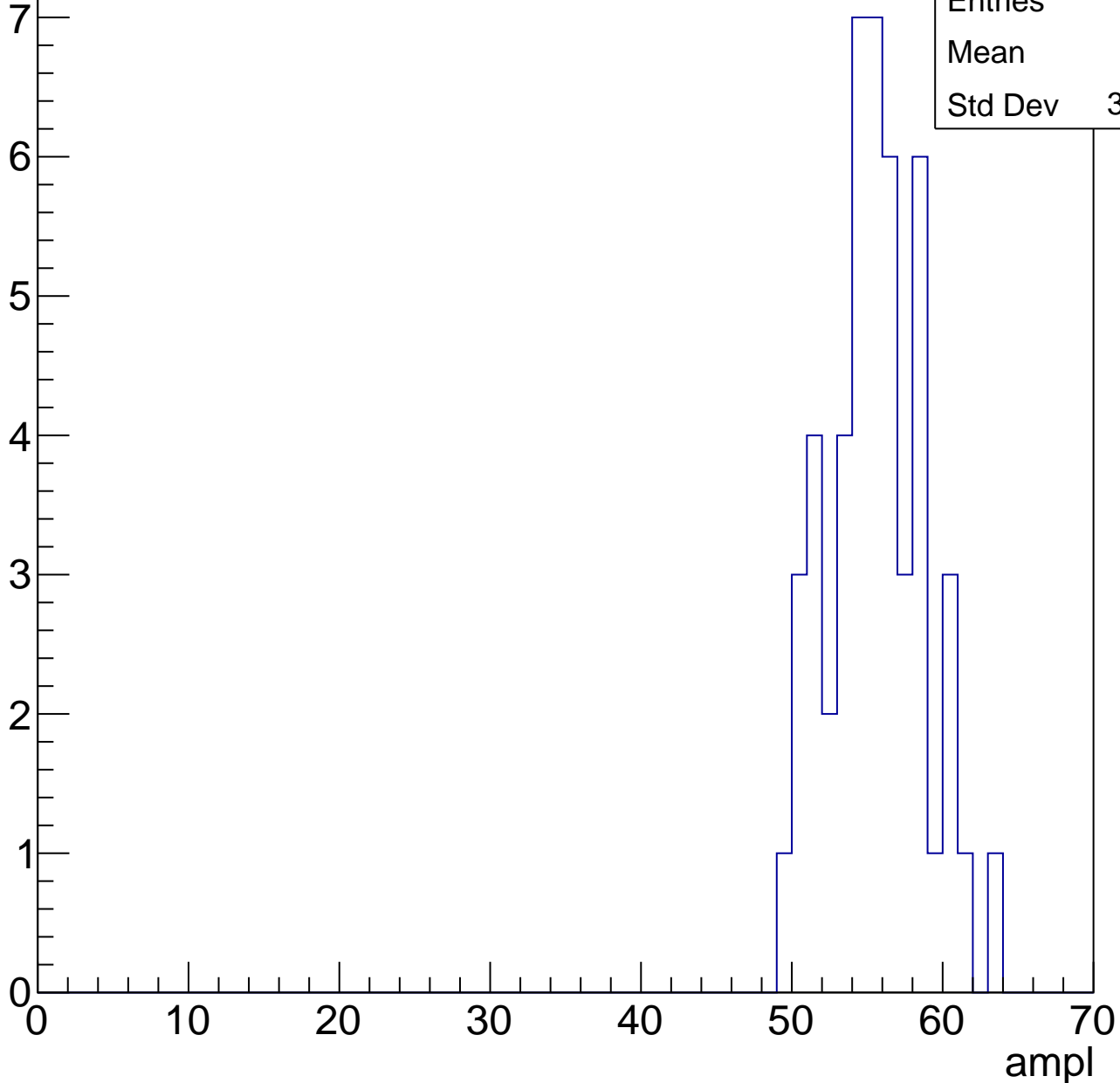


B0L002S, U18-ch13, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

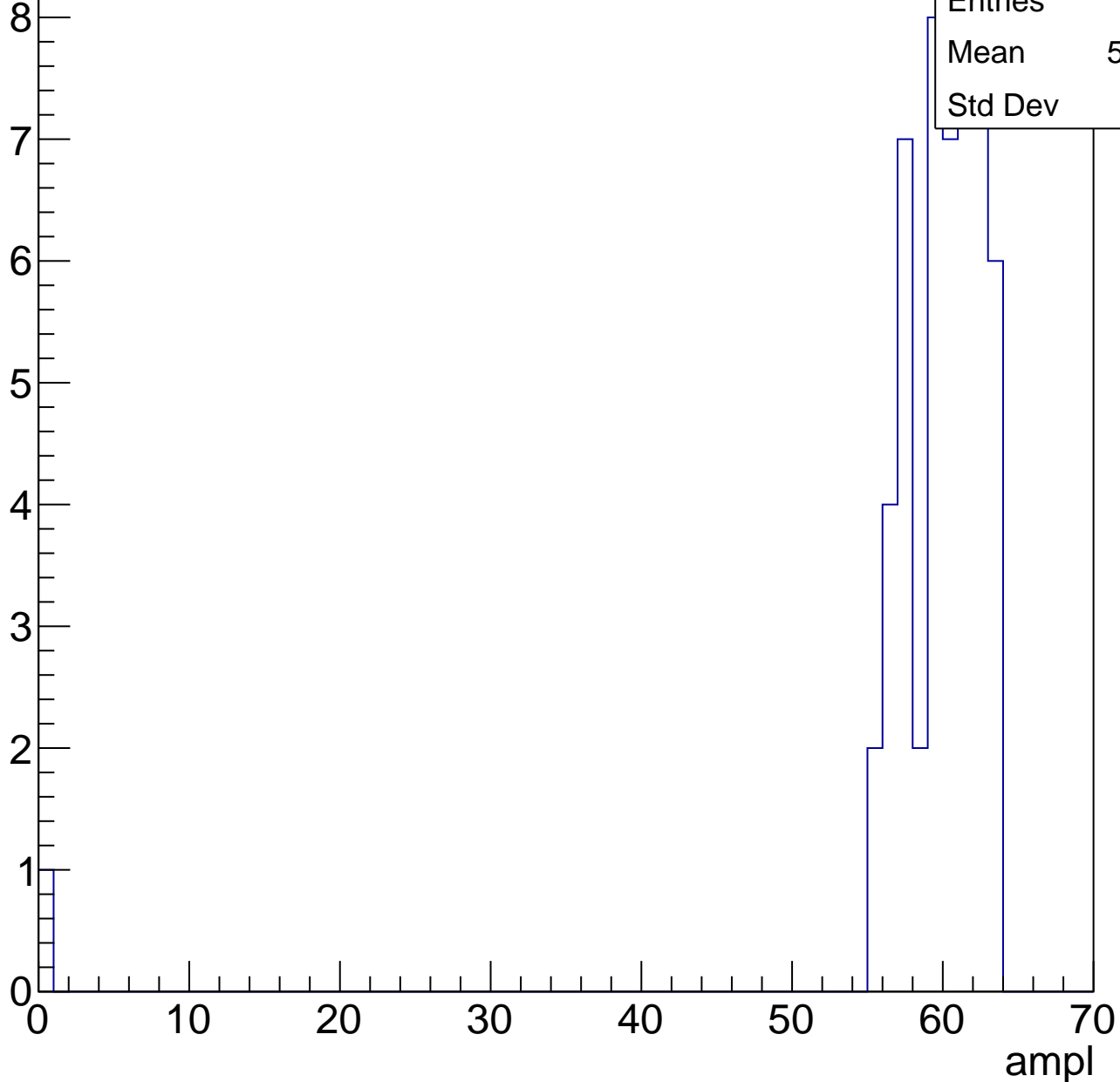
Entries	49
Mean	55.1
Std Dev	3.125



B0L002S, U18-ch13, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch13, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch13, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch14, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	29.69
Std Dev	3.188

Gaus mean : 30.5352

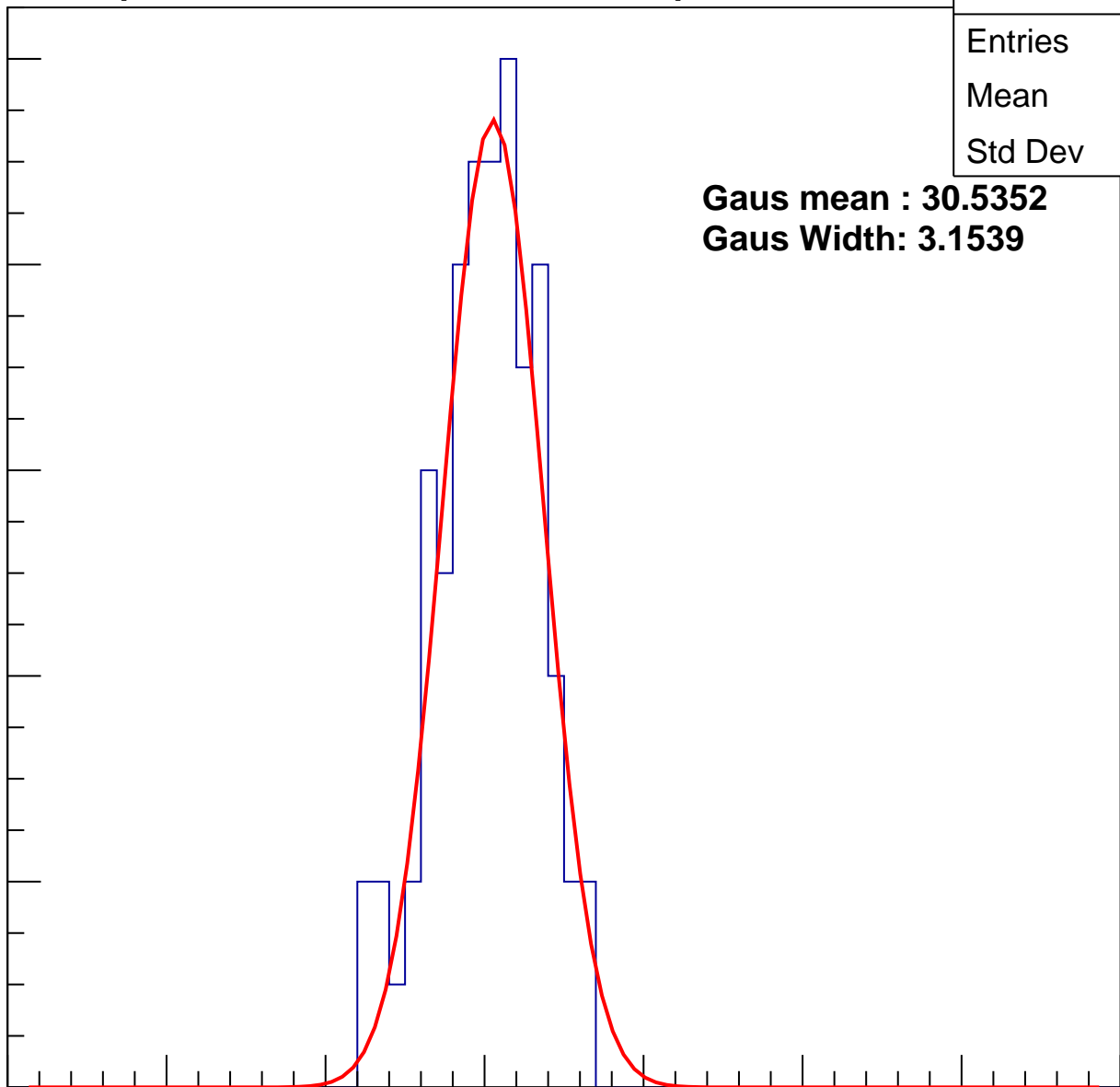
Gaus Width: 3.1539

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch14, adc1

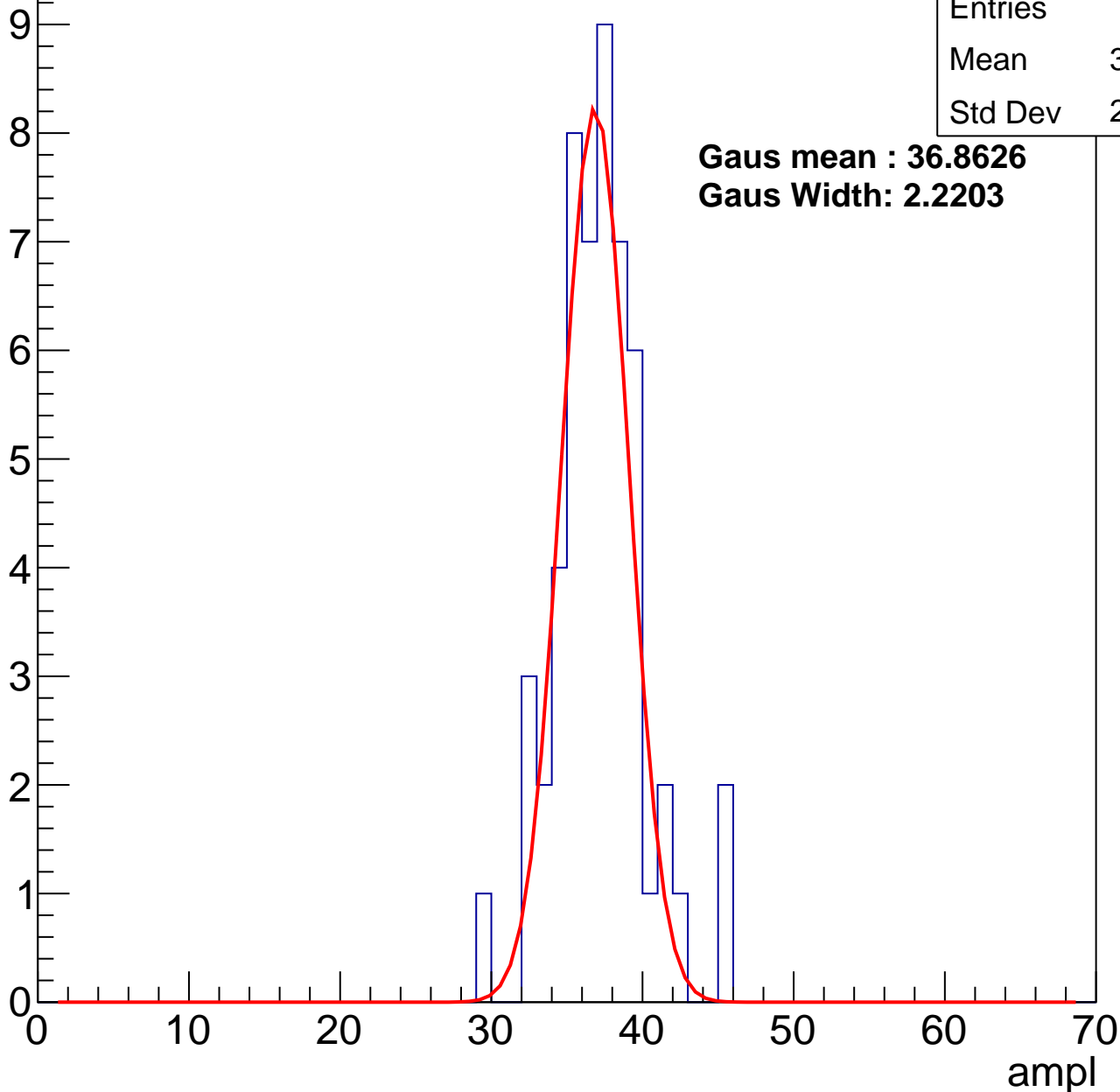
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	36.72
Std Dev	2.968

Gaus mean : 36.8626

Gaus Width: 2.2203



B0L002S, U18-ch14, adc2

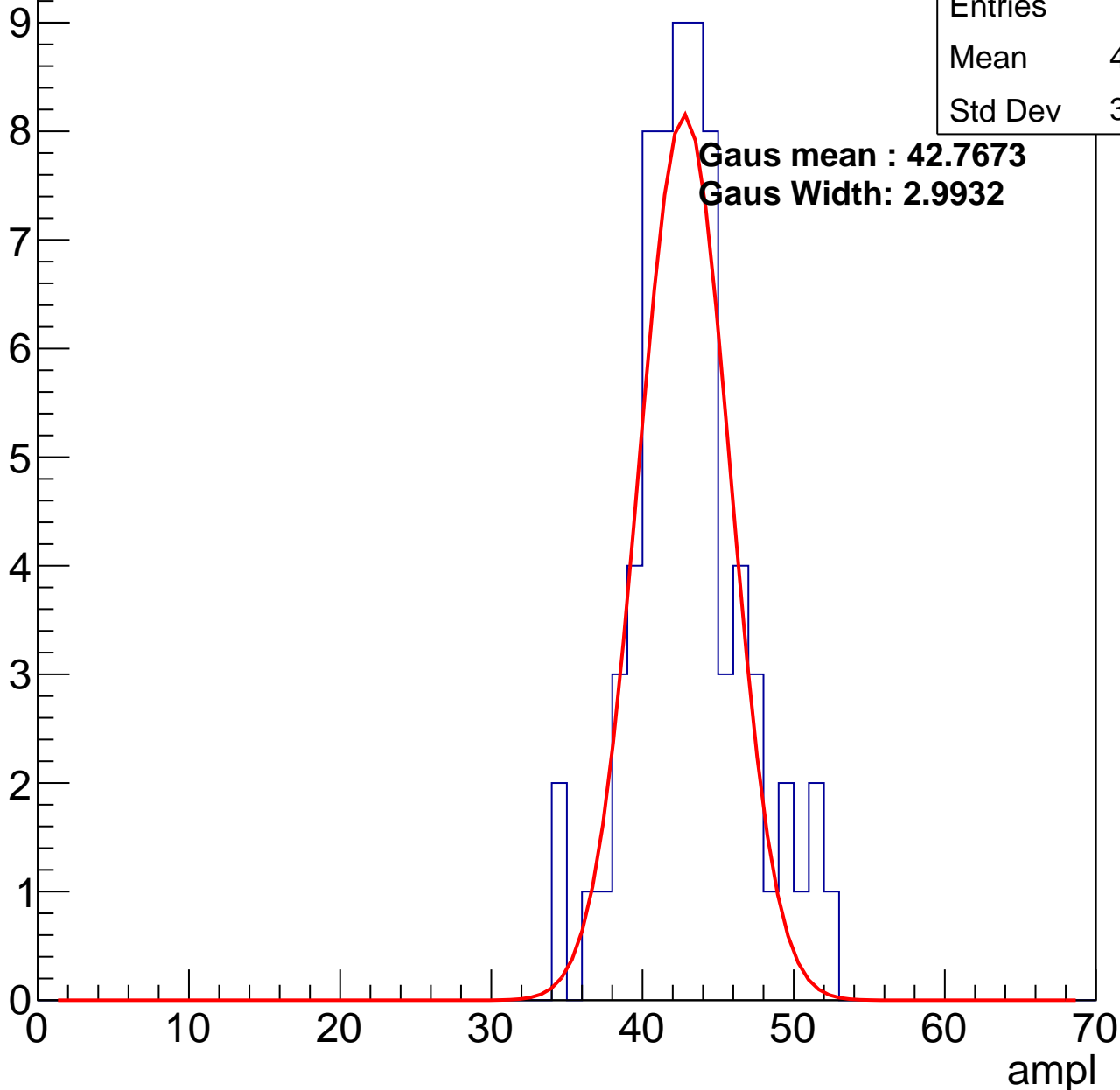
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.66
Std Dev	3.683

Gaus mean : 42.7673

Gaus Width: 2.9932

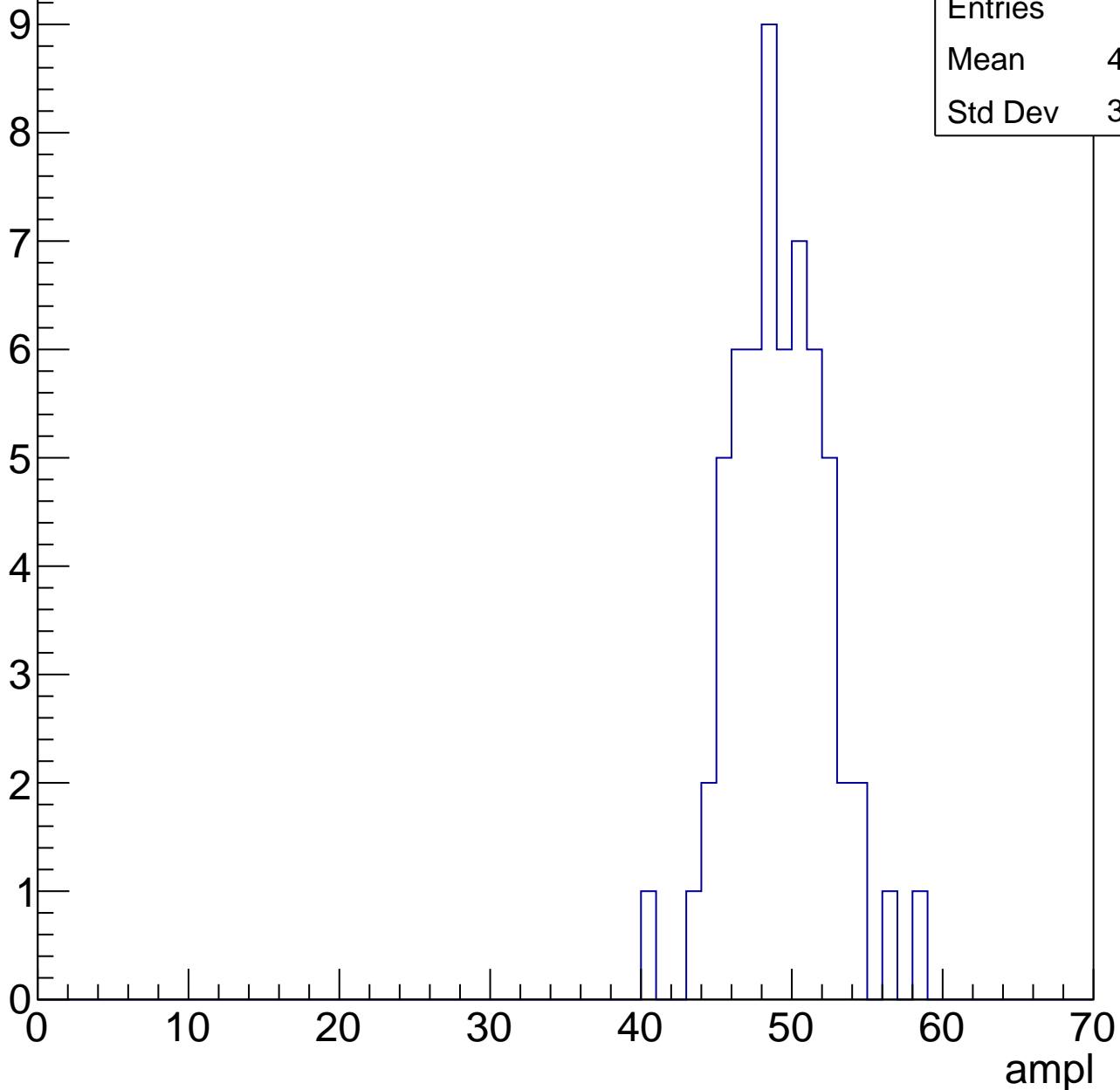


B0L002S, U18-ch14, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	48.73
Std Dev	3.209

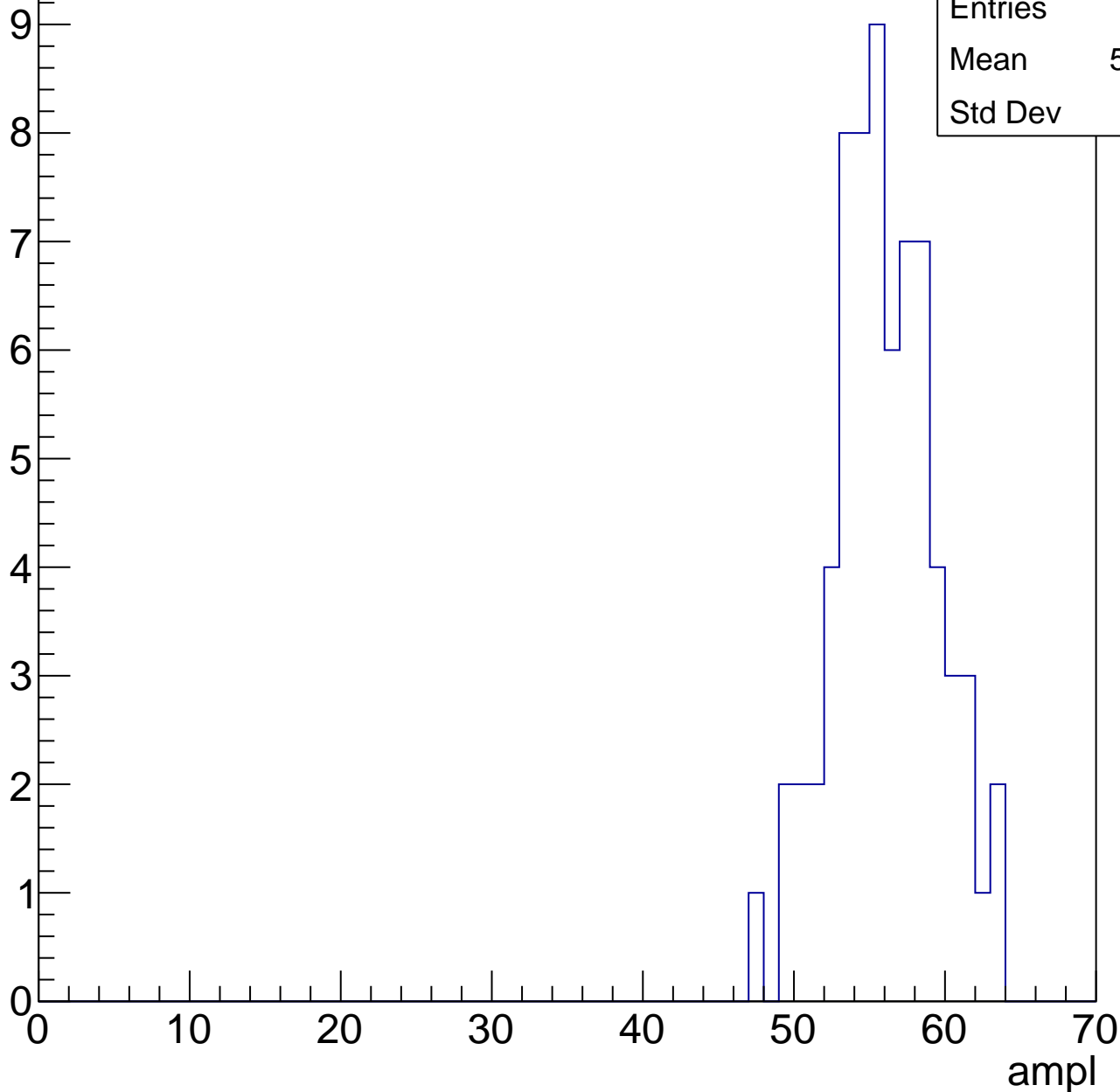


B0L002S, U18-ch14, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	55.57
Std Dev	3.39

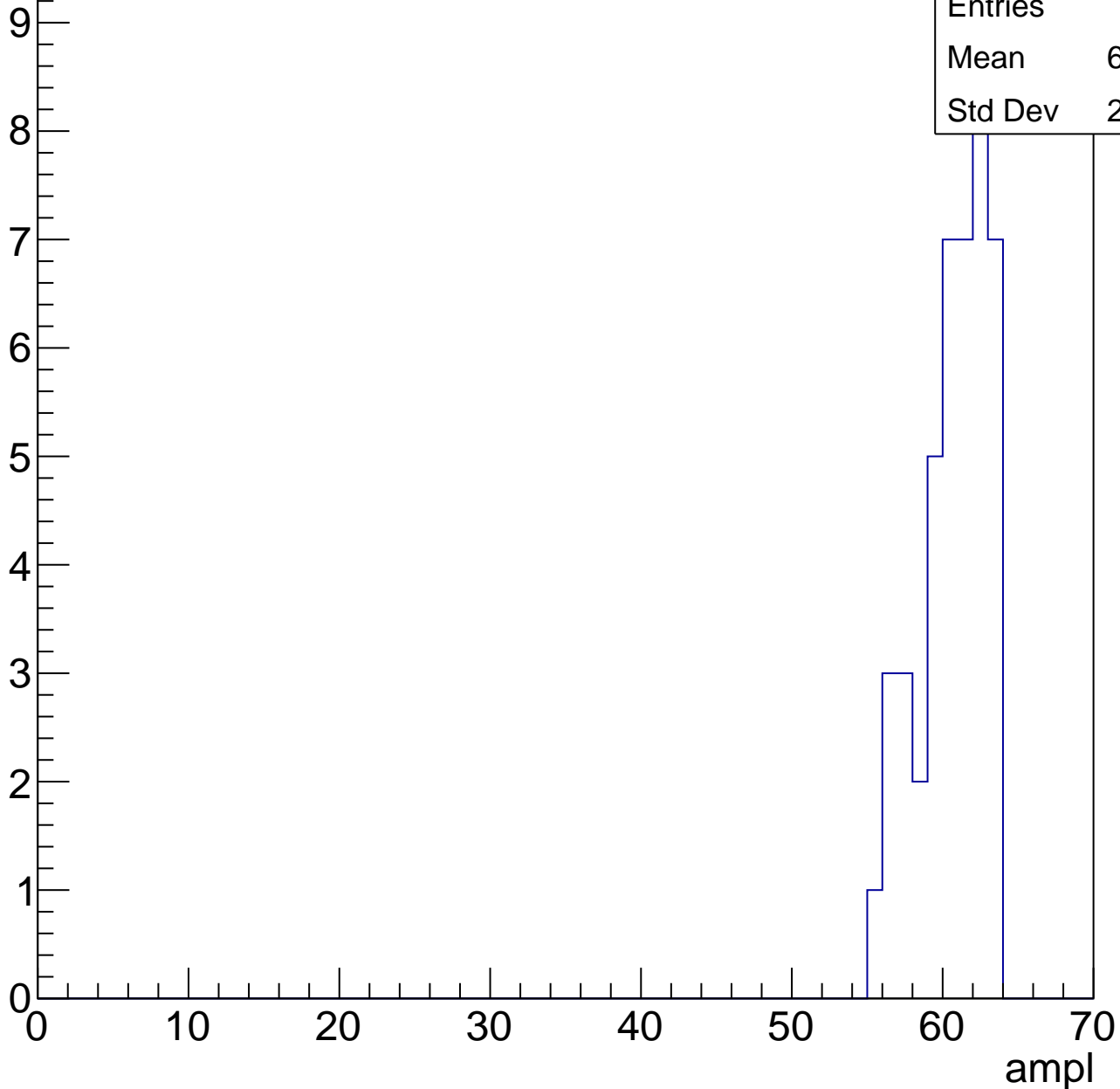


B0L002S, U18-ch14, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

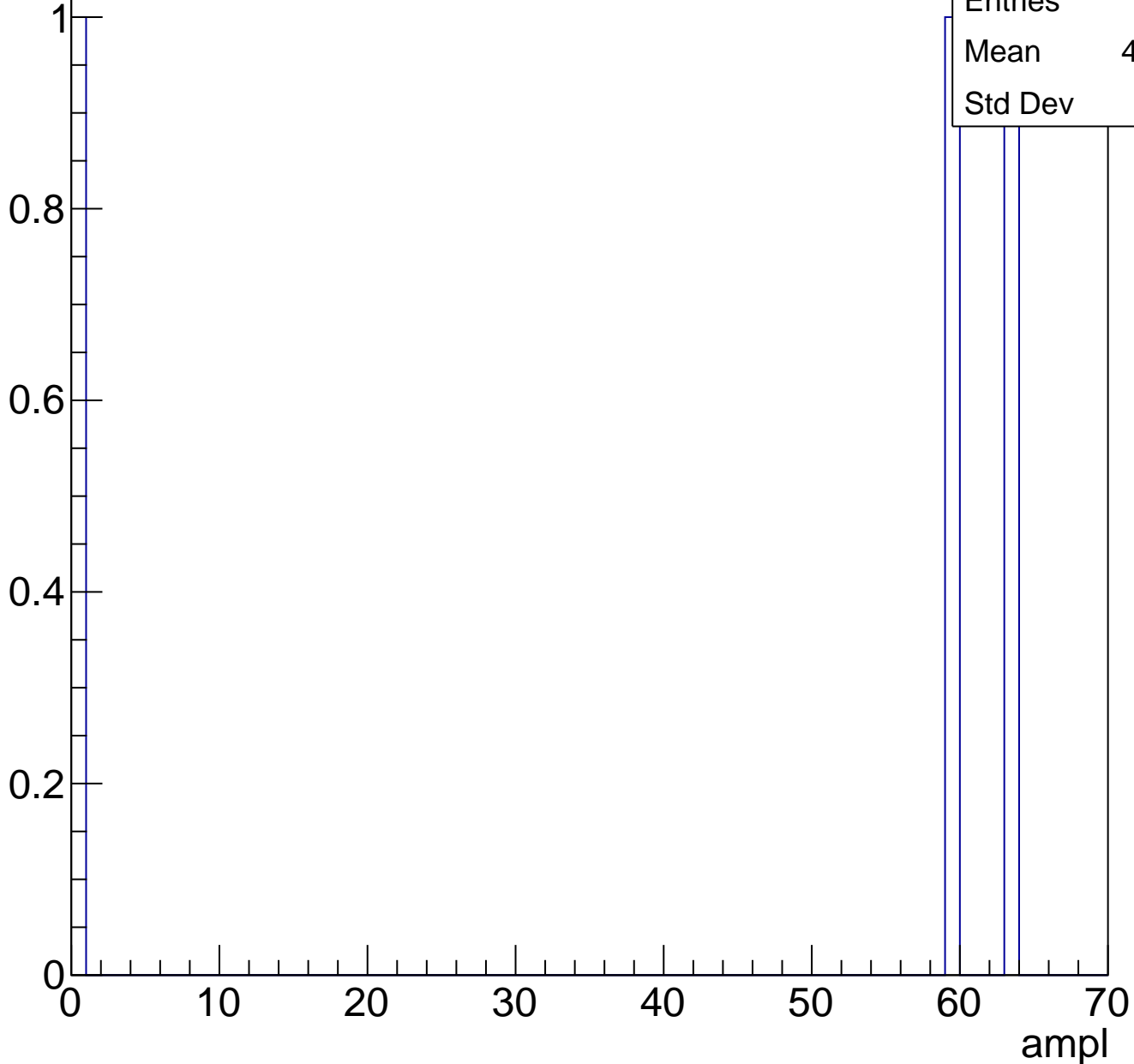
Entries	44
Mean	60.25
Std Dev	2.217



B0L002S, U18-ch14, adc6

calib_packv5_042523_0143.root, FC#8, port C1

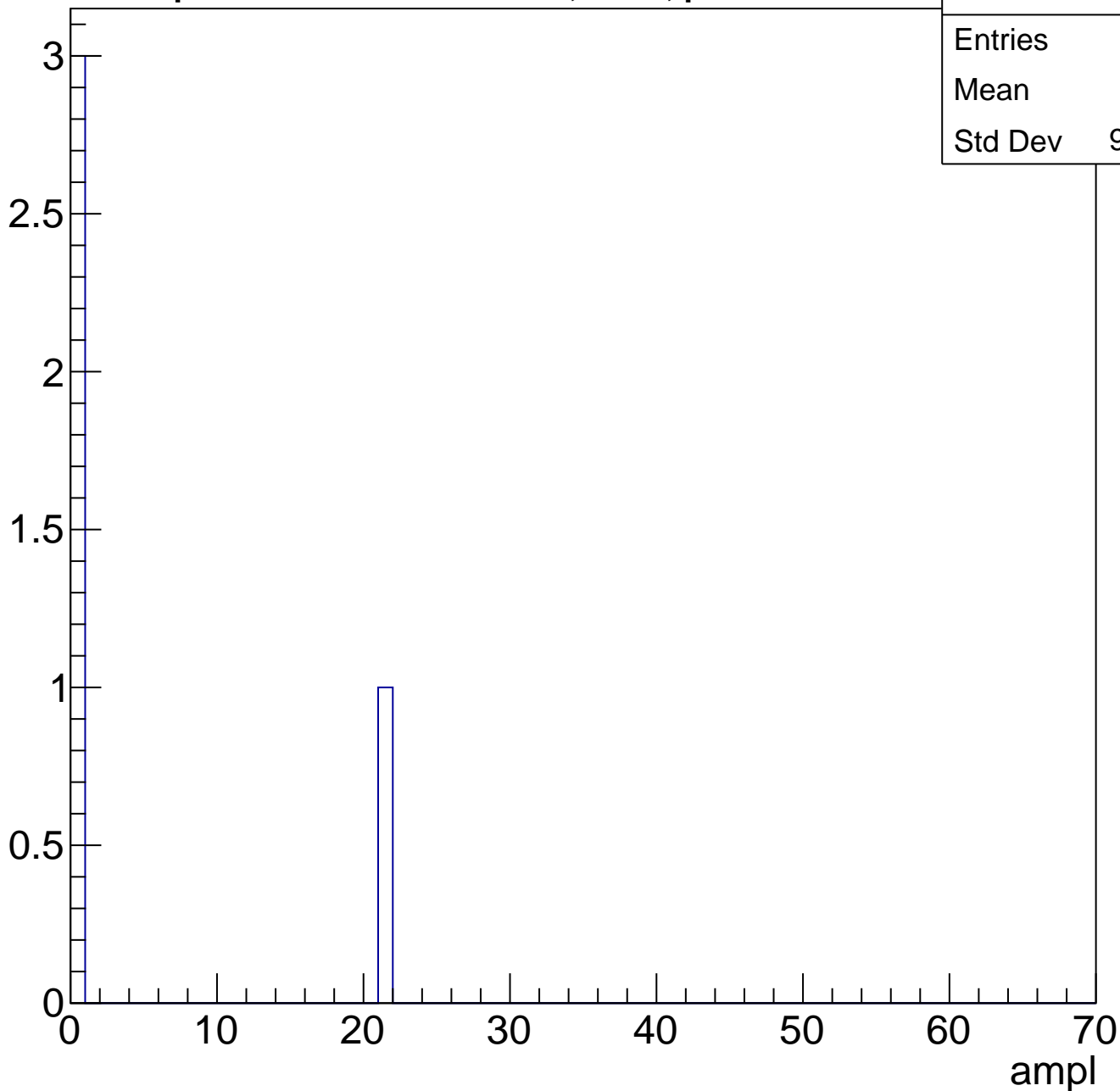
Entry



B0L002S, U18-ch14, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch15, adc0

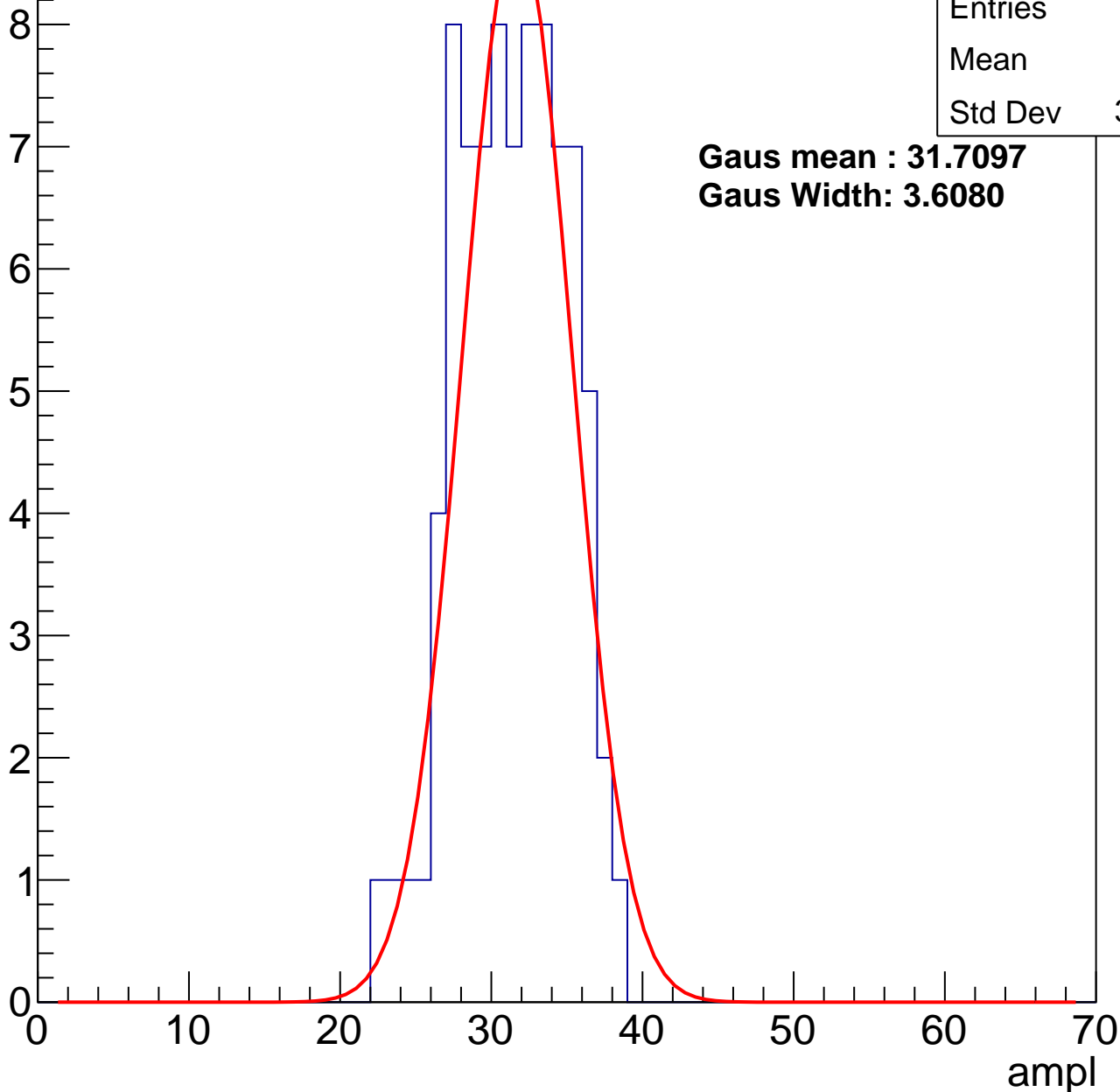
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.9
Std Dev	3.501

Gaus mean : 31.7097

Gaus Width: 3.6080



B0L002S, U18-ch15, adc1

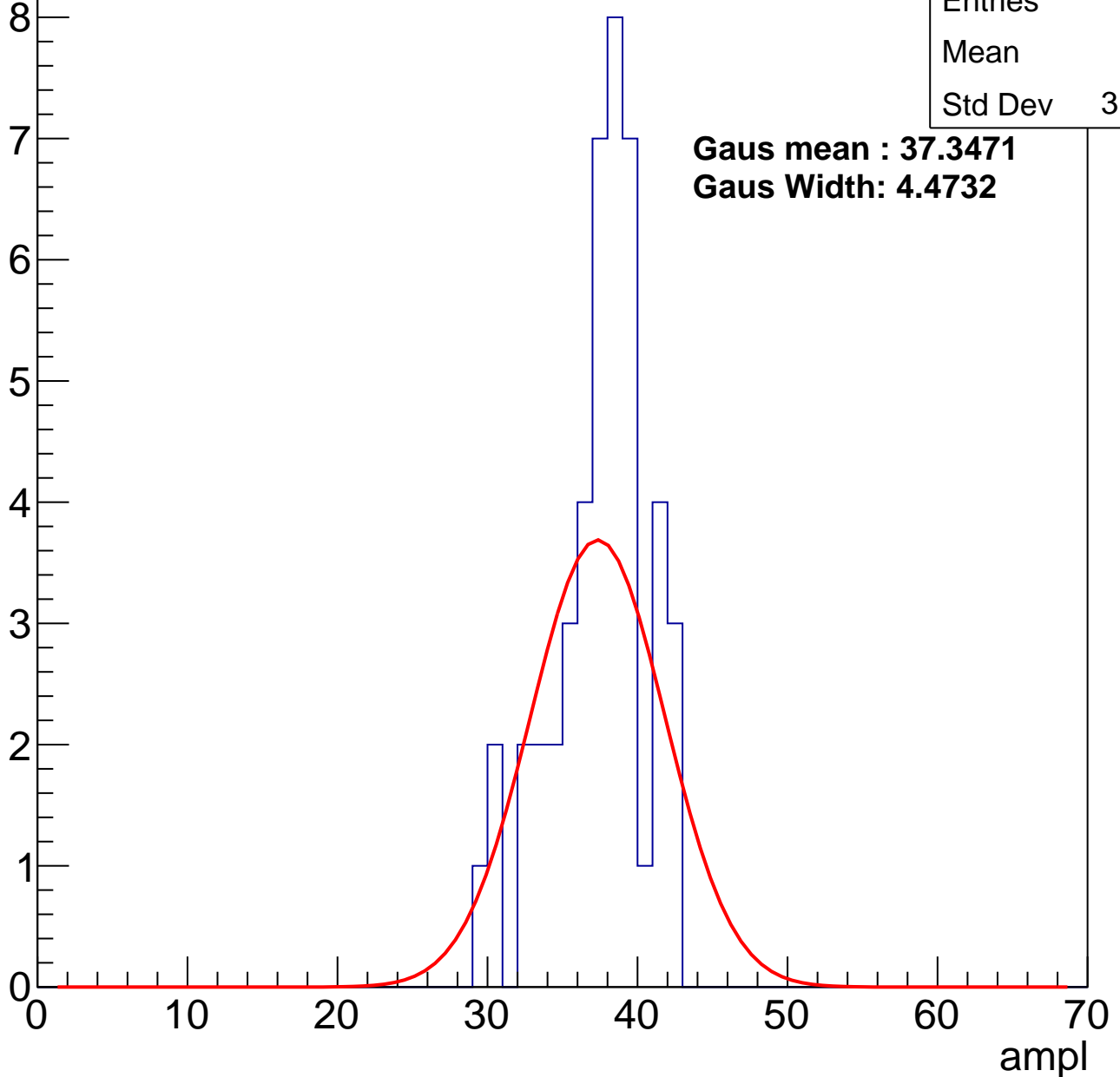
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	37
Std Dev	3.169

Gaus mean : 37.3471

Gaus Width: 4.4732



B0L002S, U18-ch15, adc2

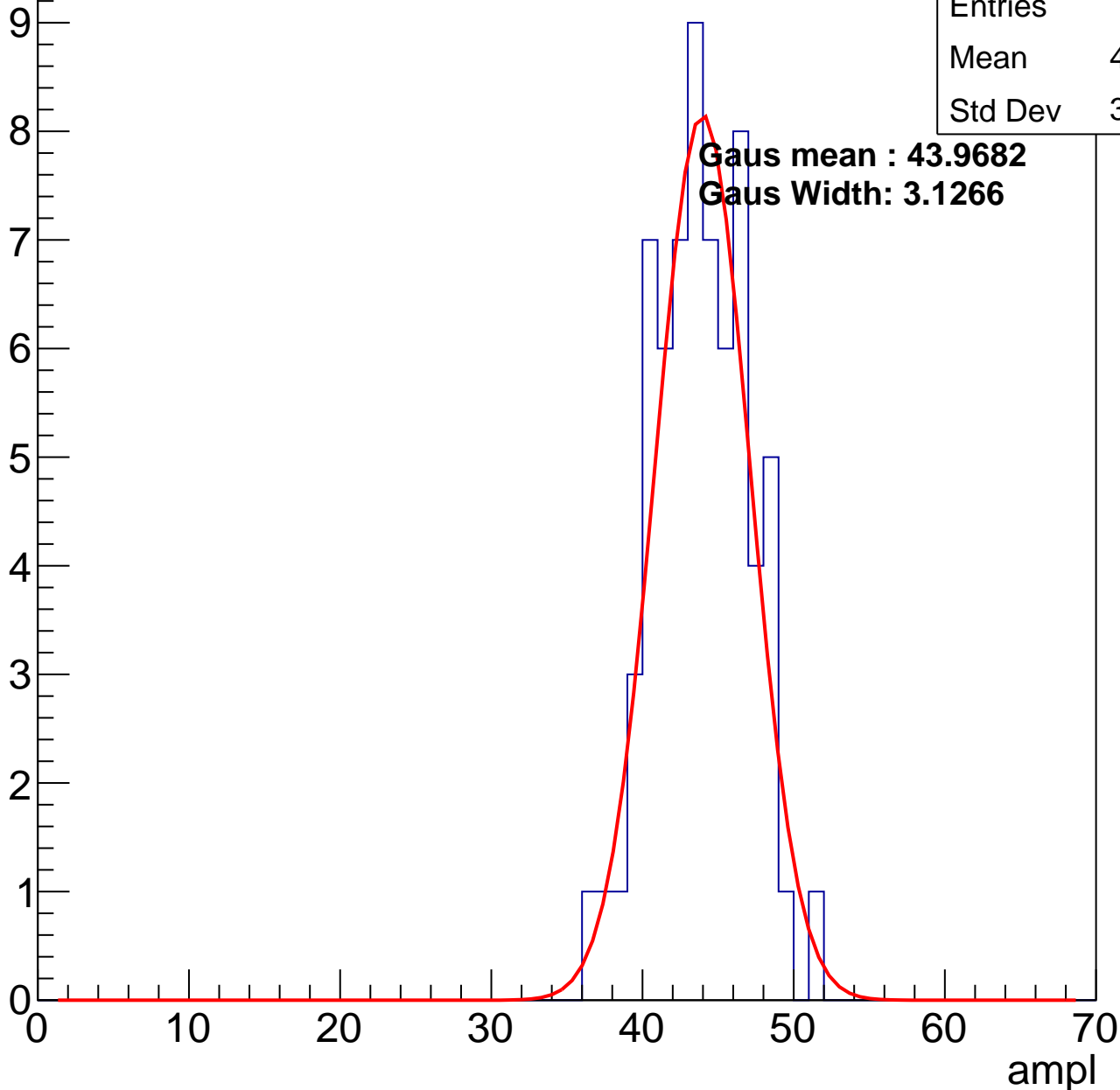
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.42
Std Dev	3.072

Gaus mean : 43.9682

Gaus Width: 3.1266

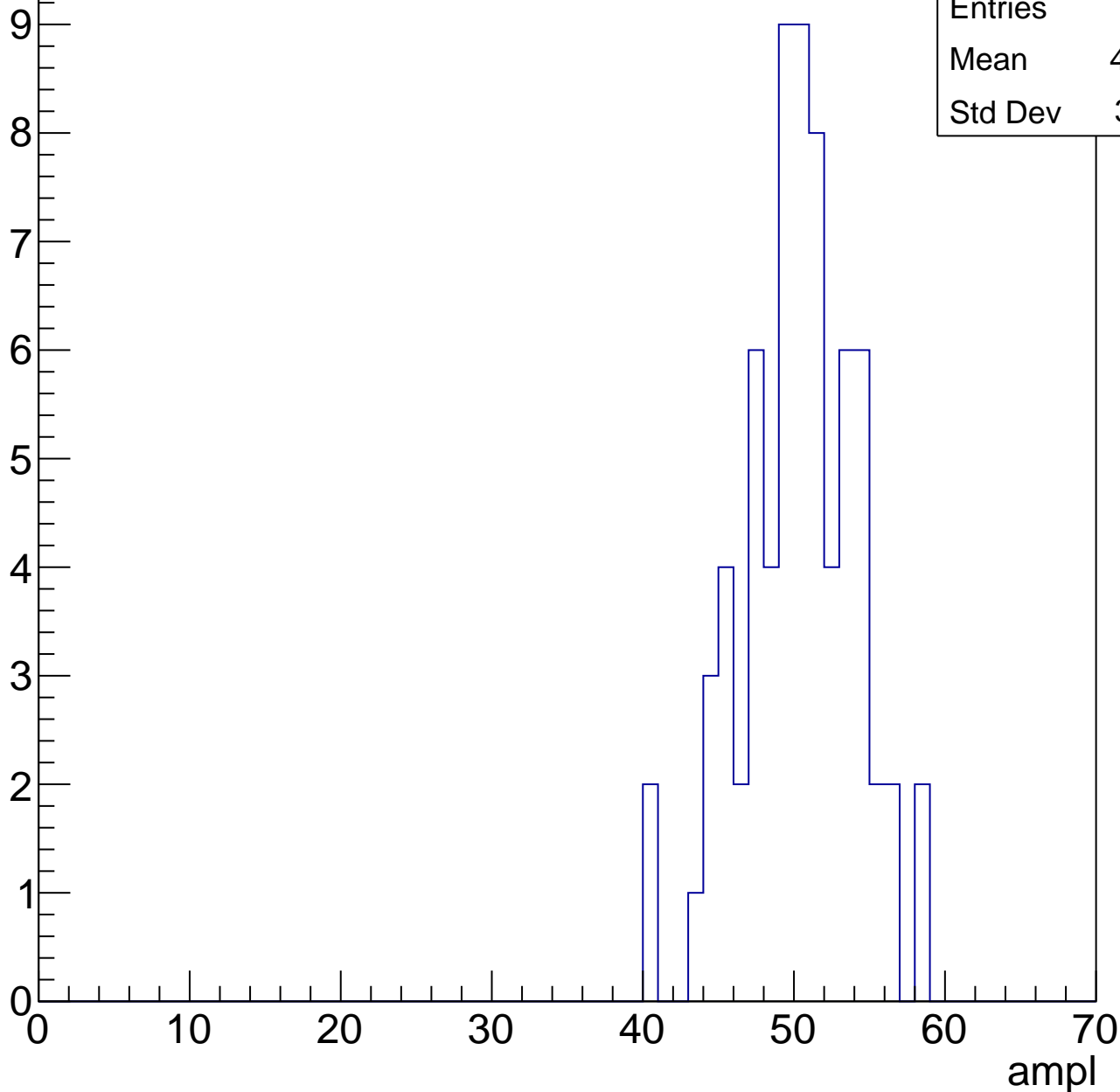


B0L002S, U18-ch15, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	49.83
Std Dev	3.761

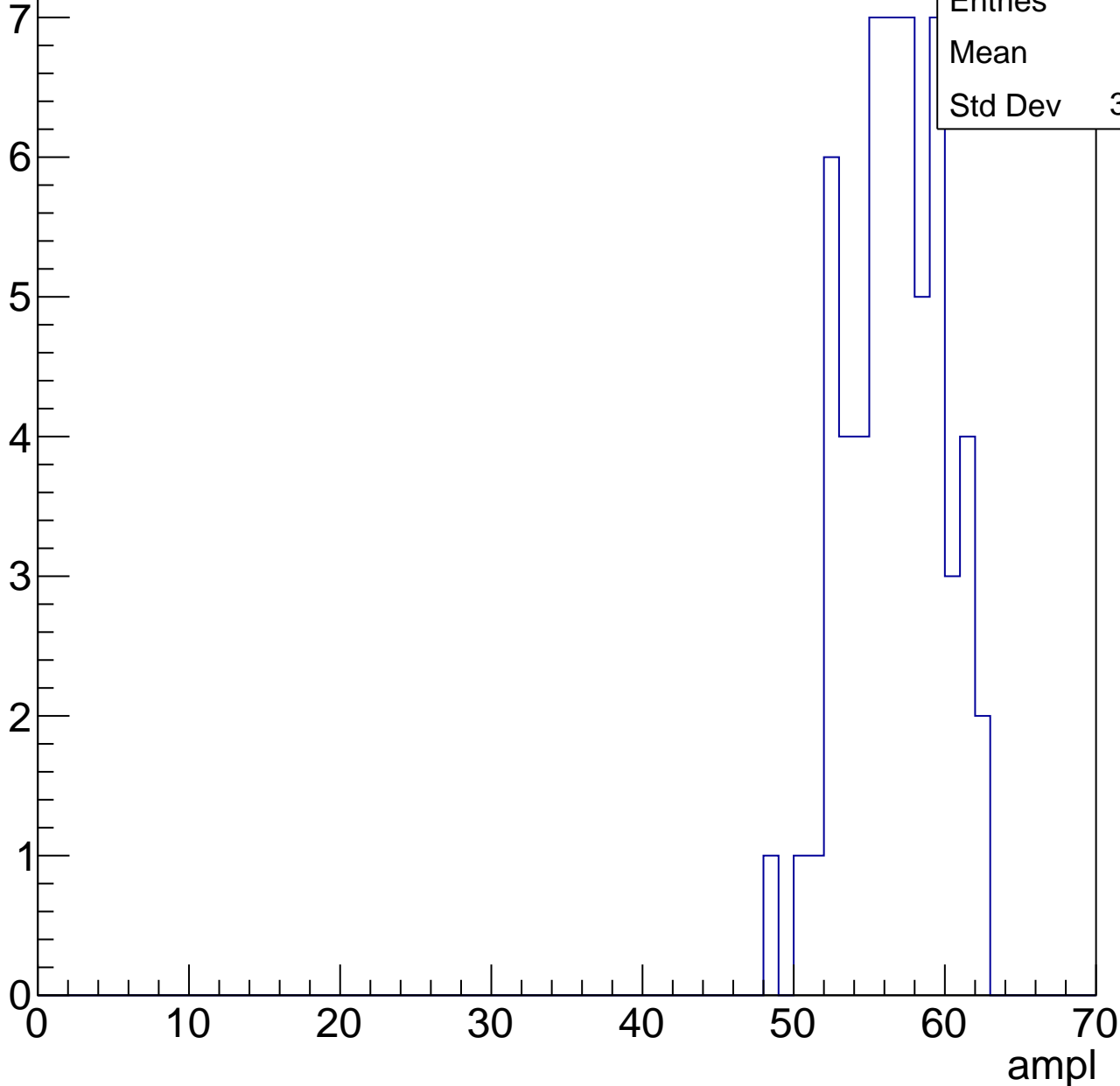


B0L002S, U18-ch15, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.2
Std Dev	3.156

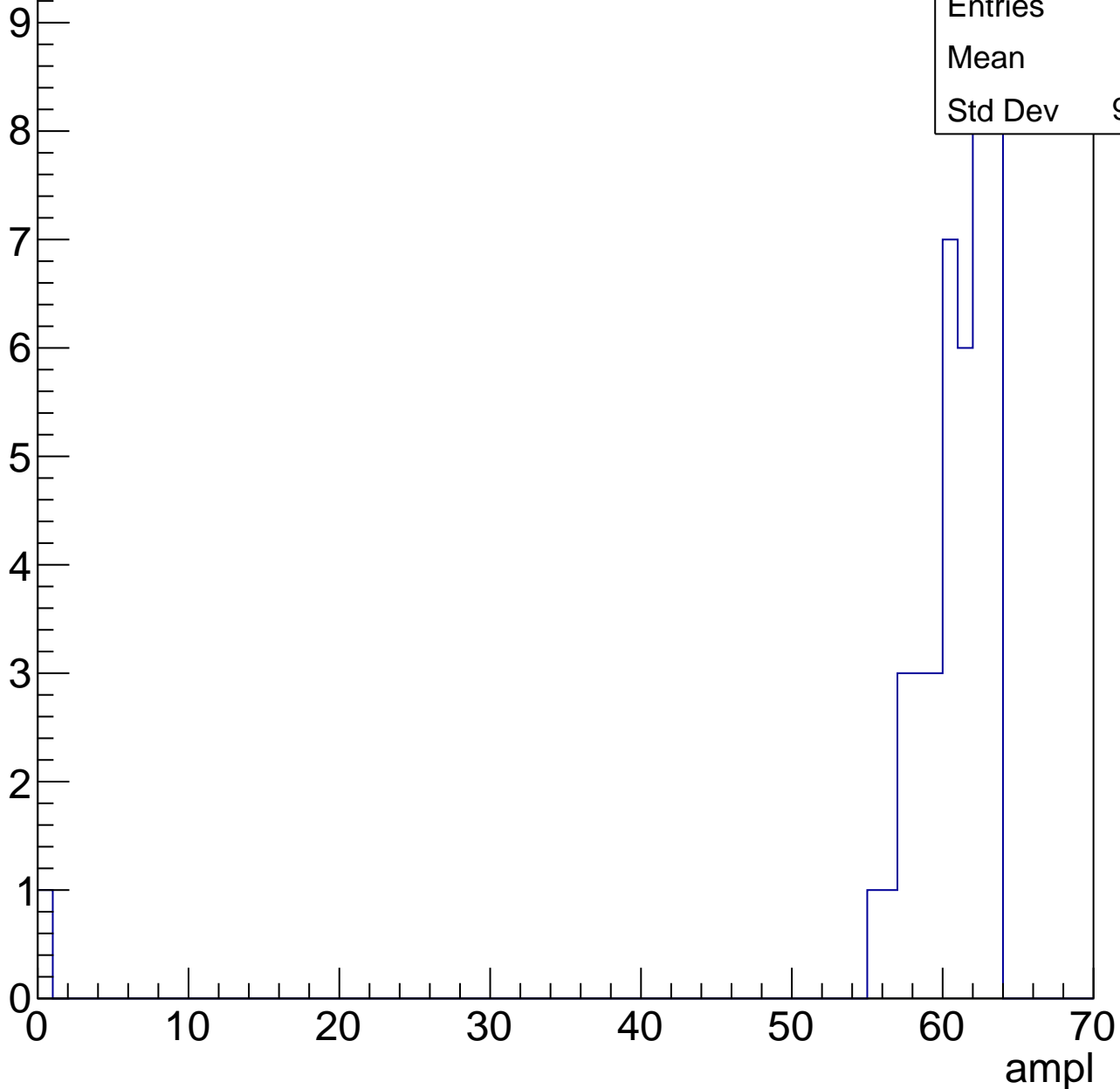


B0L002S, U18-ch15, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	59.1
Std Dev	9.471



B0L002S, U18-ch15, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch15, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch16, adc0

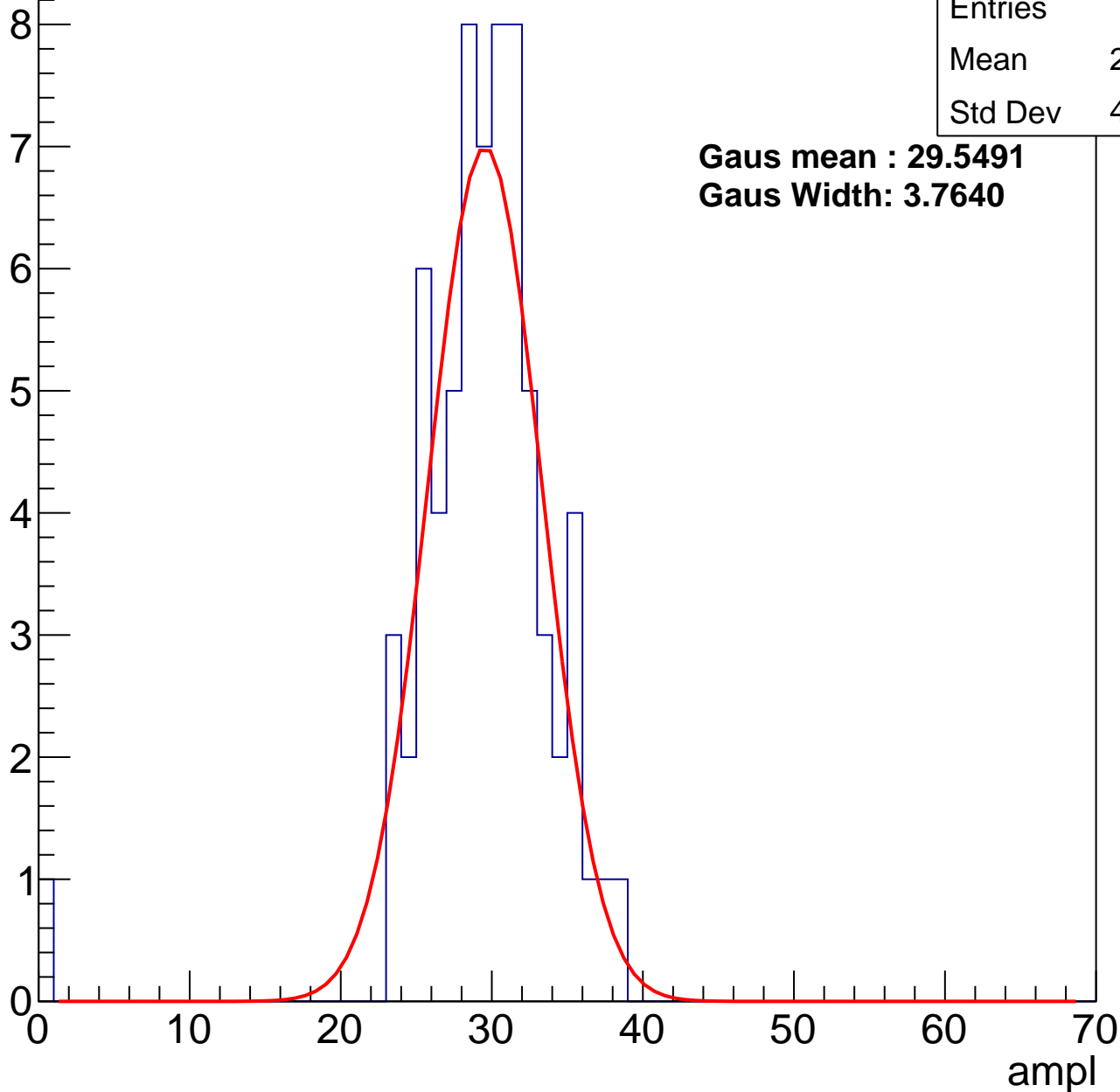
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	28.97
Std Dev	4.934

Gaus mean : 29.5491

Gaus Width: 3.7640



B0L002S, U18-ch16, adc1

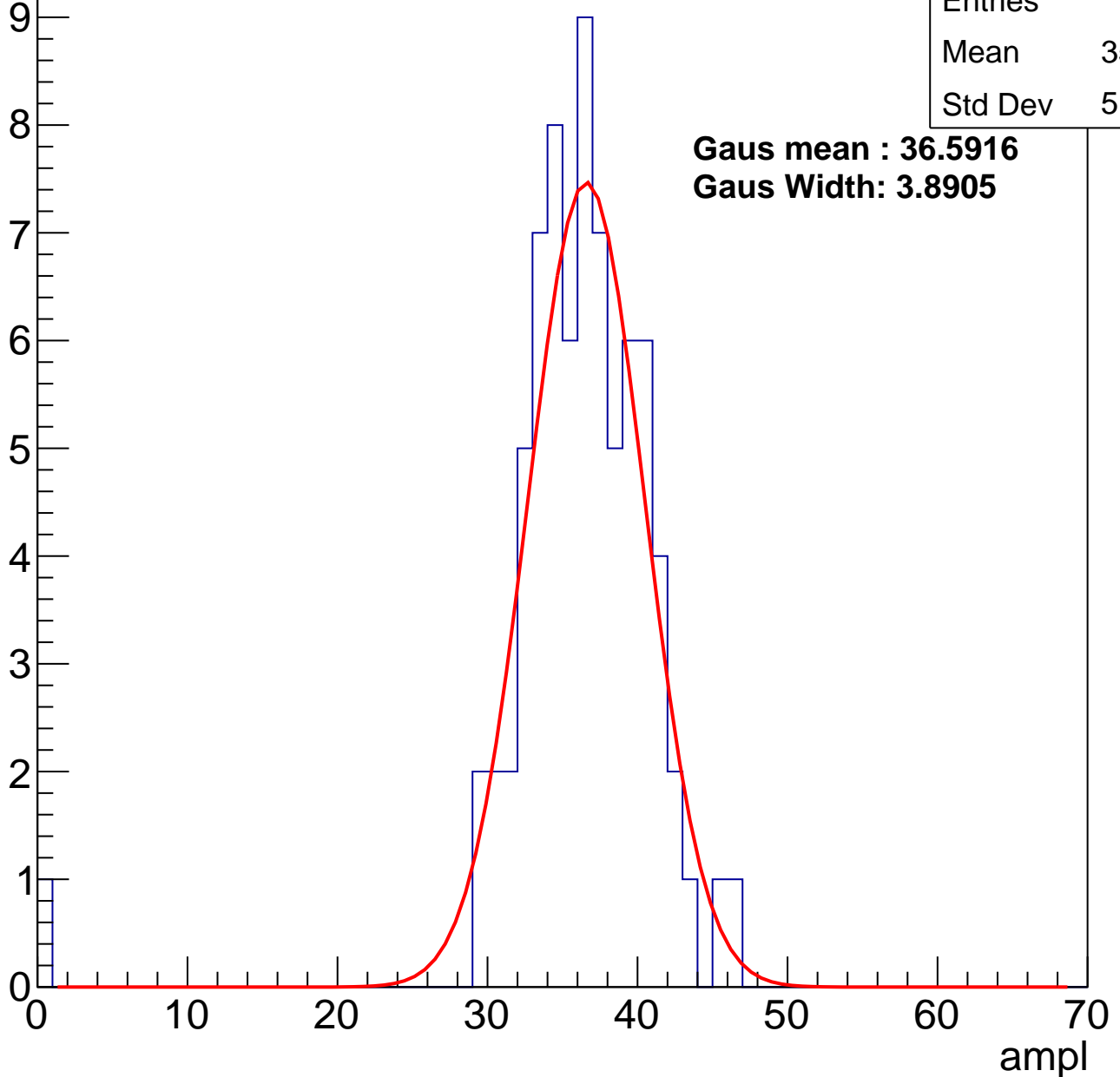
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	35.76
Std Dev	5.504

Gaus mean : 36.5916

Gaus Width: 3.8905



B0L002S, U18-ch16, adc2

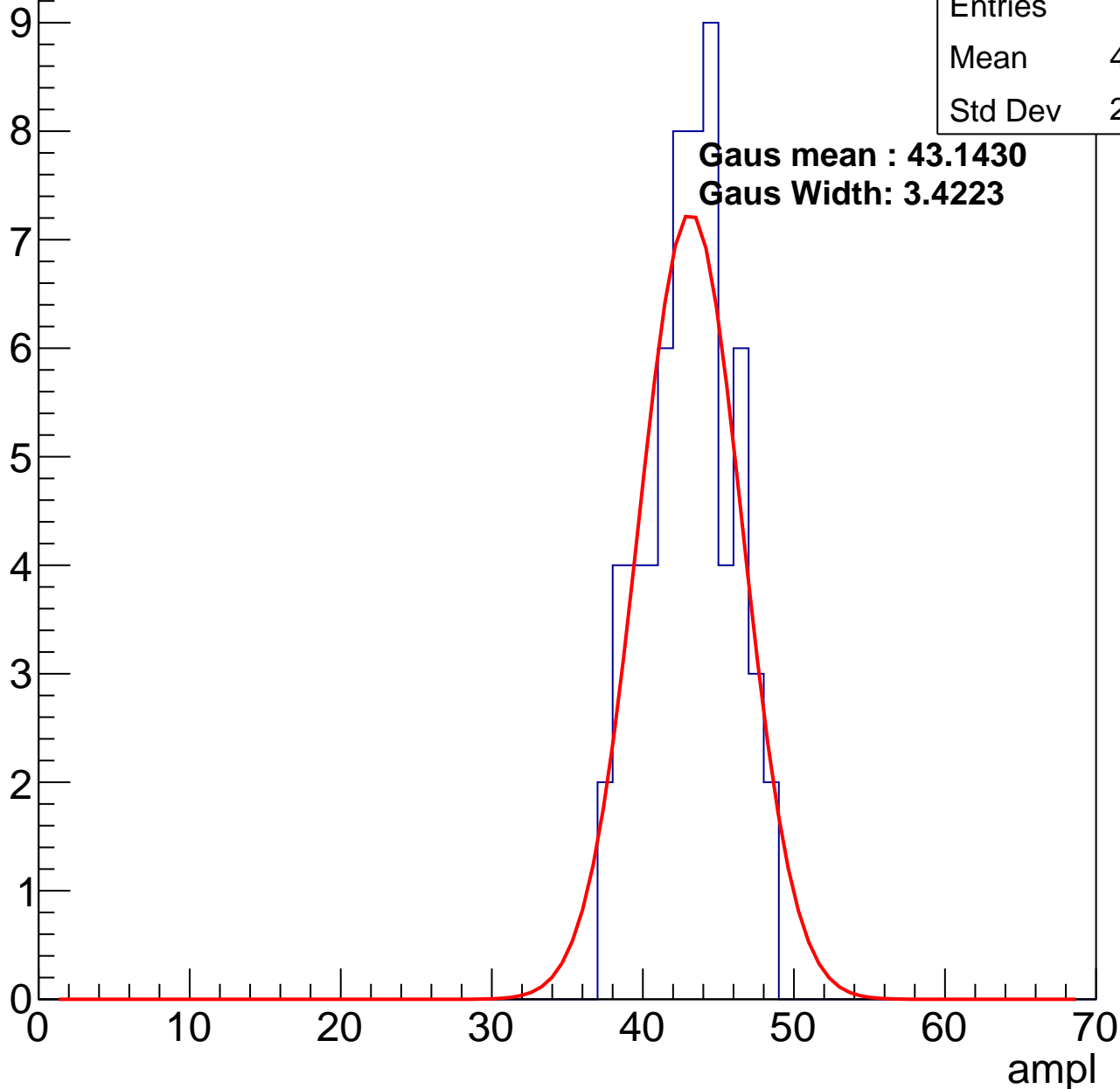
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.62
Std Dev	2.805

Gaus mean : 43.1430

Gaus Width: 3.4223



B0L002S, U18-ch16, adc3

calib_packv5_042523_0143.root, FC#8, port C1

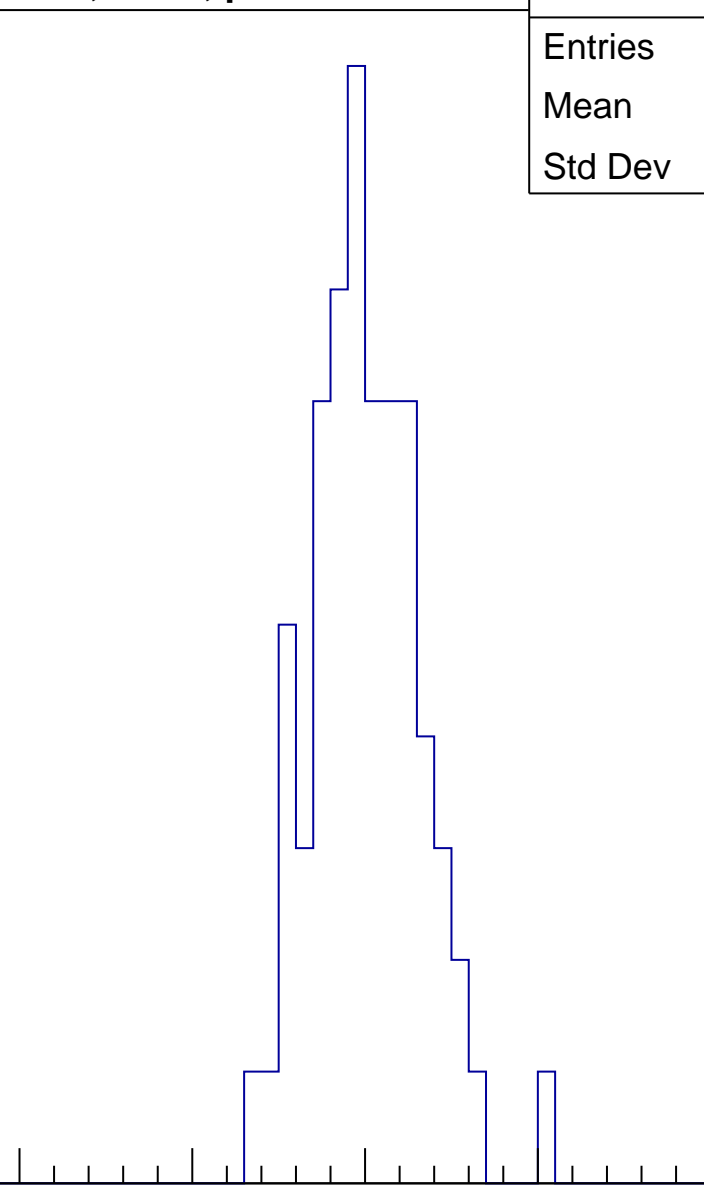
Entries	67
Mean	49.61
Std Dev	3.134

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

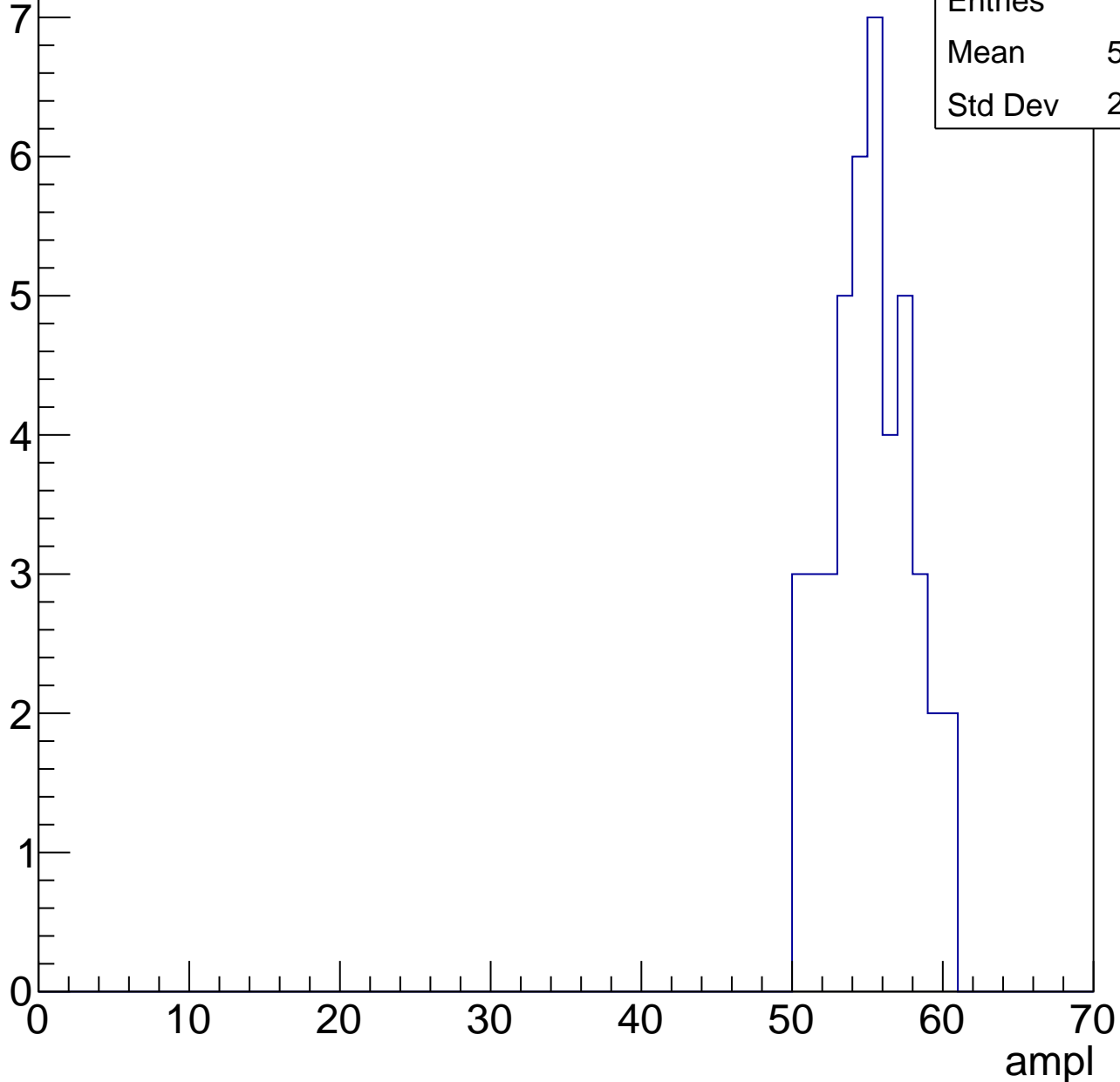
ampl



B0L002S, U18-ch16, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



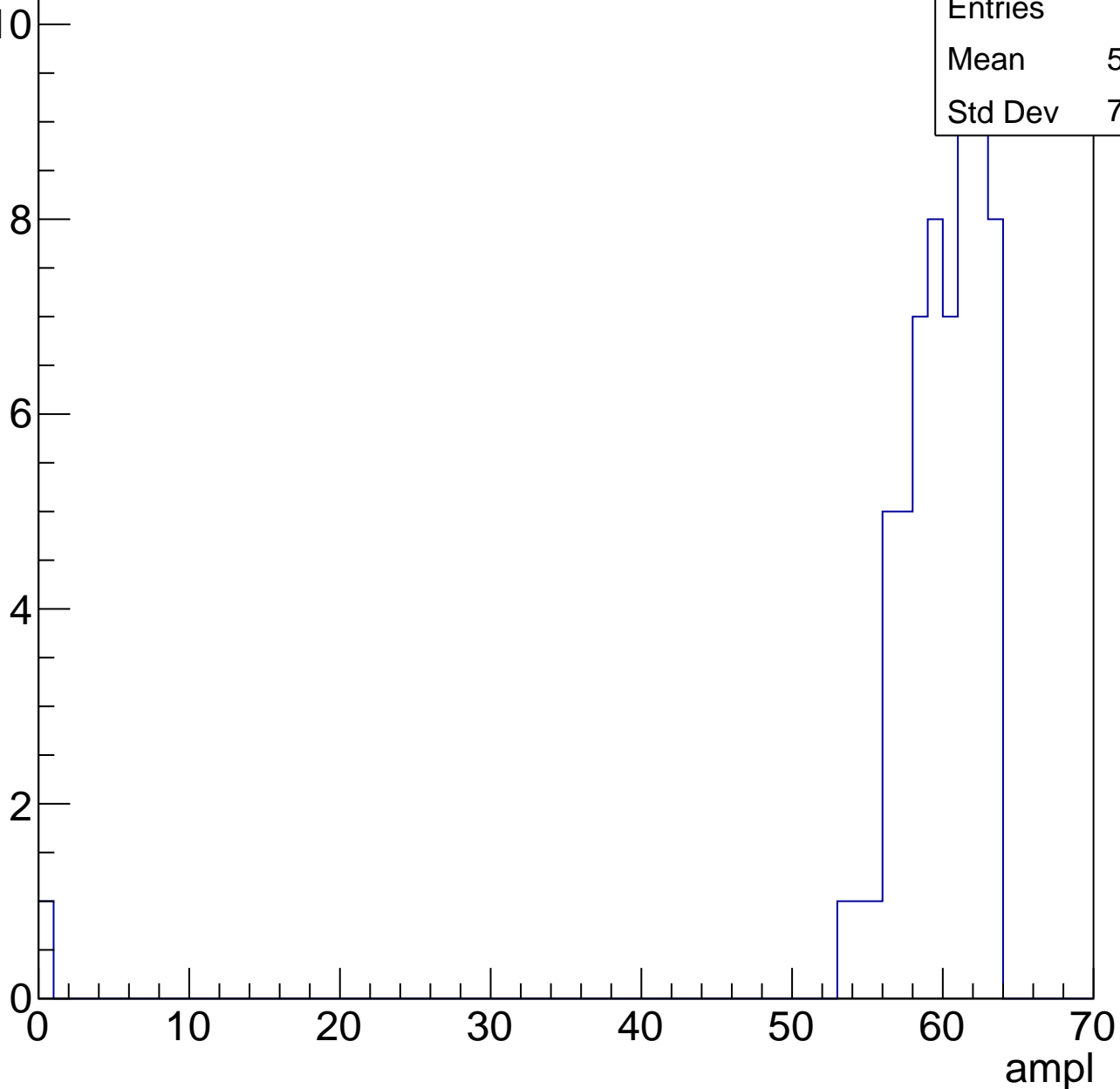
Entries	43
Mean	54.74
Std Dev	2.668

B0L002S, U18-ch16, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	58.68
Std Dev	7.847



B0L002S, U18-ch16, adc6

calib_packv5_042523_0143.root, FC#8, port C1

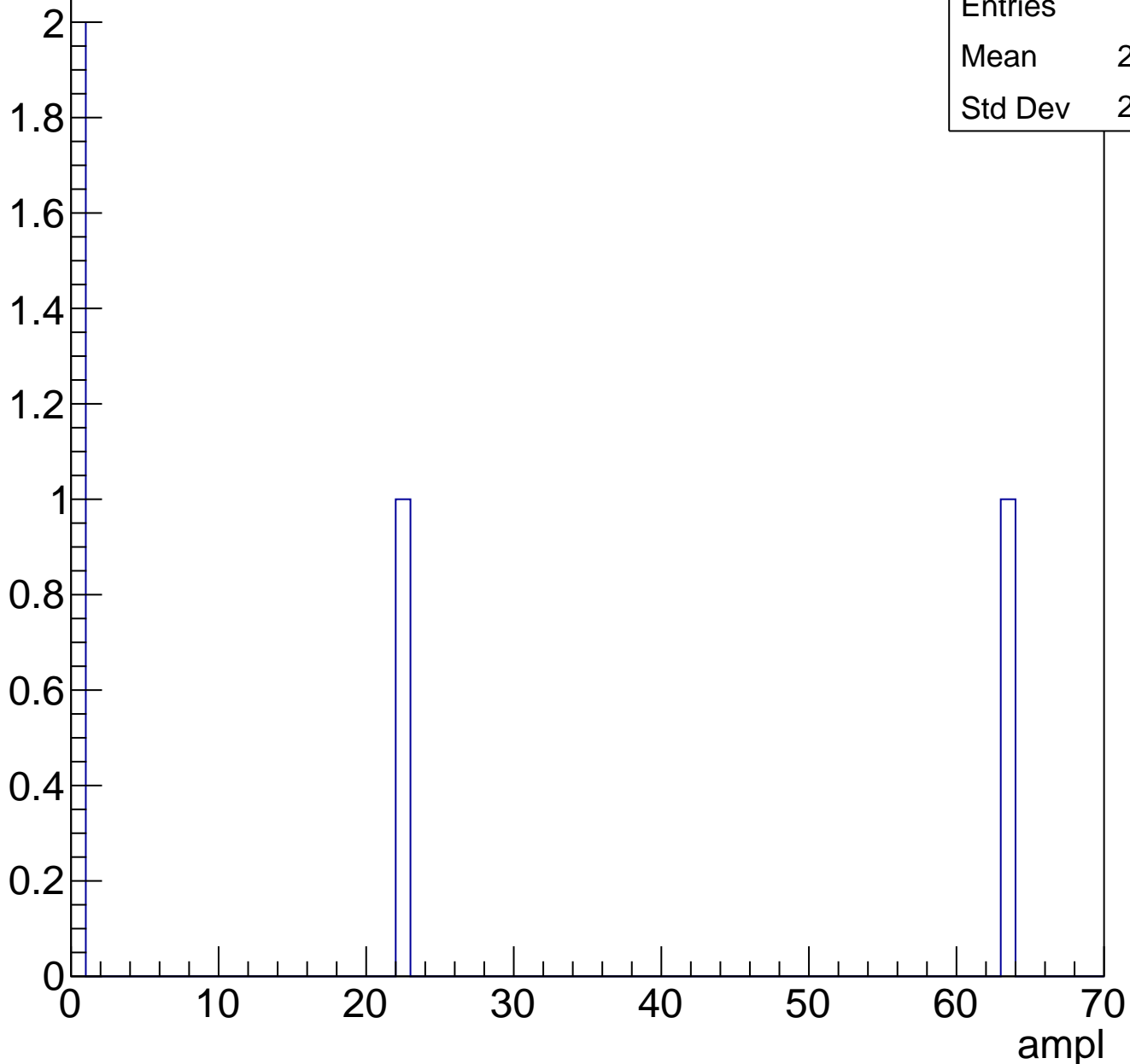
Entry



B0L002S, U18-ch16, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	21.25
Std Dev	25.72

B0L002S, U18-ch17, adc0

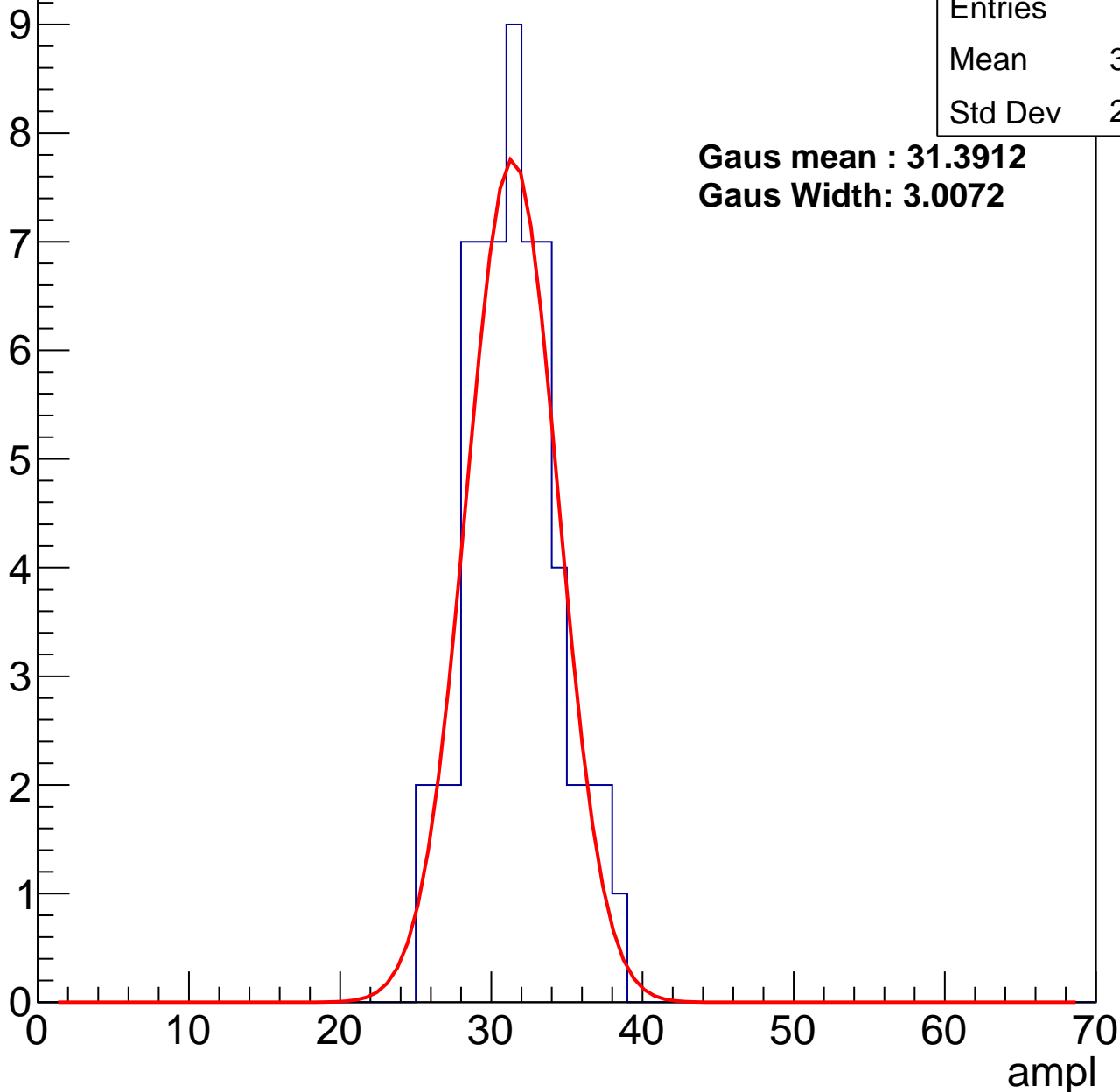
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	30.97
Std Dev	2.936

Gaus mean : 31.3912

Gaus Width: 3.0072



B0L002S, U18-ch17, adc1

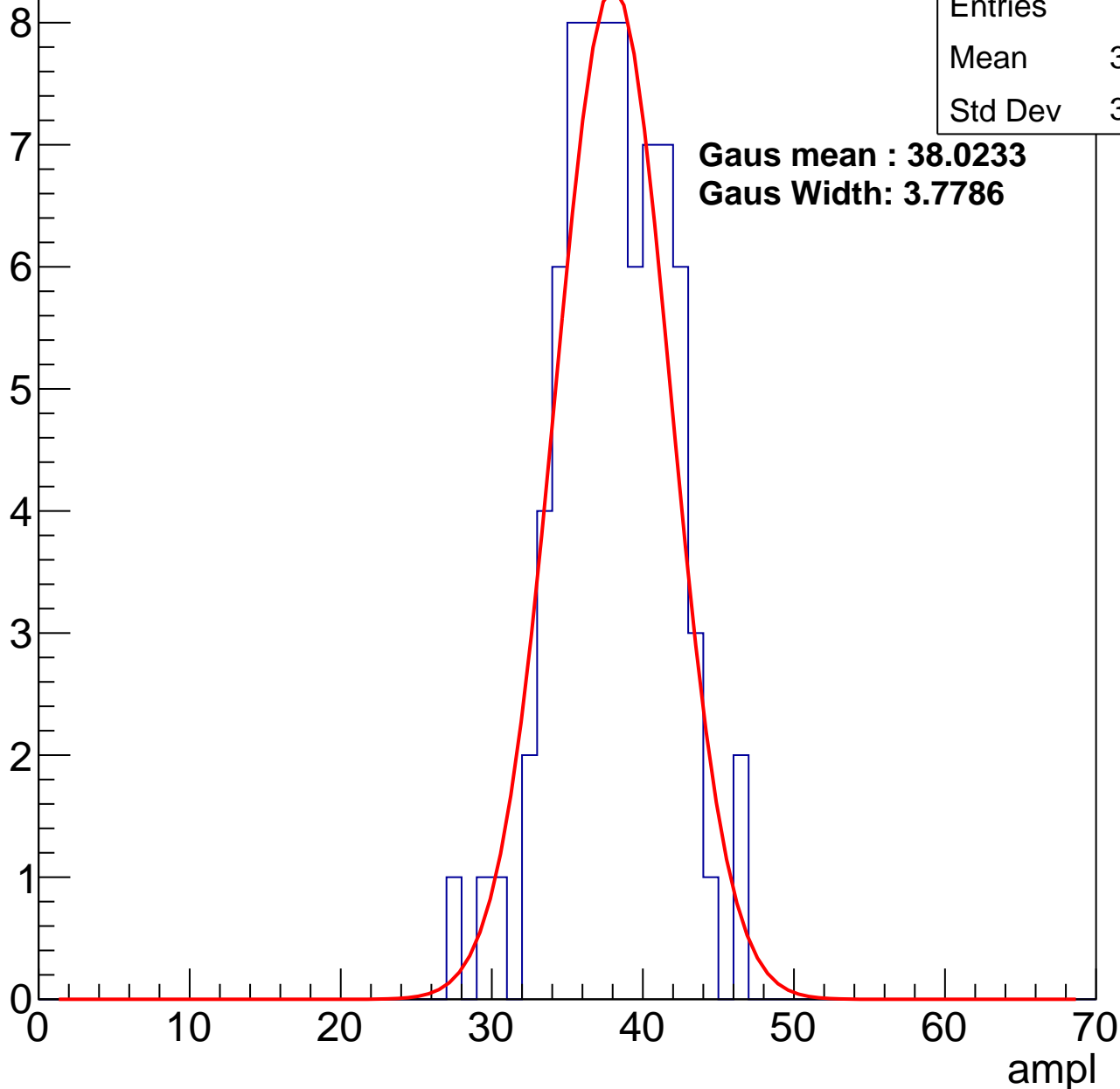
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.62
Std Dev	3.674

Gaus mean : 38.0233

Gaus Width: 3.7786



B0L002S, U18-ch17, adc2

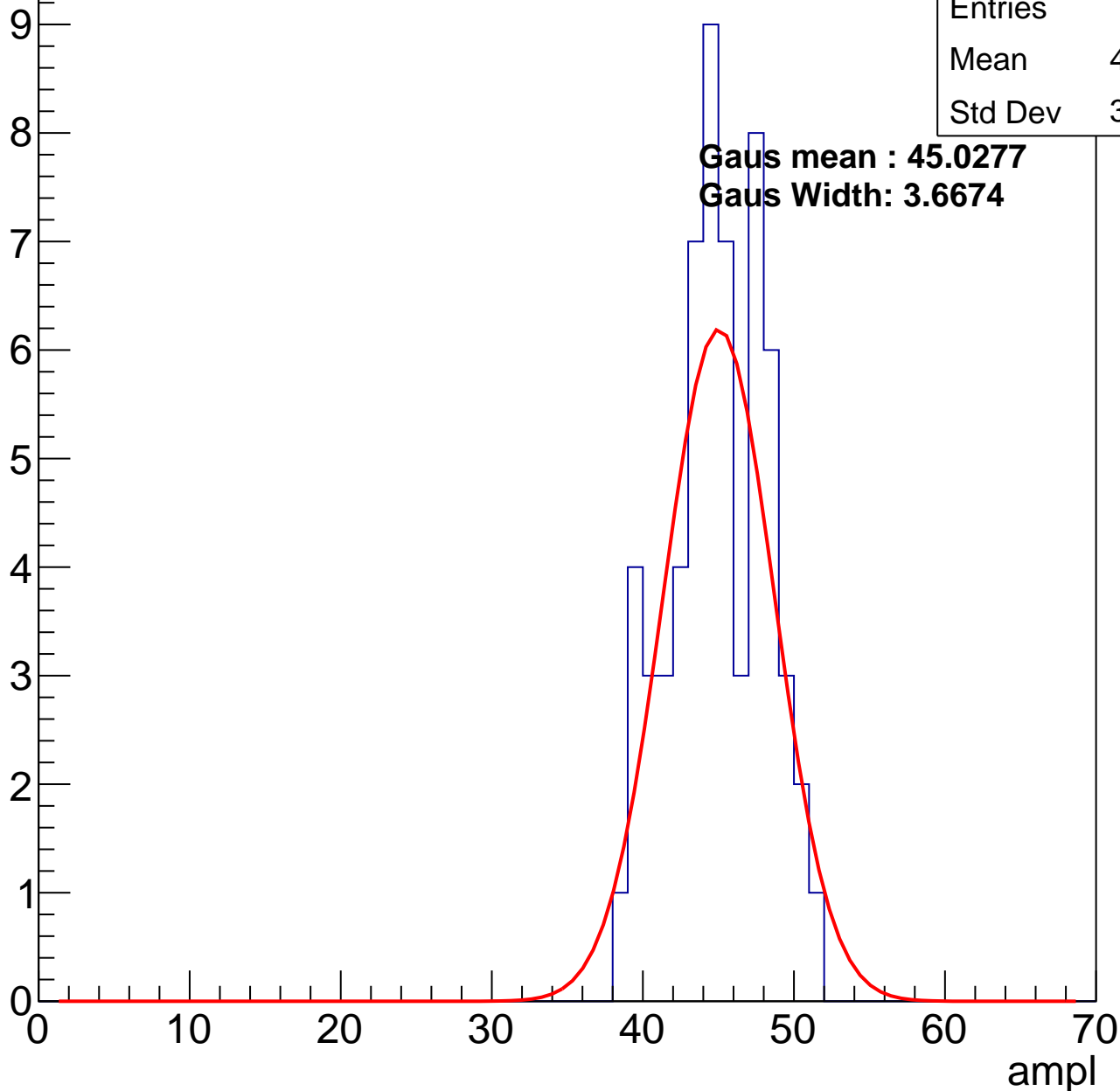
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.54
Std Dev	3.134

Gaus mean : 45.0277

Gaus Width: 3.6674

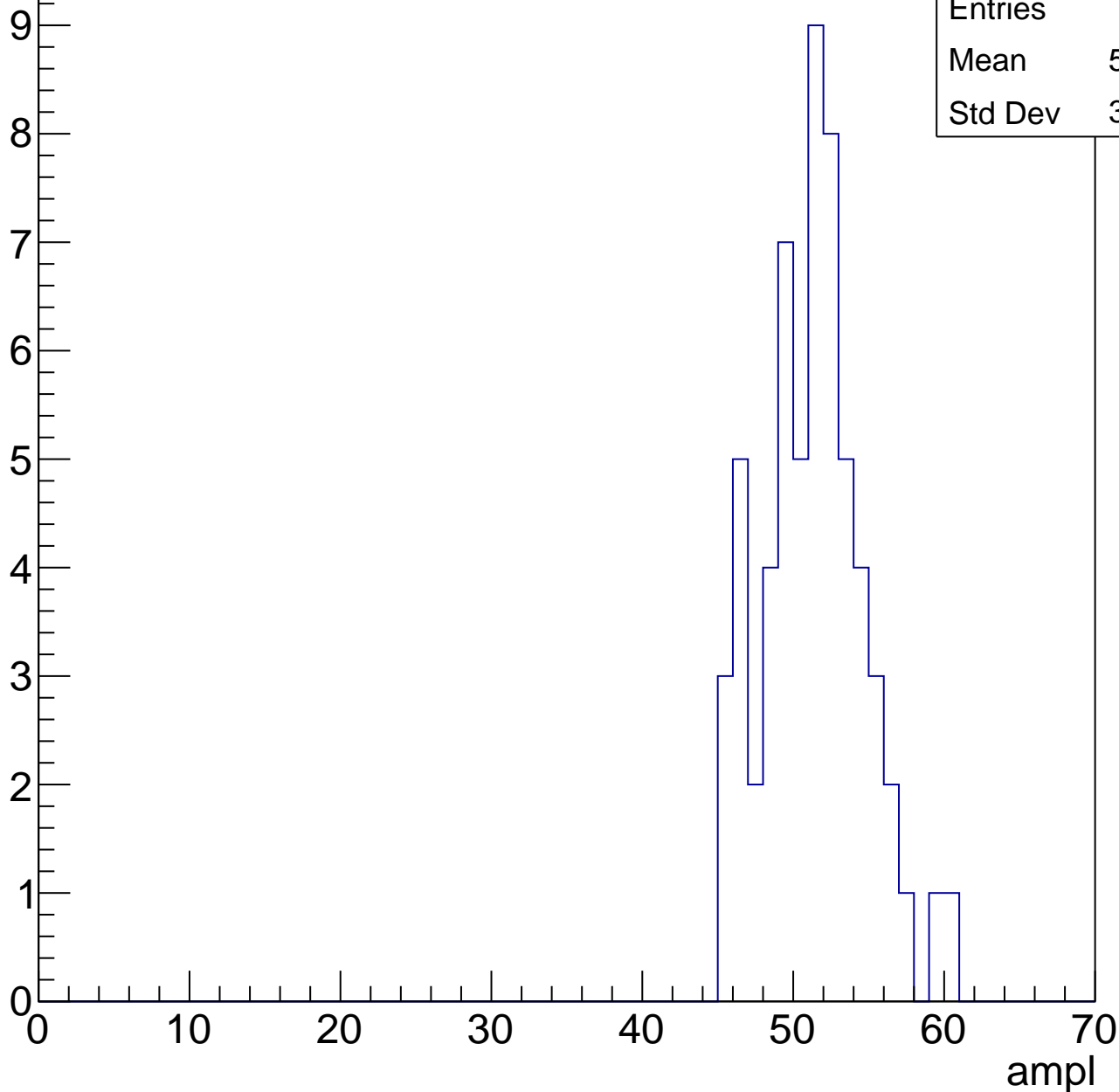


B0L002S, U18-ch17, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

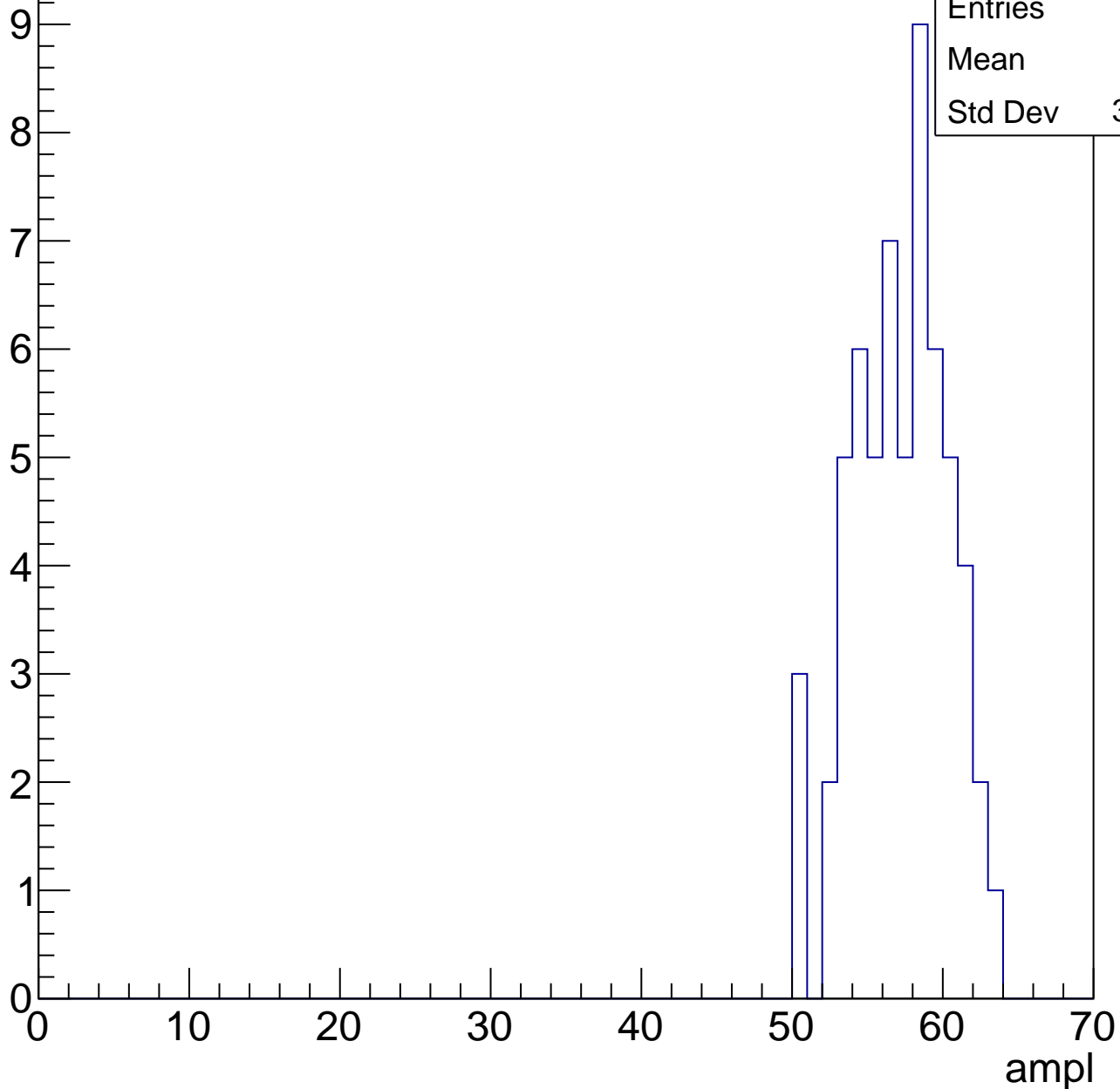
Entries	60
Mean	50.88
Std Dev	3.357



B0L002S, U18-ch17, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

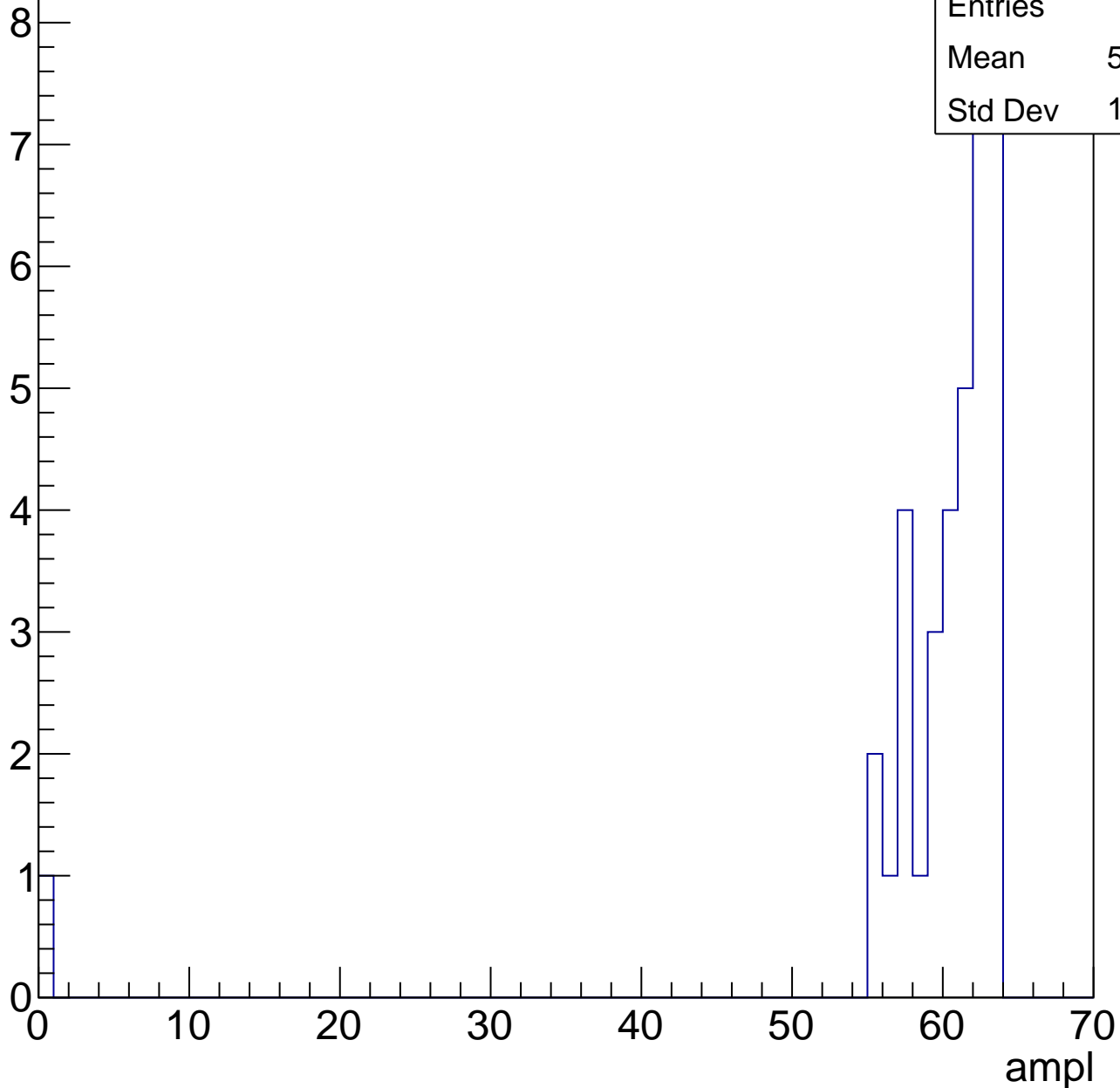


B0L002S, U18-ch17, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.76
Std Dev	10.08



B0L002S, U18-ch17, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch17, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	25
Std Dev	0

ampl

0 10 20 30 40 50 60 70

B0L002S, U18-ch18, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	29.73
Std Dev	6.069

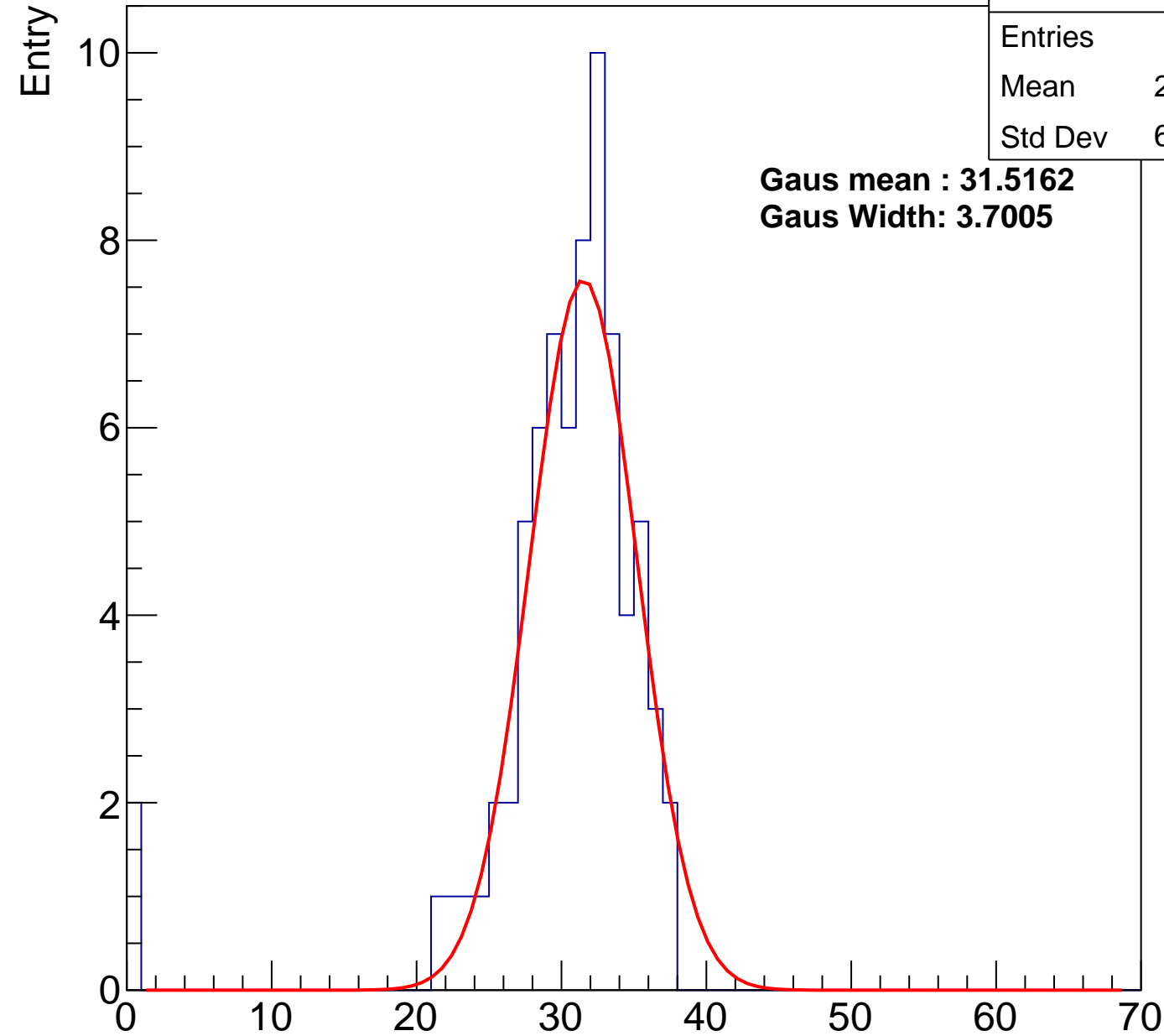
Gaus mean : 31.5162

Gaus Width: 3.7005

Entry

10
8
6
4
2
0

ampl



B0L002S, U18-ch18, adc1

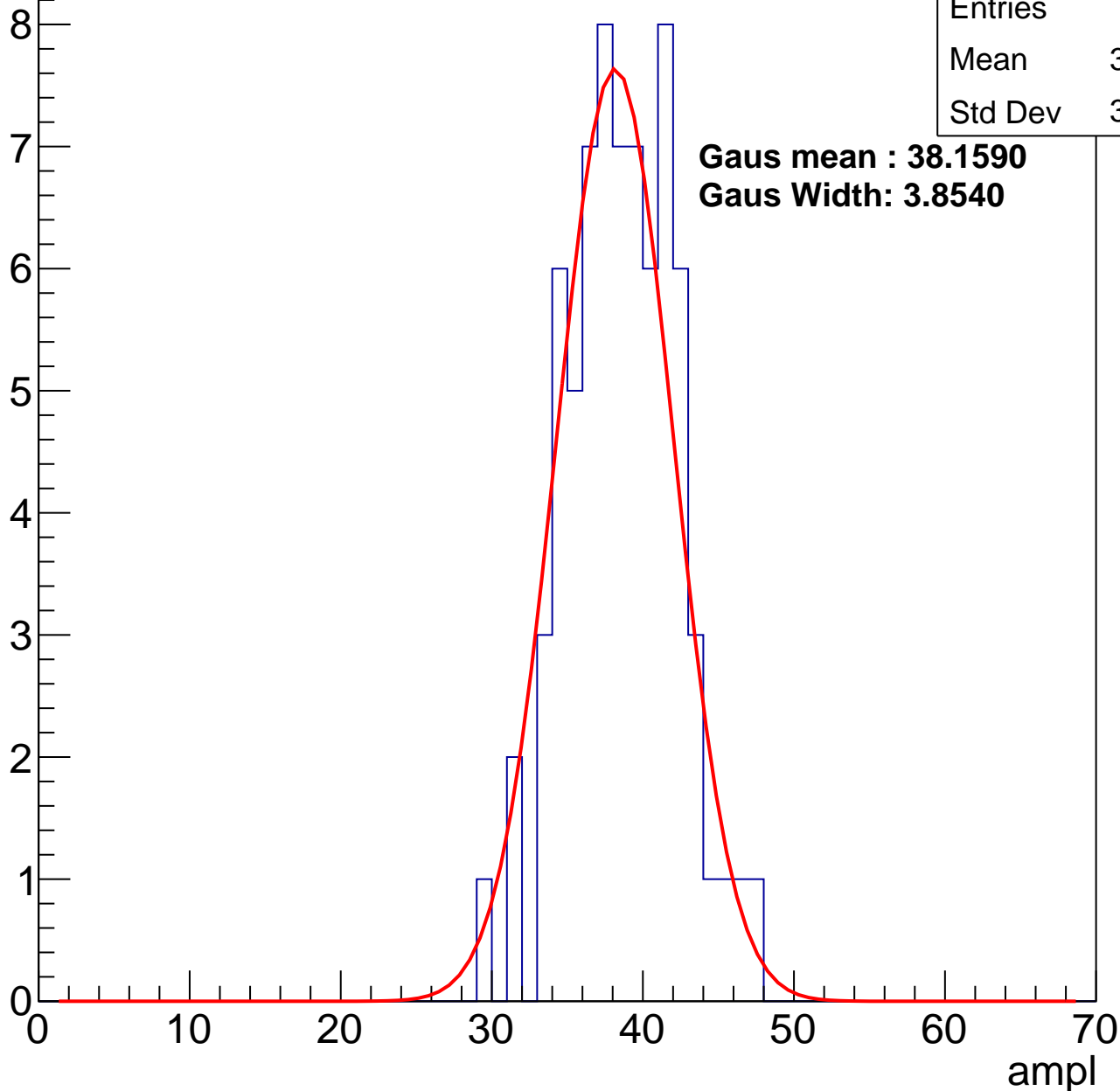
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	38.18
Std Dev	3.574

Gaus mean : 38.1590

Gaus Width: 3.8540



B0L002S, U18-ch18, adc2

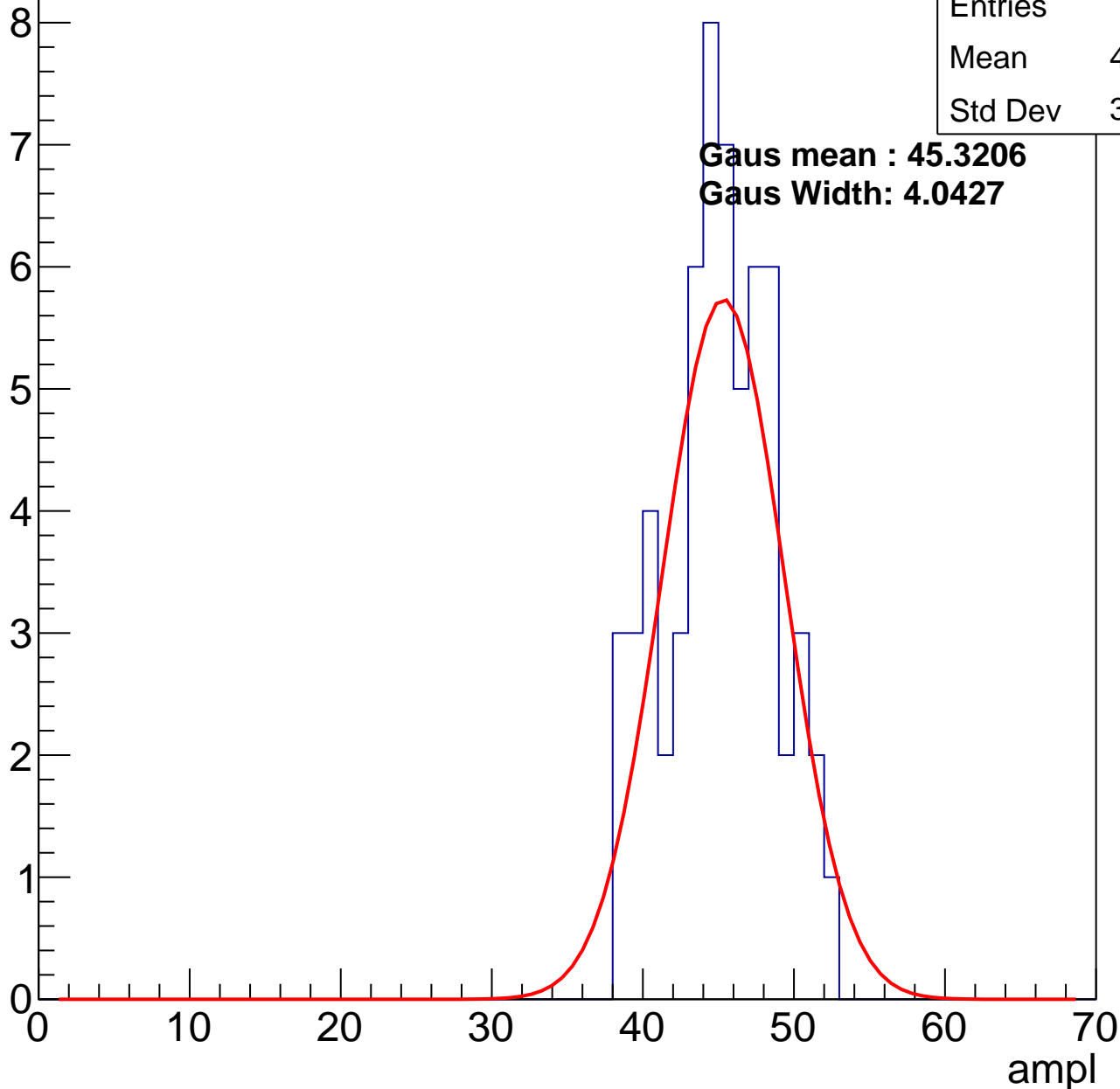
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.69
Std Dev	3.509

Gaus mean : 45.3206

Gaus Width: 4.0427

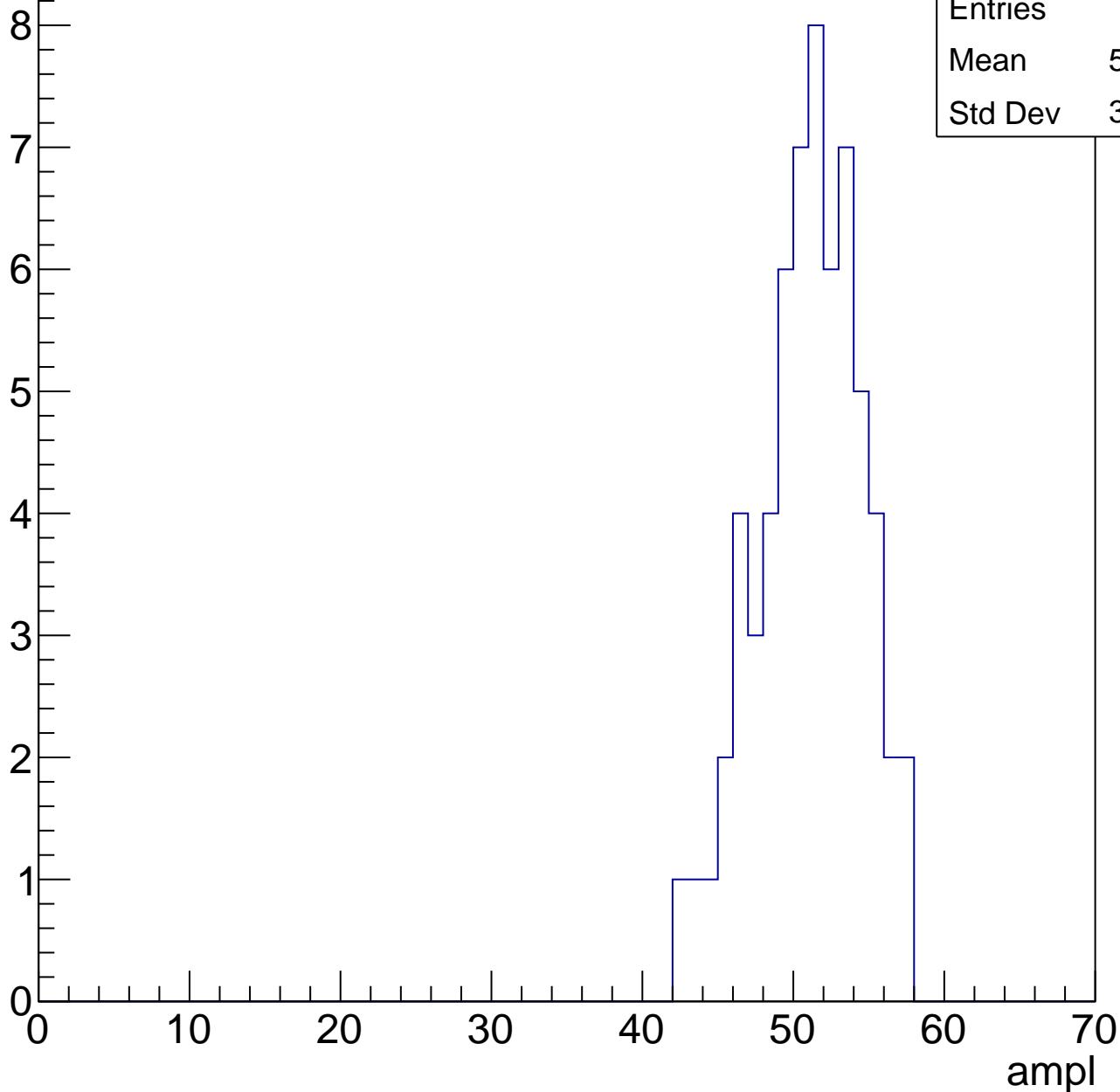


B0L002S, U18-ch18, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.59
Std Dev	3.416

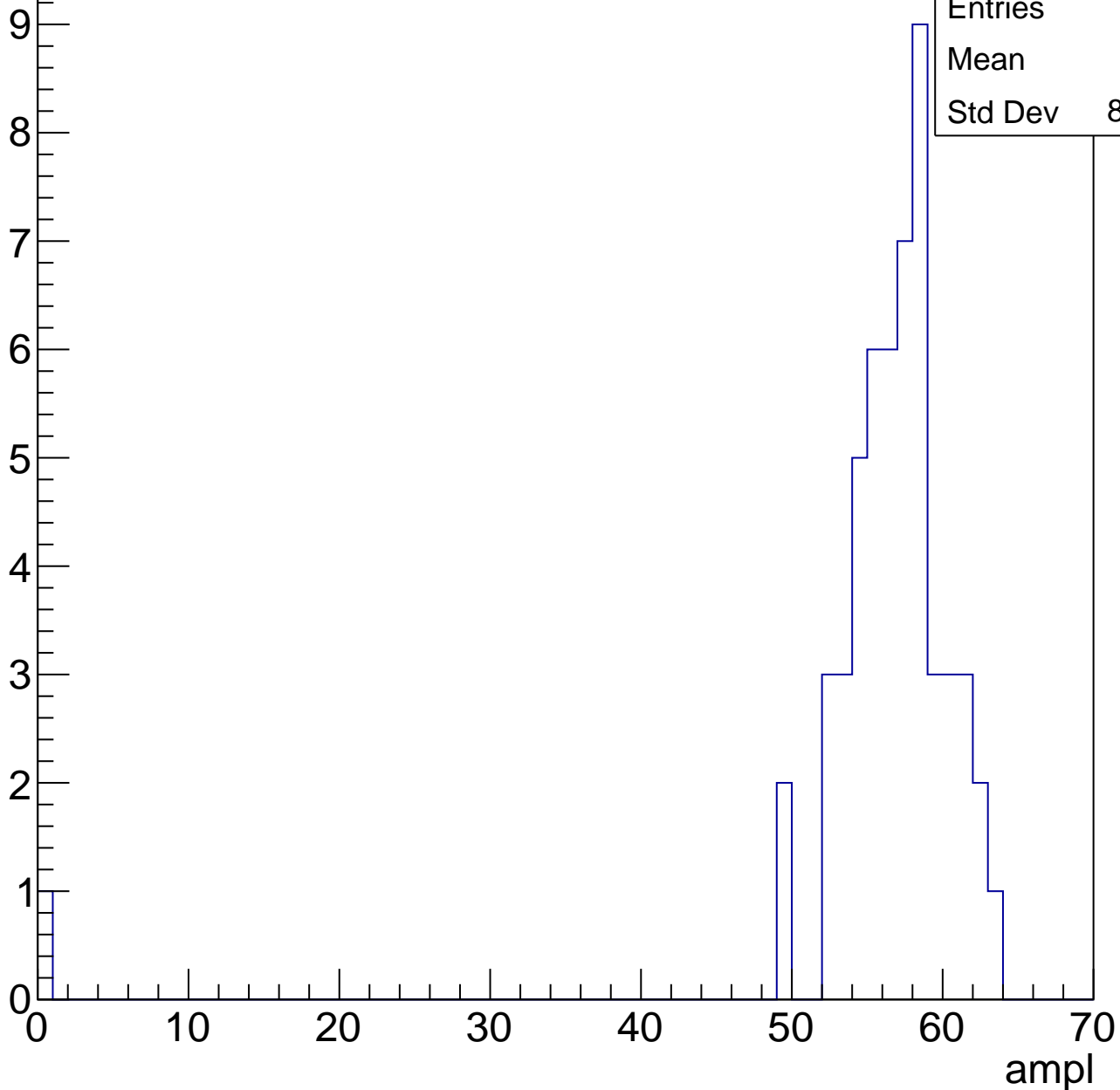


B0L002S, U18-ch18, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

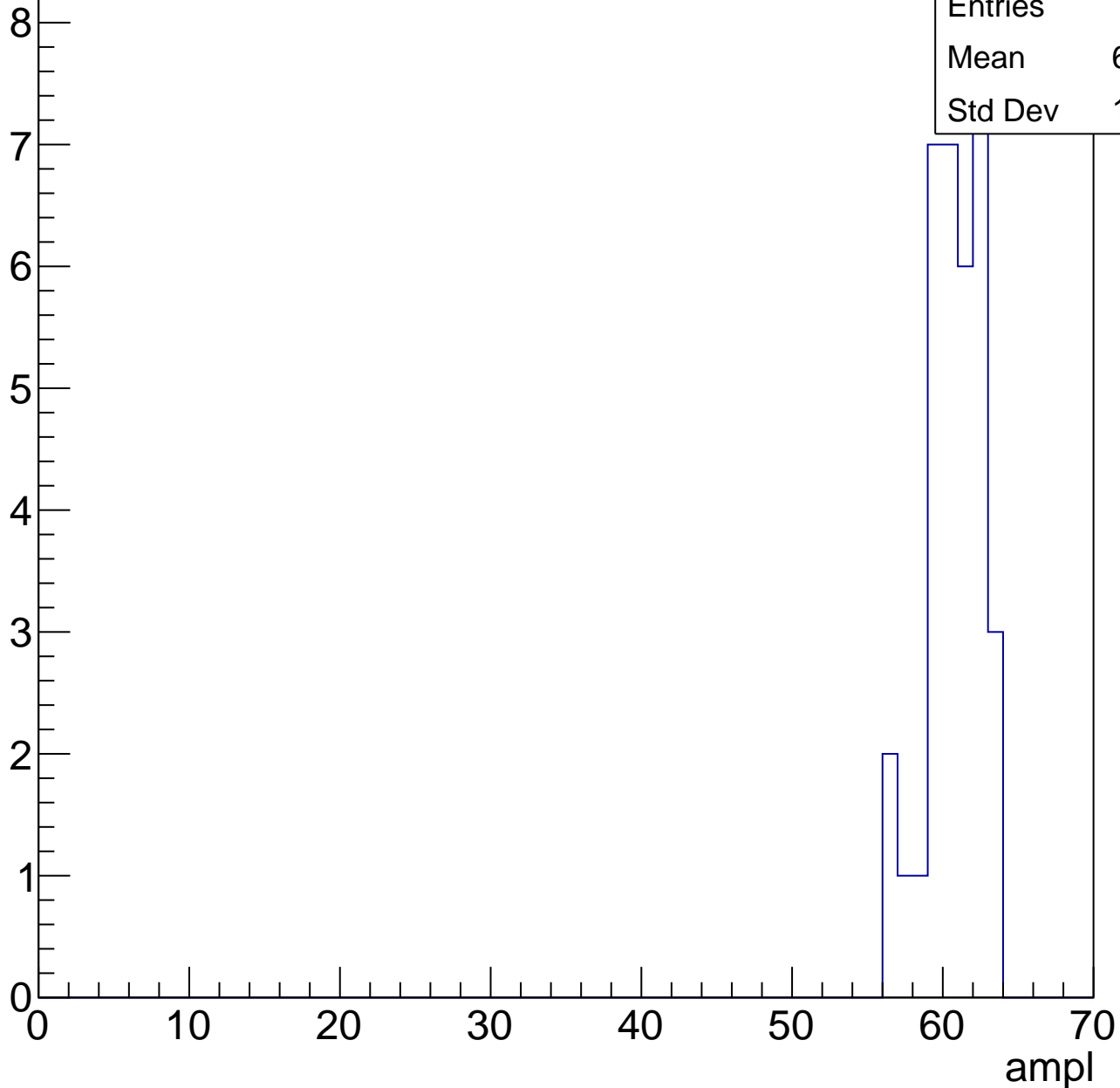
Entries	54
Mean	55.5
Std Dev	8.205



B0L002S, U18-ch18, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

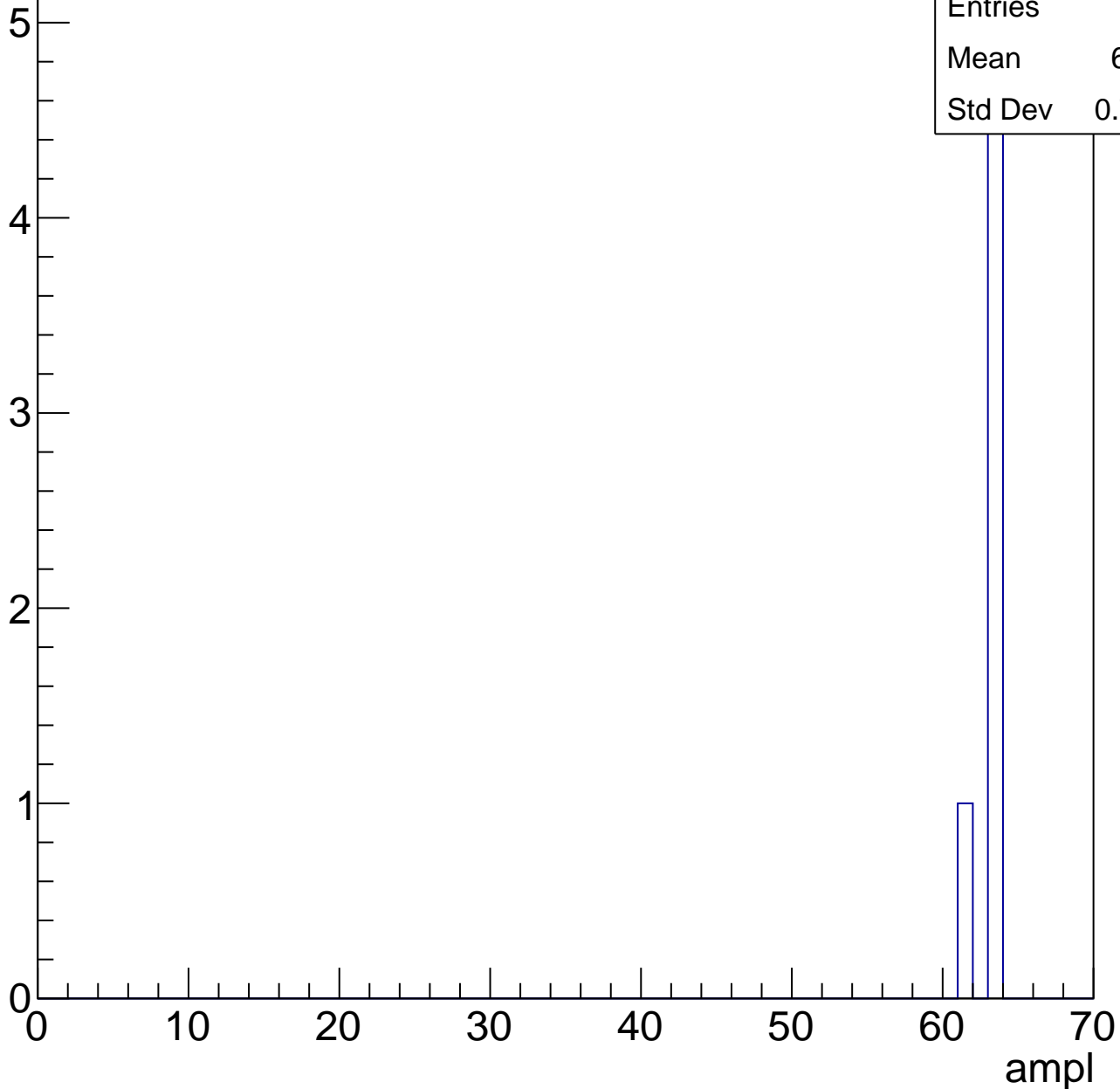


B0L002S, U18-ch18, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	6
Mean	62.67
Std Dev	0.7454



B0L002S, U18-ch18, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch19, adc0

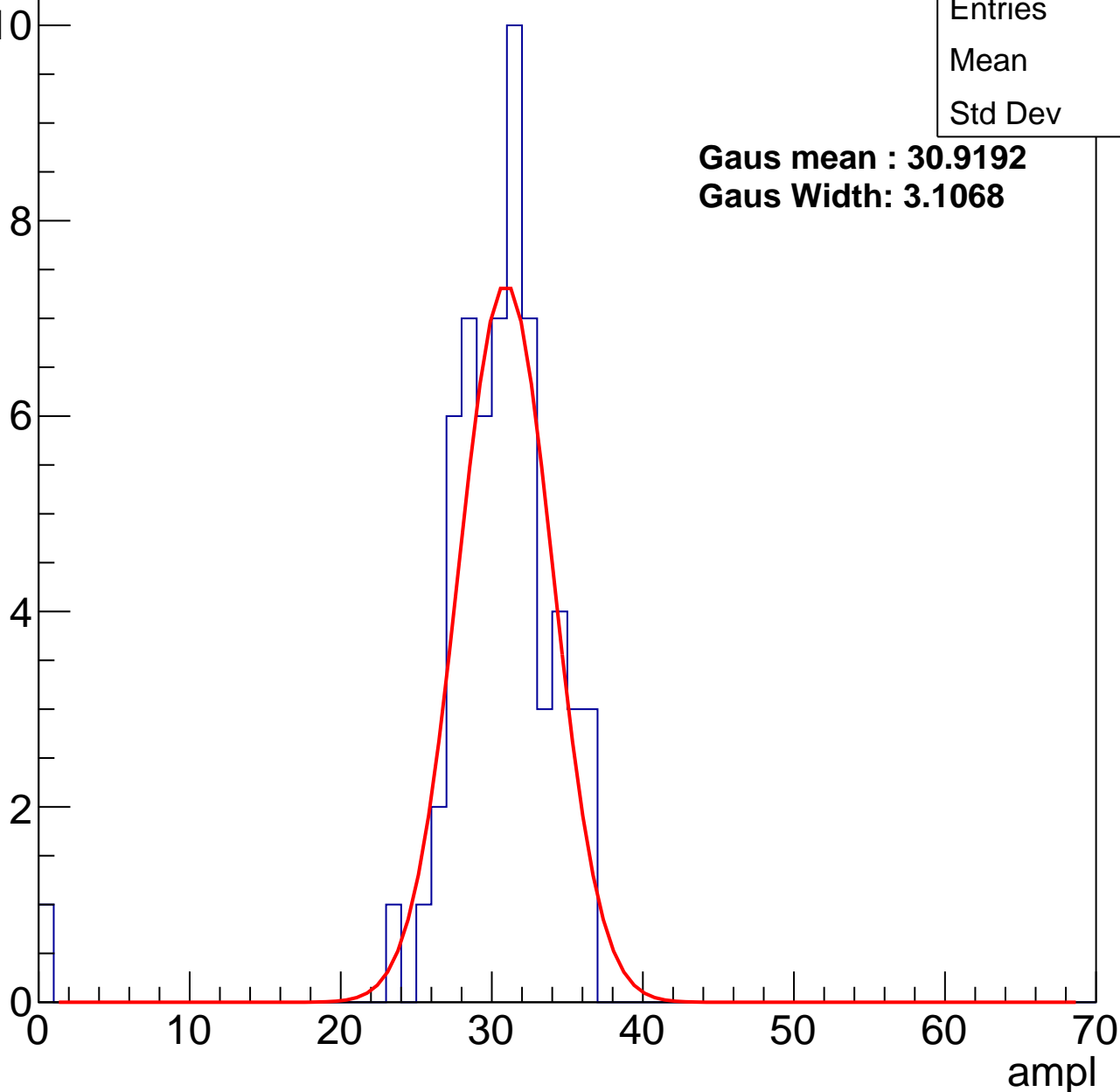
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.9
Std Dev	4.8

Gaus mean : 30.9192

Gaus Width: 3.1068



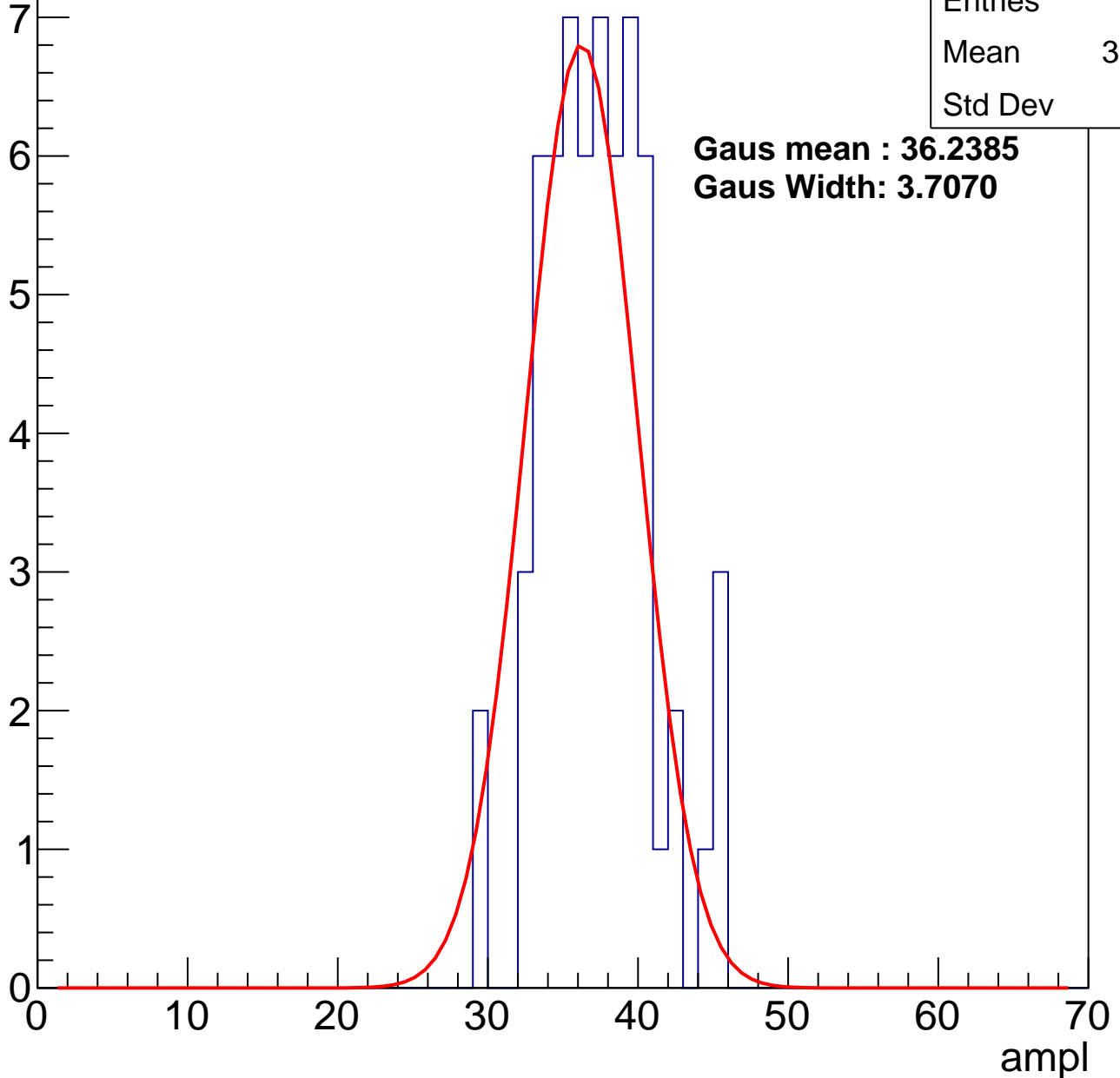
B0L002S, U18-ch19, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.84
Std Dev	3.52

Gaus mean : 36.2385
Gaus Width: 3.7070



B0L002S, U18-ch19, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	91
Mean	44.21
Std Dev	3.968

Gaus mean : 44.4518

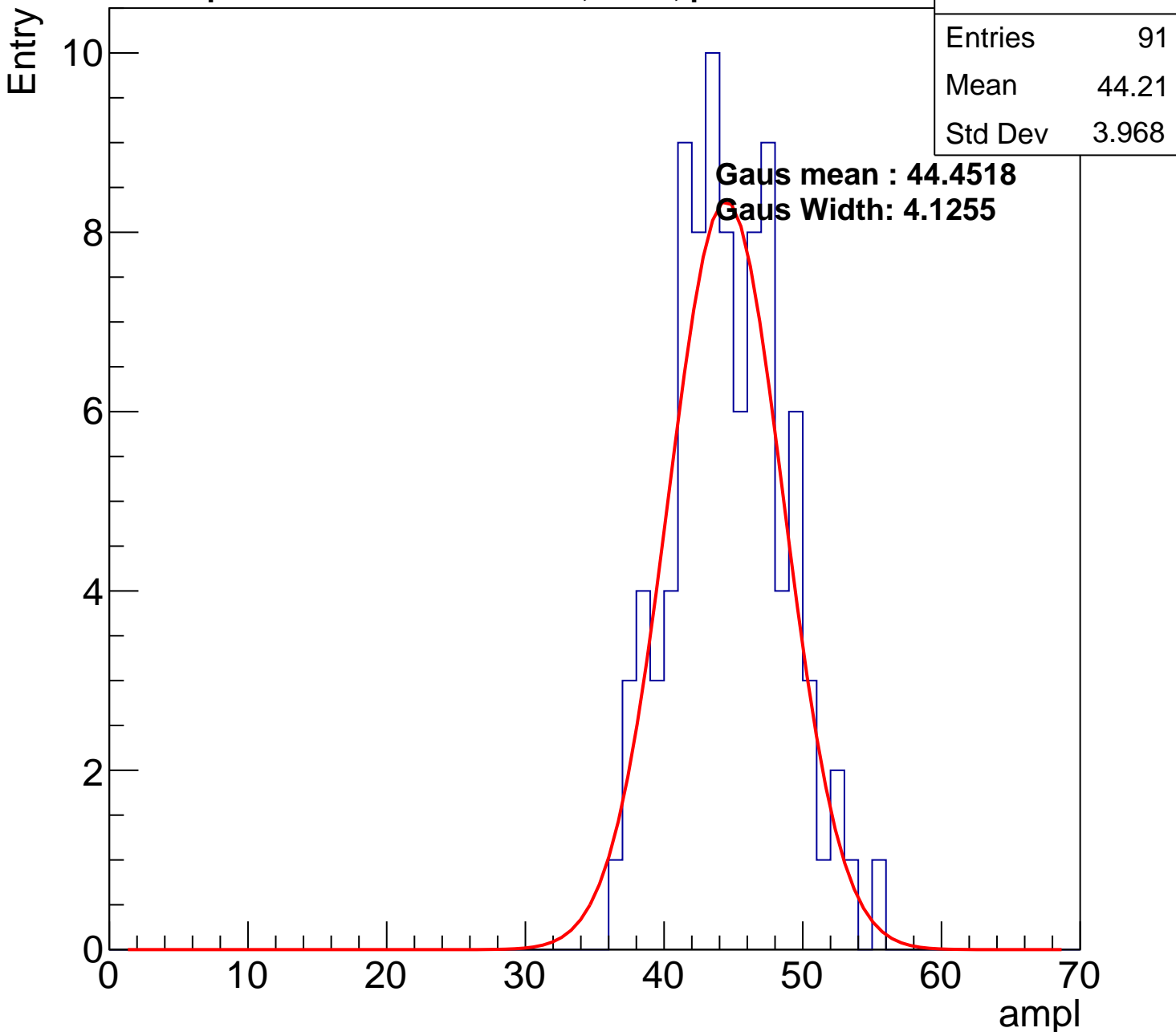
Gaus Width: 4.1255

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

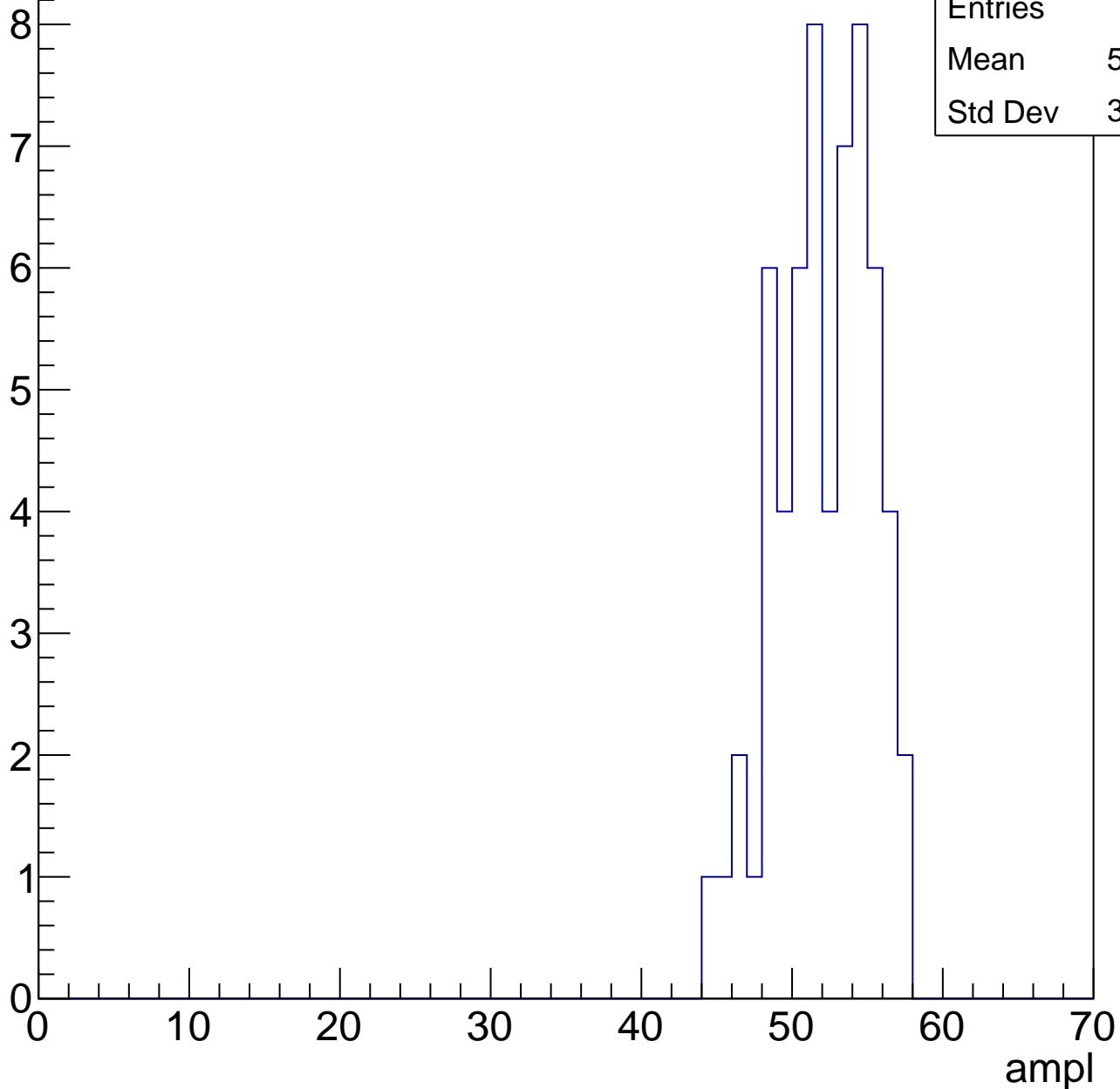


B0L002S, U18-ch19, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

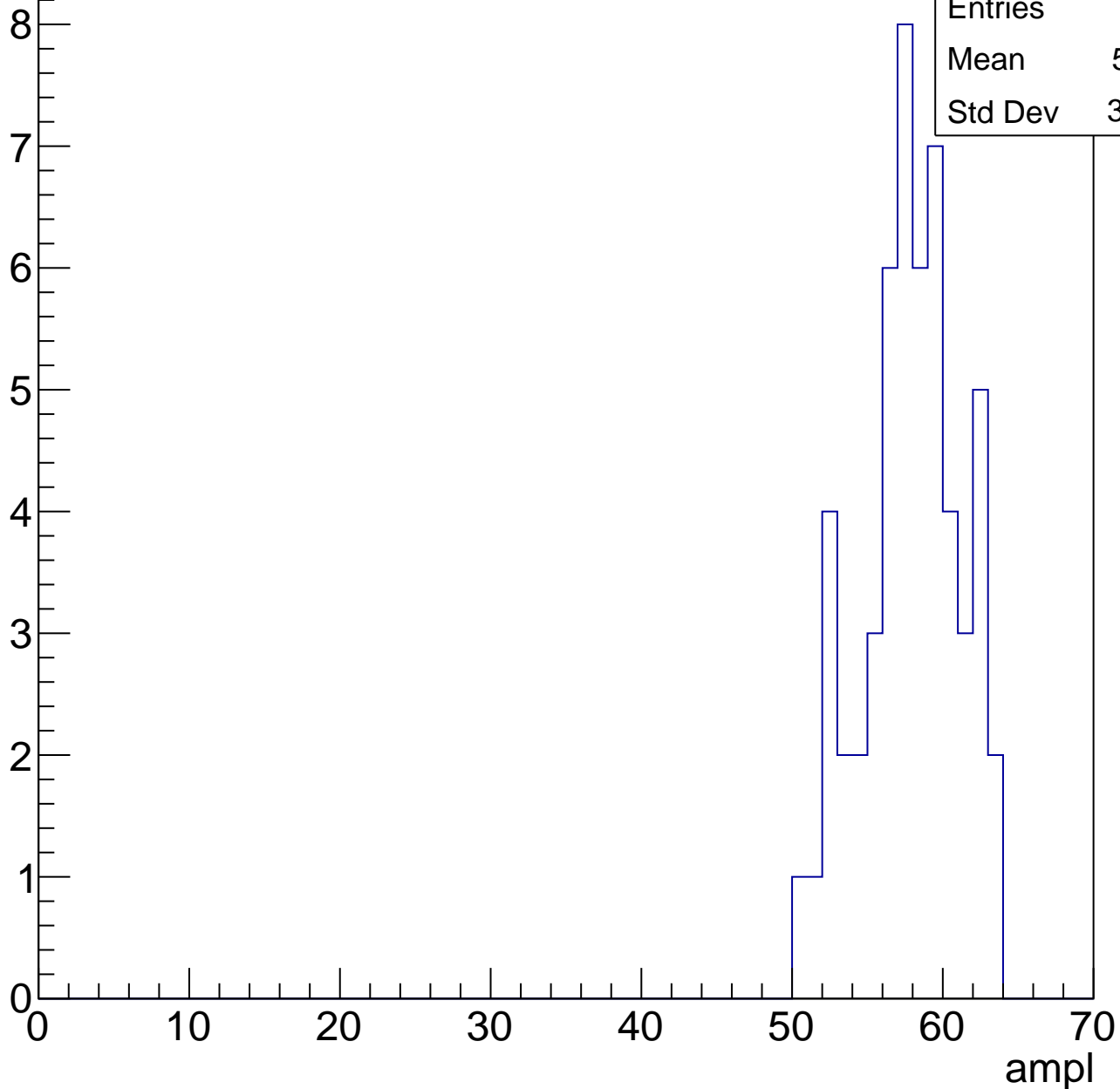
Entries	60
Mean	51.65
Std Dev	3.092



B0L002S, U18-ch19, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



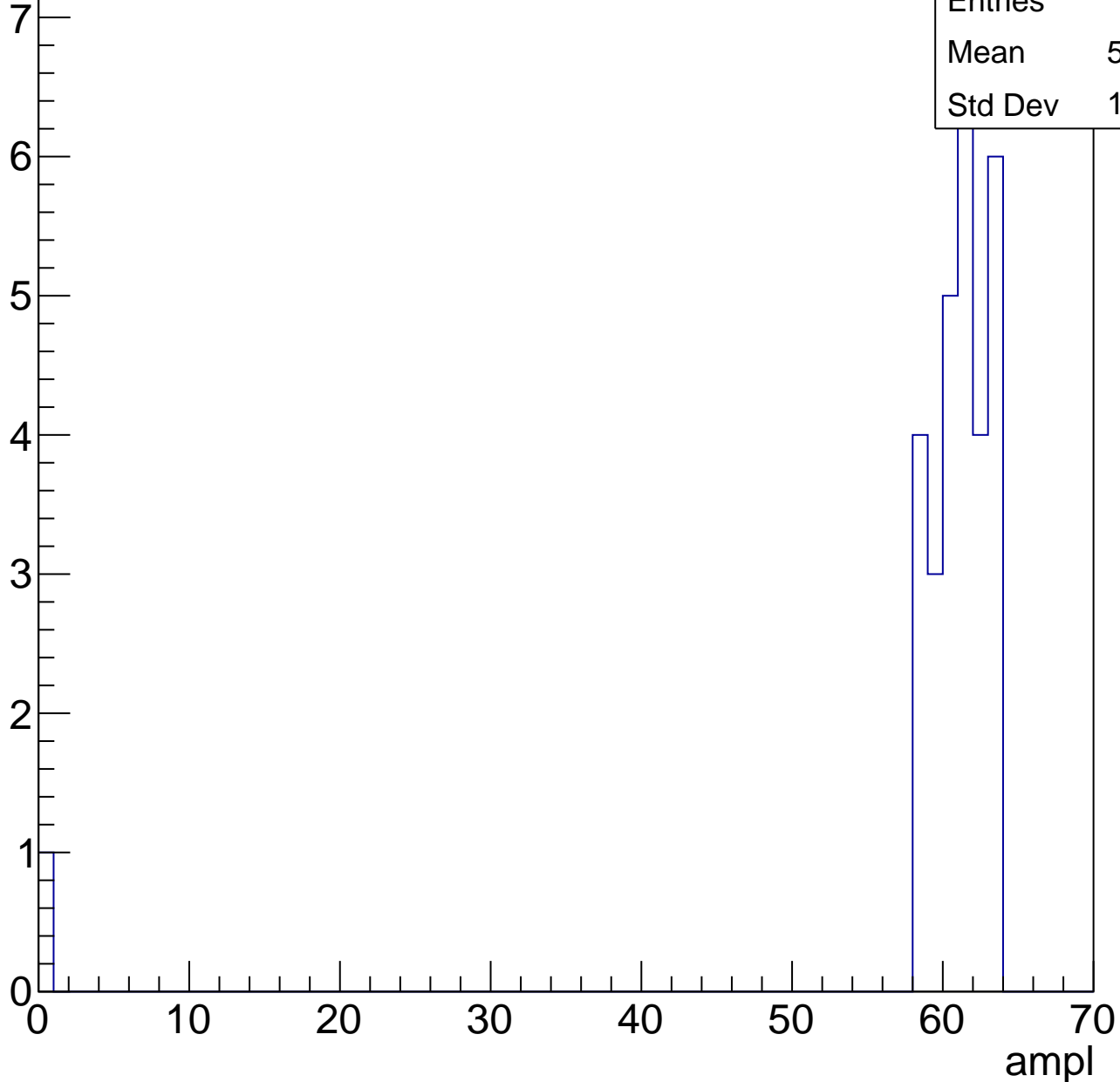
Entries	54
Mean	57.41
Std Dev	3.218

B0L002S, U18-ch19, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.73
Std Dev	11.03



B0L002S, U18-ch19, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch19, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch20, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	30.81
Std Dev	3.732

Gaus mean : 31.1334

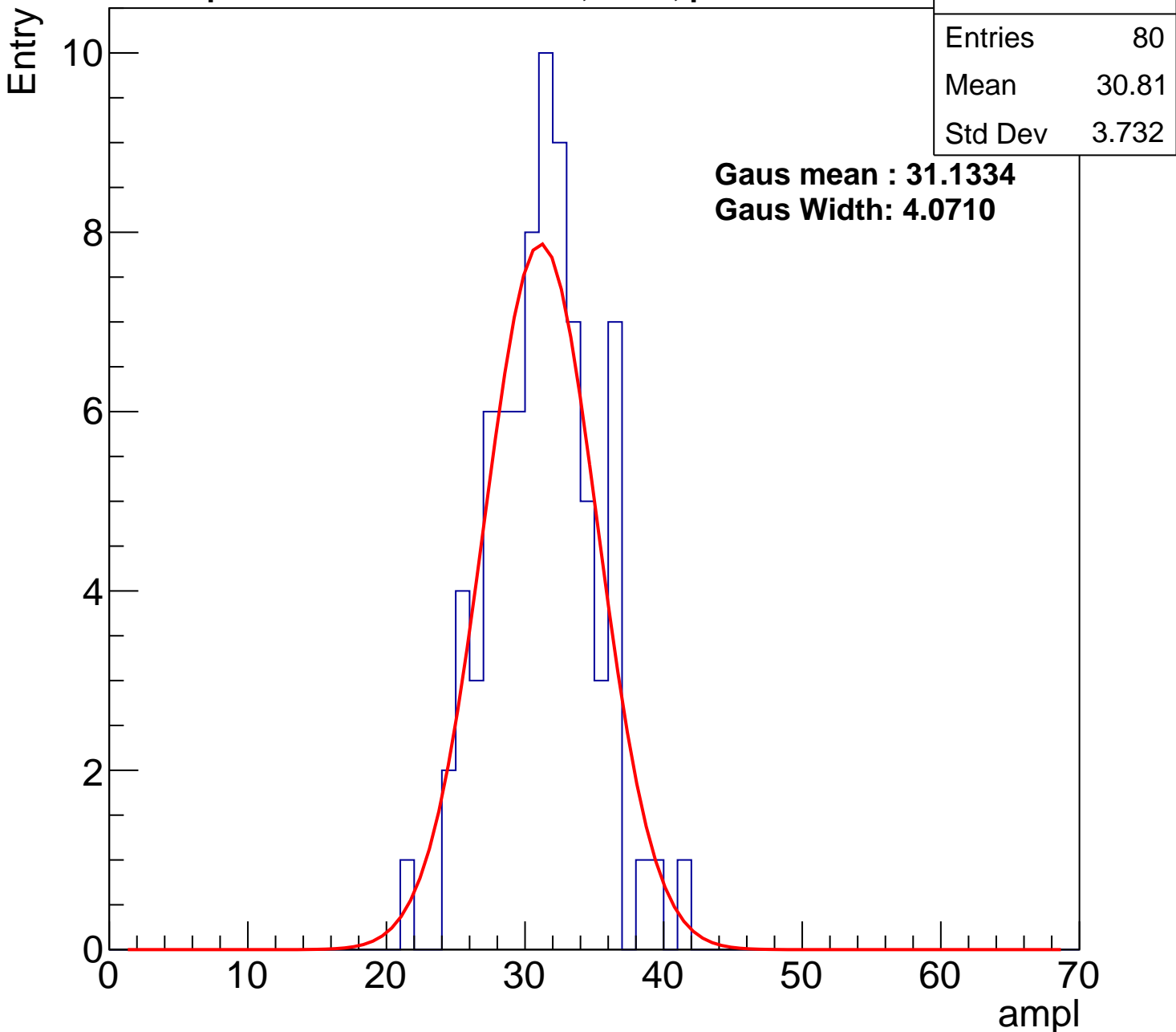
Gaus Width: 4.0710

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch20, adc1

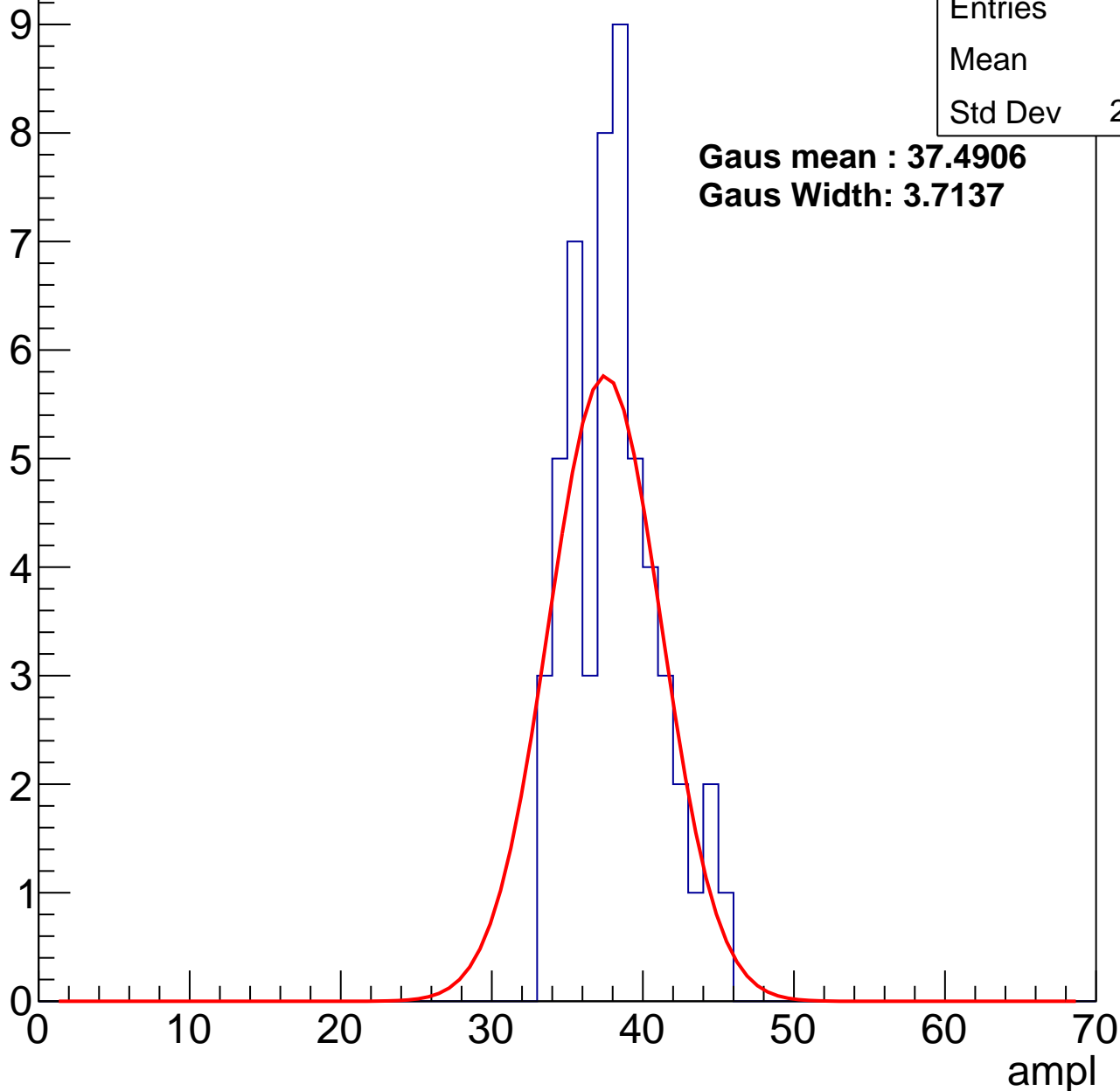
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	37.7
Std Dev	2.943

Gaus mean : 37.4906

Gaus Width: 3.7137



B0L002S, U18-ch20, adc2

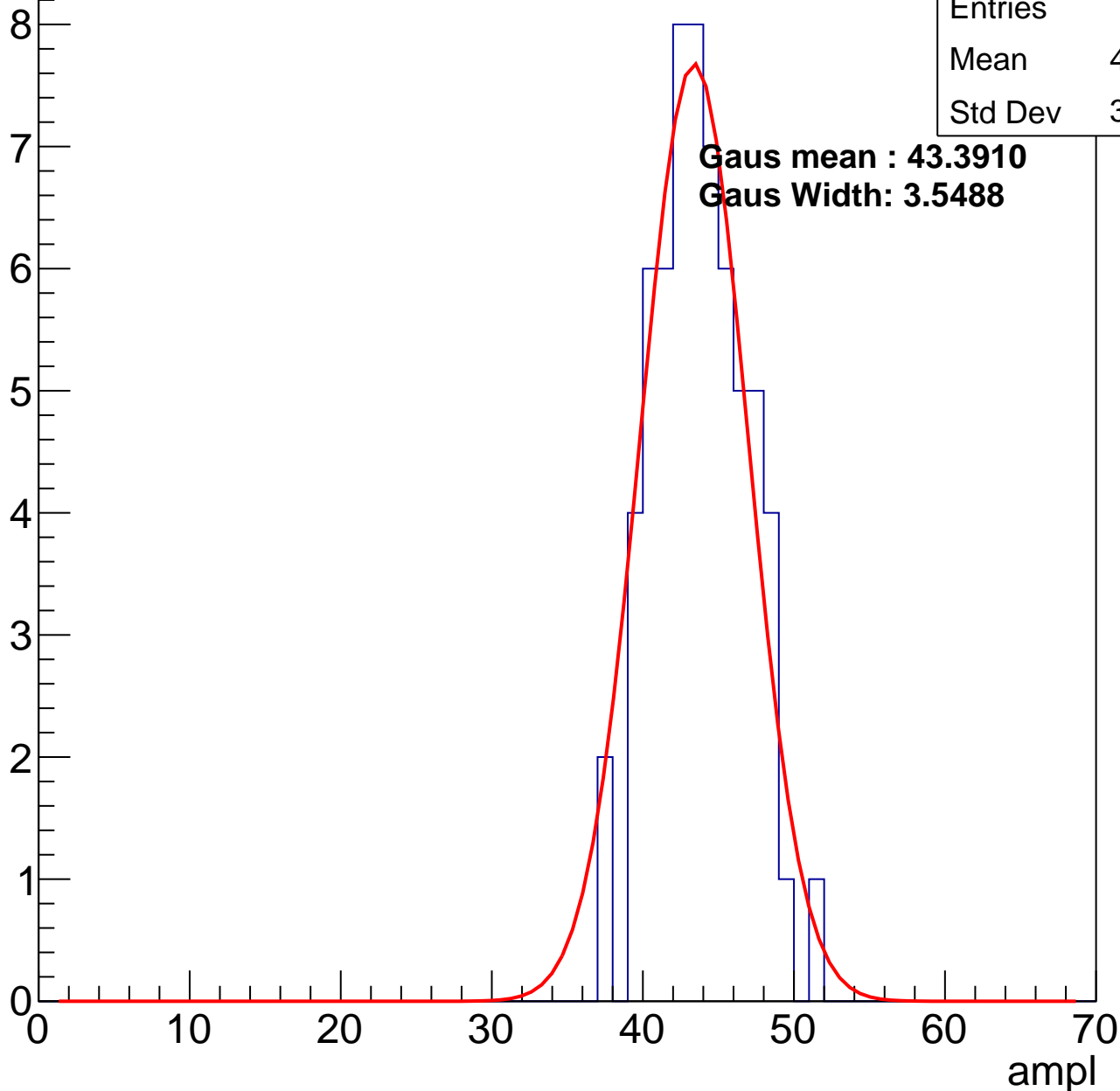
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.35
Std Dev	3.009

Gaus mean : 43.3910

Gaus Width: 3.5488

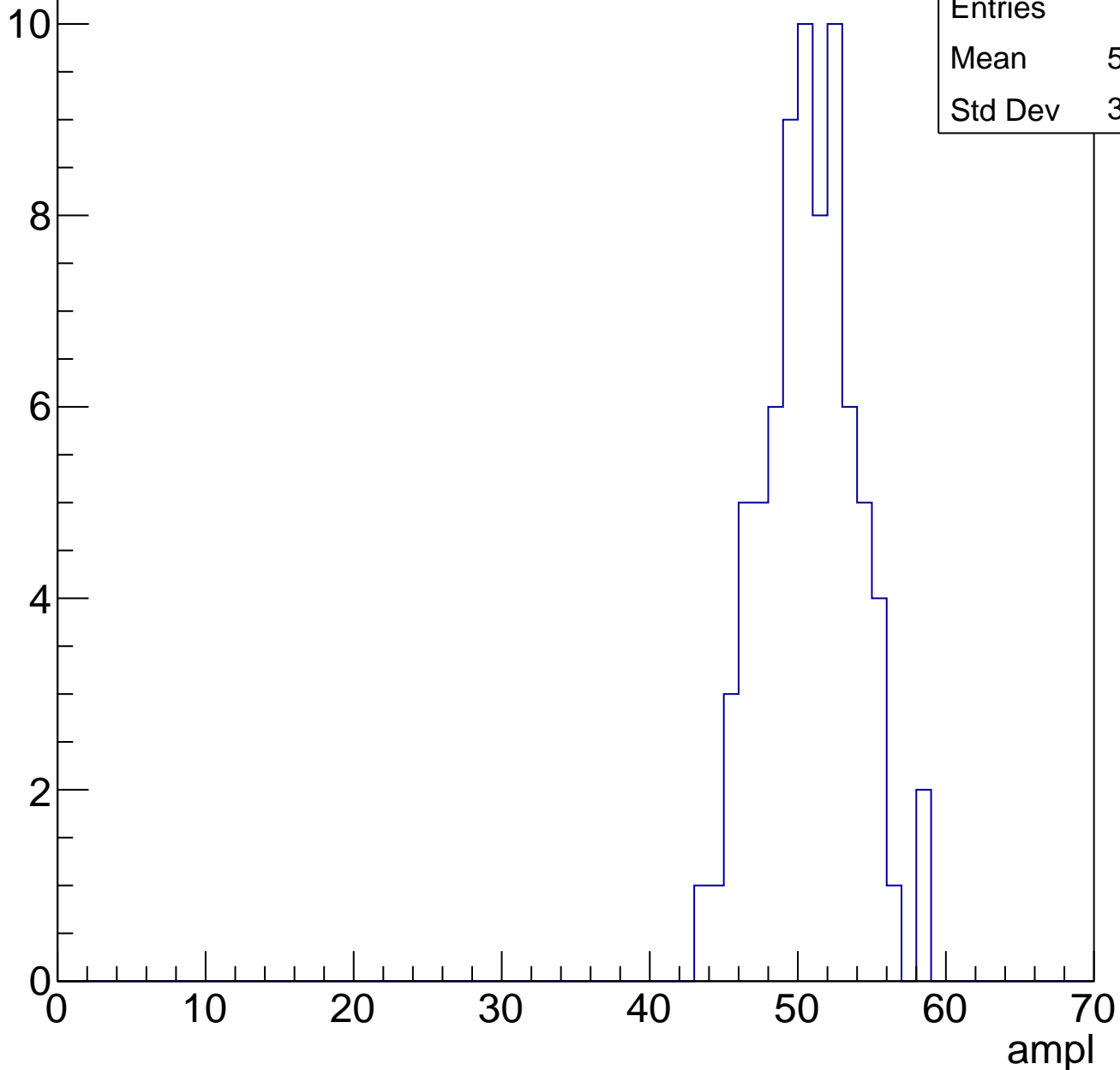


B0L002S, U18-ch20, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	50.32
Std Dev	3.155

Entry

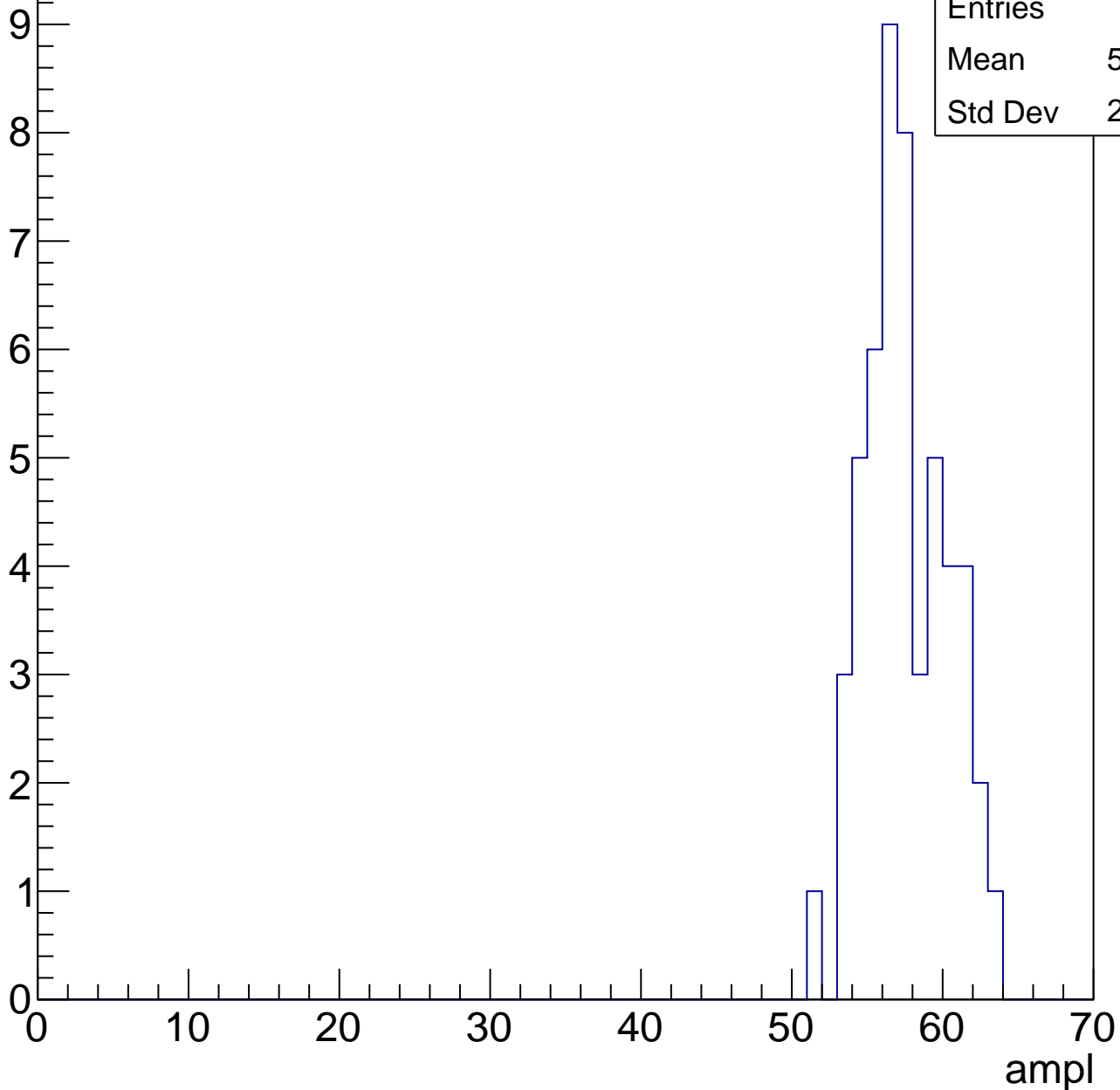


B0L002S, U18-ch20, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	57.06
Std Dev	2.696

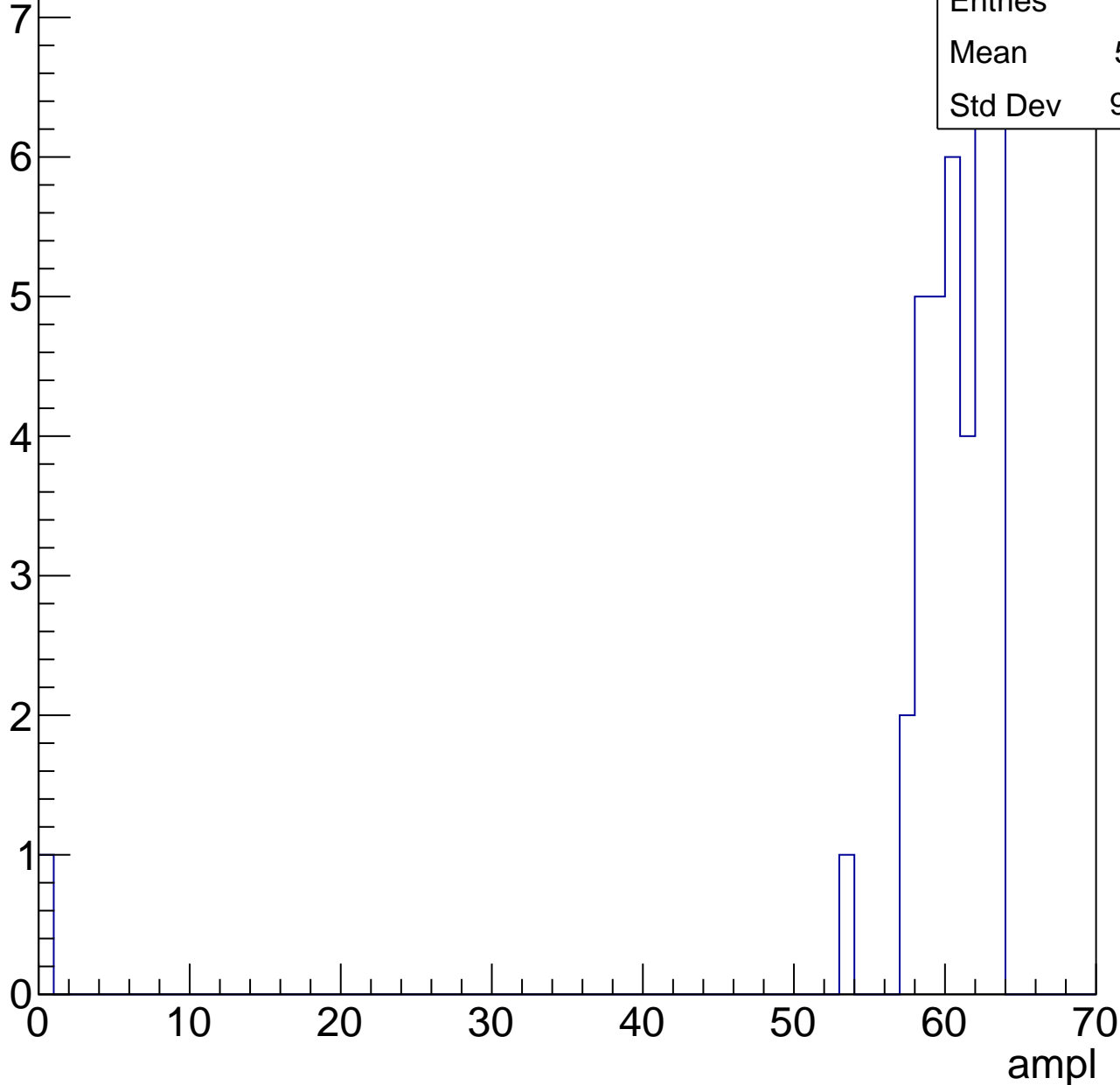


B0L002S, U18-ch20, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.71
Std Dev	9.899



B0L002S, U18-ch20, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62
Std Dev	0.8944

ampl

0 10 20 30 40 50 60 70

B0L002S, U18-ch20, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch21, adc0

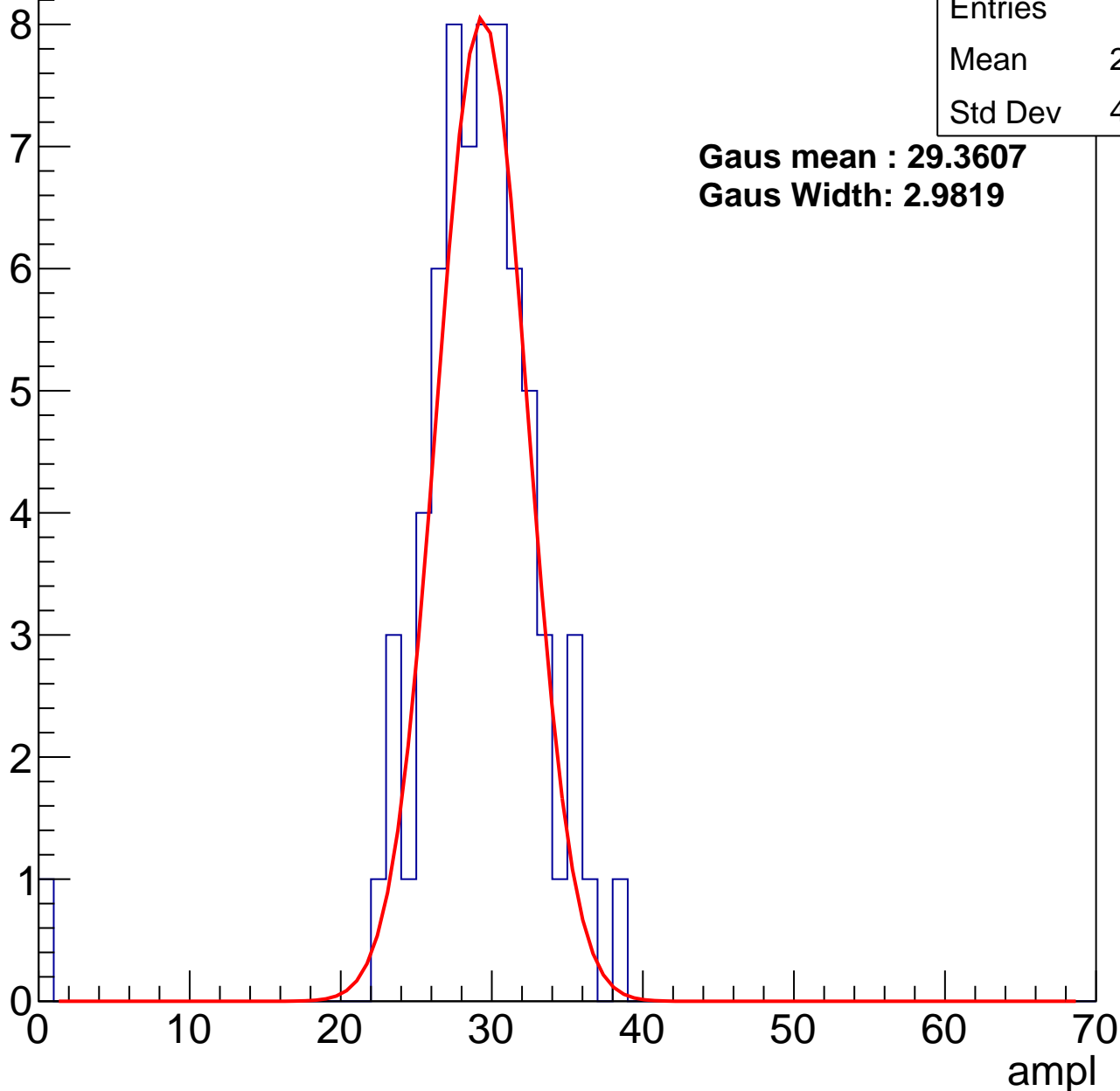
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	28.55
Std Dev	4.817

Gaus mean : 29.3607

Gaus Width: 2.9819



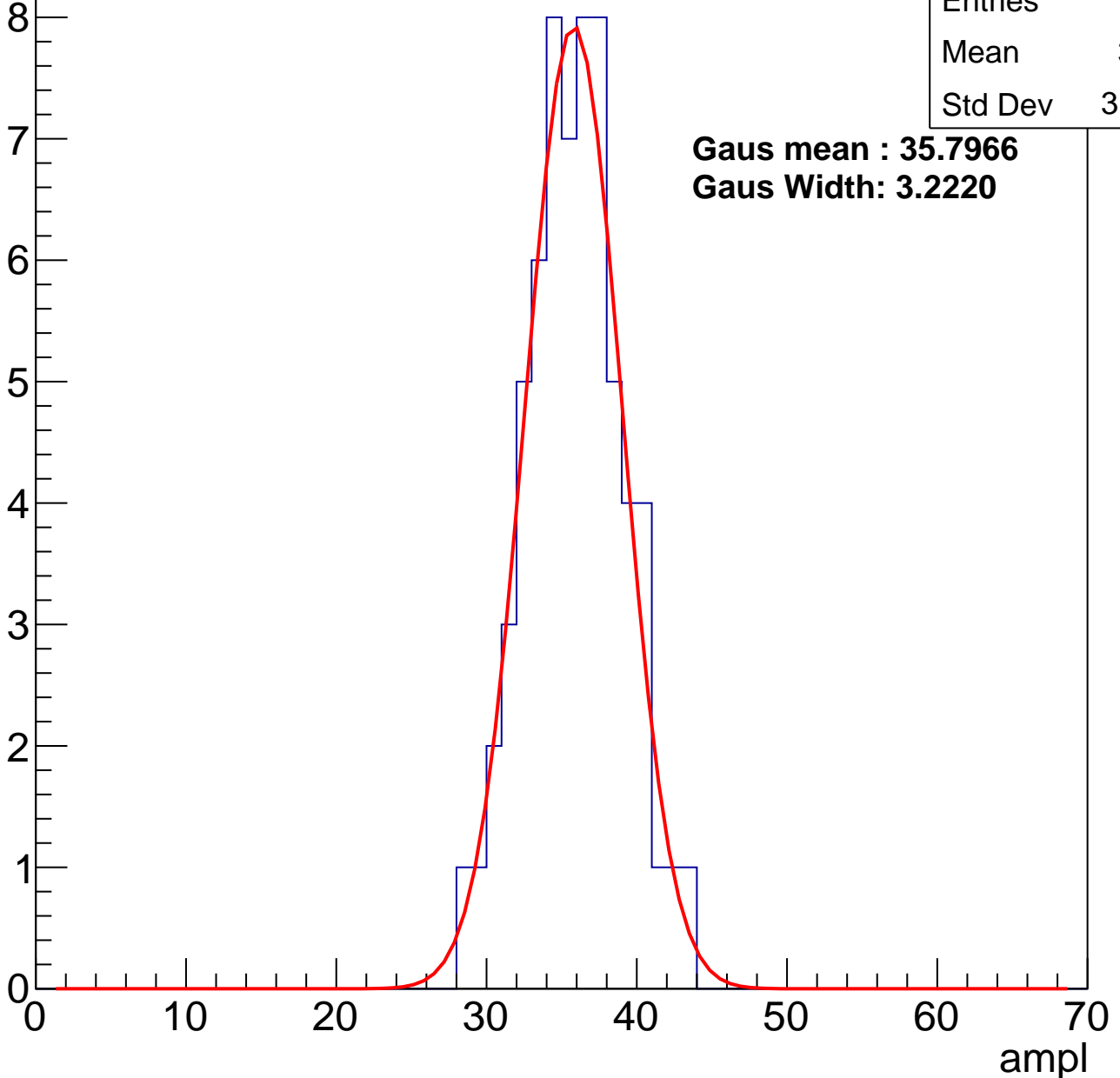
B0L002S, U18-ch21, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.4
Std Dev	3.156

Gaus mean : 35.7966
Gaus Width: 3.2220



B0L002S, U18-ch21, adc2

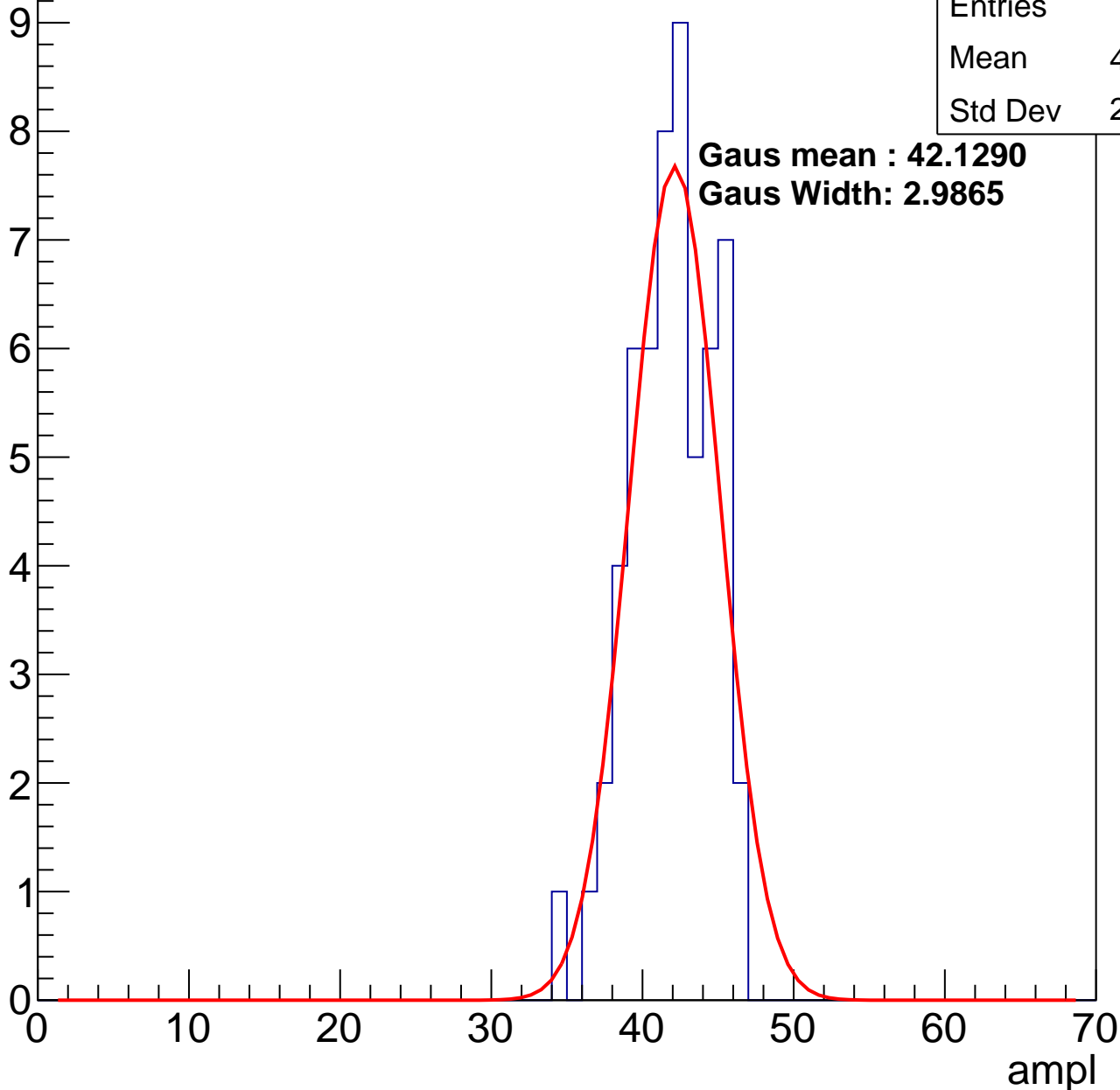
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	41.44
Std Dev	2.669

Gaus mean : 42.1290

Gaus Width: 2.9865

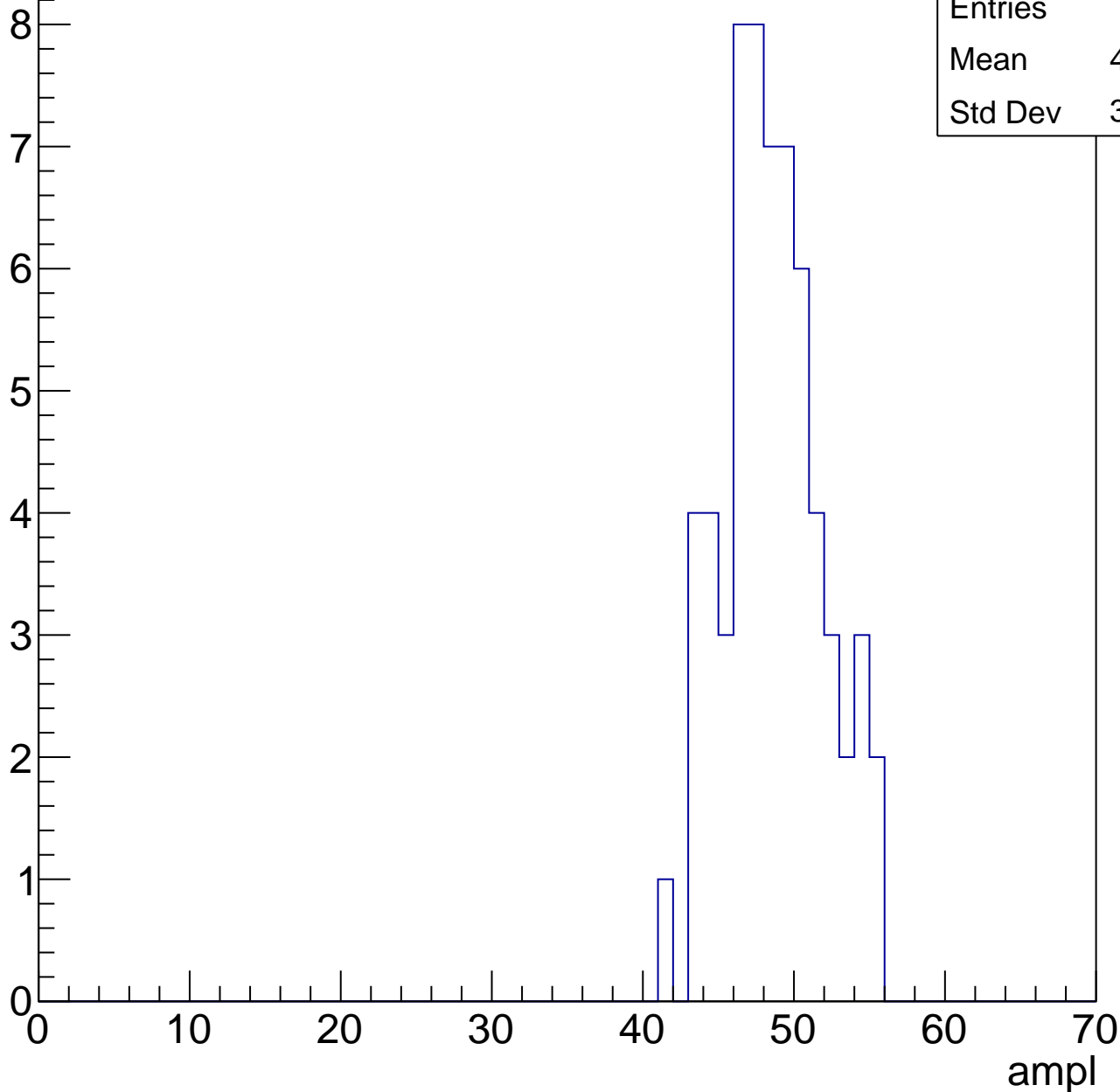


B0L002S, U18-ch21, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	48.15
Std Dev	3.237

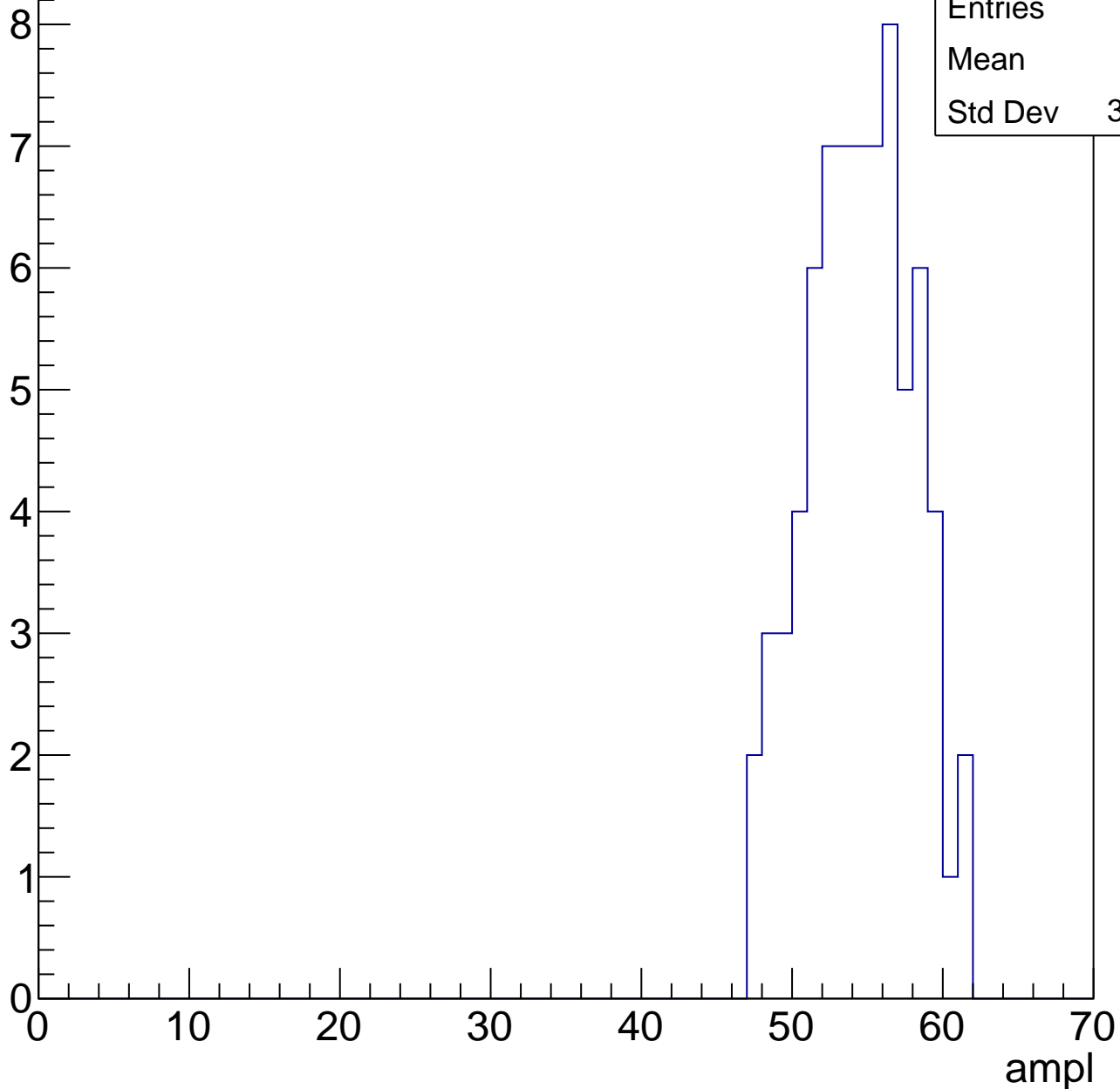


B0L002S, U18-ch21, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	54
Std Dev	3.432

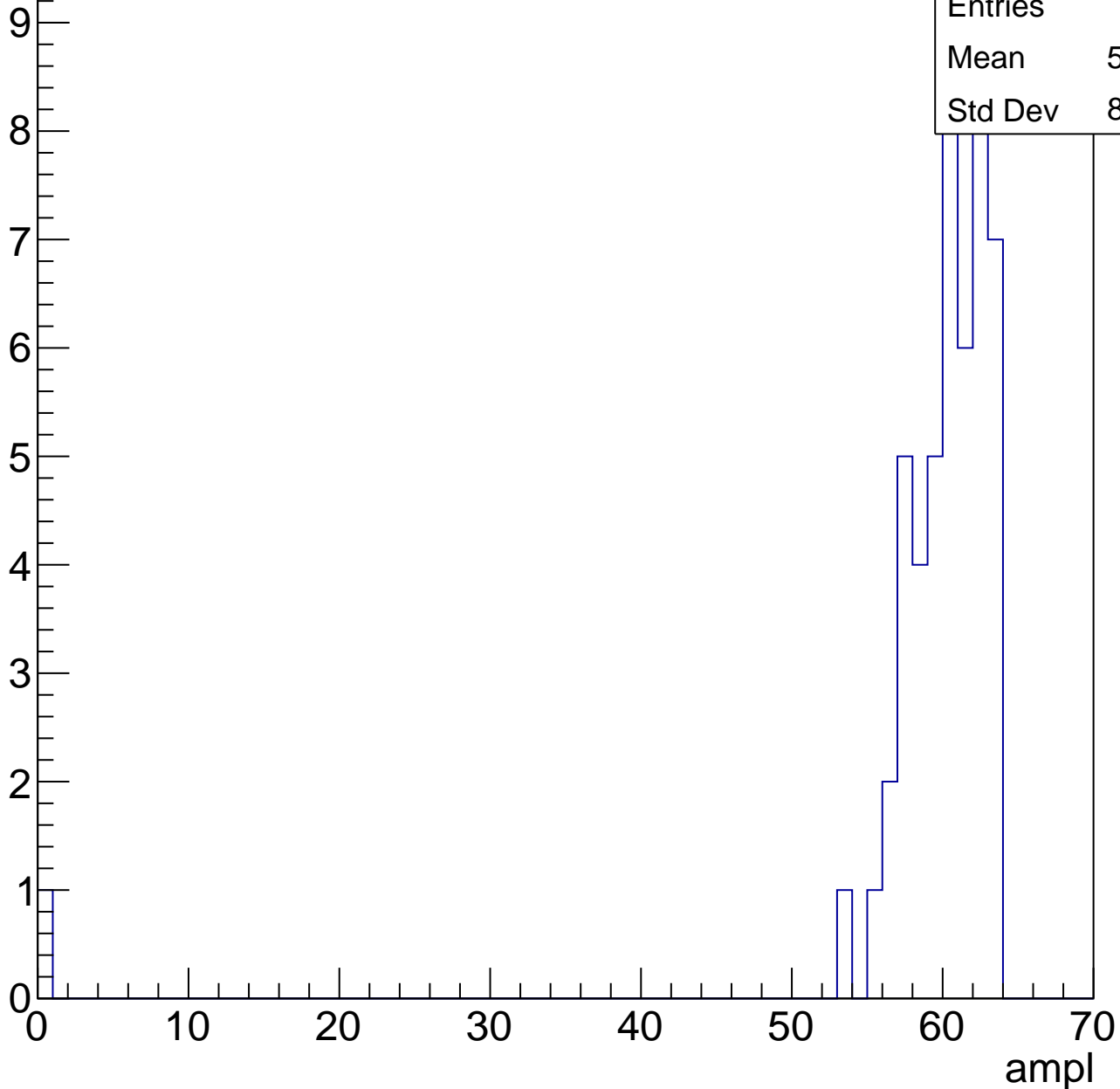


B0L002S, U18-ch21, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

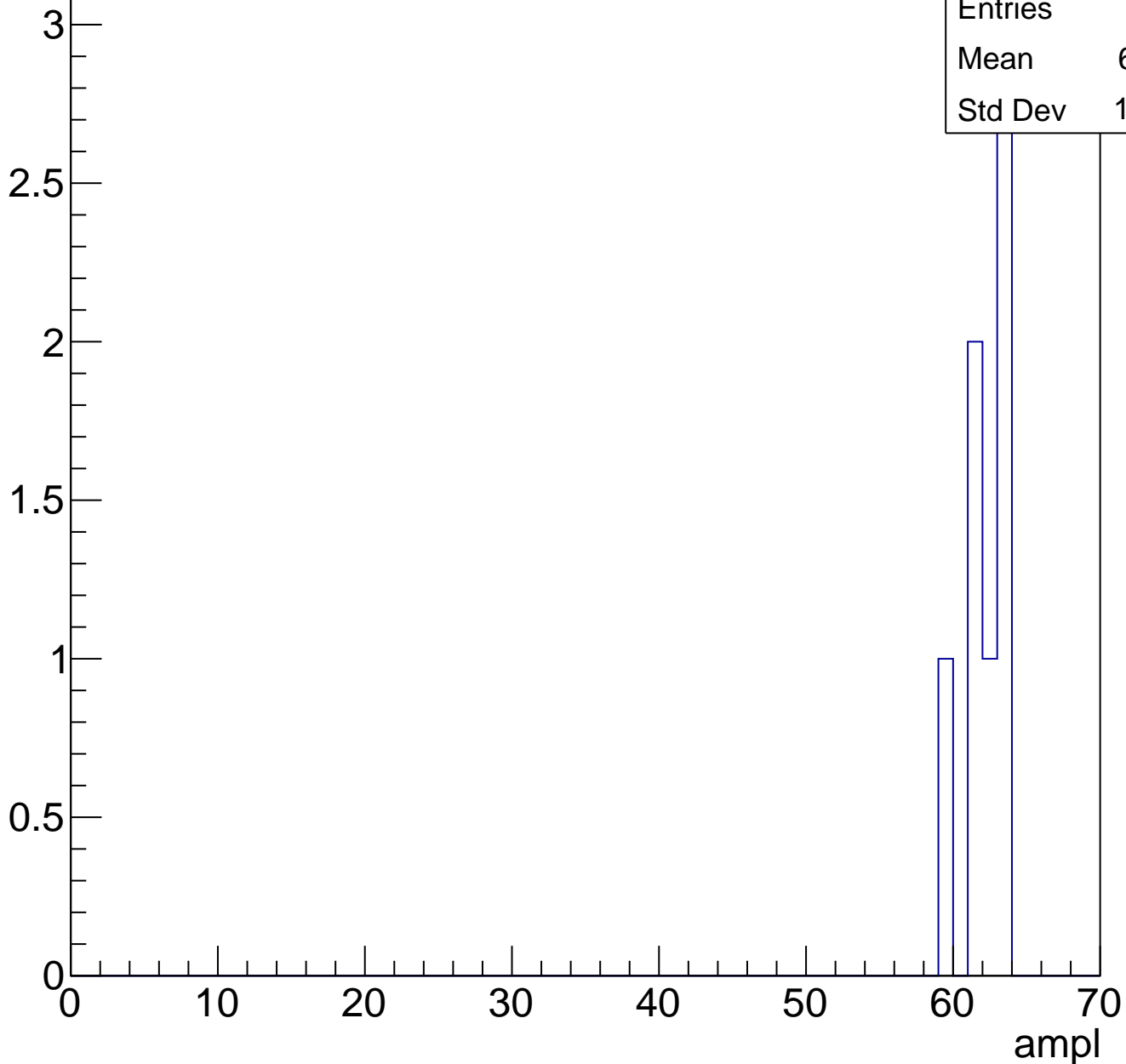
Entries	50
Mean	58.74
Std Dev	8.715



B0L002S, U18-ch21, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch21, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch22, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	73
Mean	30.7
Std Dev	4.951

Gaus mean : 31.2094

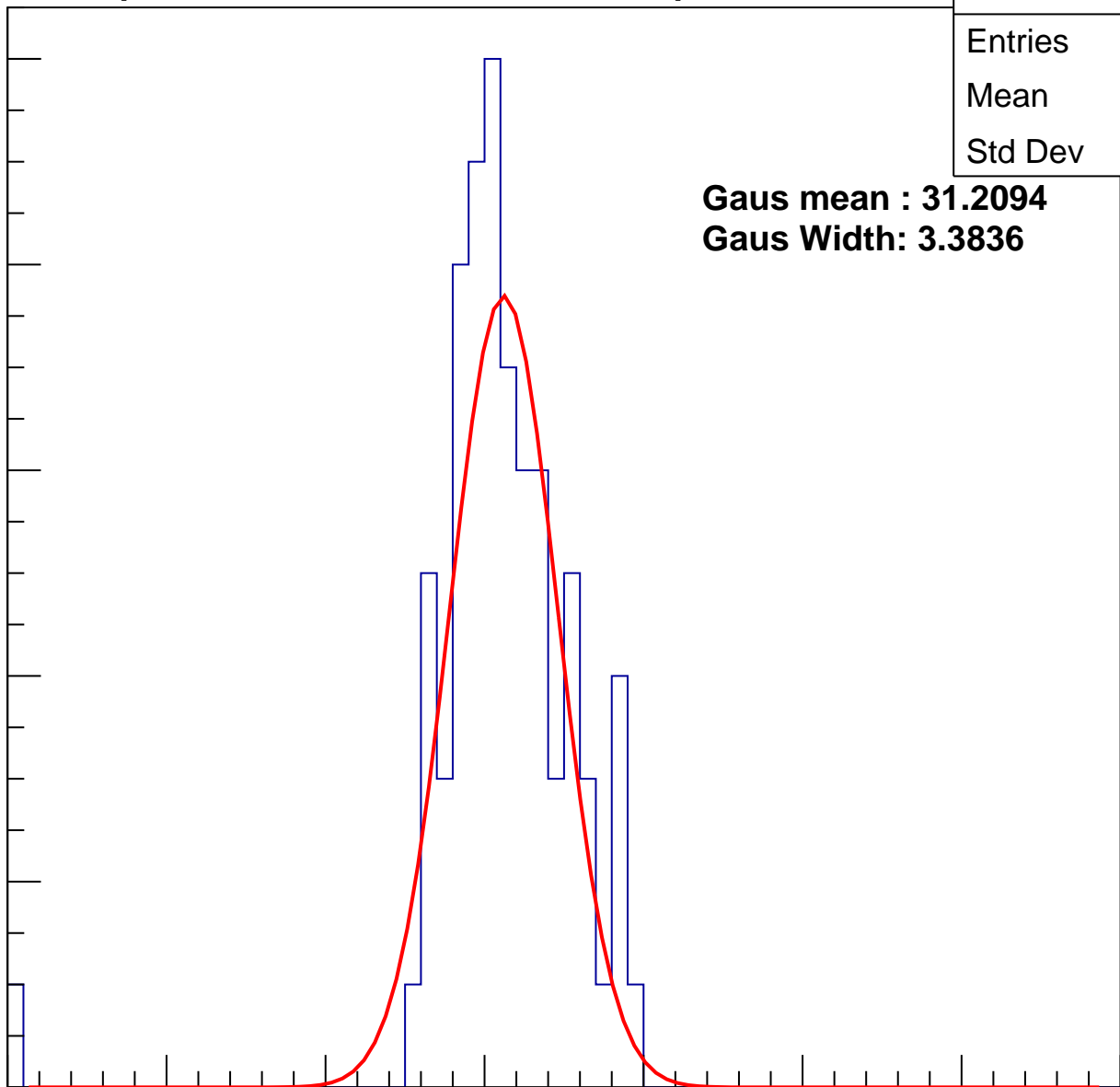
Gaus Width: 3.3836

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch22, adc1

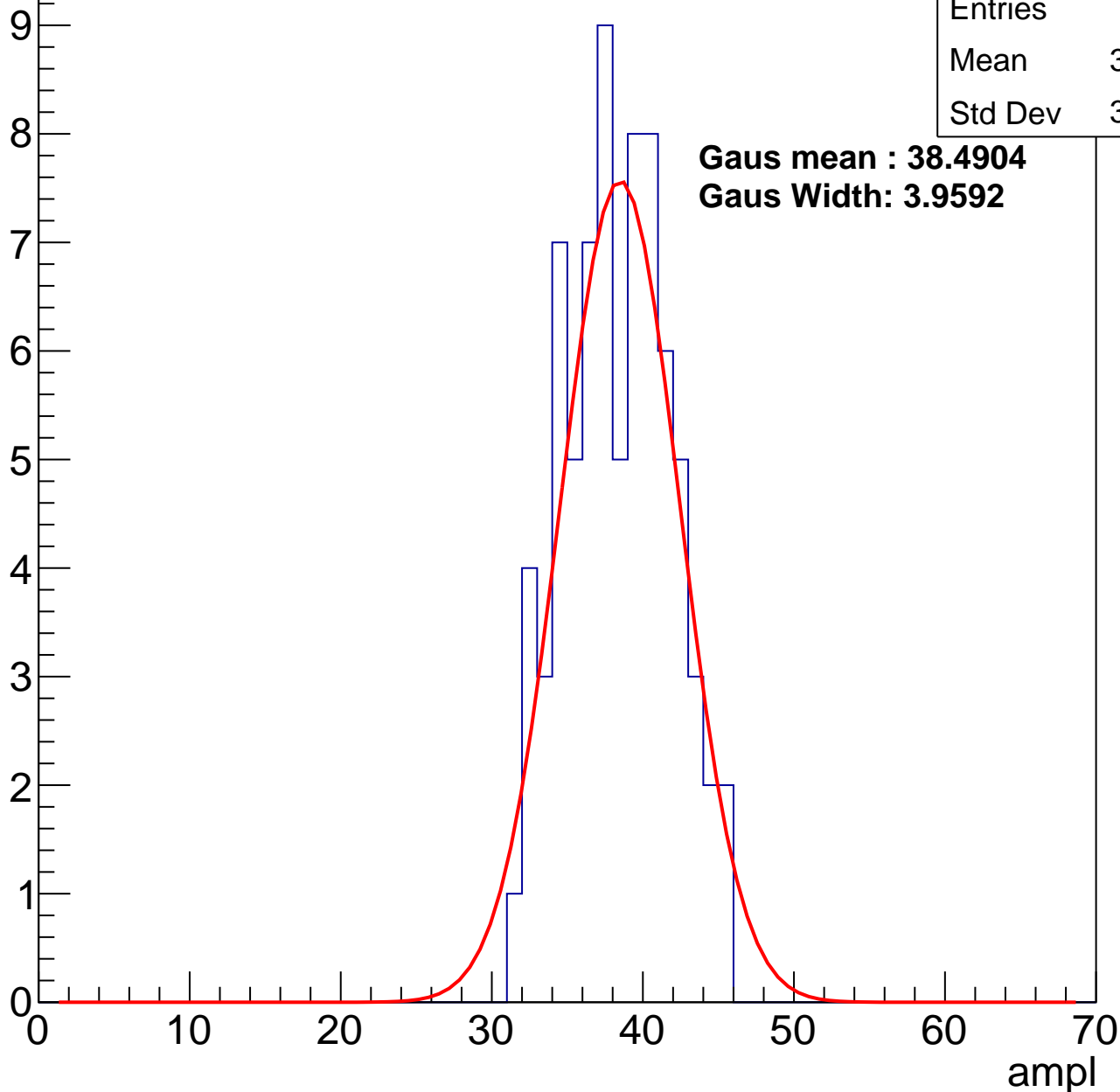
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.88
Std Dev	3.425

Gaus mean : 38.4904

Gaus Width: 3.9592



B0L002S, U18-ch22, adc2

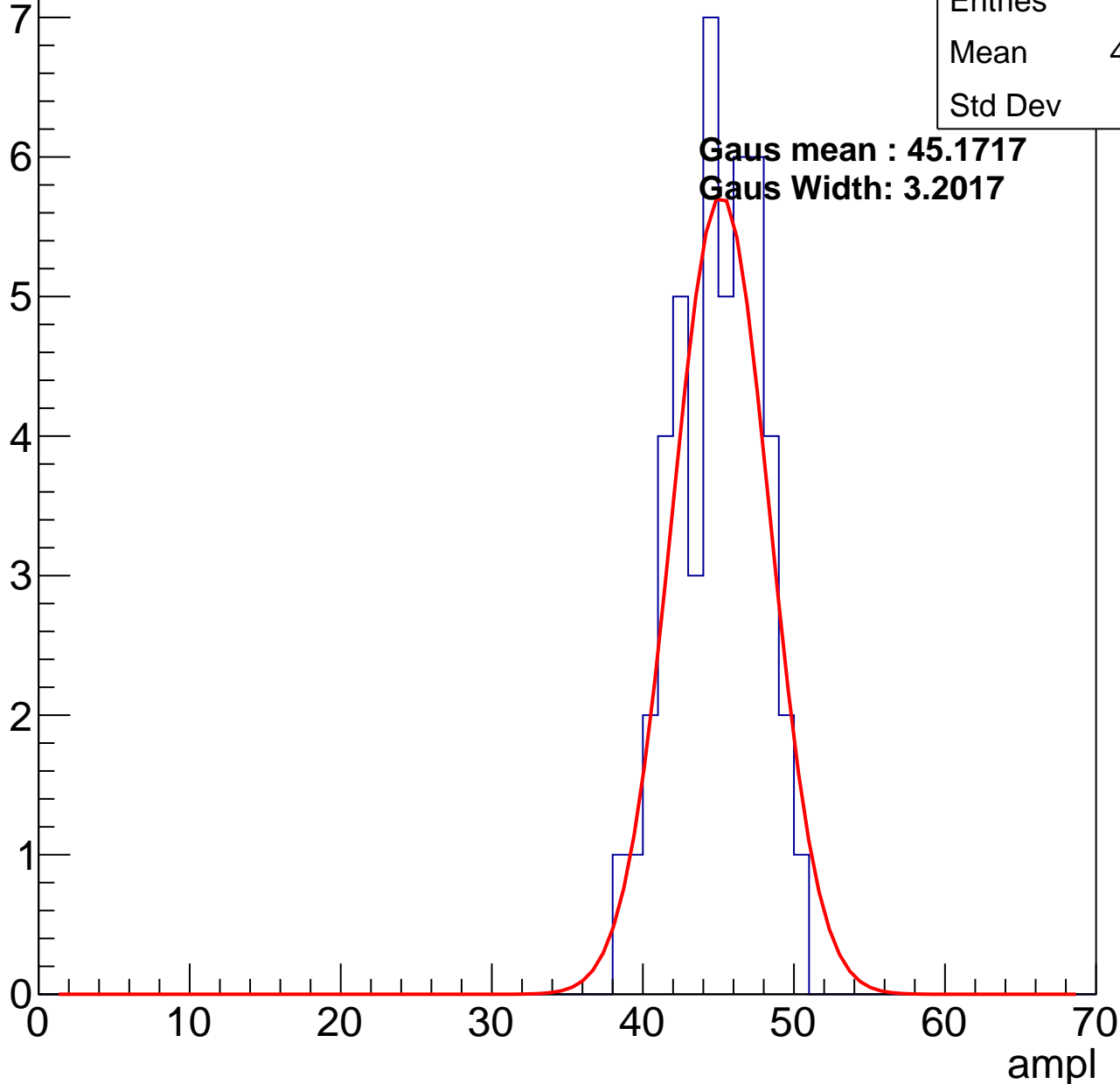
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	44.49
Std Dev	2.82

Gaus mean : 45.1717

Gaus Width: 3.2017

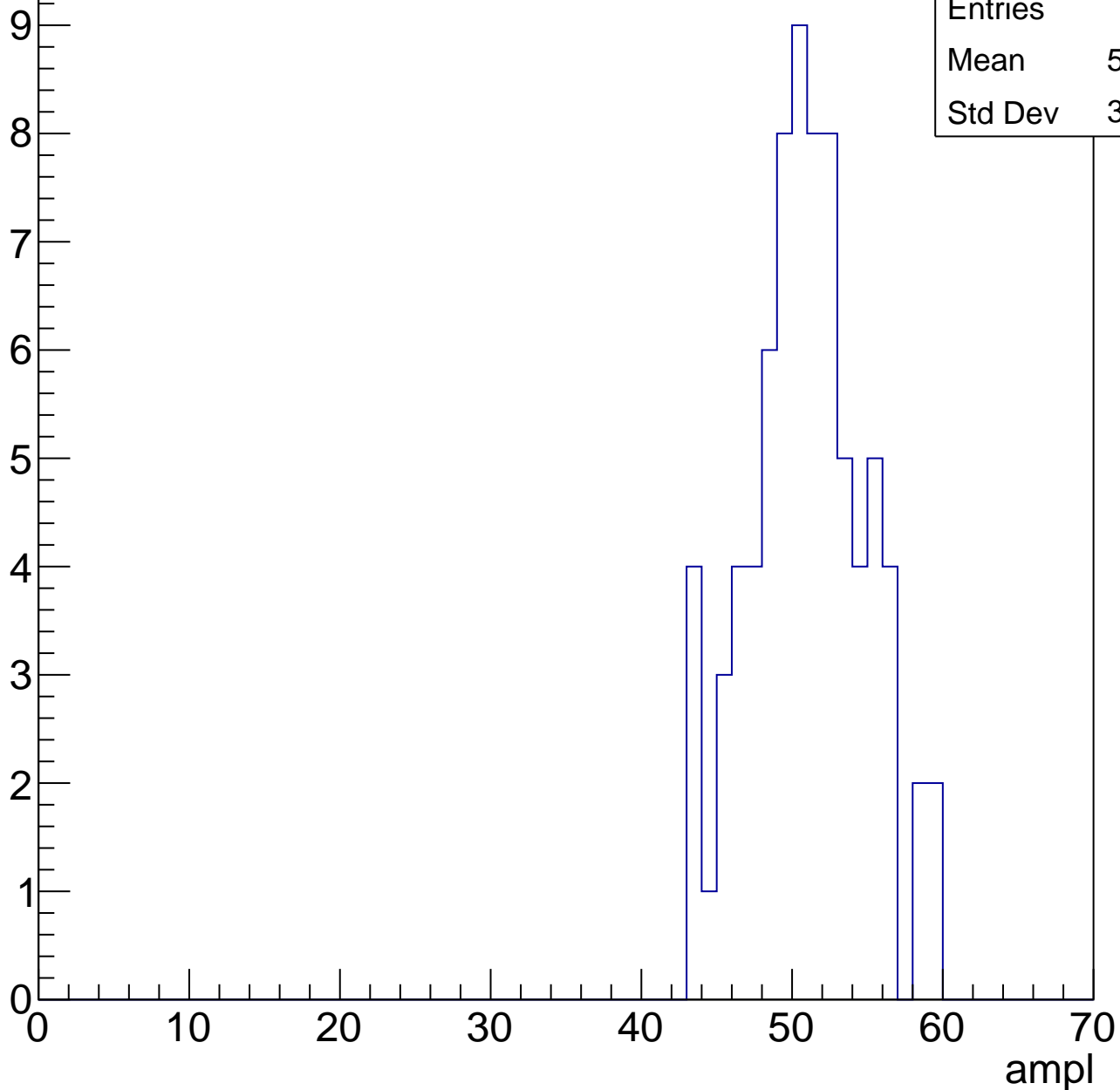


B0L002S, U18-ch22, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

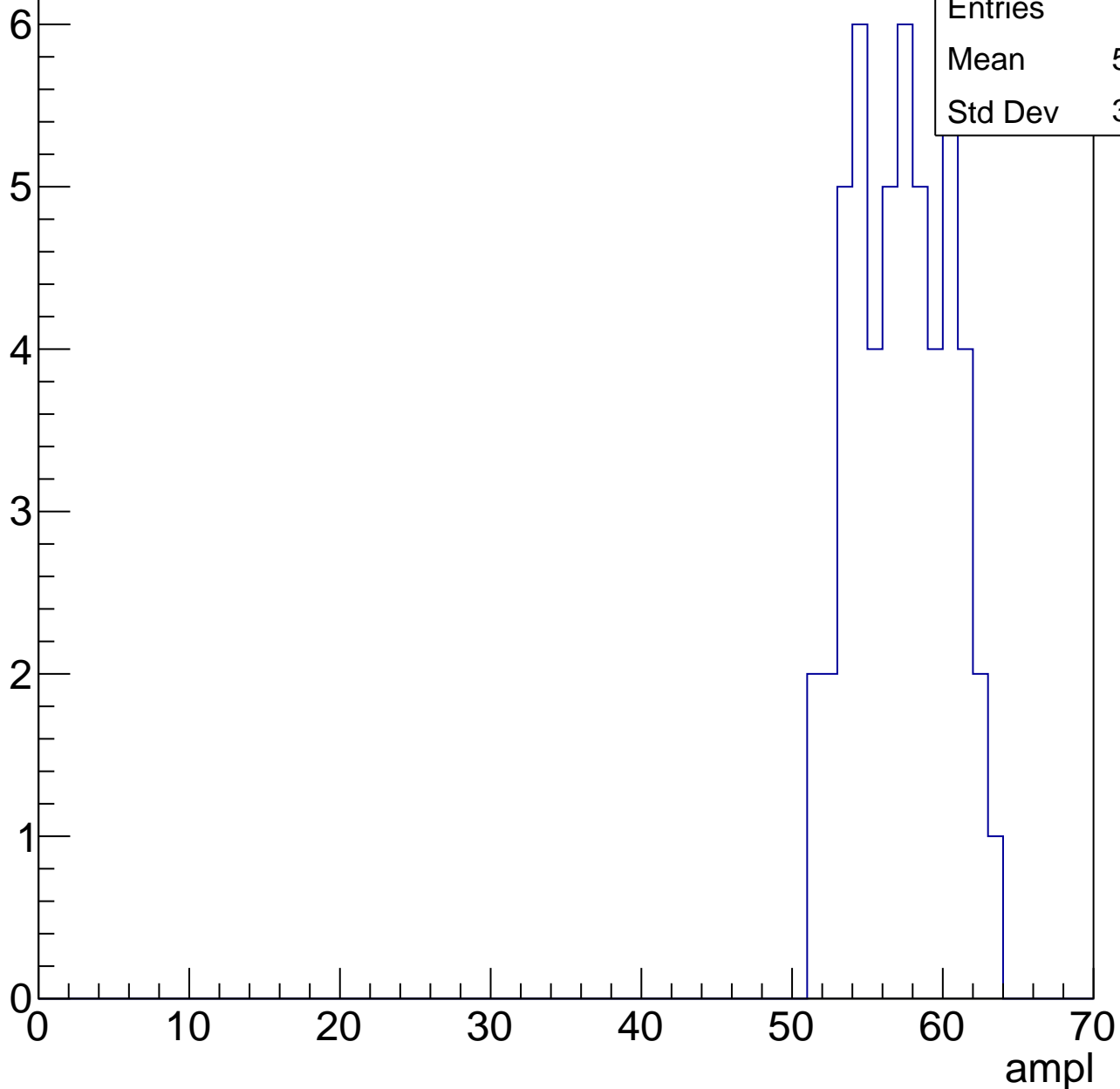
Entries	77
Mean	50.53
Std Dev	3.823



B0L002S, U18-ch22, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



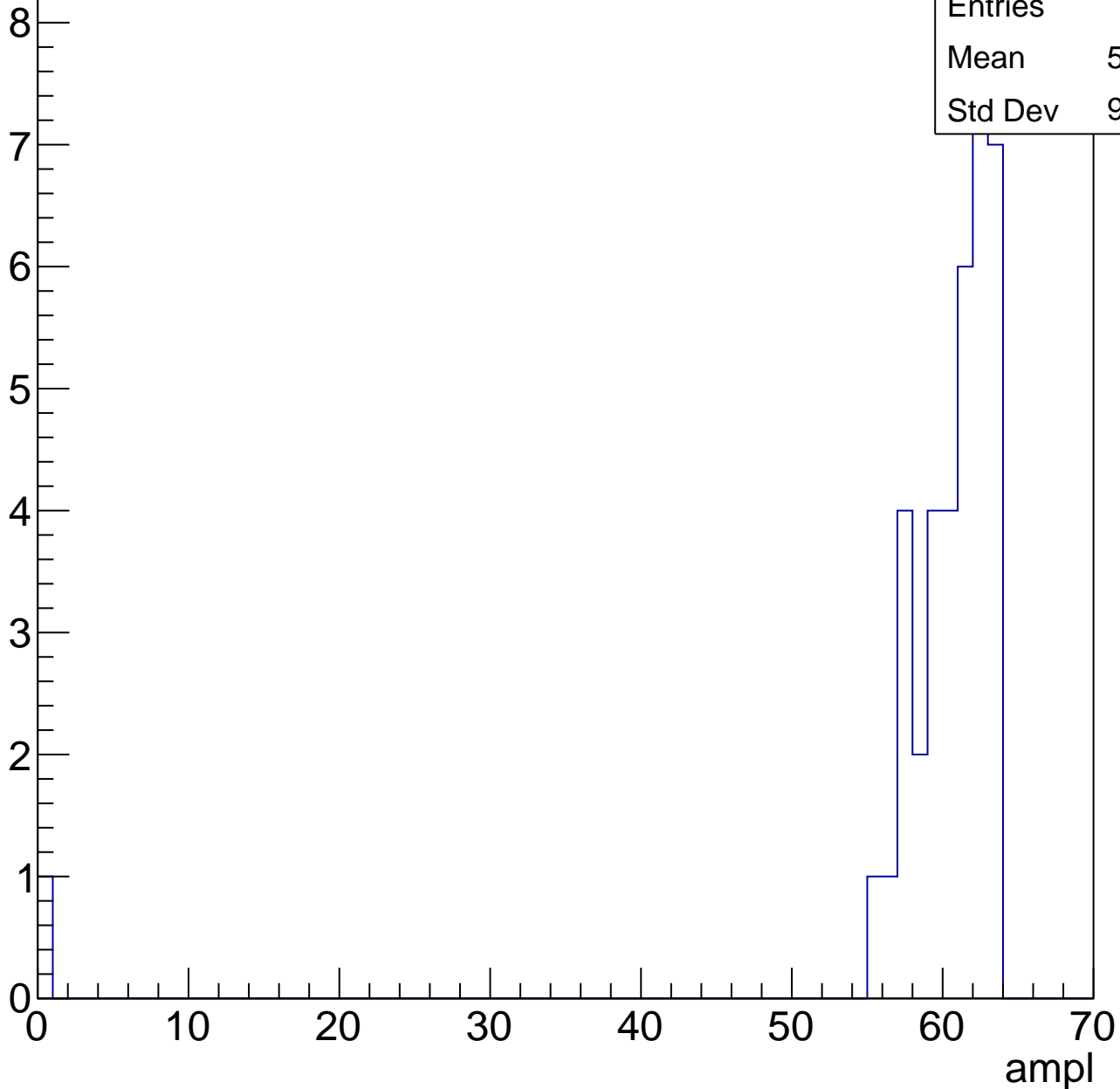
Entries	52
Mean	56.81
Std Dev	3.101

B0L002S, U18-ch22, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

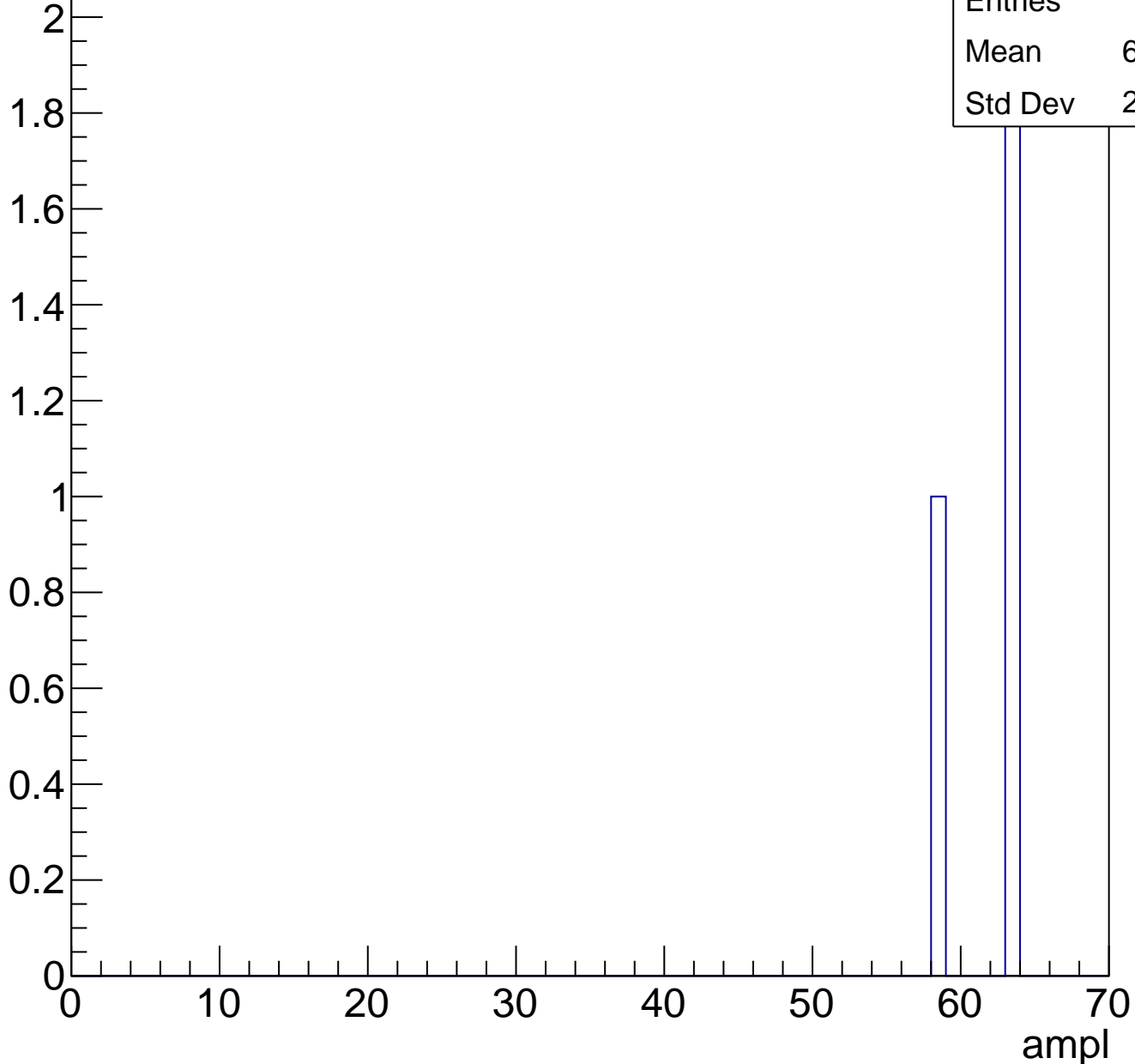
Entries	38
Mean	58.79
Std Dev	9.913



B0L002S, U18-ch22, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch22, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch23, adc0

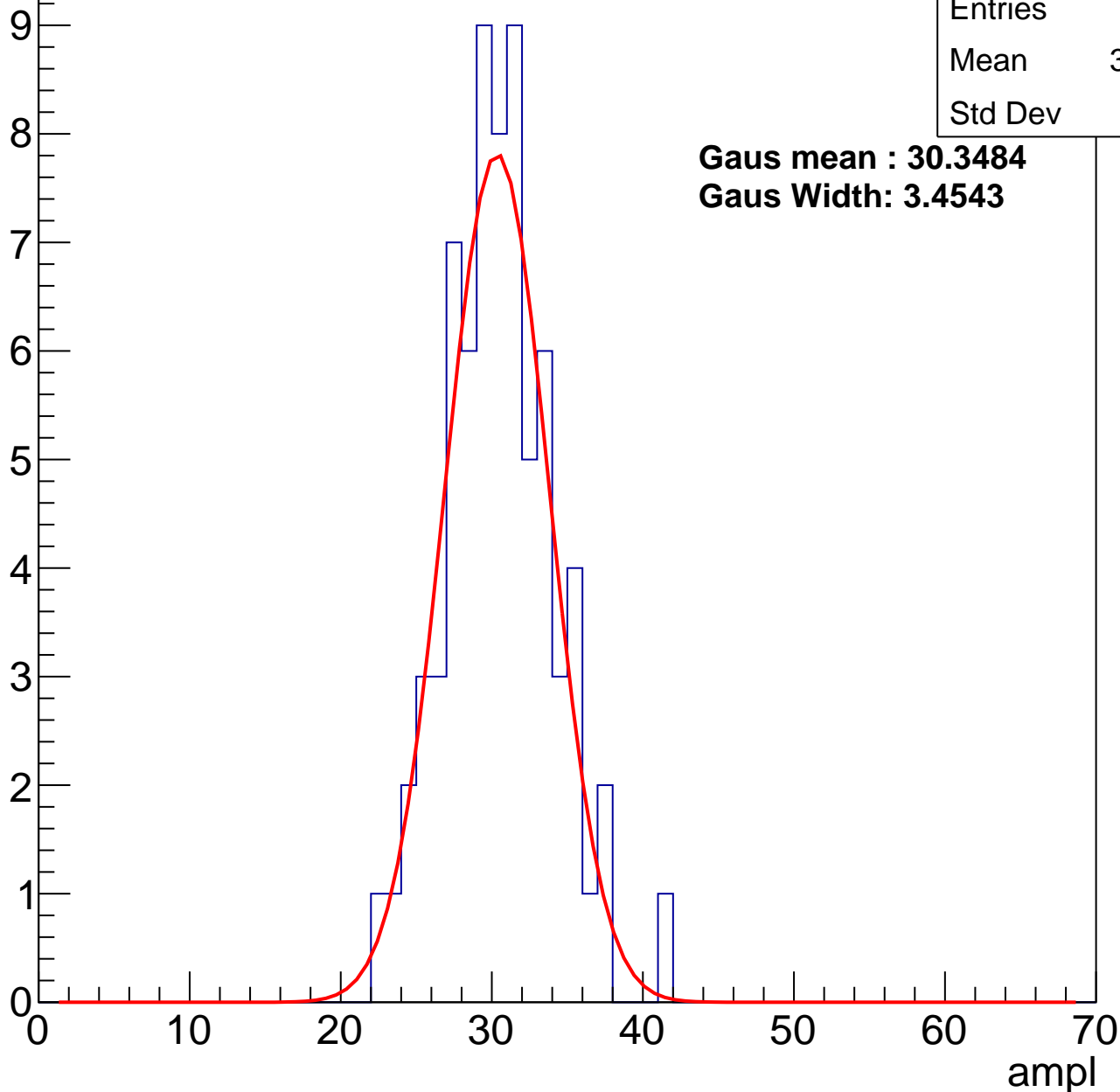
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.06
Std Dev	3.54

Gaus mean : 30.3484

Gaus Width: 3.4543



B0L002S, U18-ch23, adc1

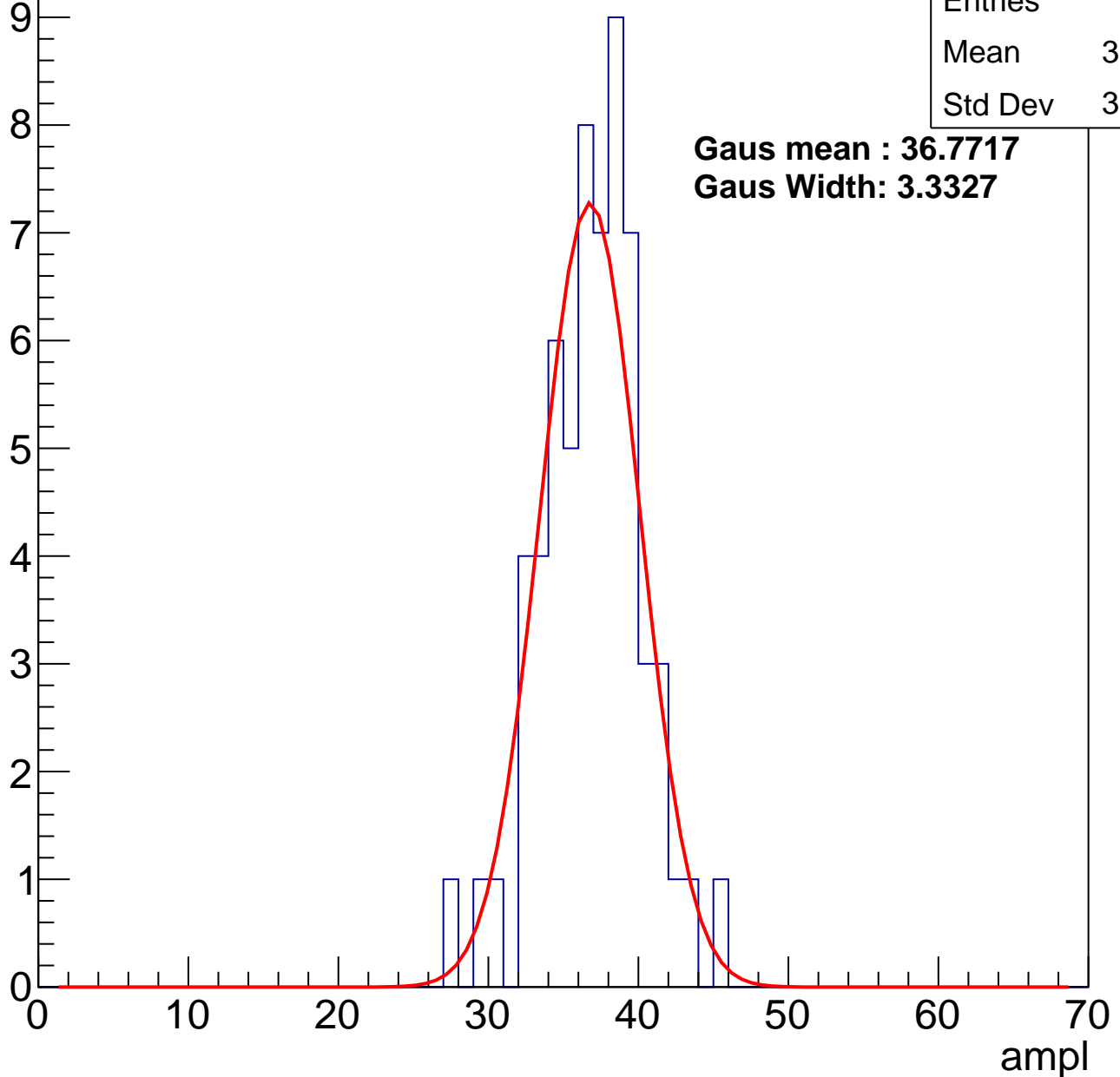
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.45
Std Dev	3.305

Gaus mean : 36.7717

Gaus Width: 3.3327



B0L002S, U18-ch23, adc2

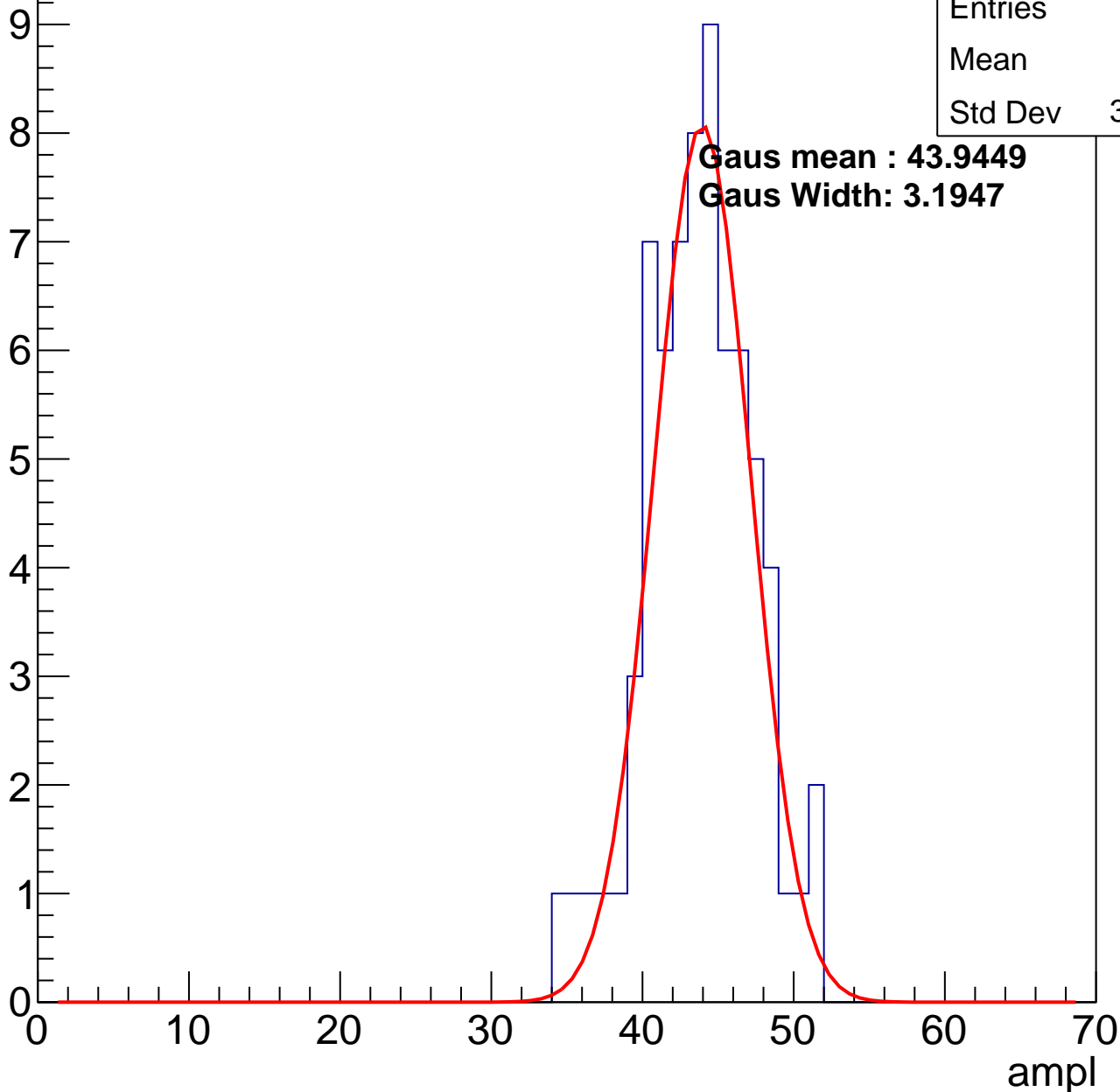
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.3
Std Dev	3.527

Gaus mean : 43.9449

Gaus Width: 3.1947

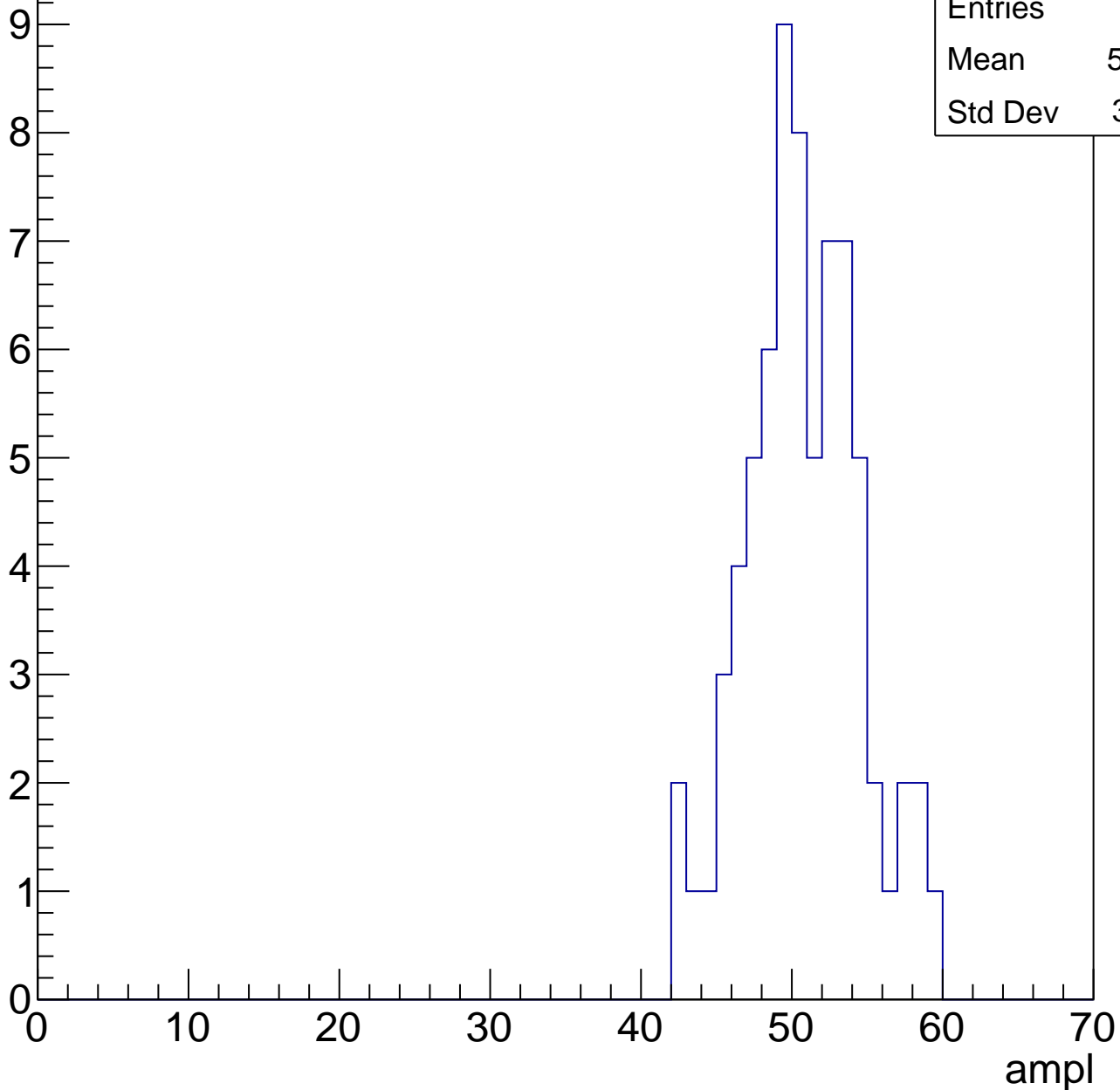


B0L002S, U18-ch23, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	50.27
Std Dev	3.741

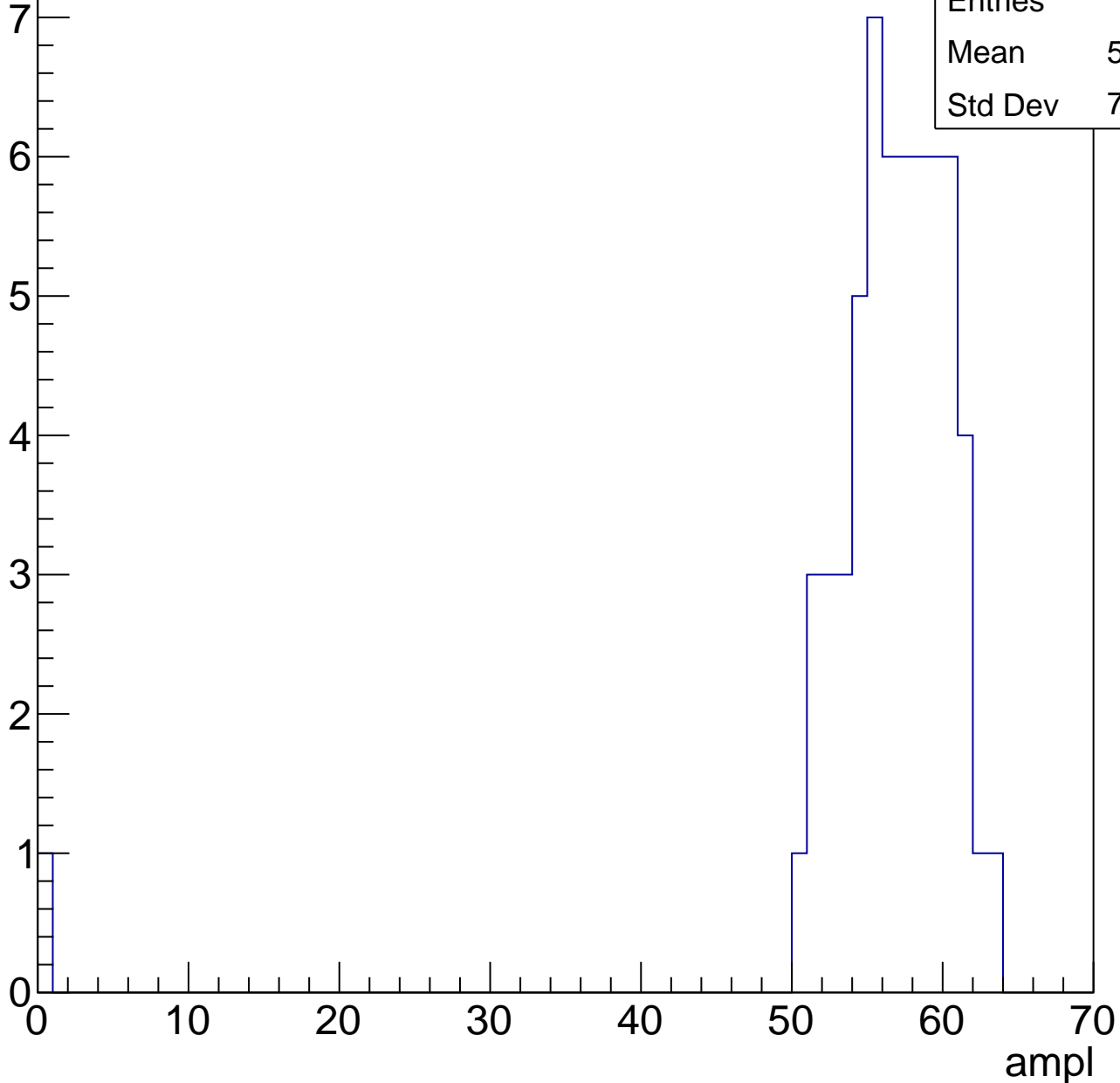


B0L002S, U18-ch23, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	55.63
Std Dev	7.925

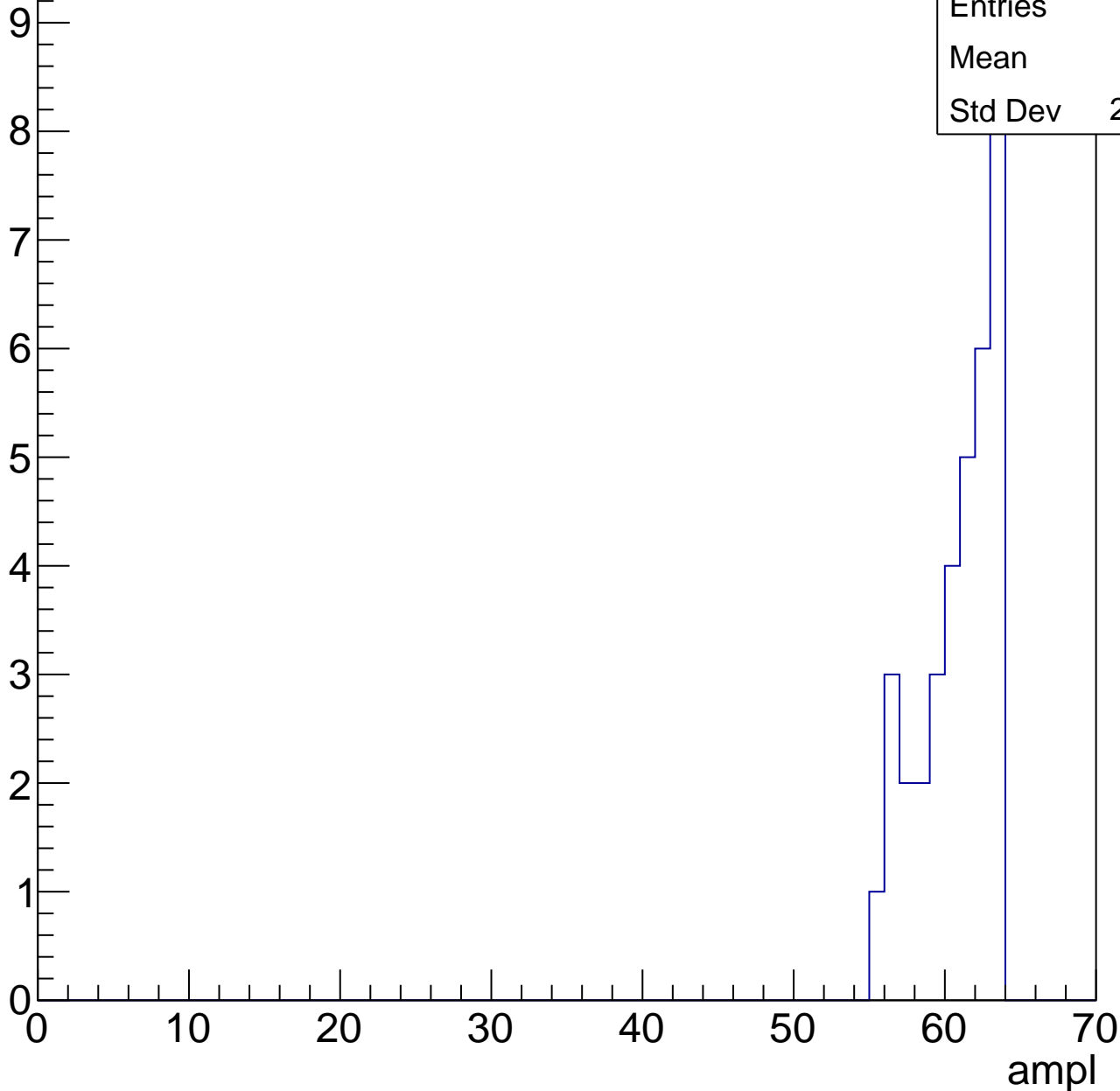


B0L002S, U18-ch23, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	60.4
Std Dev	2.428



B0L002S, U18-ch23, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch23, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch24, adc0

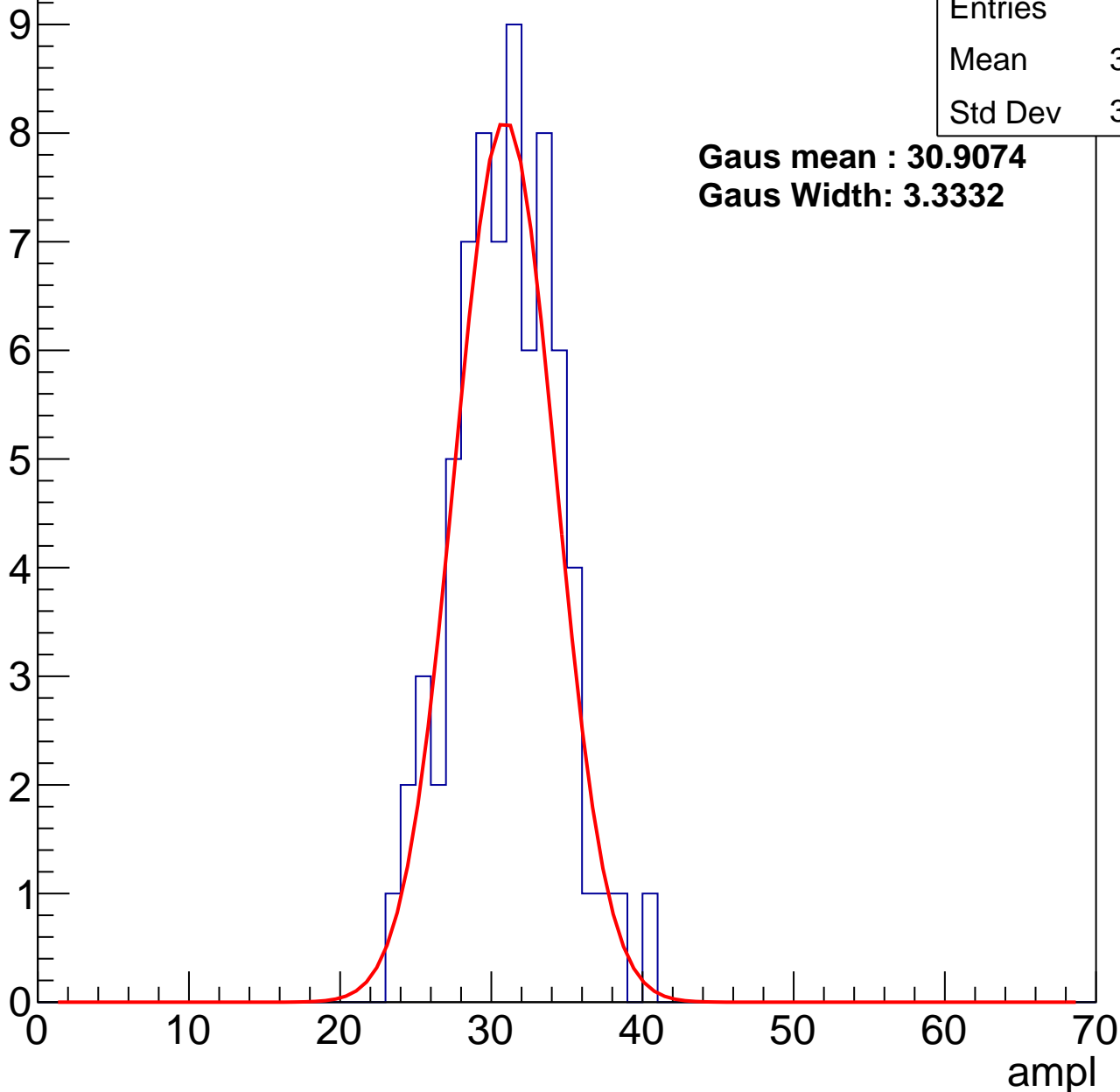
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.57
Std Dev	3.407

Gaus mean : 30.9074

Gaus Width: 3.3332



B0L002S, U18-ch24, adc1

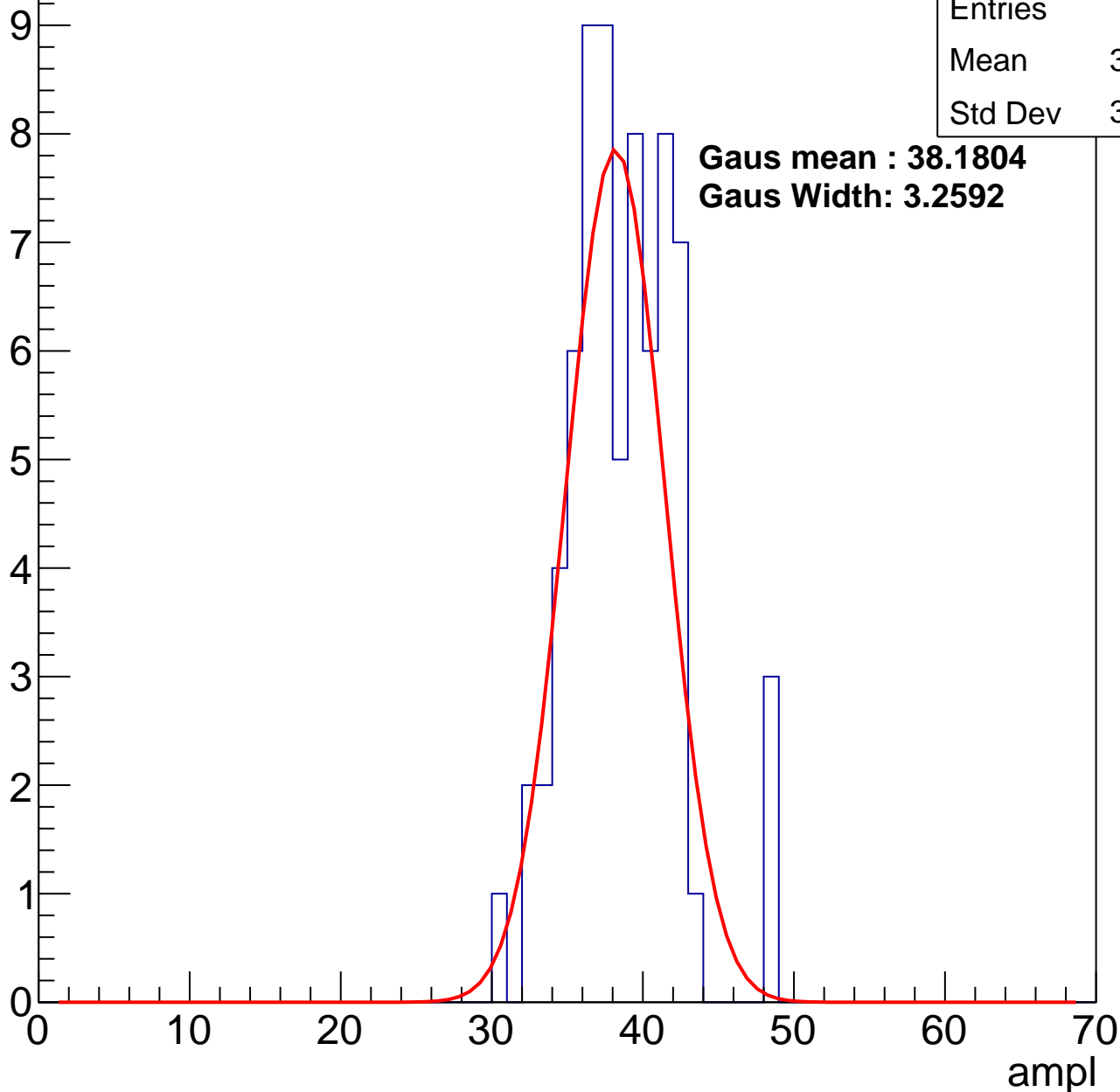
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38.23
Std Dev	3.533

Gaus mean : 38.1804

Gaus Width: 3.2592



B0L002S, U18-ch24, adc2

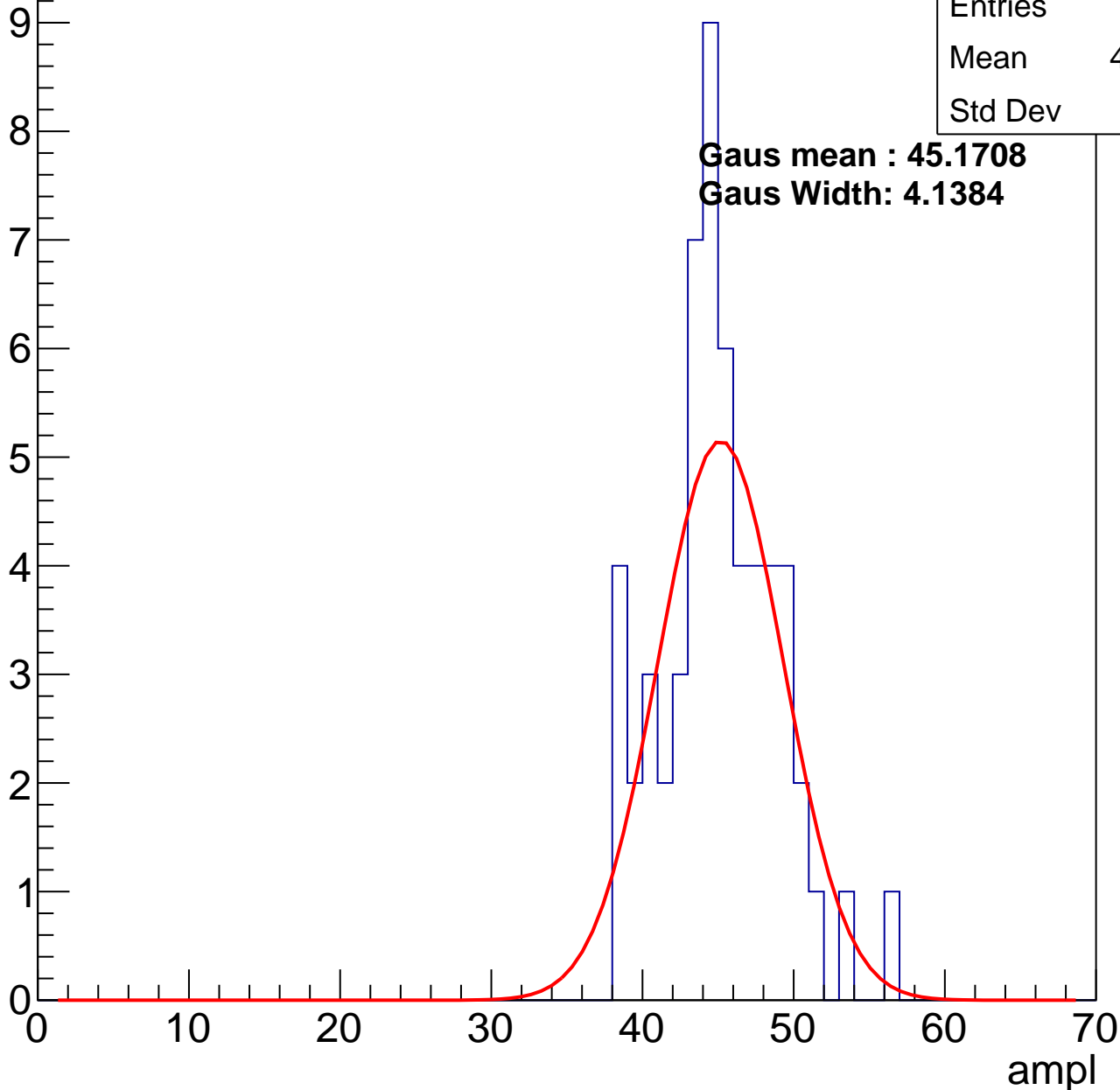
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	44.65
Std Dev	3.8

Gaus mean : 45.1708

Gaus Width: 4.1384

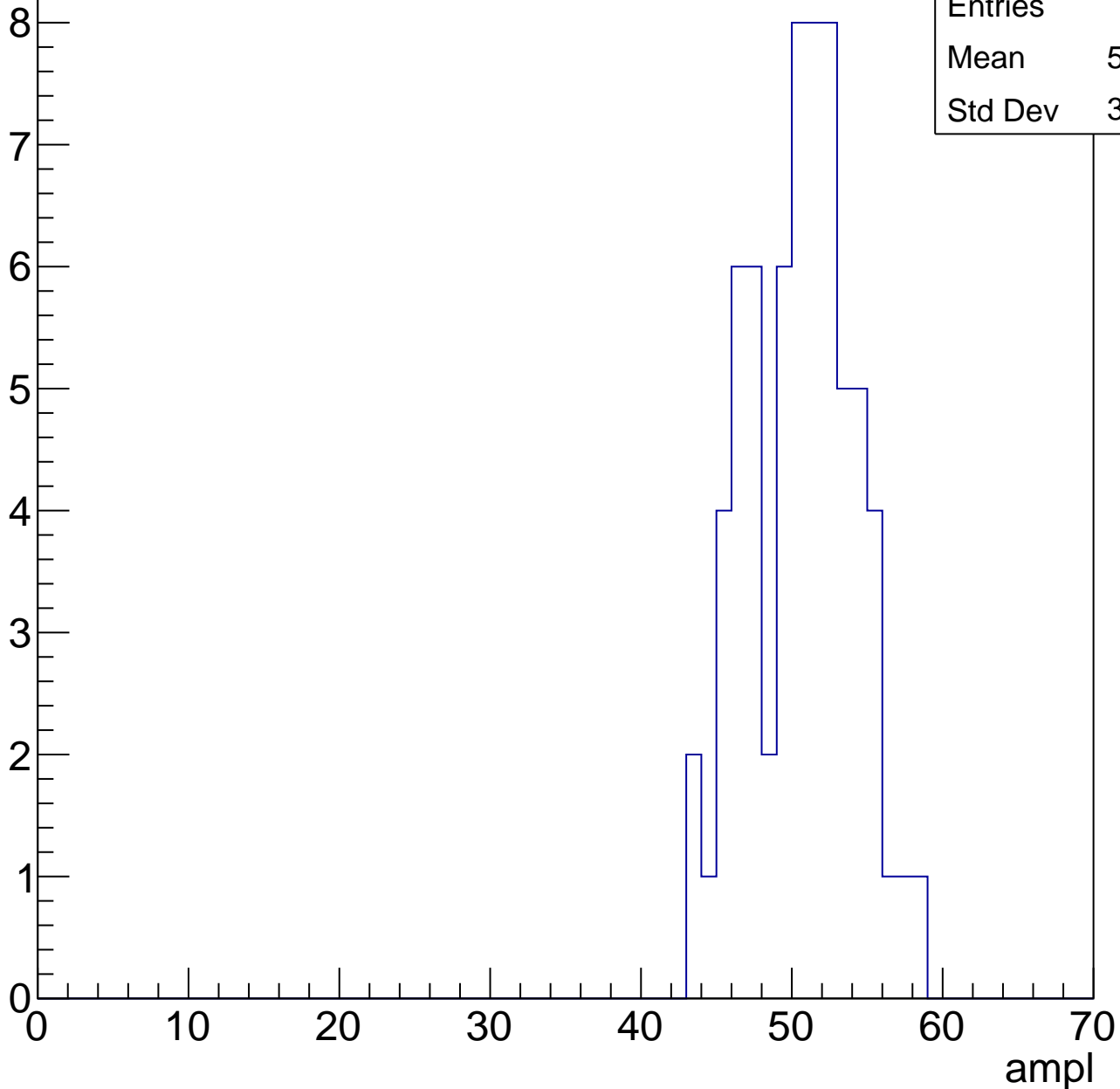


B0L002S, U18-ch24, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.12
Std Dev	3.454

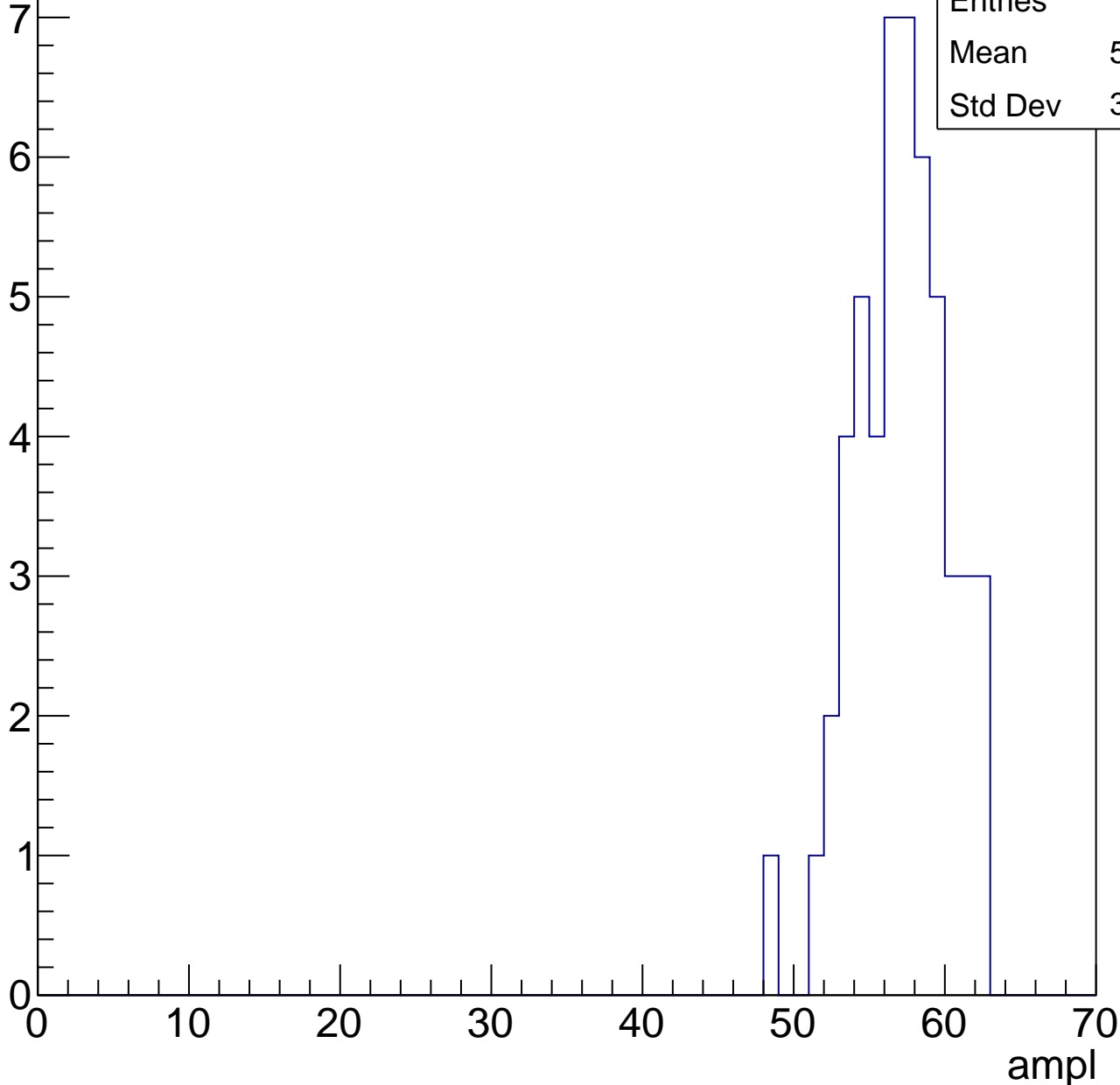


B0L002S, U18-ch24, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.63
Std Dev	3.029

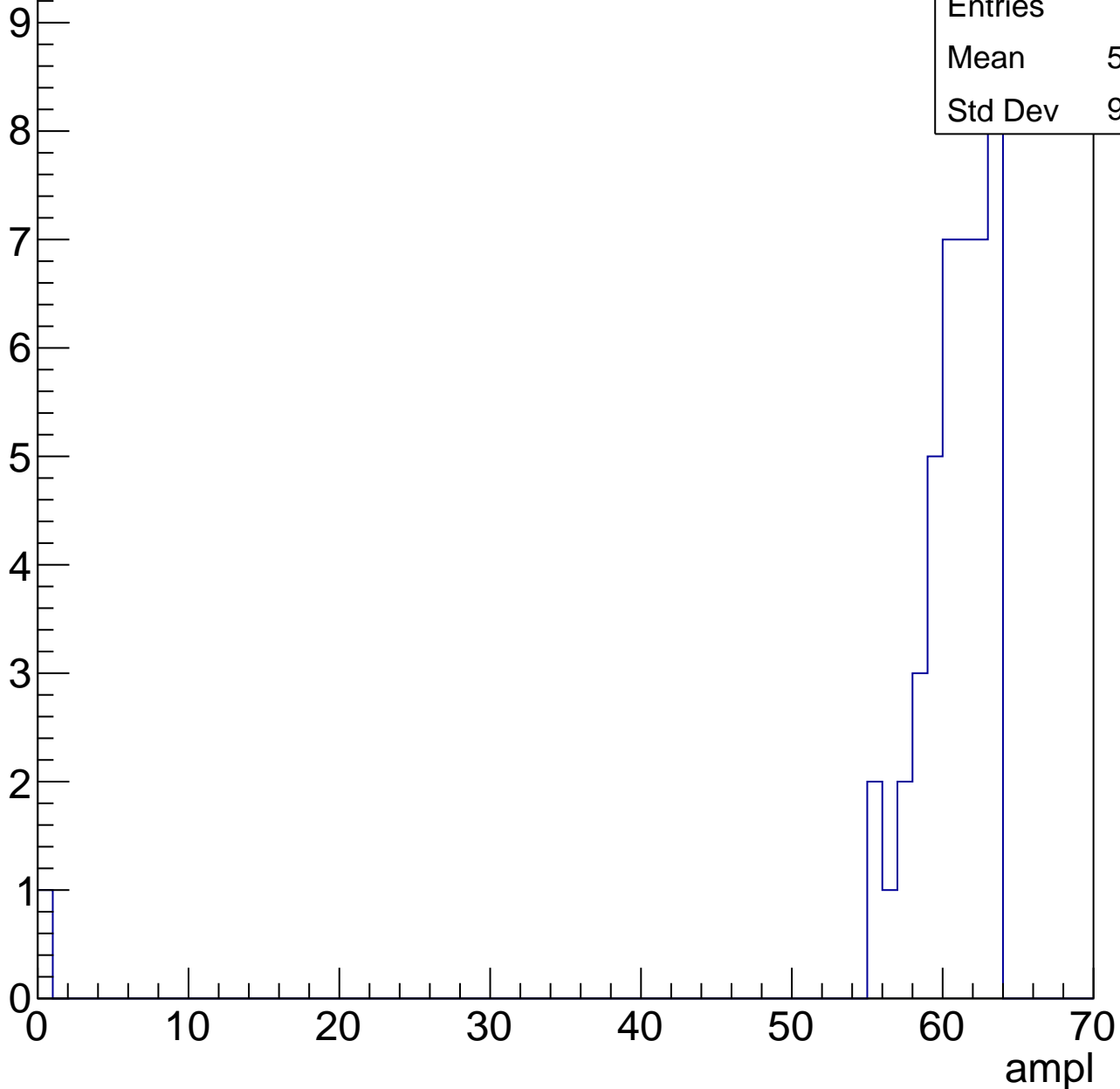


B0L002S, U18-ch24, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	59.02
Std Dev	9.263



B0L002S, U18-ch24, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch24, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch25, adc0

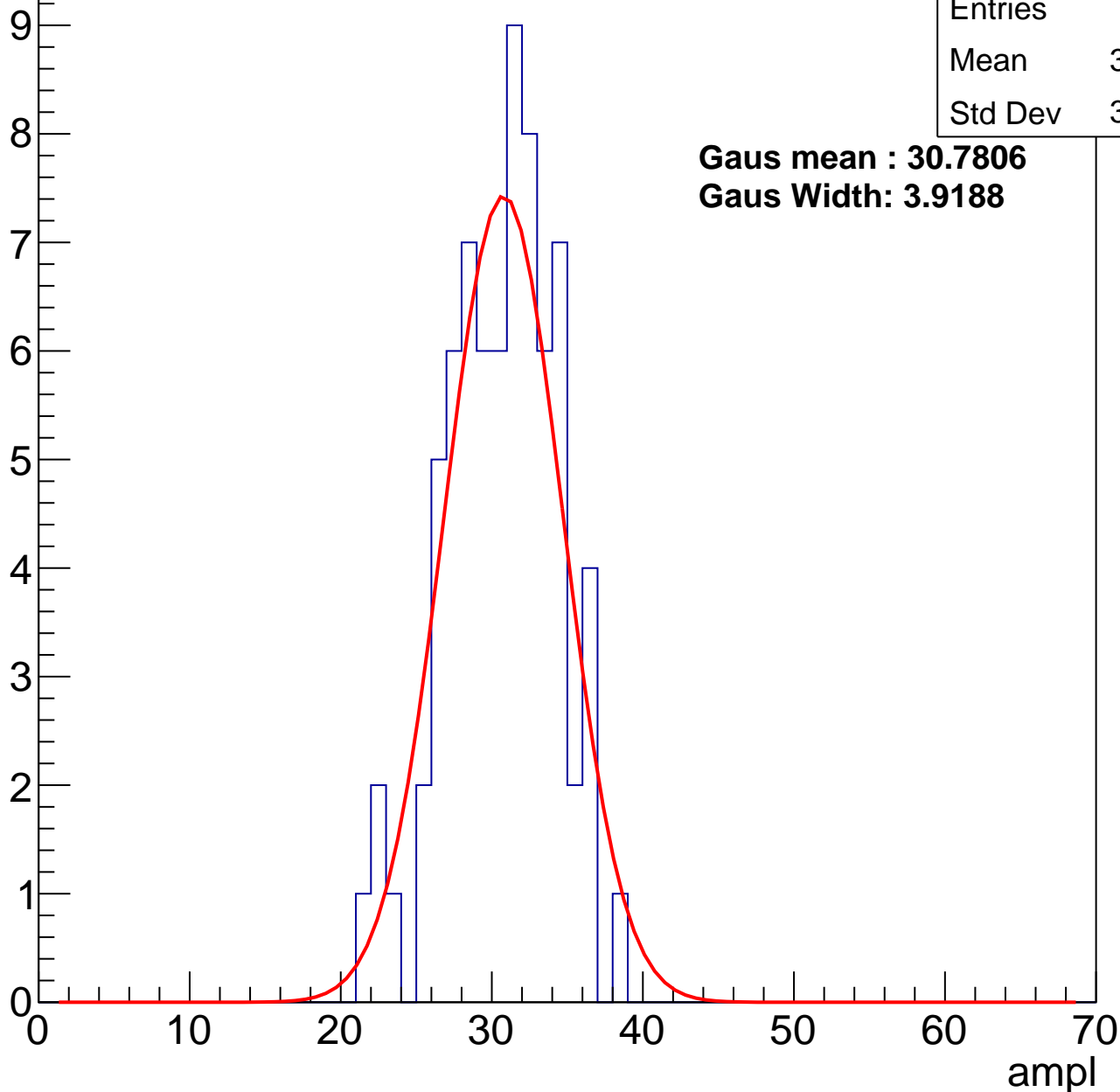
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	30.18
Std Dev	3.586

Gaus mean : 30.7806

Gaus Width: 3.9188



B0L002S, U18-ch25, adc1

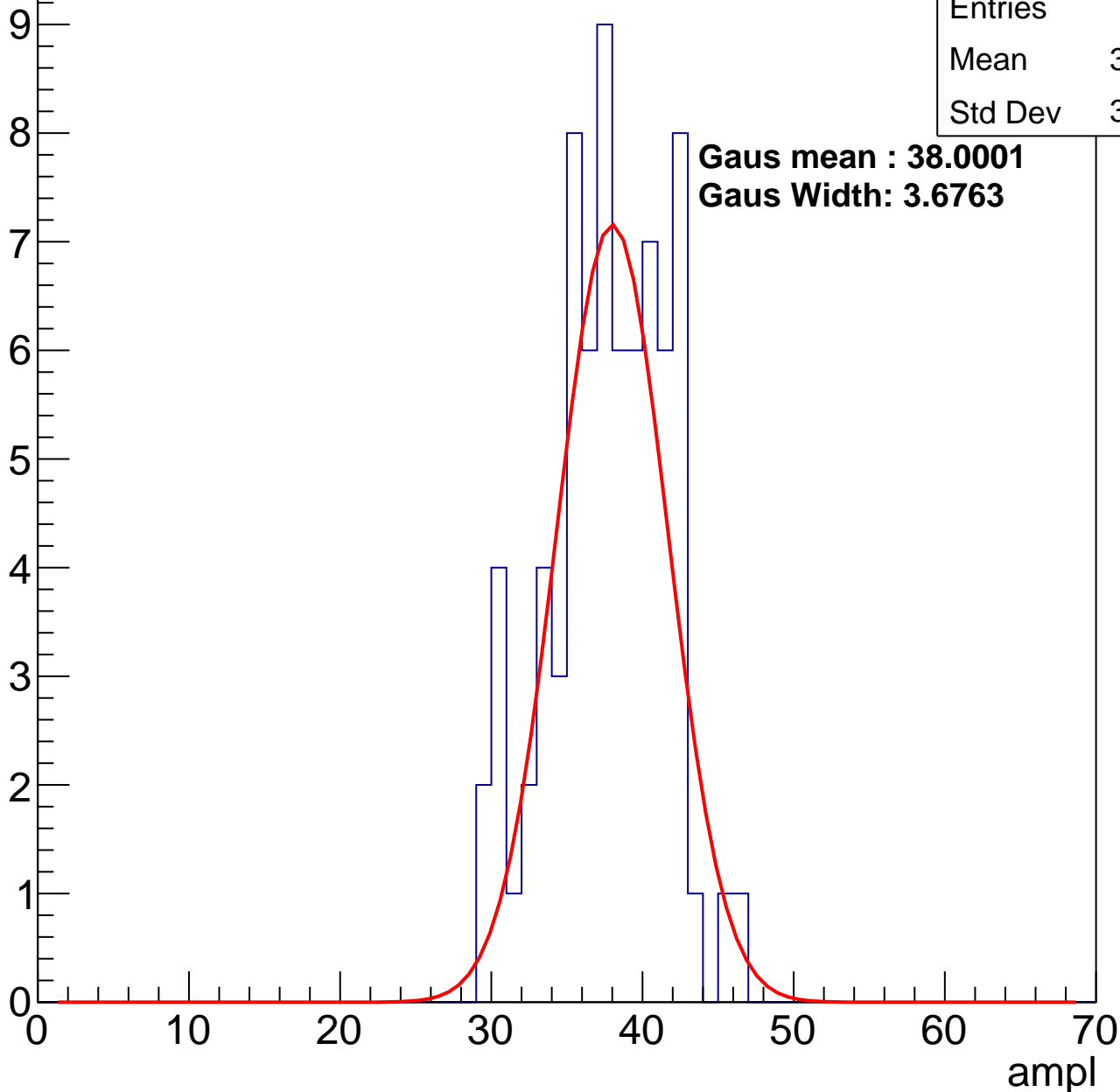
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.25
Std Dev	3.837

Gaus mean : 38.0001

Gaus Width: 3.6763



B0L002S, U18-ch25, adc2

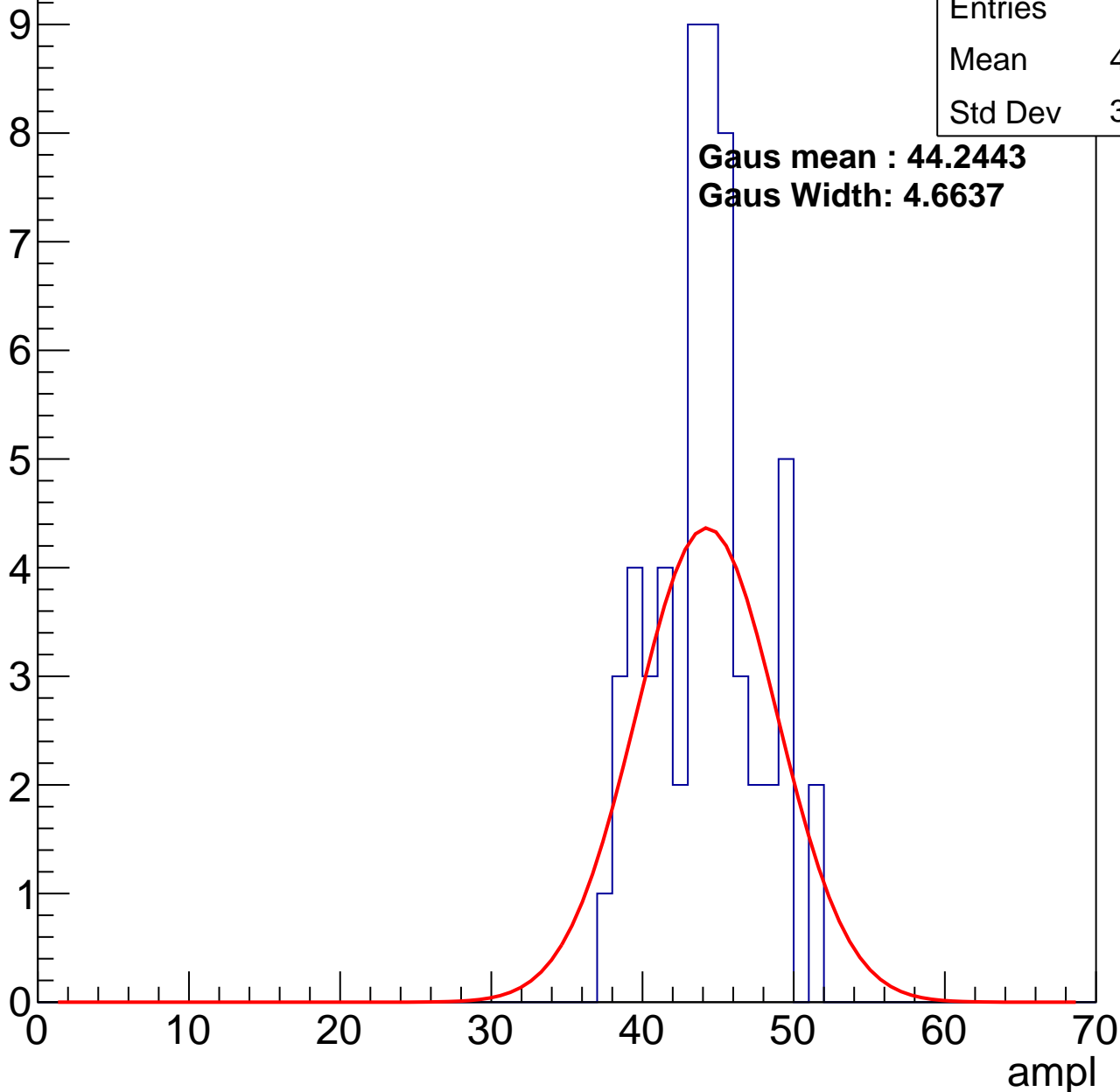
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.74
Std Dev	3.369

Gaus mean : 44.2443

Gaus Width: 4.6637

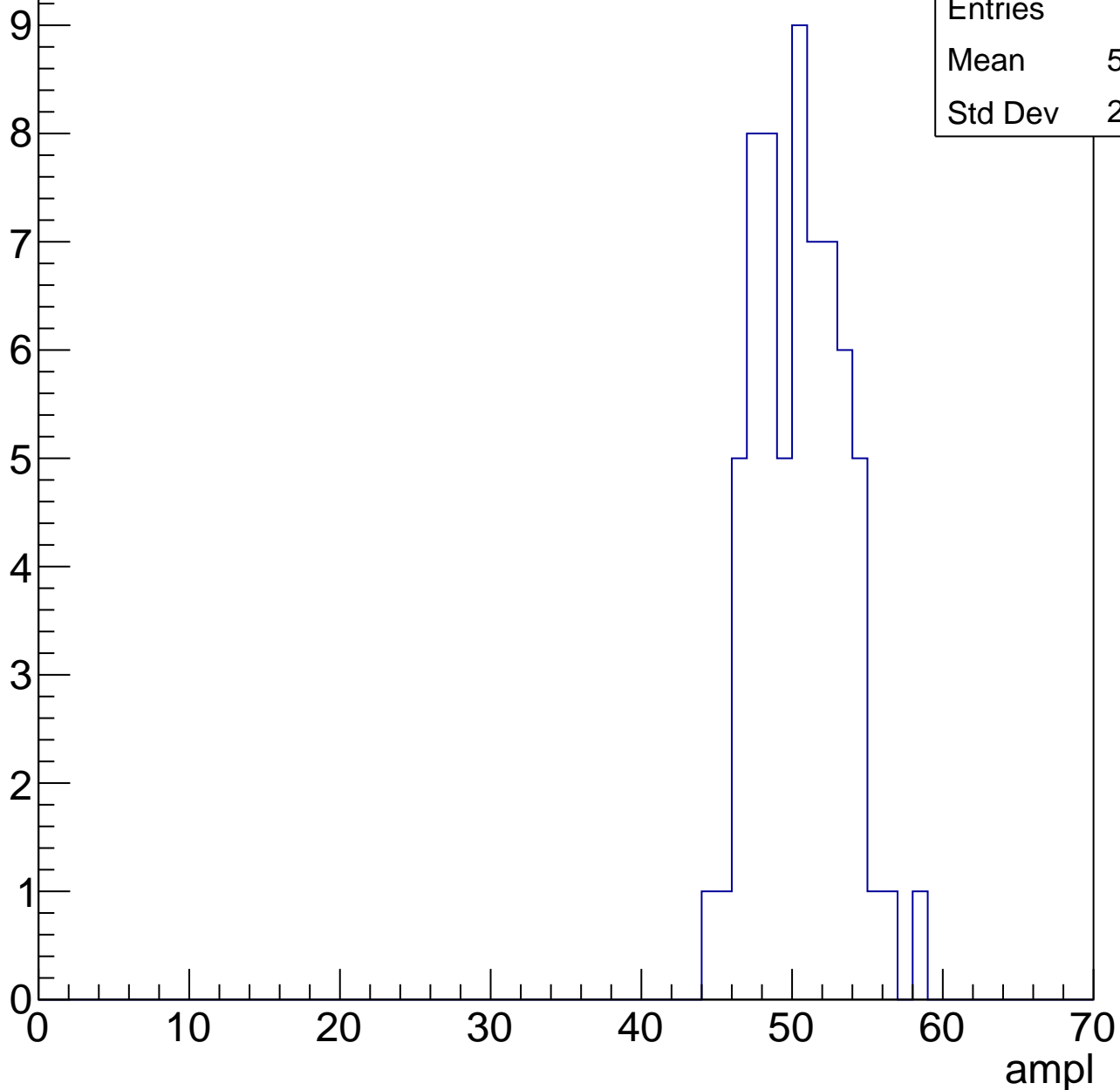


B0L002S, U18-ch25, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.03
Std Dev	2.893

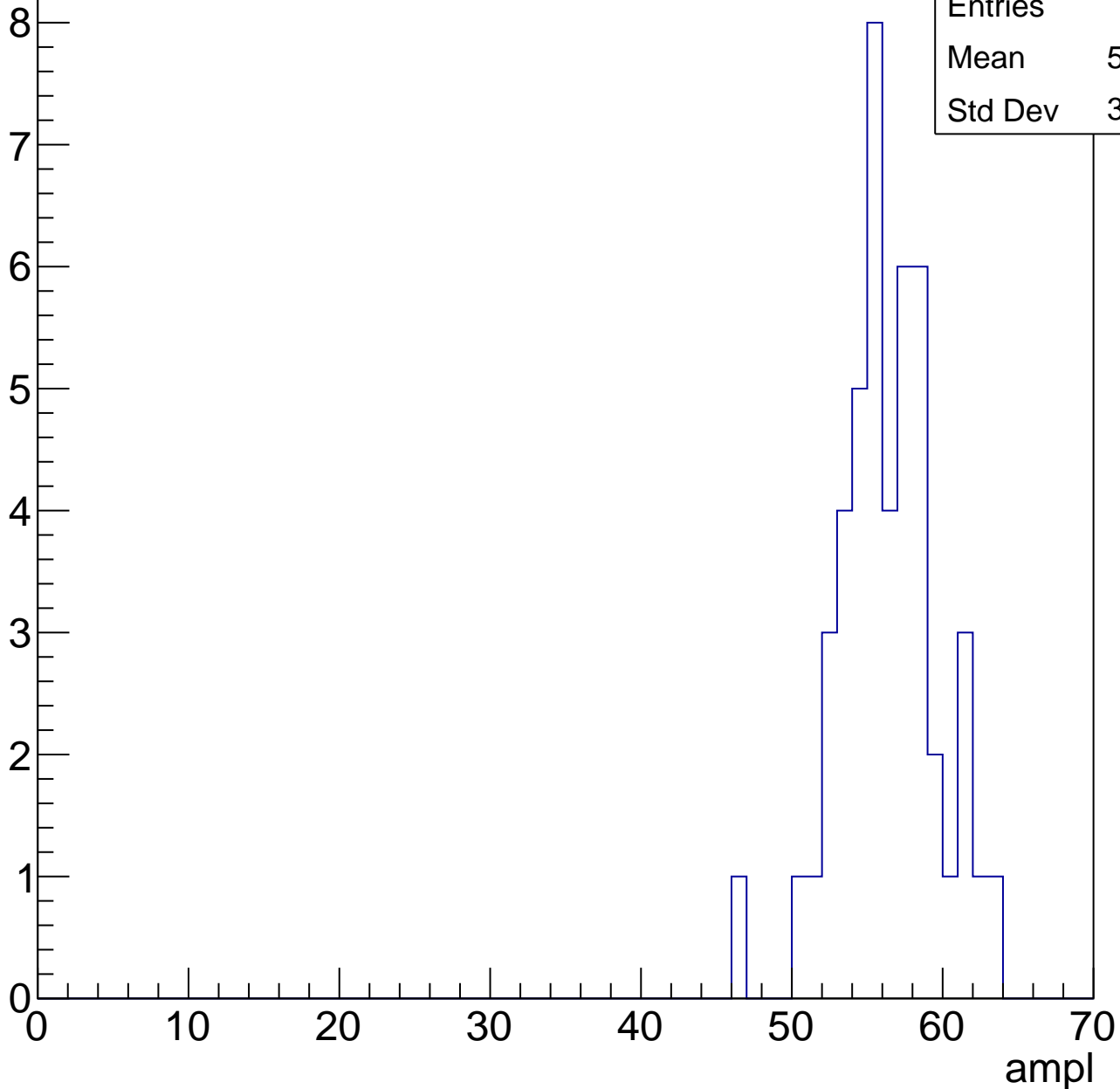


B0L002S, U18-ch25, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

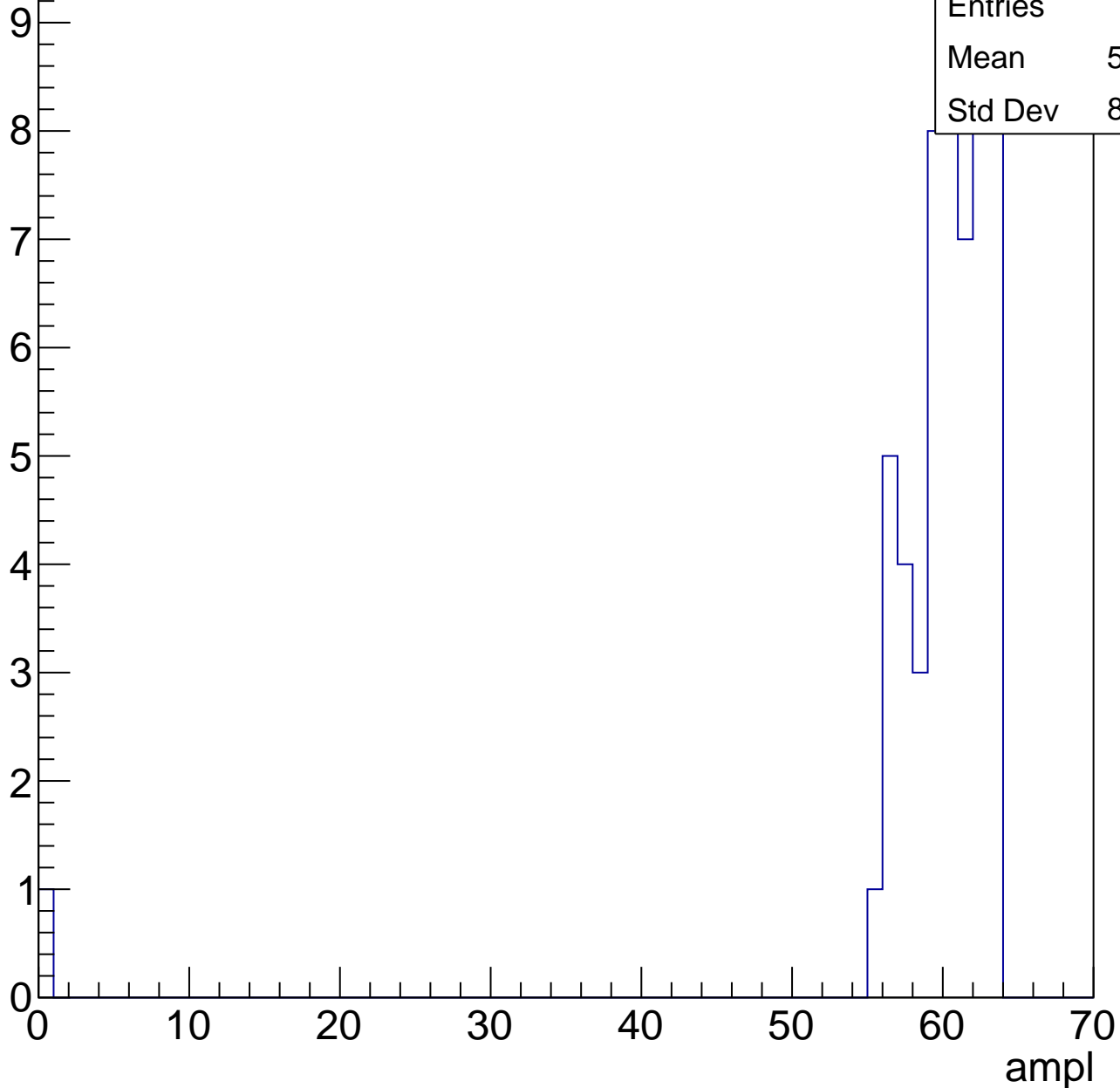
Entries	47
Mean	55.85
Std Dev	3.248



B0L002S, U18-ch25, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch25, adc6

calib_packv5_042523_0143.root, FC#8, port C1

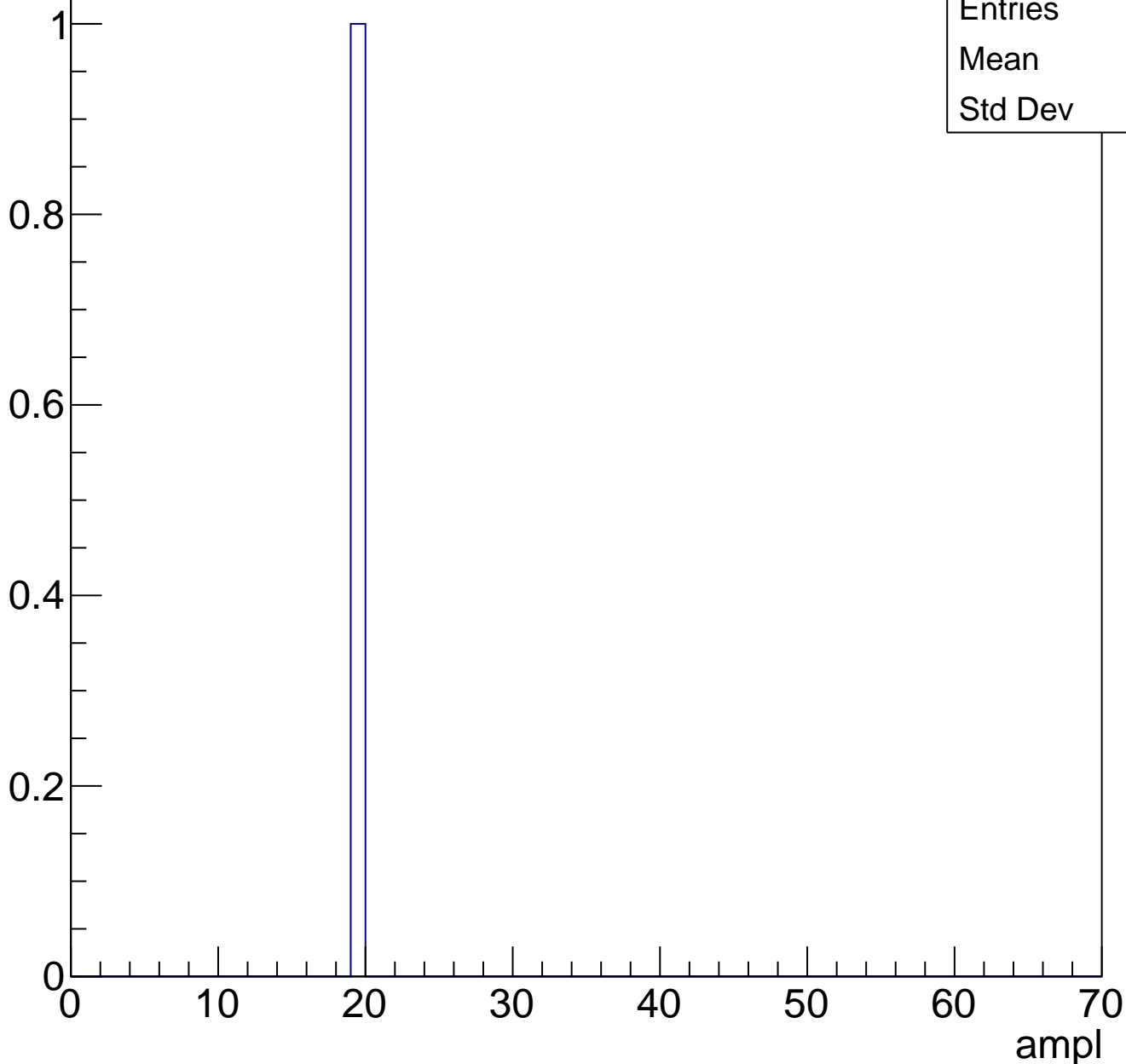
Entry



B0L002S, U18-ch25, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch26, adc0

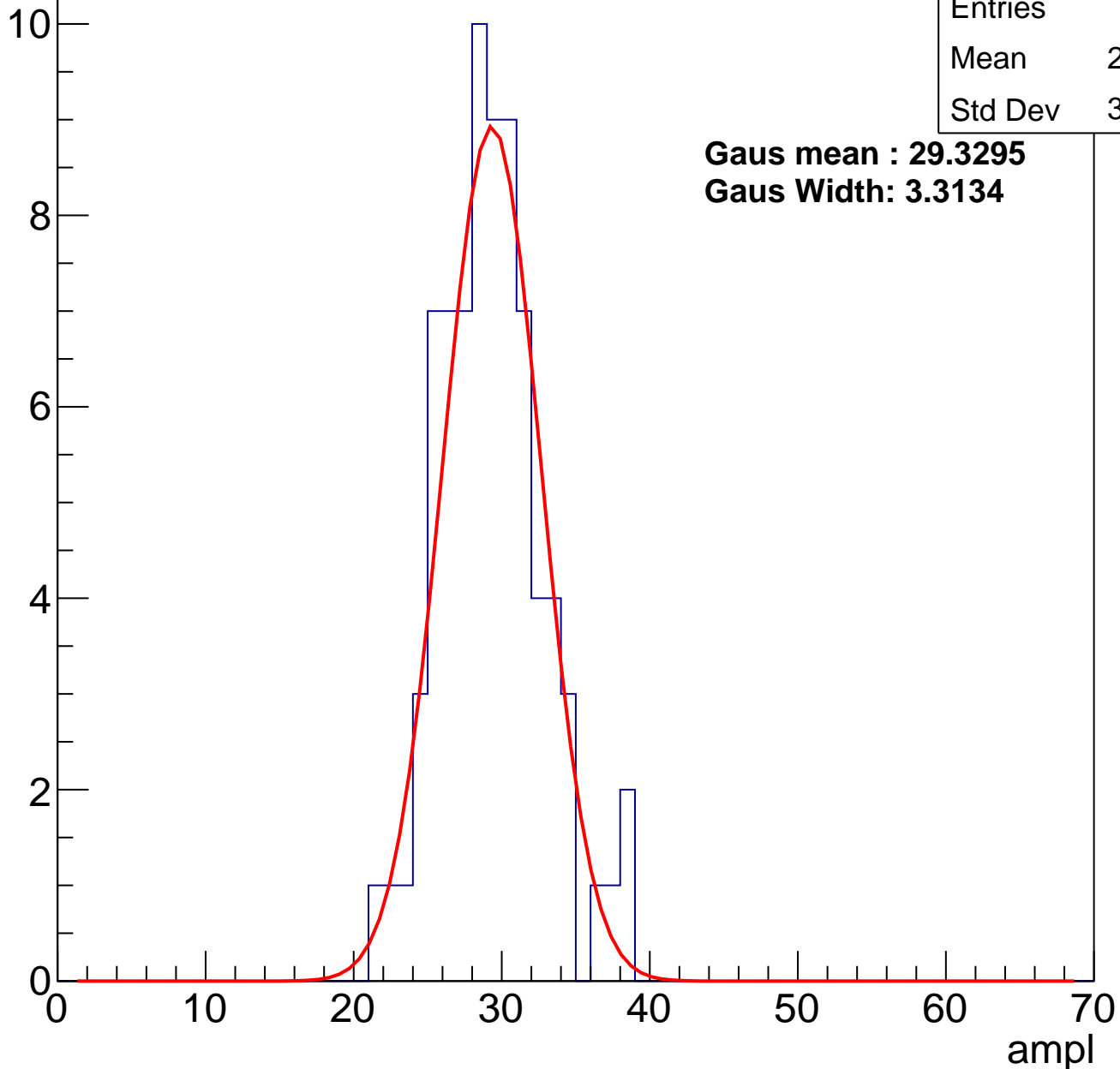
calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	28.87
Std Dev	3.462

Gaus mean : 29.3295

Gaus Width: 3.3134

Entry



B0L002S, U18-ch26, adc1

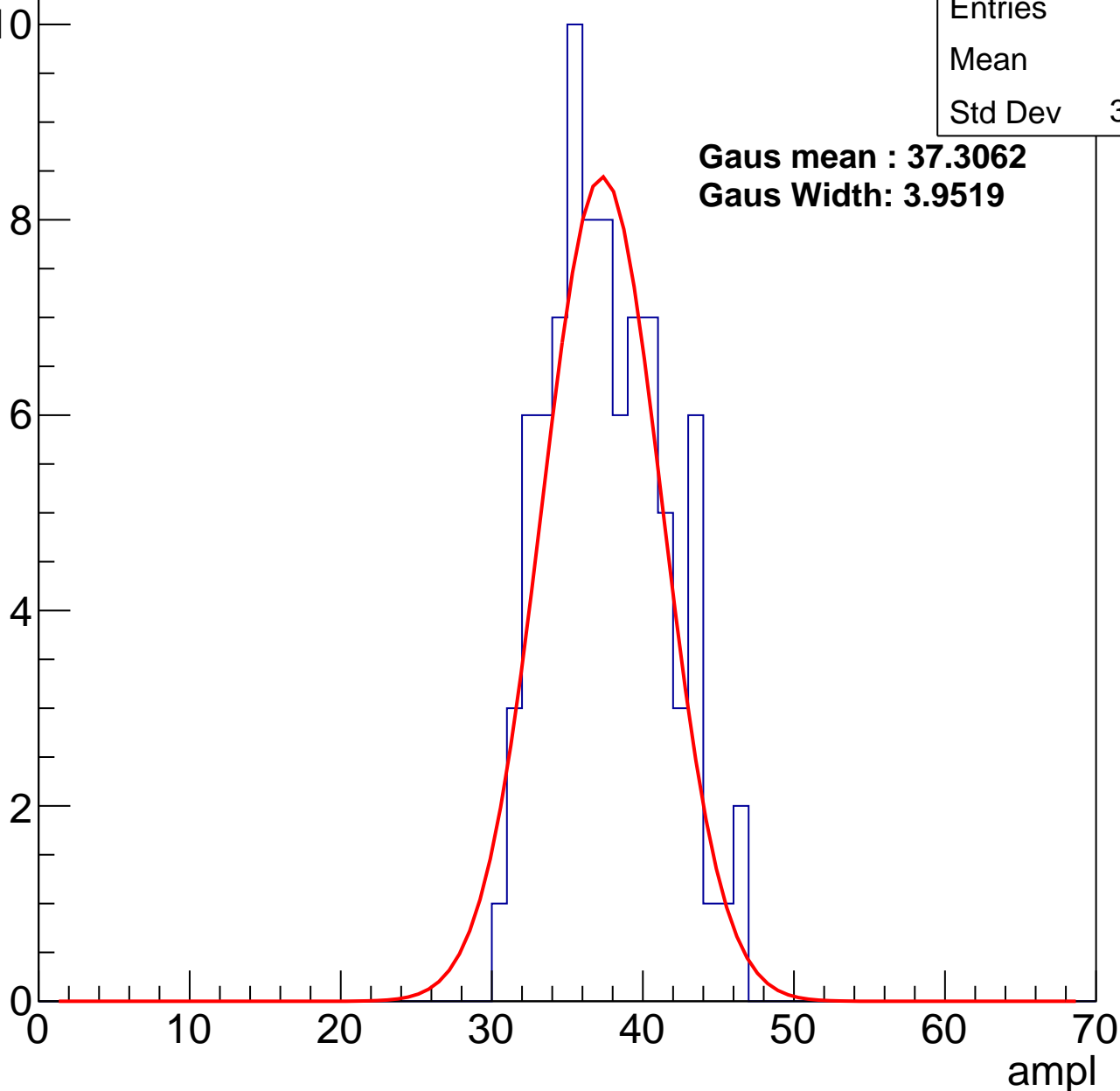
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	87
Mean	37.2
Std Dev	3.796

Gaus mean : 37.3062

Gaus Width: 3.9519



B0L002S, U18-ch26, adc2

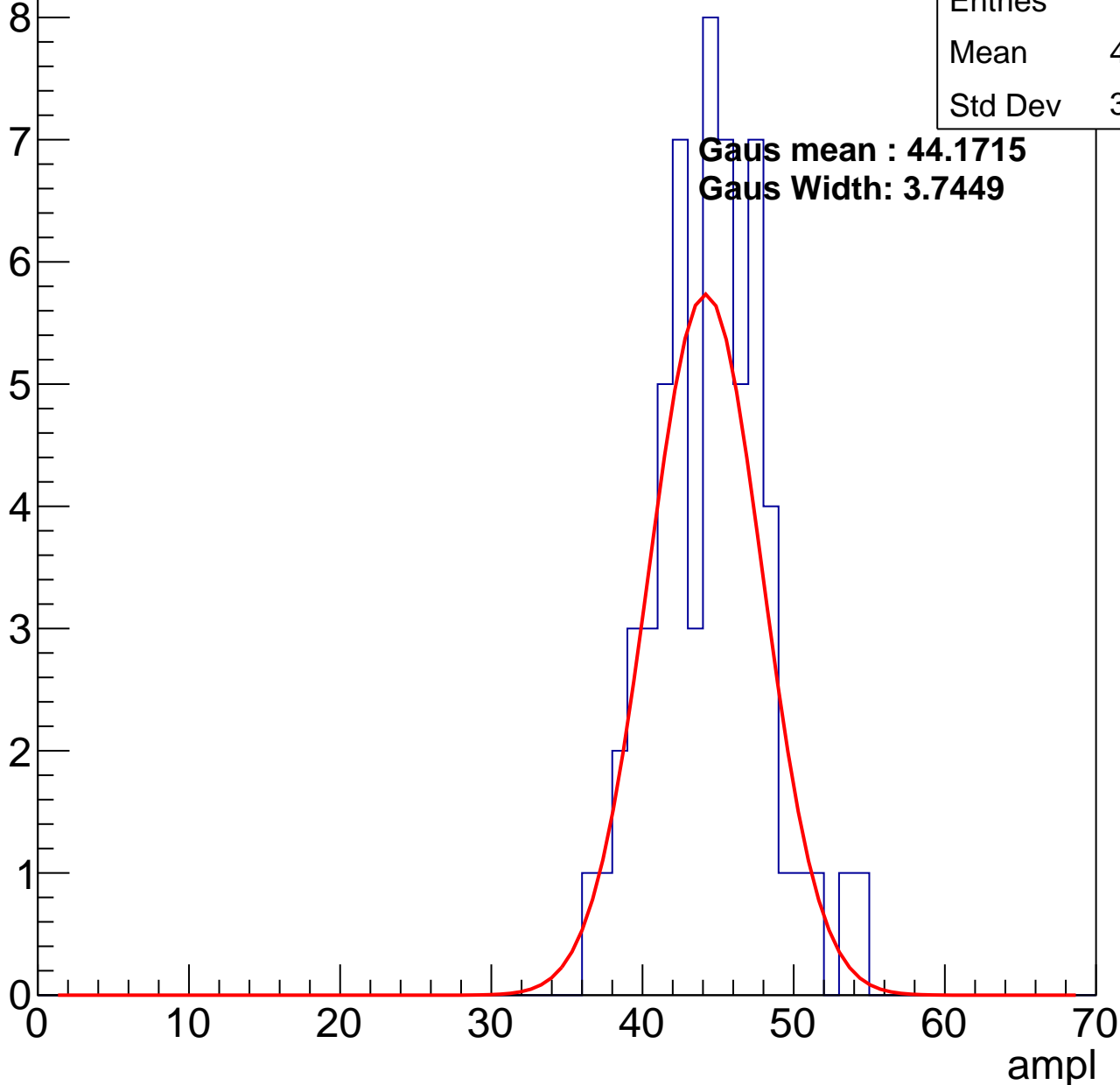
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	44.08
Std Dev	3.686

Gaus mean : 44.1715

Gaus Width: 3.7449

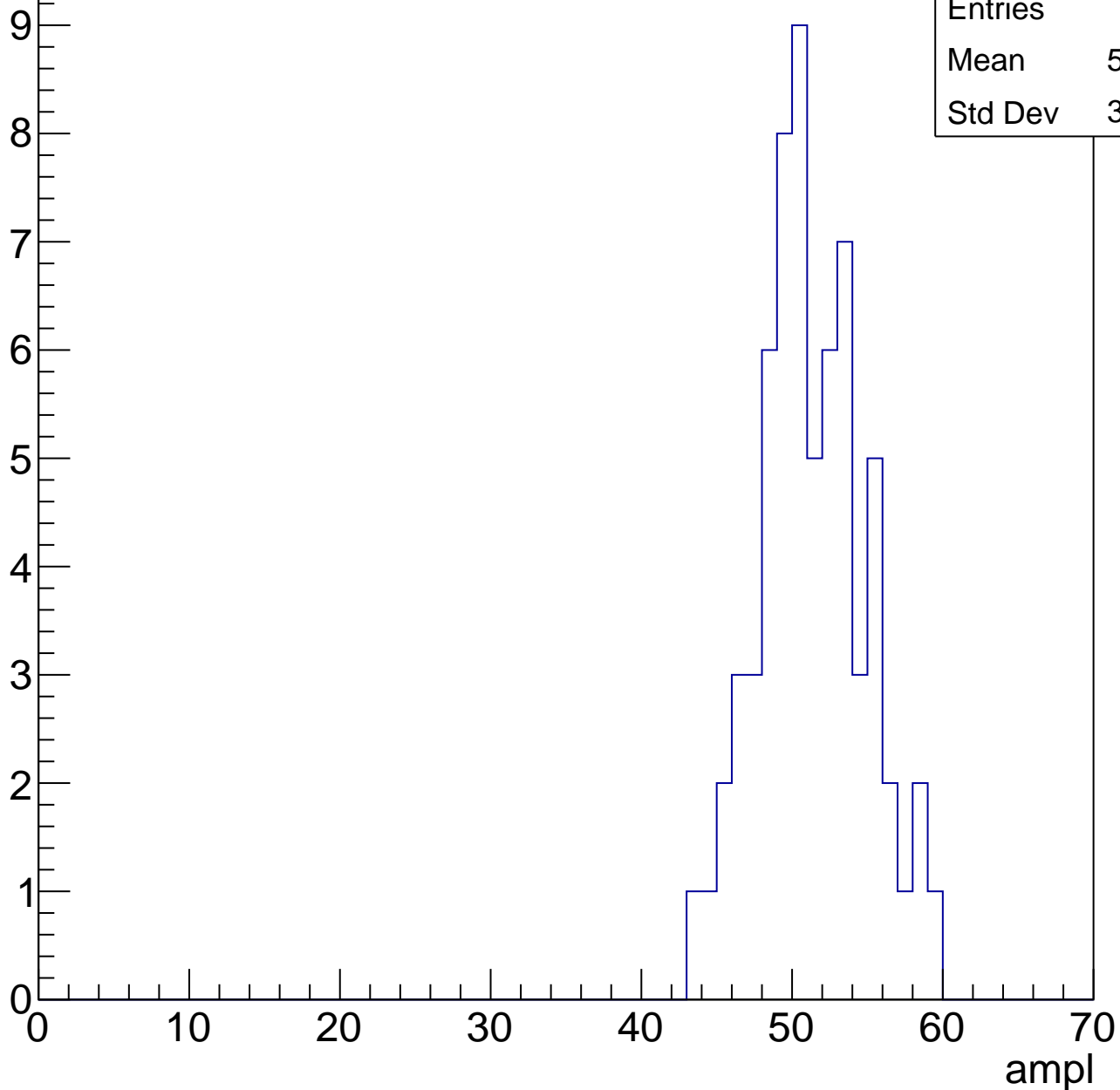


B0L002S, U18-ch26, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.85
Std Dev	3.487

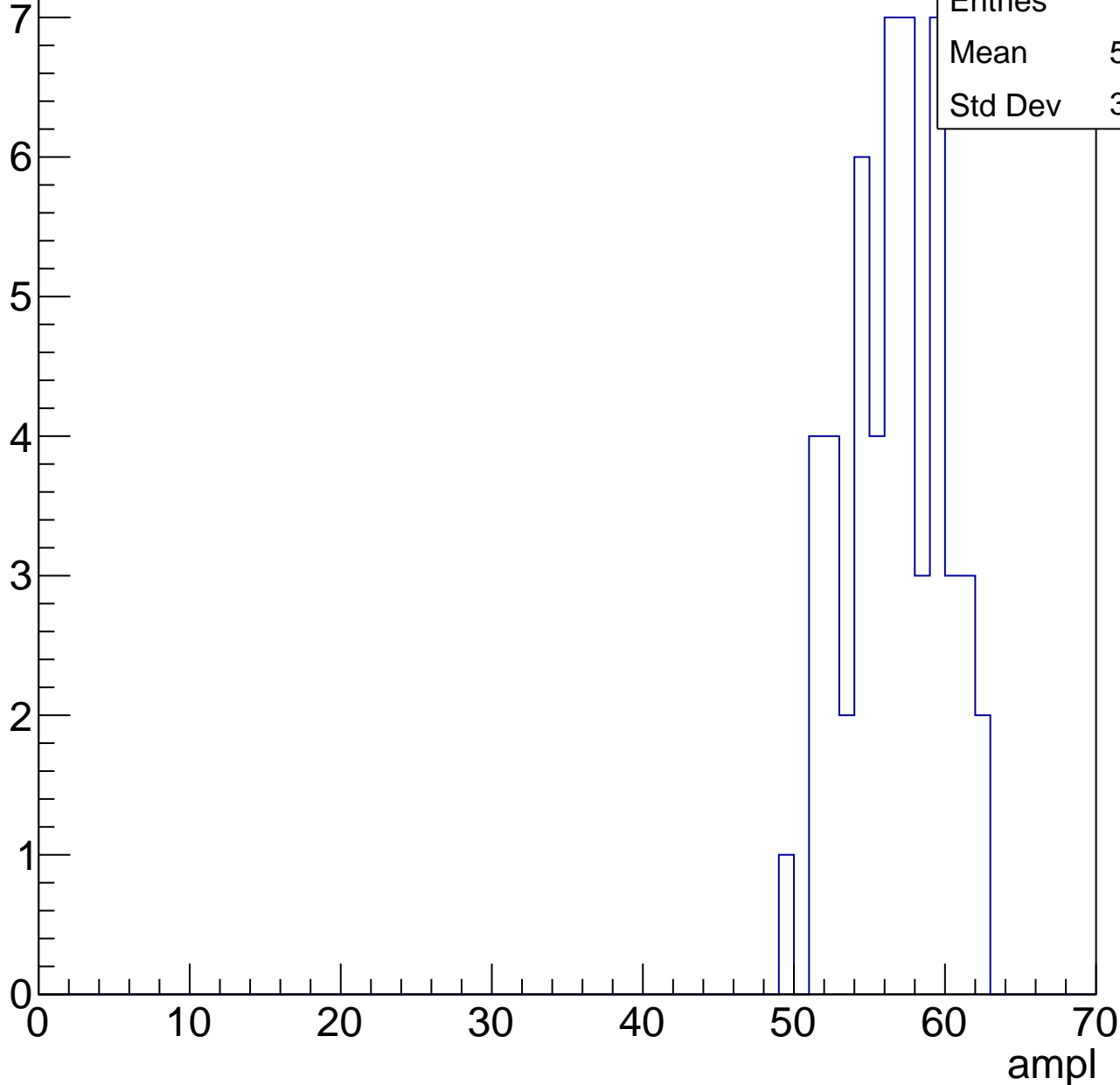


B0L002S, U18-ch26, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	56.15
Std Dev	3.177

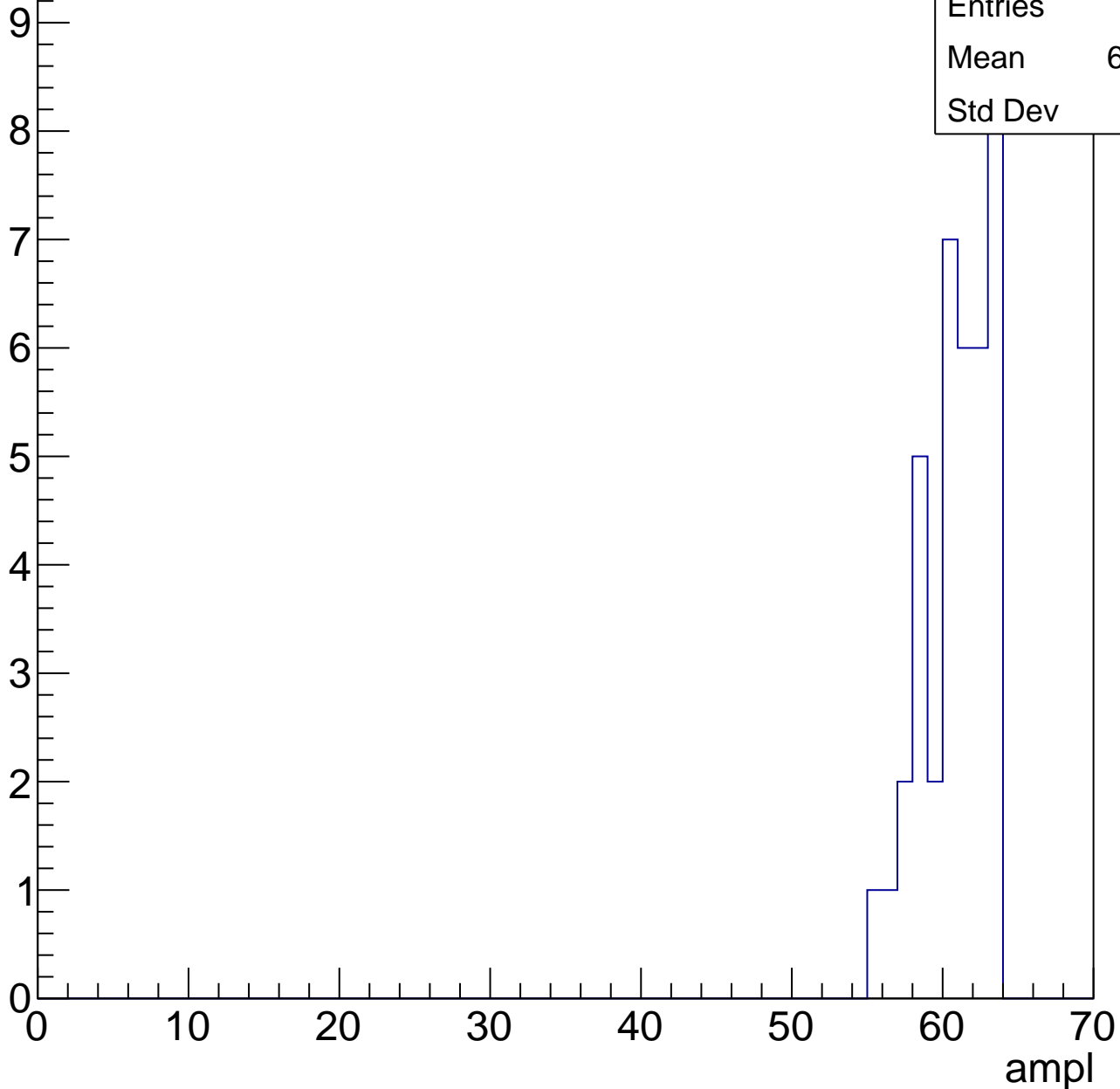


B0L002S, U18-ch26, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	60.46
Std Dev	2.17



B0L002S, U18-ch26, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch26, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch27, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entry

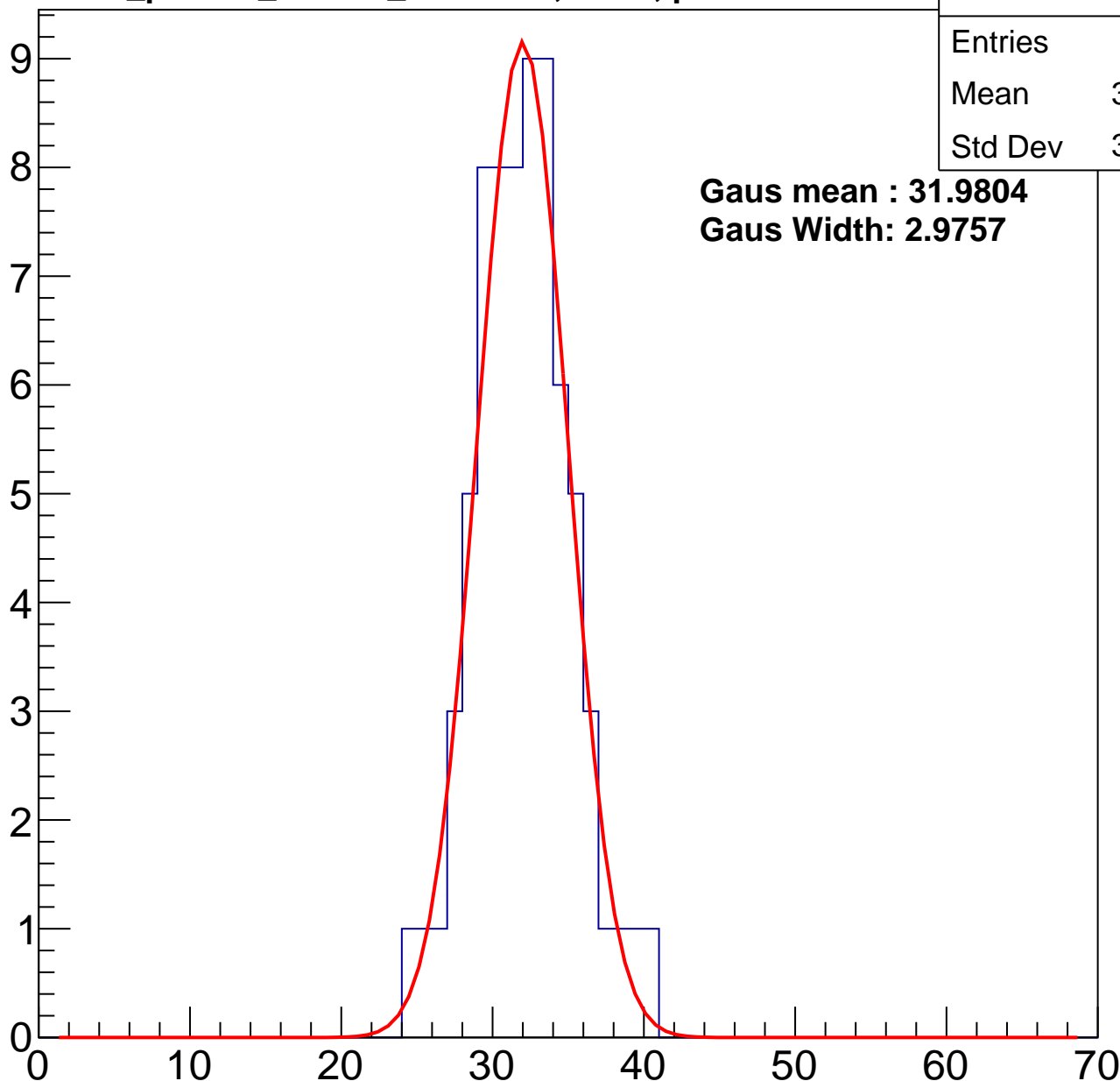
9
8
7
6
5
4
3
2
1
0

Entries	71
Mean	31.58
Std Dev	3.147

Gaus mean : 31.9804

Gaus Width: 2.9757

ampl



B0L002S, U18-ch27, adc1

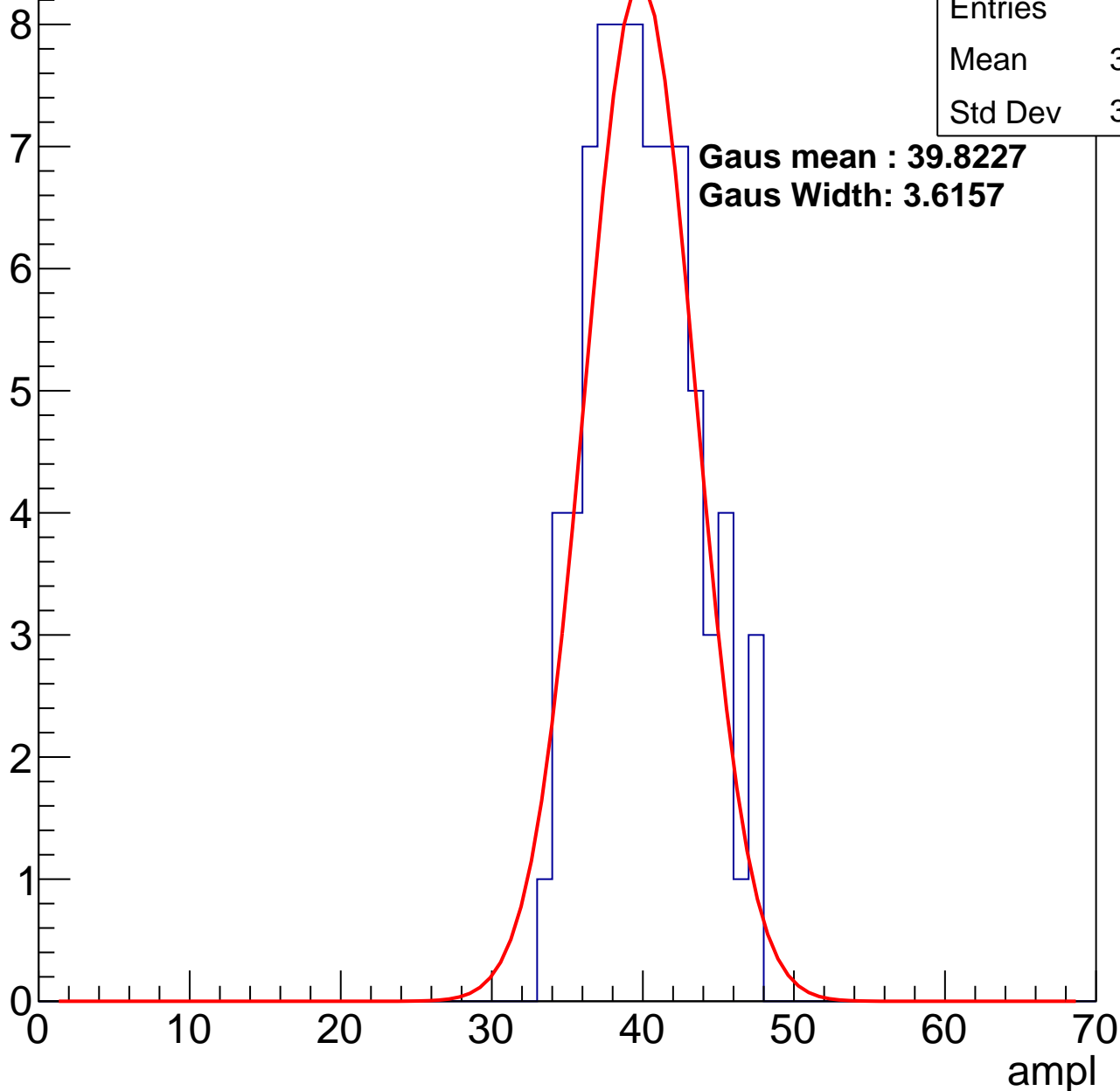
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	39.58
Std Dev	3.447

Gaus mean : 39.8227

Gaus Width: 3.6157



B0L002S, U18-ch27, adc2

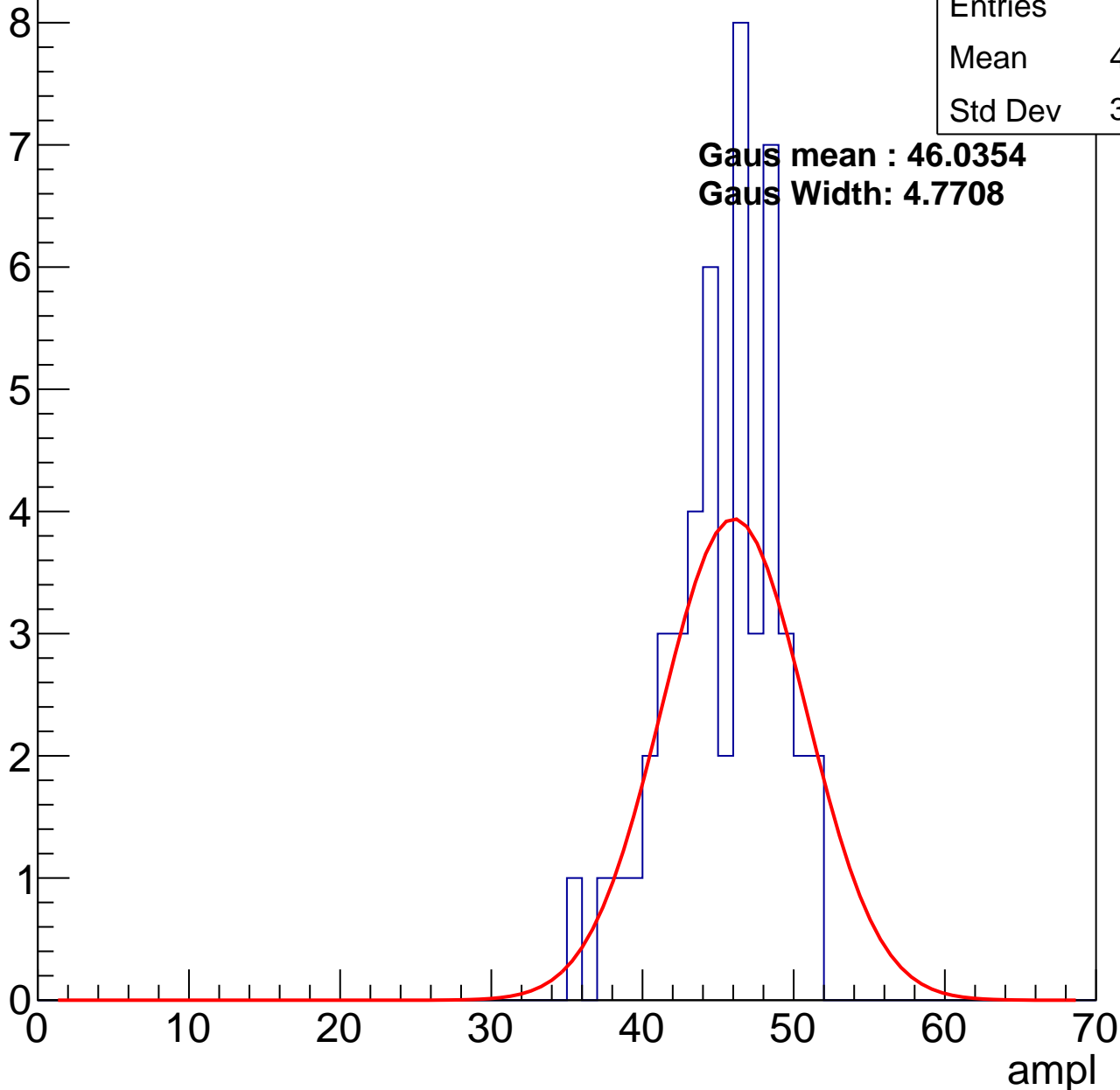
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	44.86
Std Dev	3.637

Gaus mean : 46.0354

Gaus Width: 4.7708

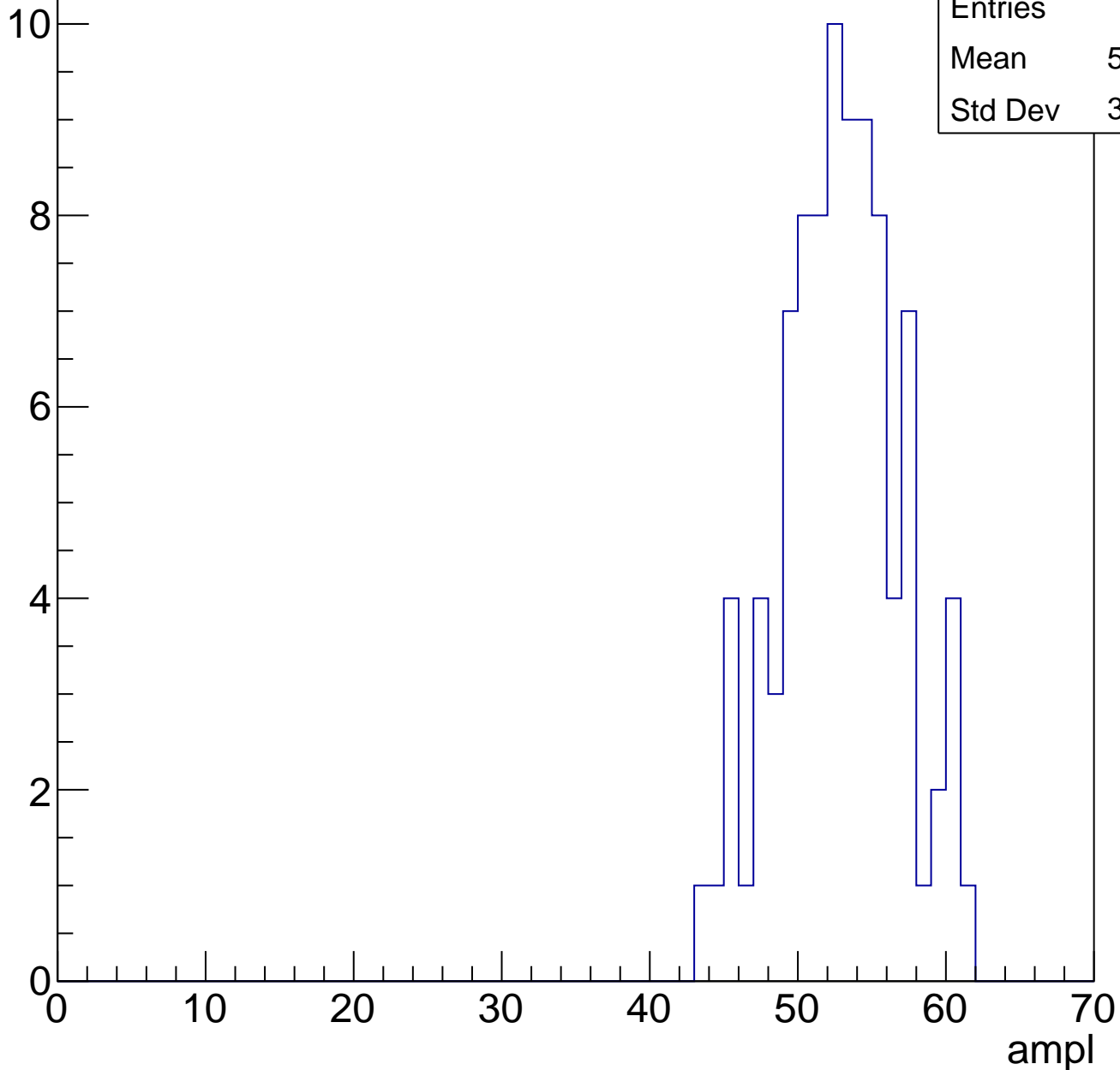


B0L002S, U18-ch27, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	92
Mean	52.38
Std Dev	3.959

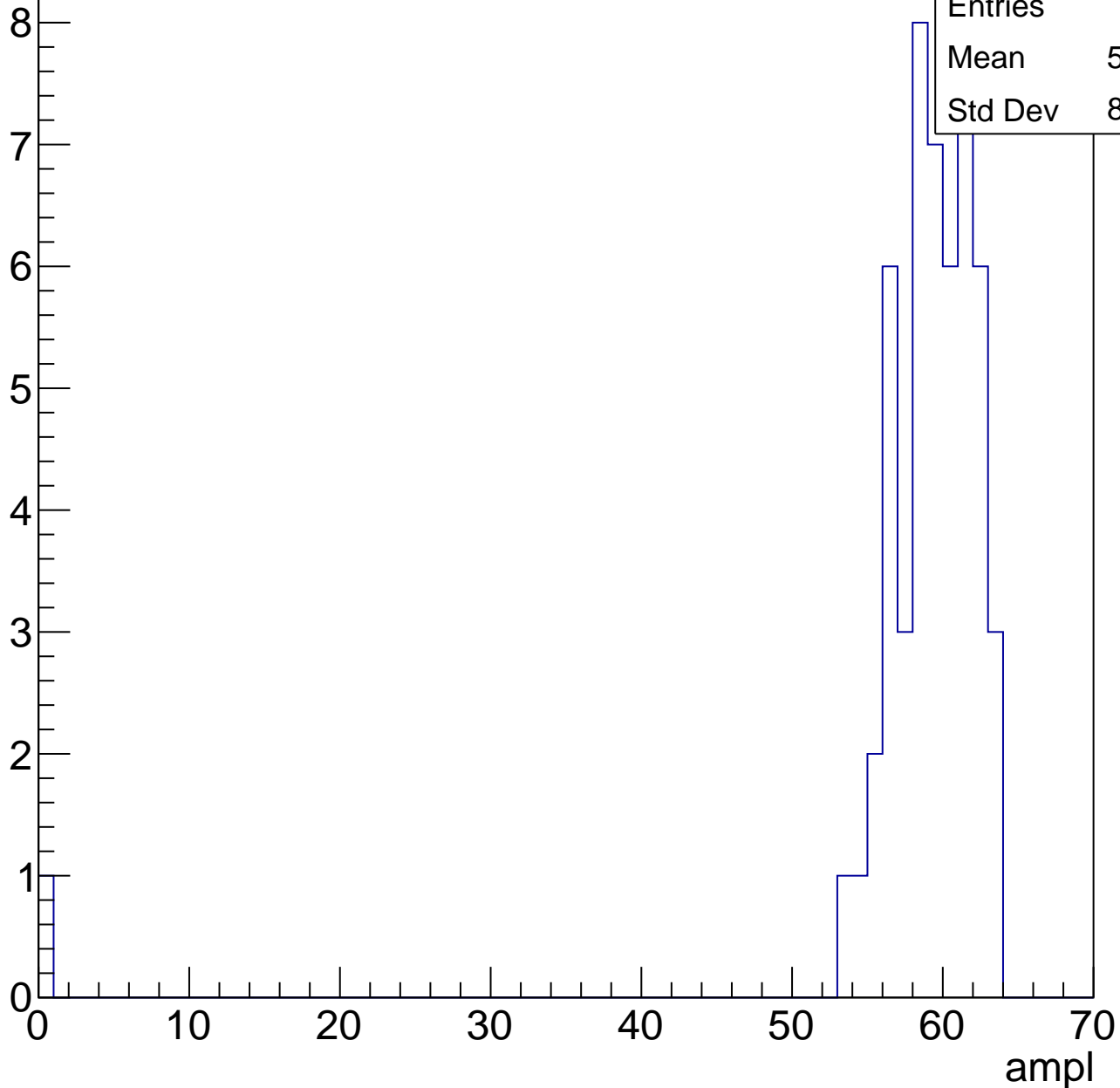
Entry



B0L002S, U18-ch27, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

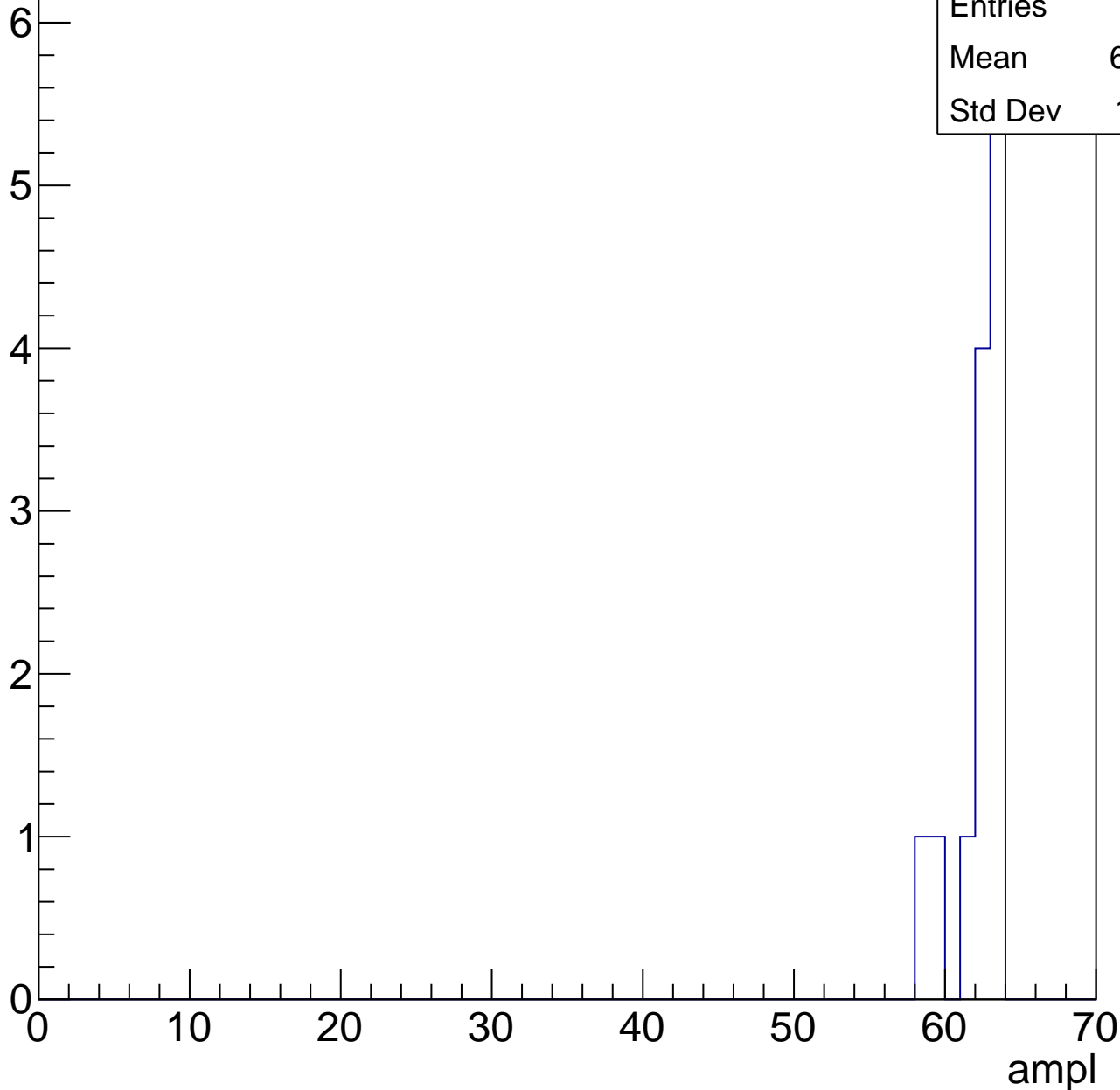


B0L002S, U18-ch27, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	13
Mean	61.85
Std Dev	1.561



B0L002S, U18-ch27, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch27, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch28, adc0

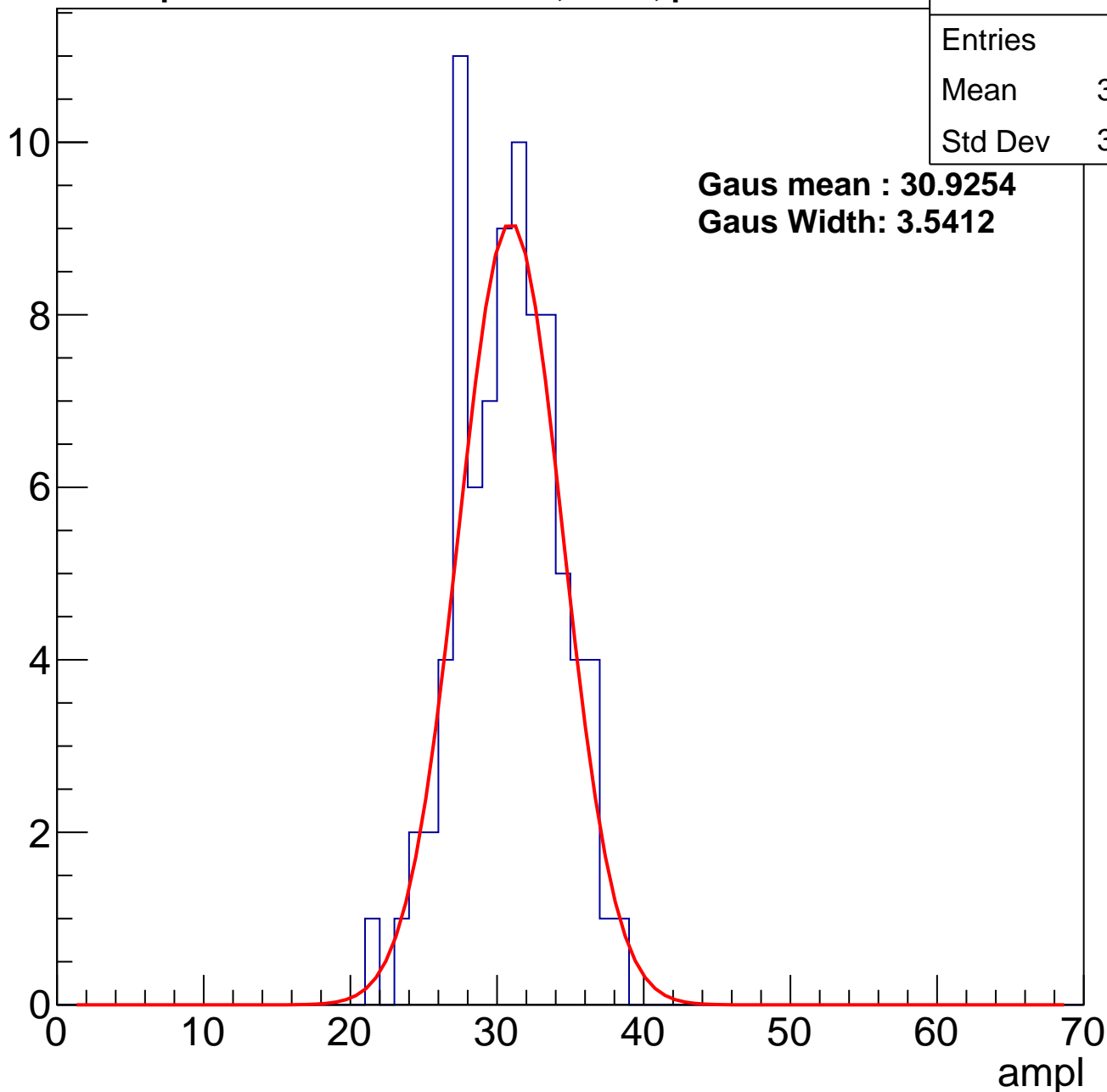
calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	30.27
Std Dev	3.434

Gaus mean : 30.9254

Gaus Width: 3.5412

Entry



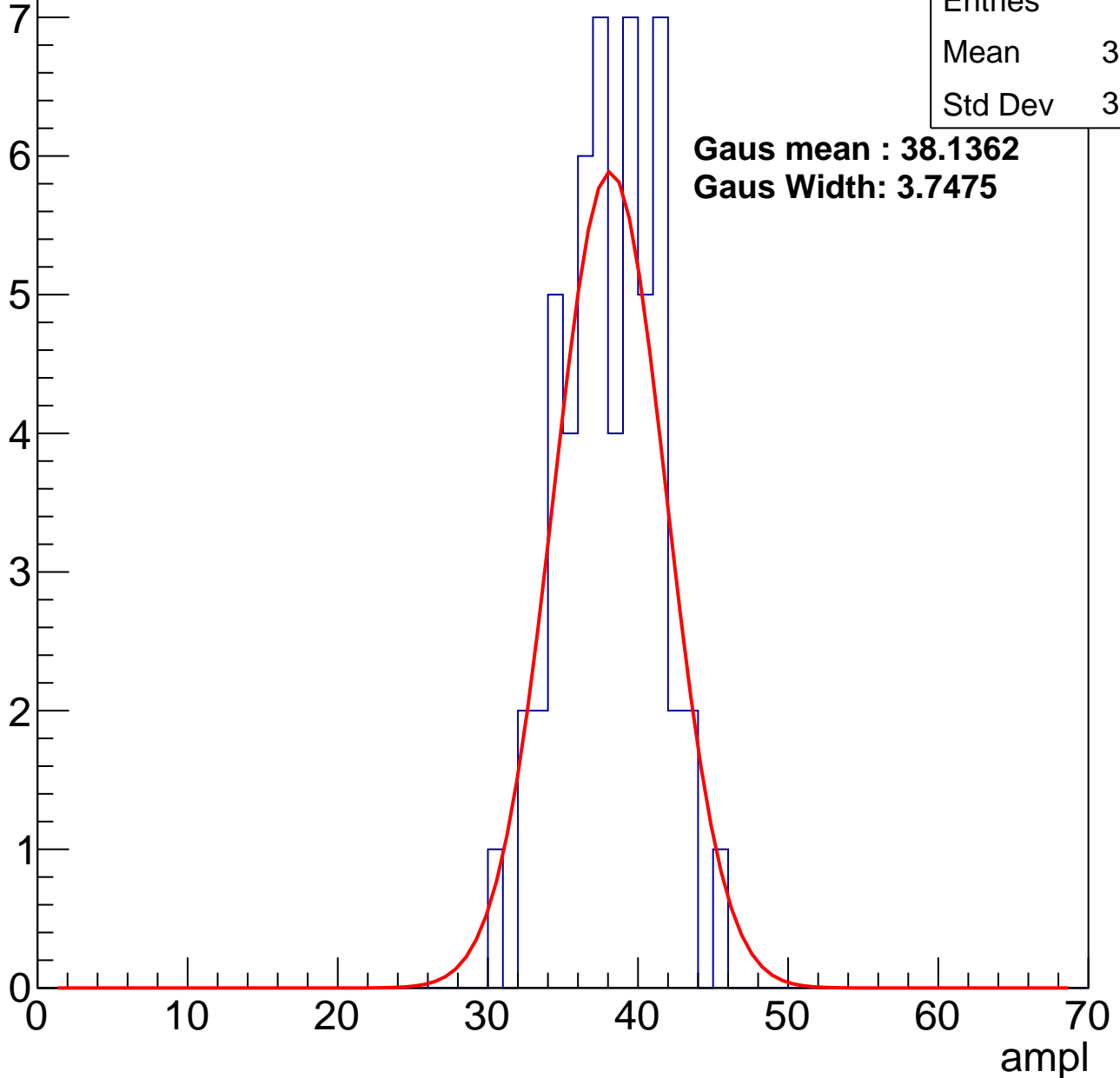
B0L002S, U18-ch28, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	37.67
Std Dev	3.157

Gaus mean : 38.1362
Gaus Width: 3.7475



B0L002S, U18-ch28, adc2

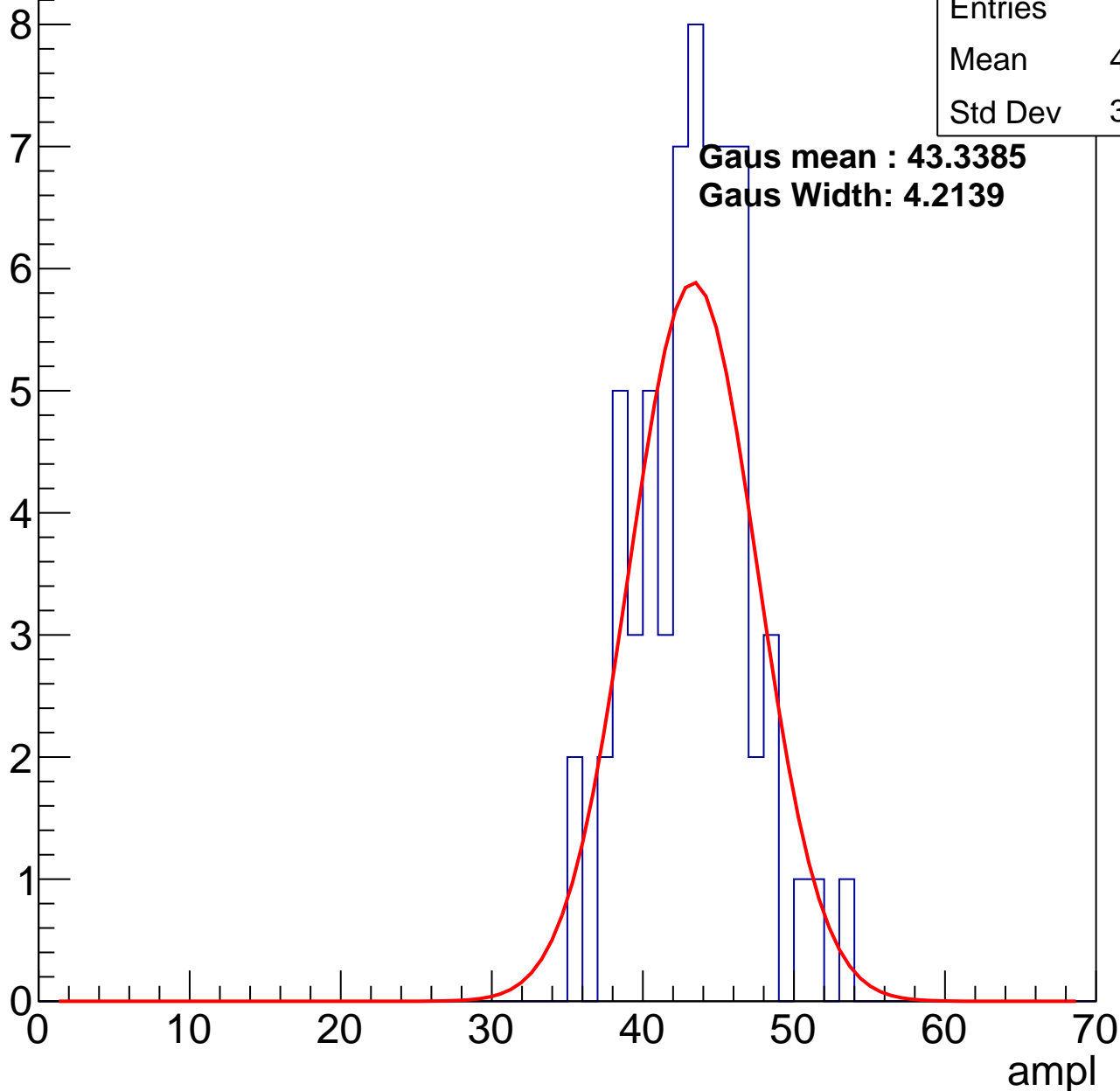
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.95
Std Dev	3.655

Gaus mean : 43.3385

Gaus Width: 4.2139



B0L002S, U18-ch28, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	50.19
Std Dev	3.354

Entry

10

8

6

4

2

0

0

10

20

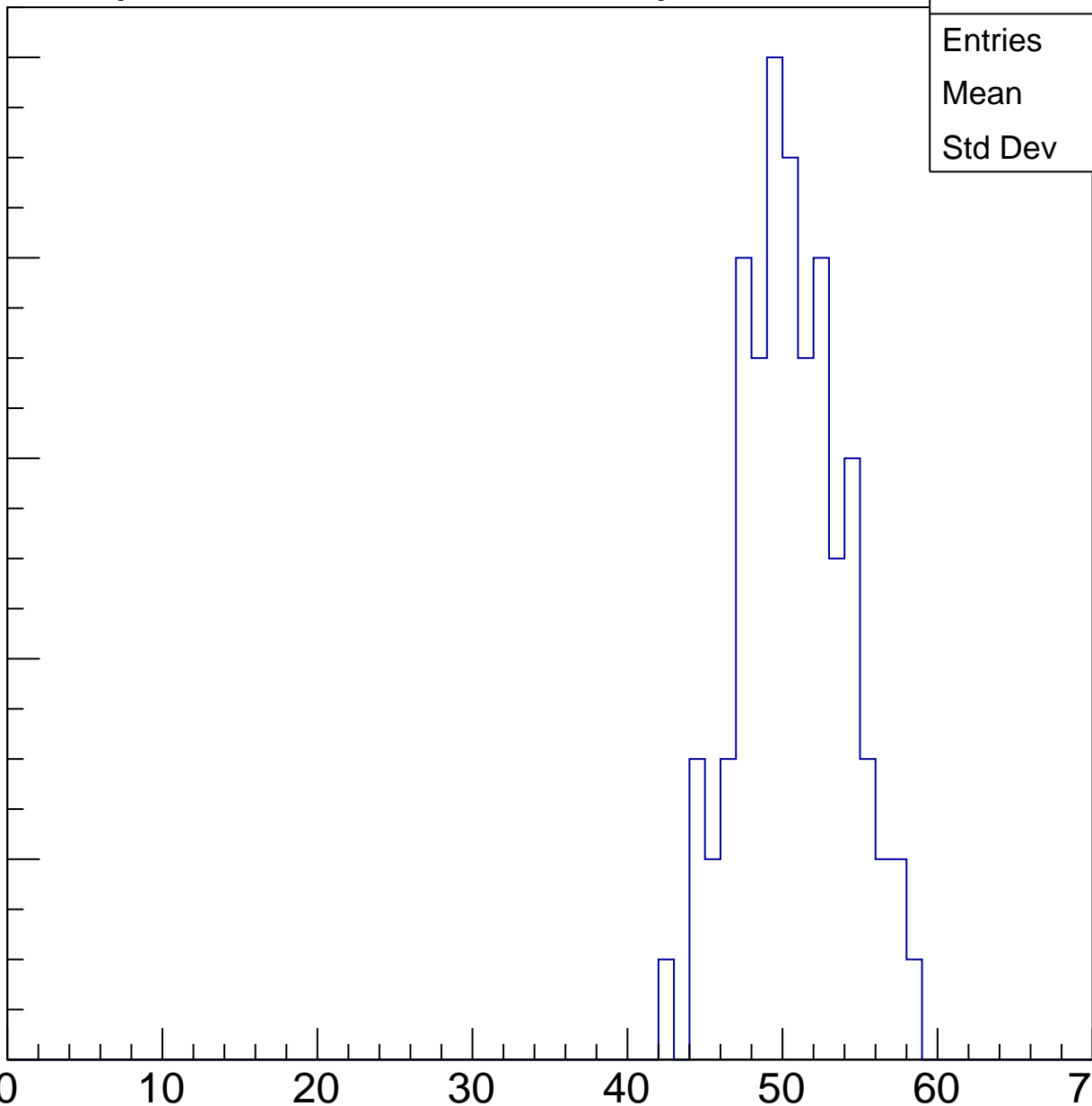
30

40

50

60

ampl

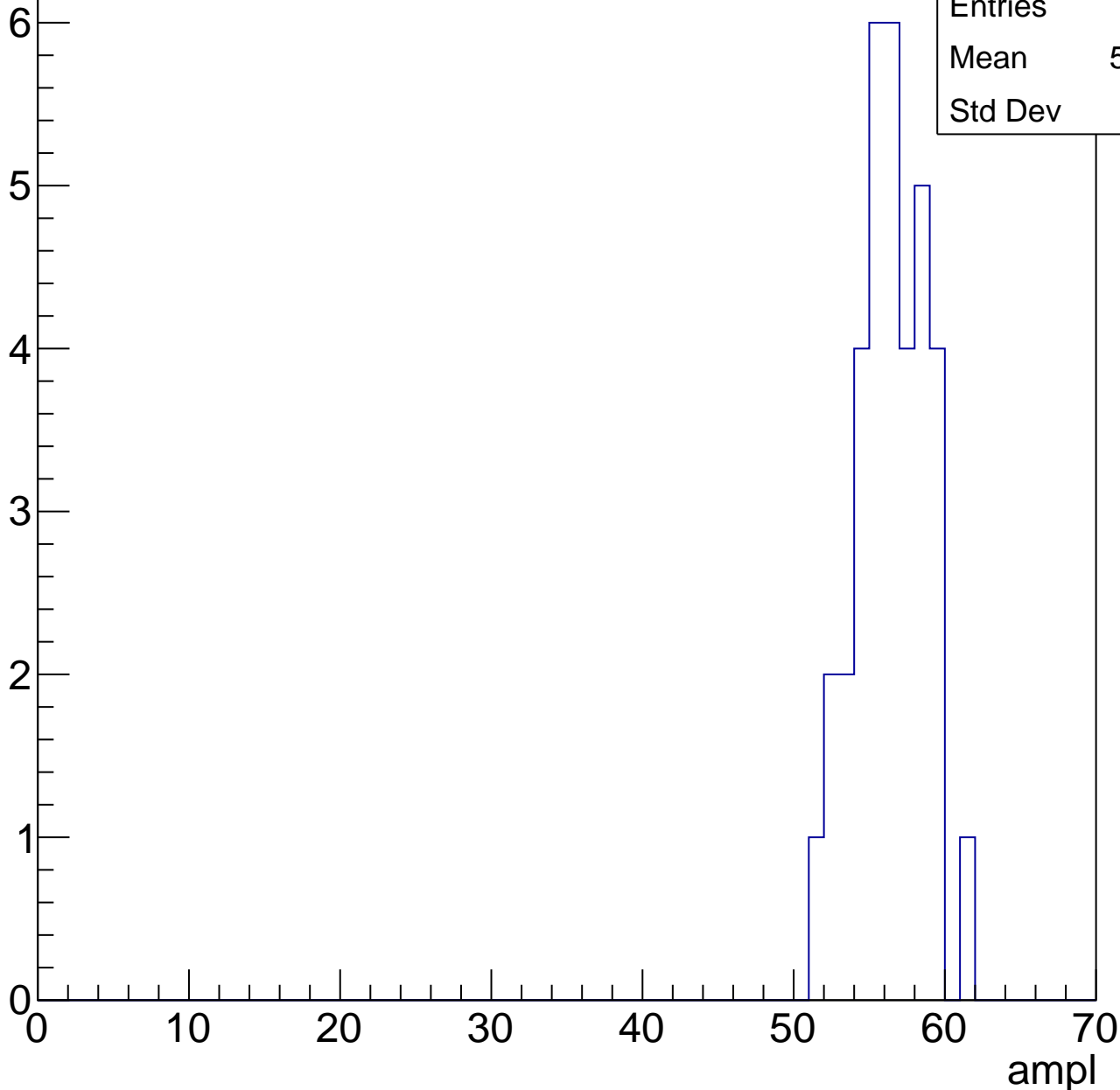


B0L002S, U18-ch28, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

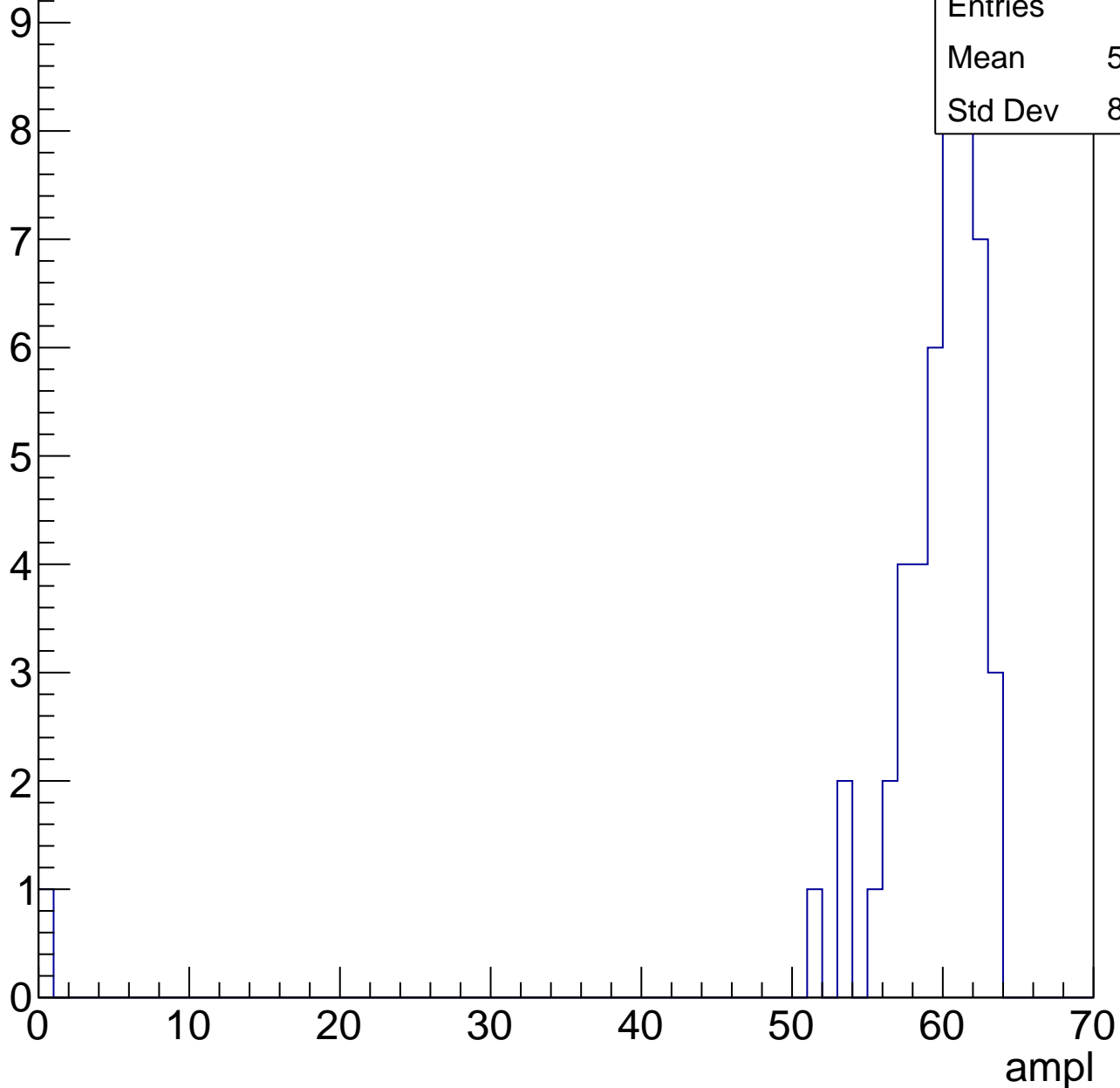
Entries	35
Mean	55.94
Std Dev	2.28



B0L002S, U18-ch28, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

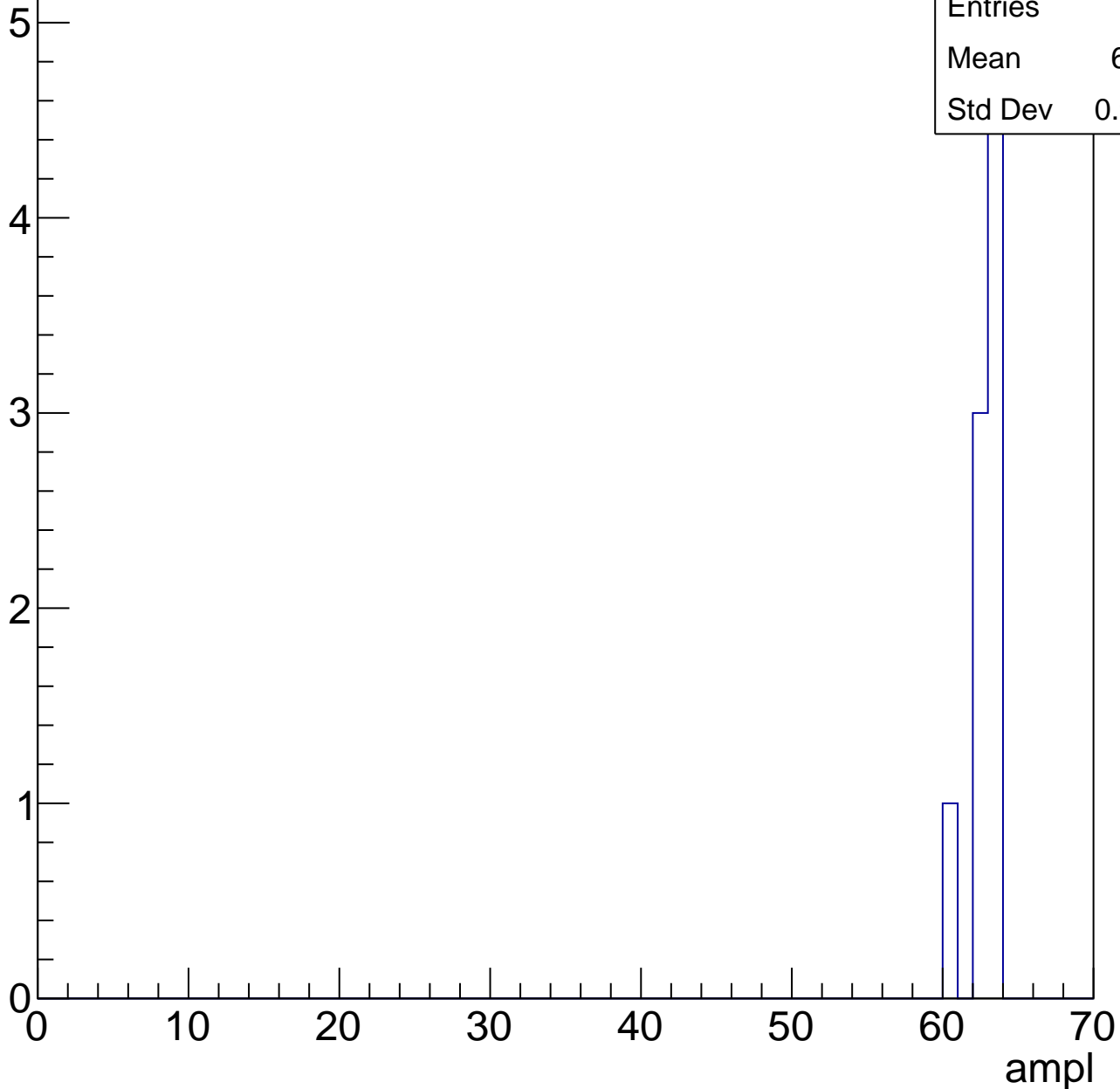


B0L002S, U18-ch28, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	62.33
Std Dev	0.9428



B0L002S, U18-ch28, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch29, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	28.68
Std Dev	4.89

Gaus mean : 29.6485

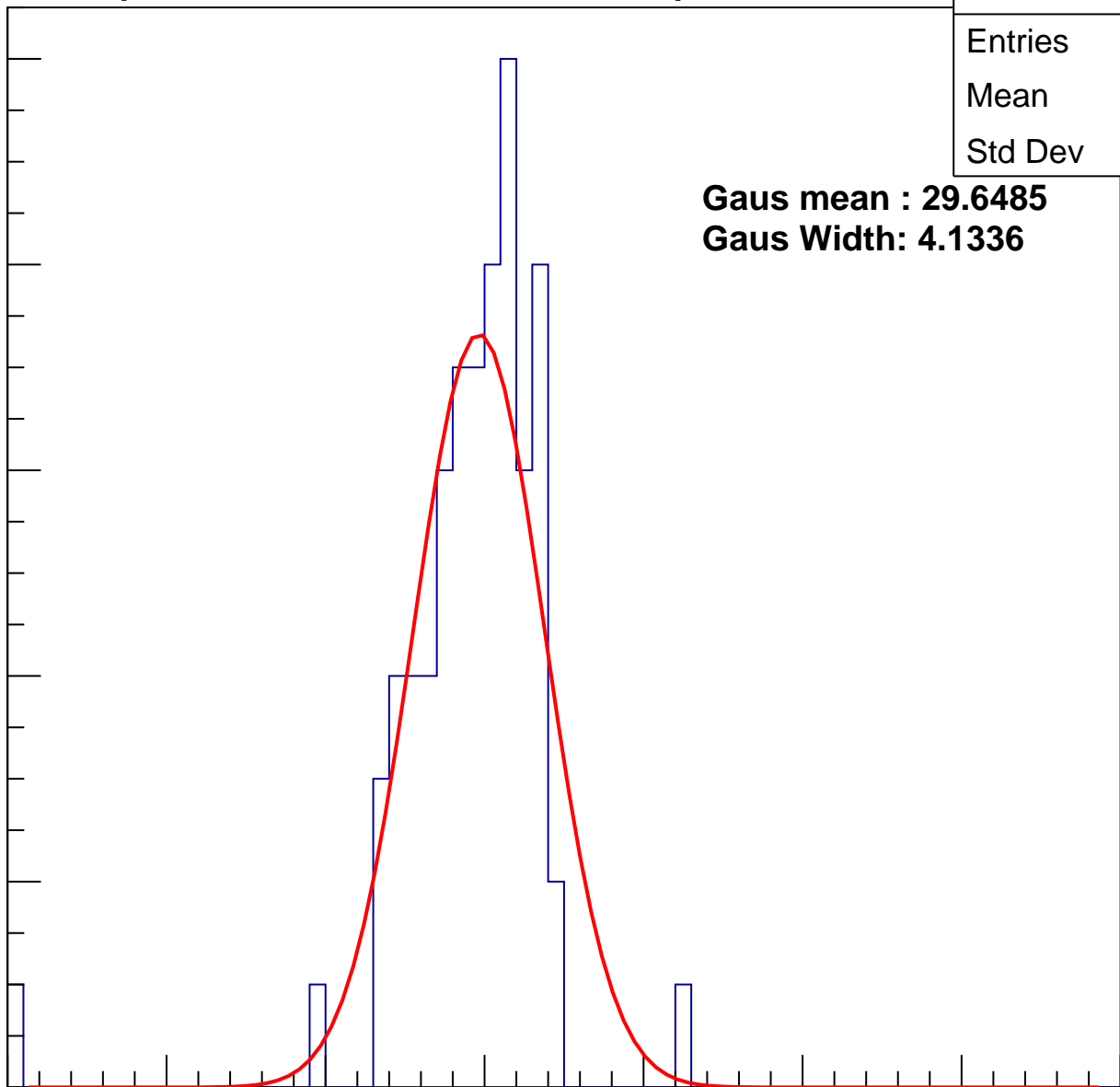
Gaus Width: 4.1336

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch29, adc1

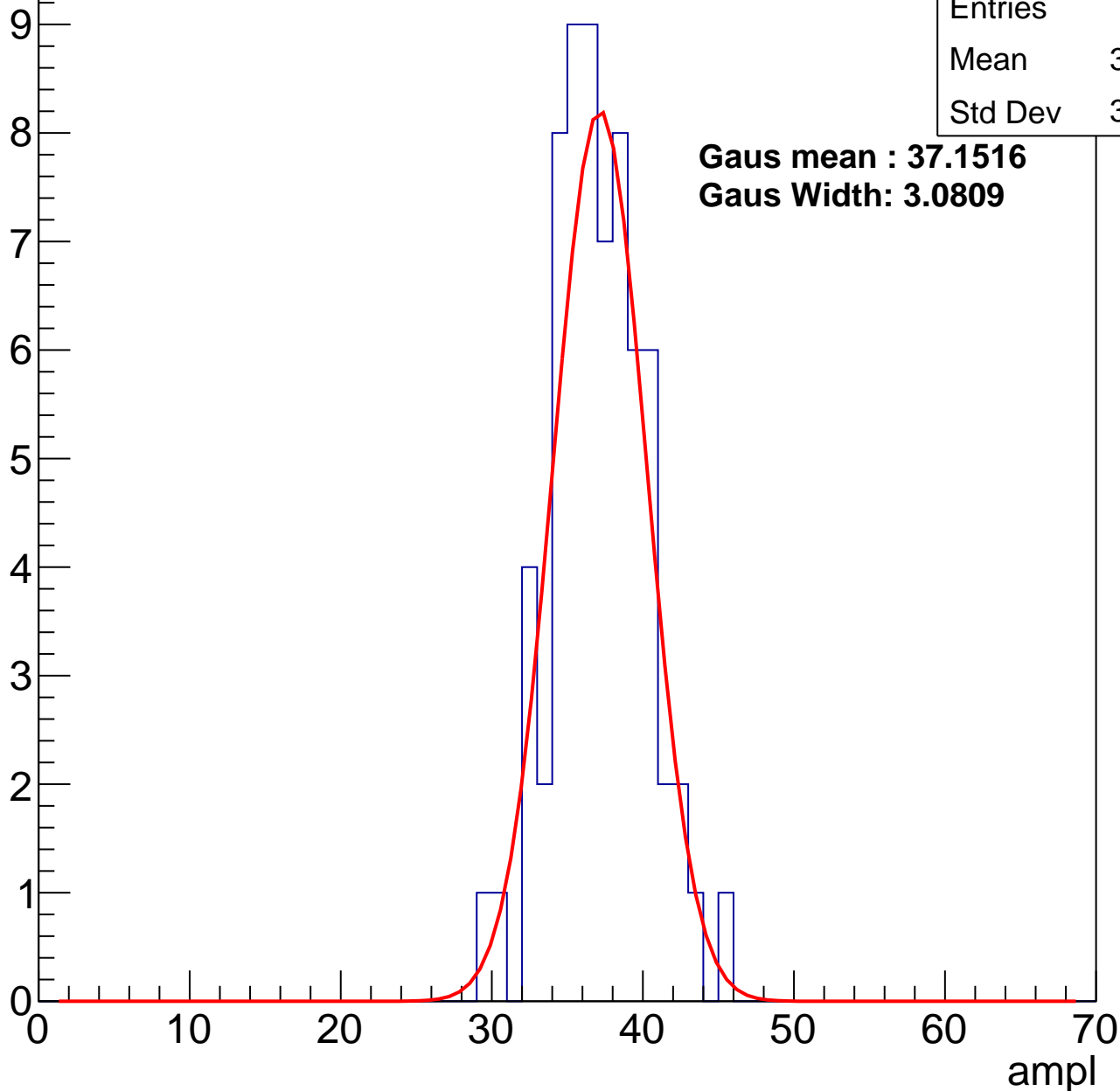
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	36.64
Std Dev	3.045

Gaus mean : 37.1516

Gaus Width: 3.0809



B0L002S, U18-ch29, adc2

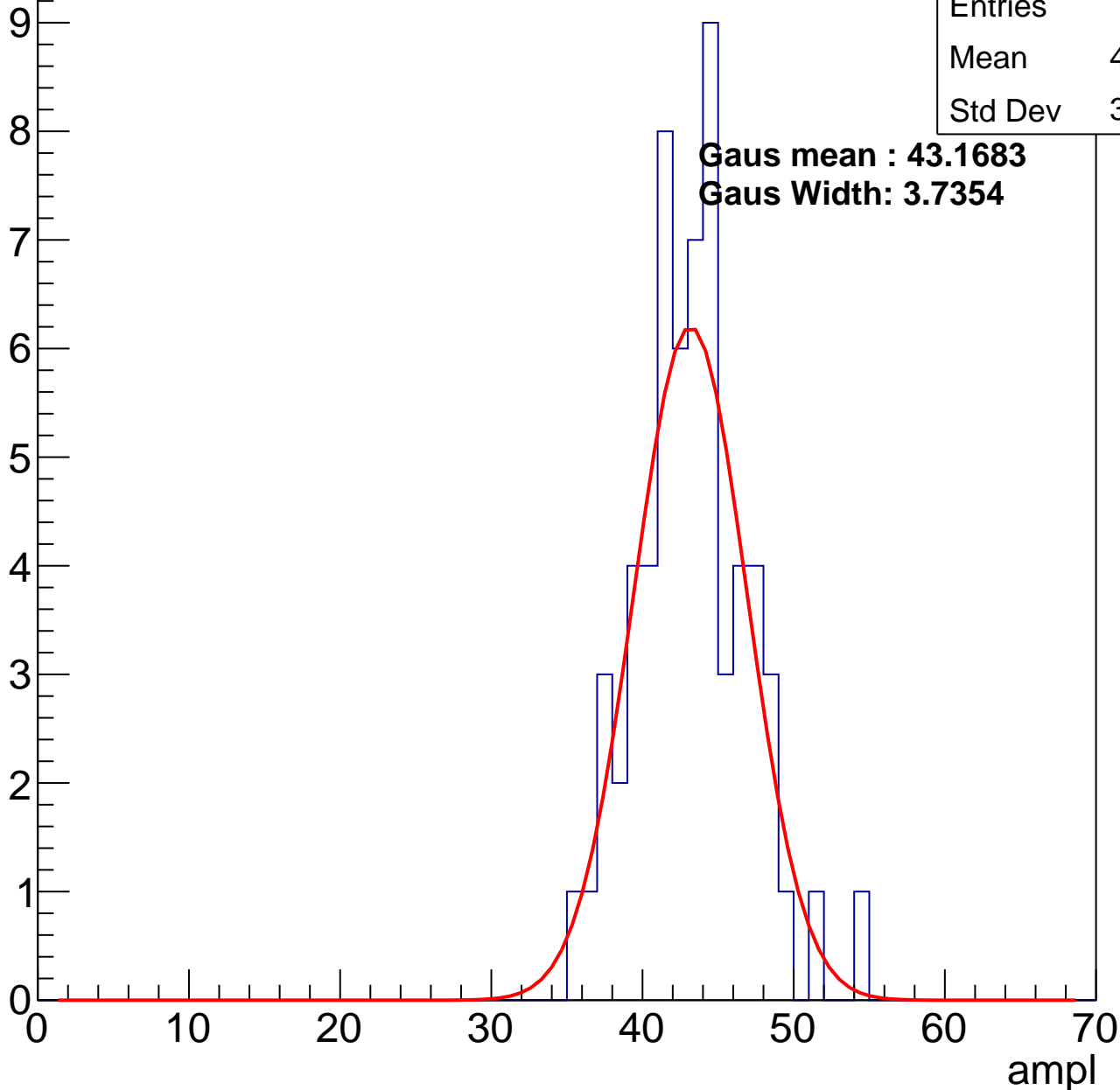
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.84
Std Dev	3.646

Gaus mean : 43.1683

Gaus Width: 3.7354

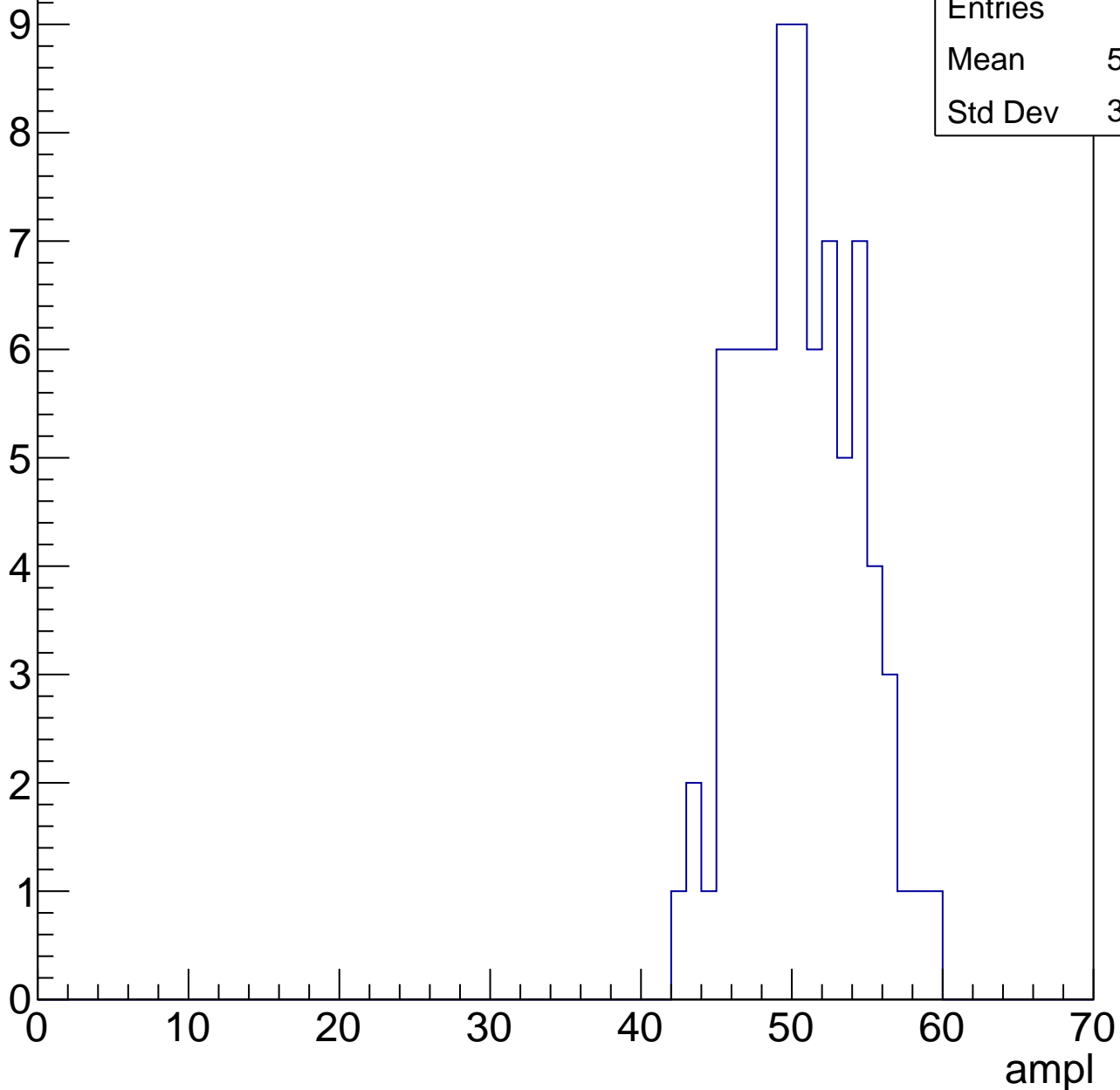


B0L002S, U18-ch29, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	50.05
Std Dev	3.725

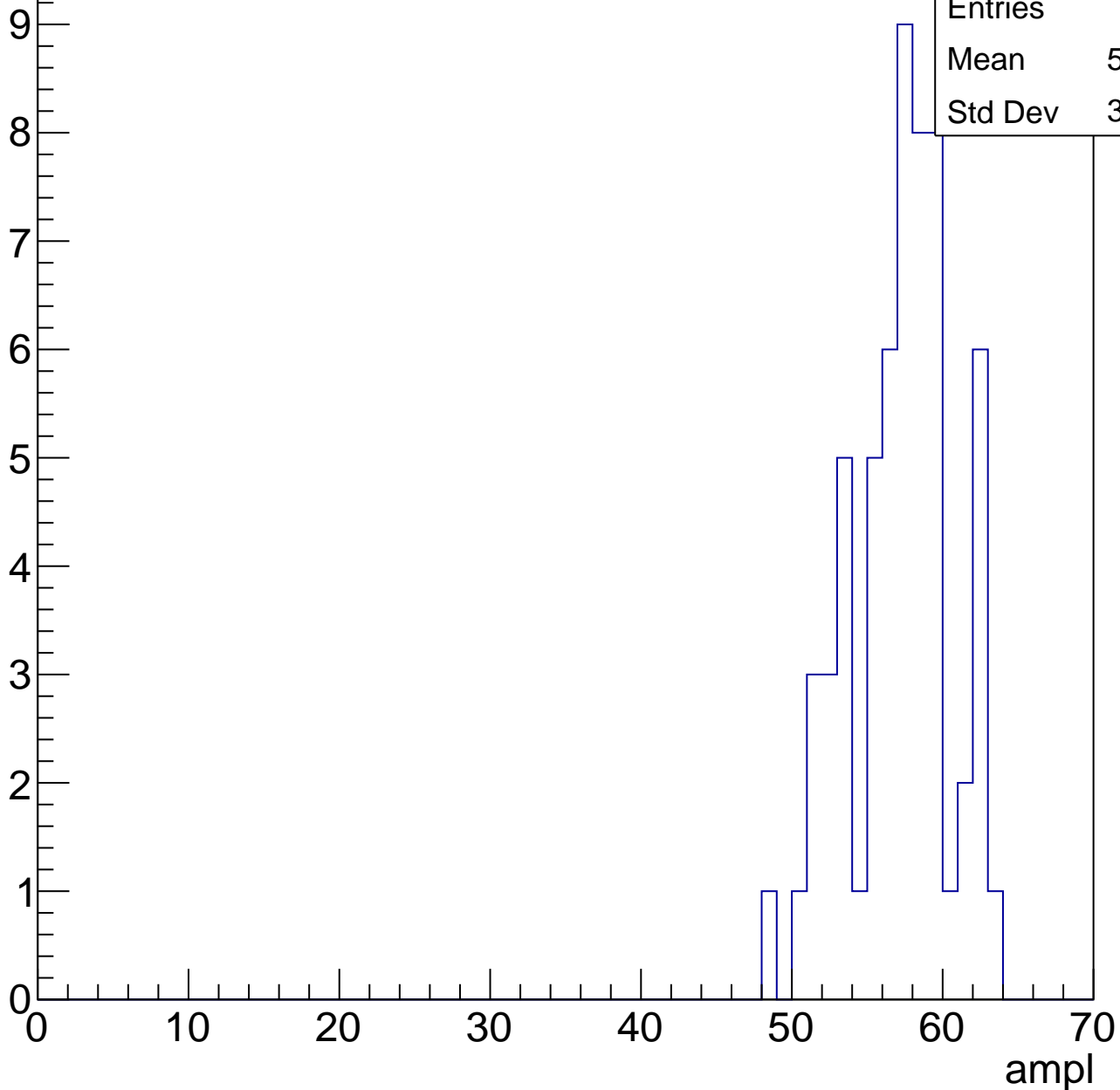


B0L002S, U18-ch29, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.72
Std Dev	3.392

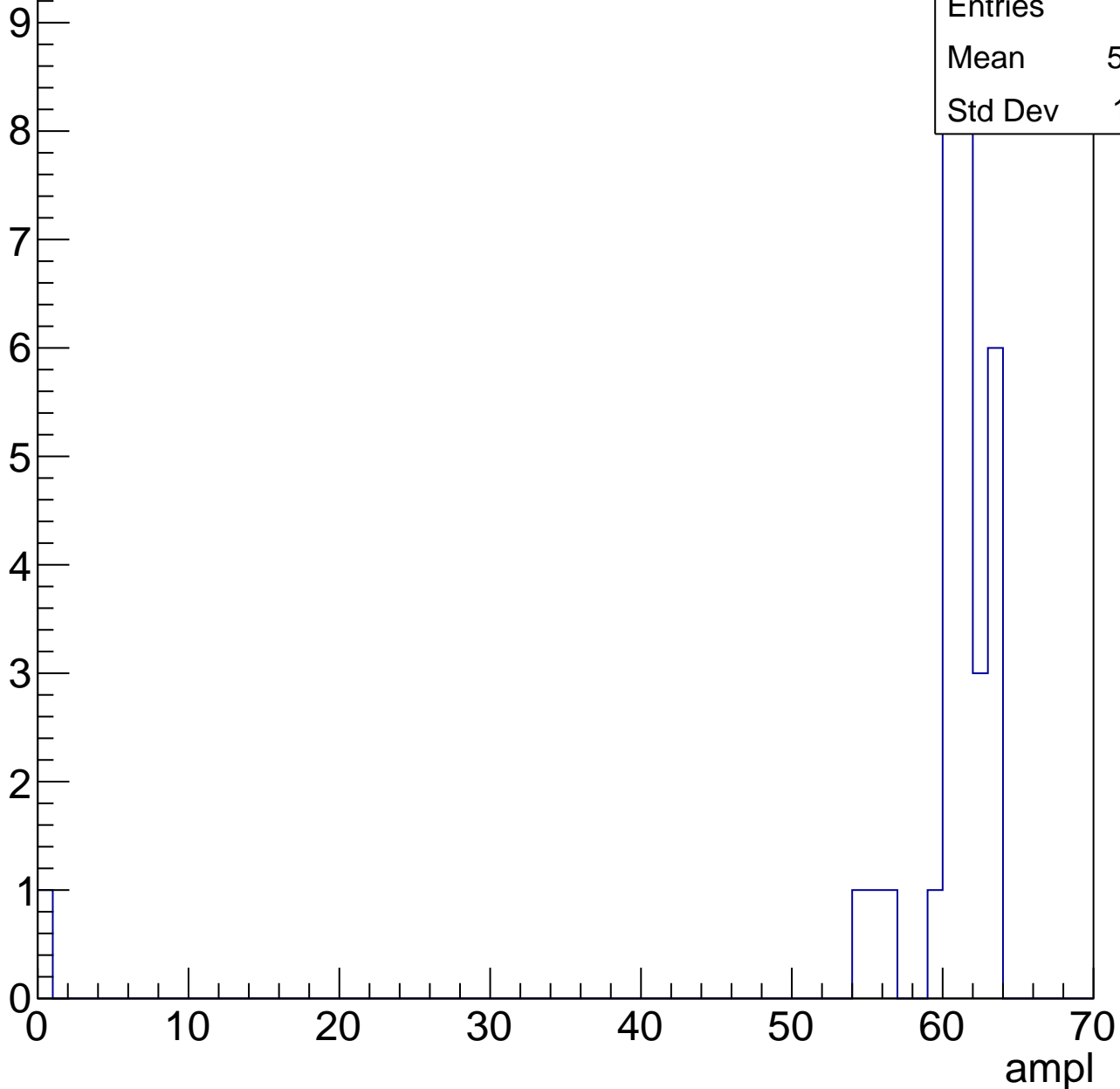


B0L002S, U18-ch29, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

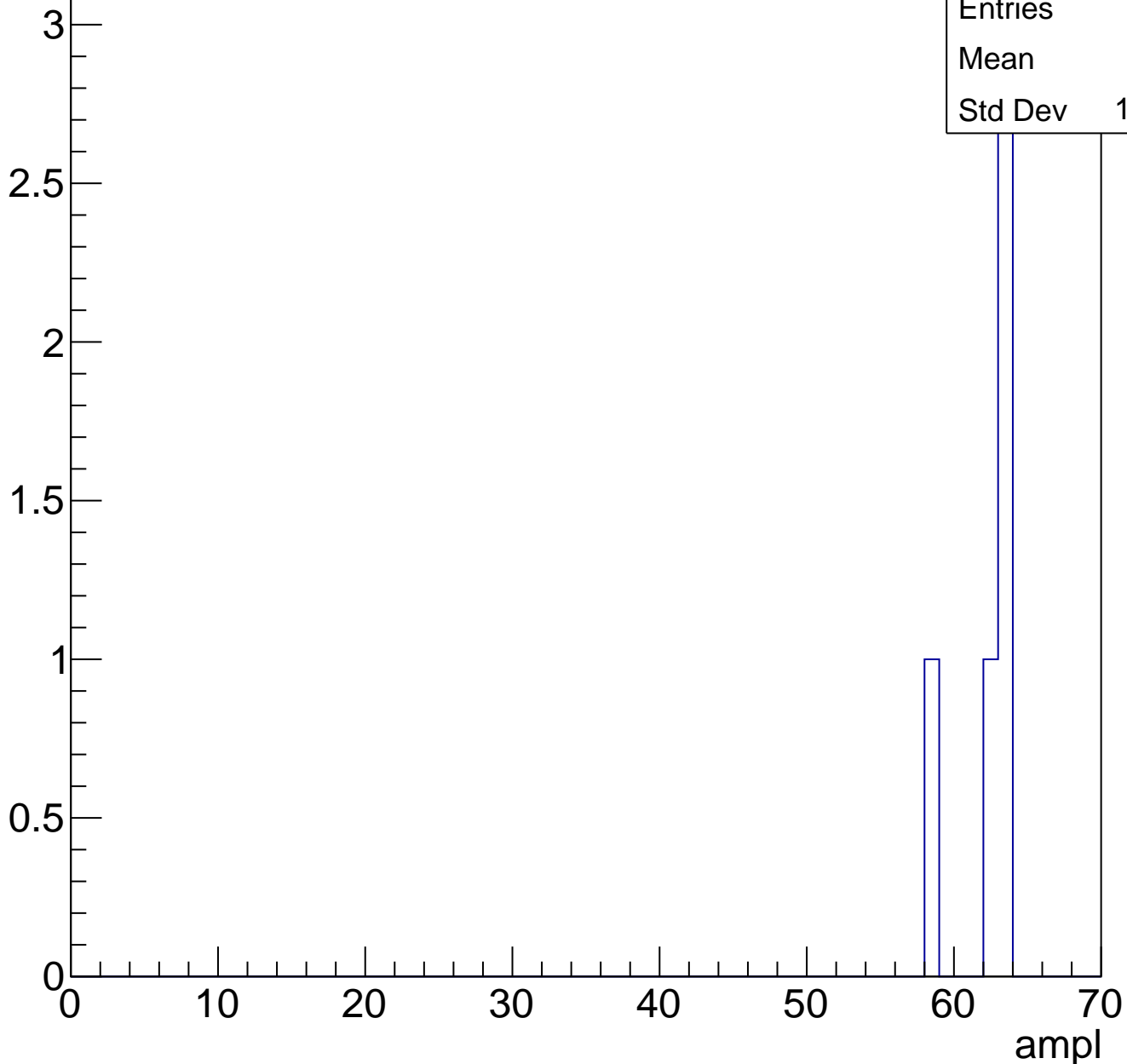
Entries	31
Mean	58.58
Std Dev	10.91



B0L002S, U18-ch29, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch29, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch30, adc0

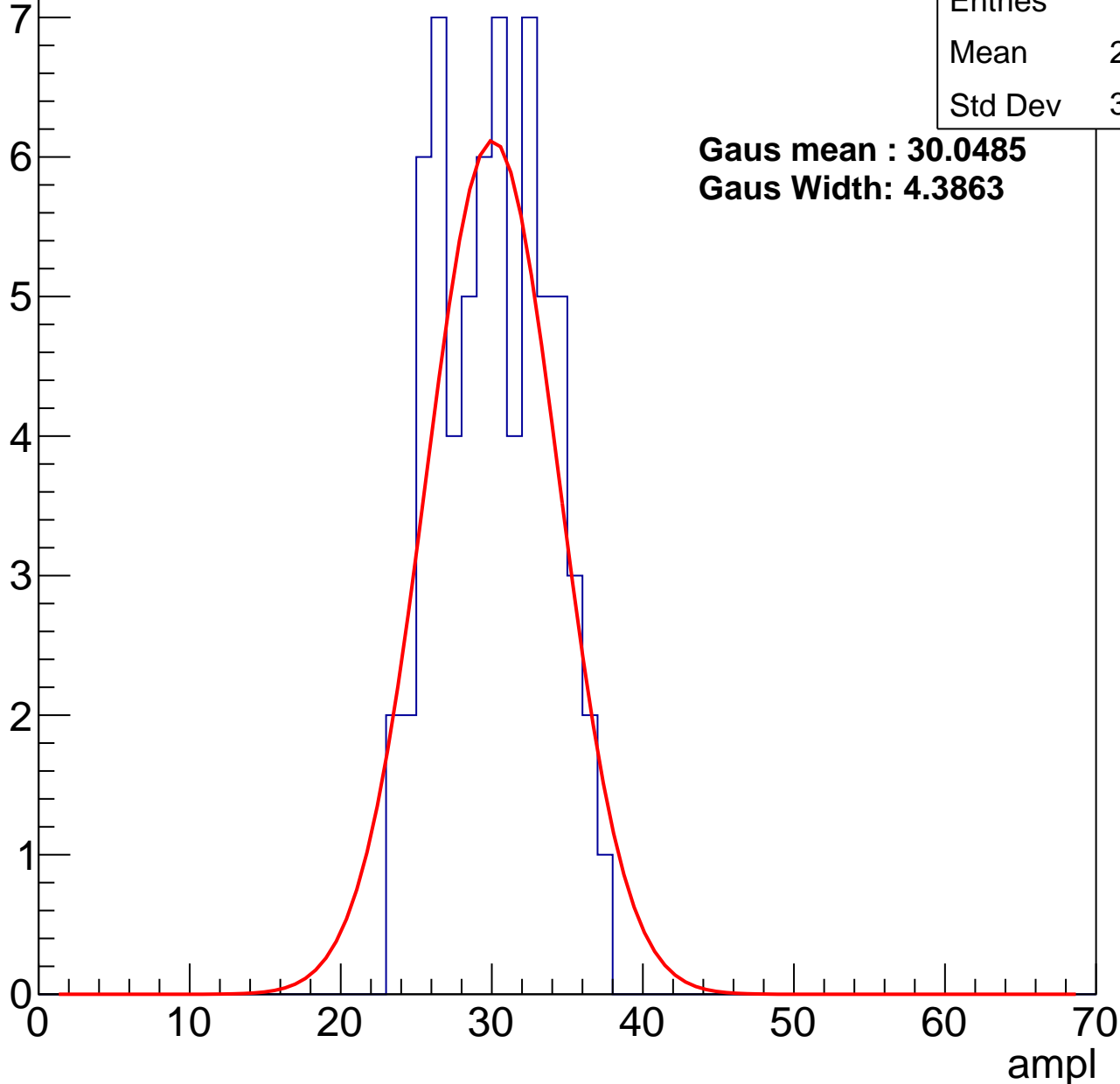
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.62
Std Dev	3.562

Gaus mean : 30.0485

Gaus Width: 4.3863



B0L002S, U18-ch30, adc1

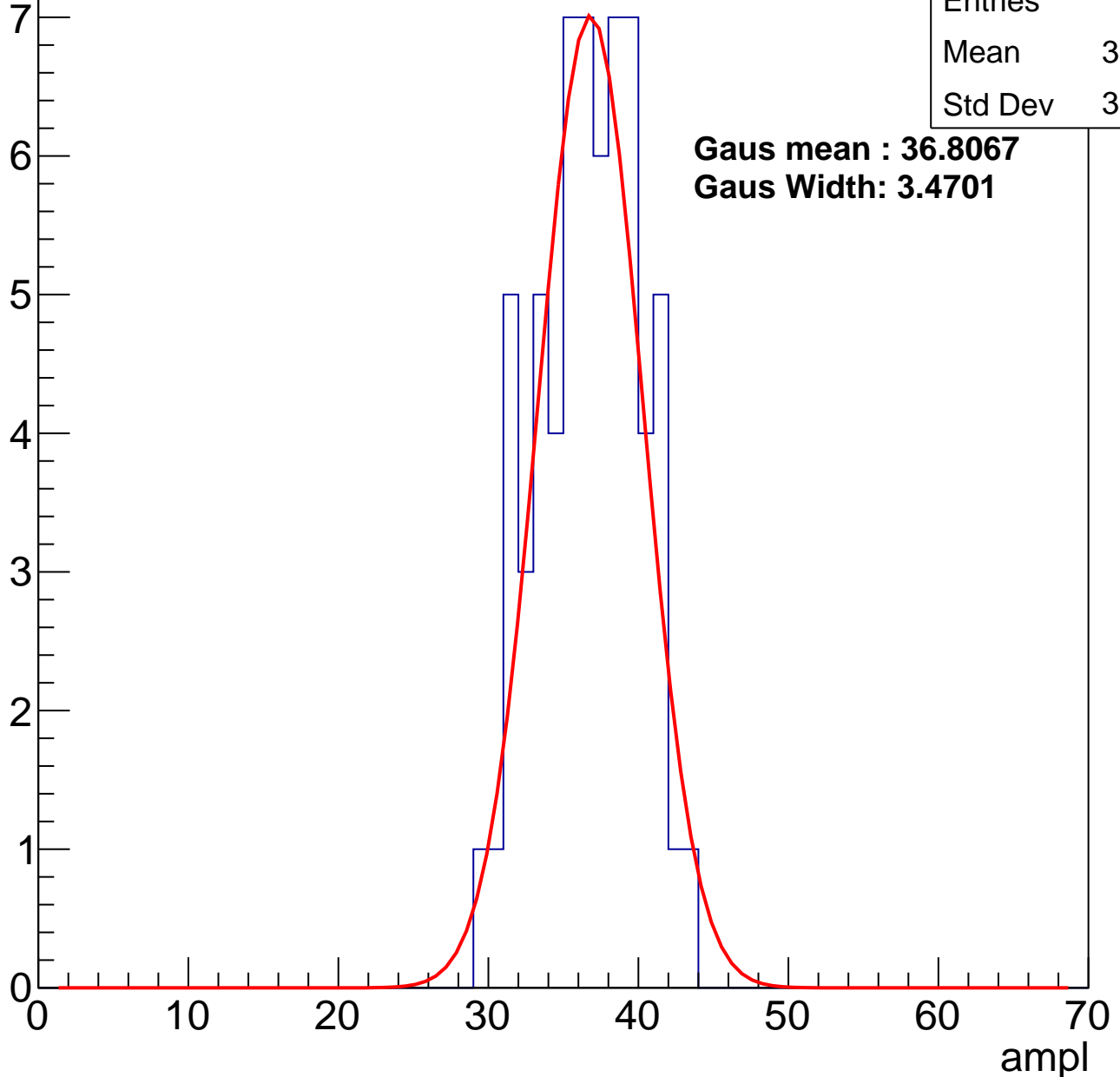
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.23
Std Dev	3.292

Gaus mean : 36.8067

Gaus Width: 3.4701



B0L002S, U18-ch30, adc2

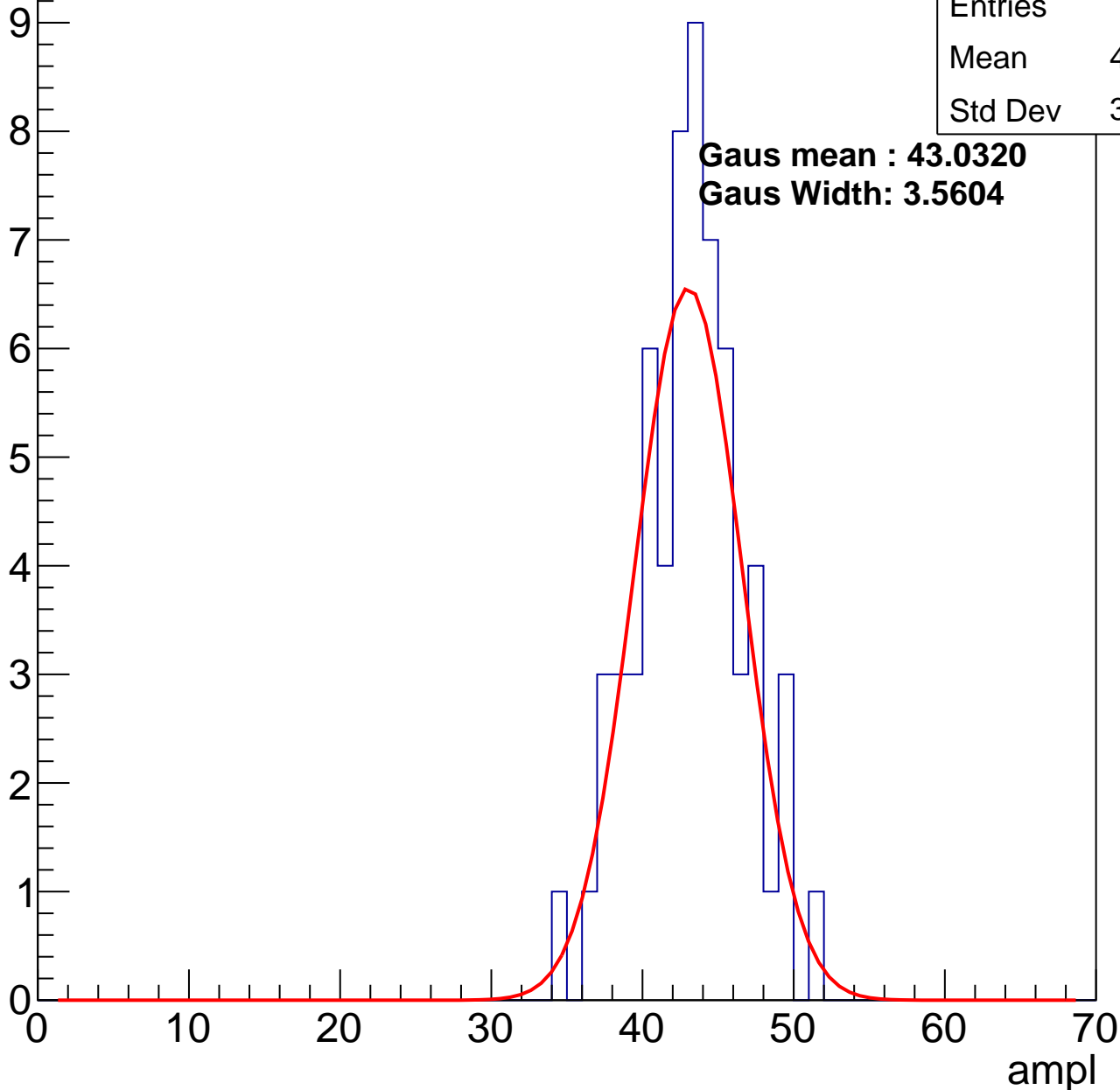
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	42.68
Std Dev	3.454

Gaus mean : 43.0320

Gaus Width: 3.5604



B0L002S, U18-ch30, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	50.24
Std Dev	3.72

Entry

10

8

6

4

2

0

0

10

20

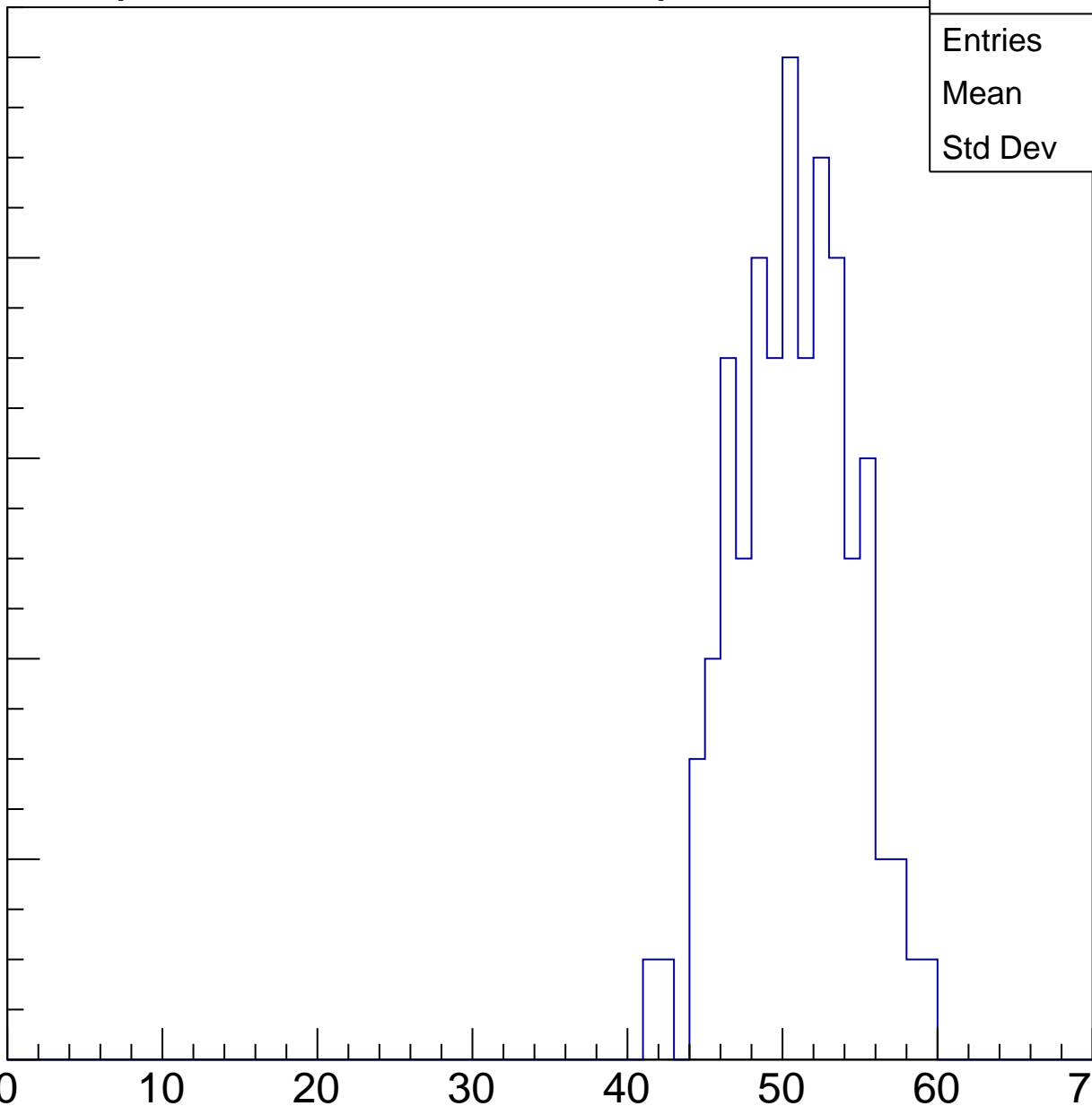
30

40

50

60

ampl

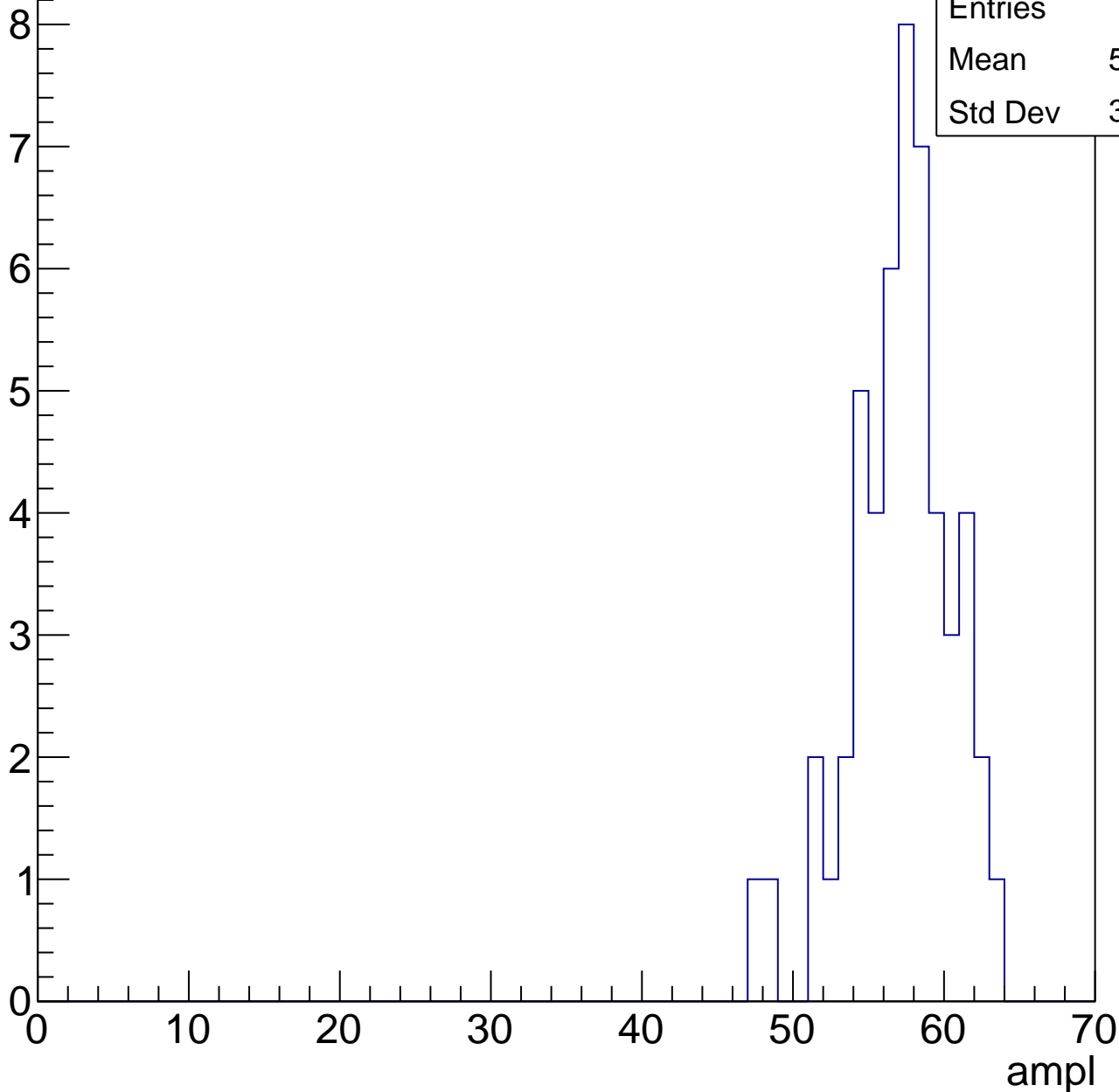


B0L002S, U18-ch30, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.67
Std Dev	3.347

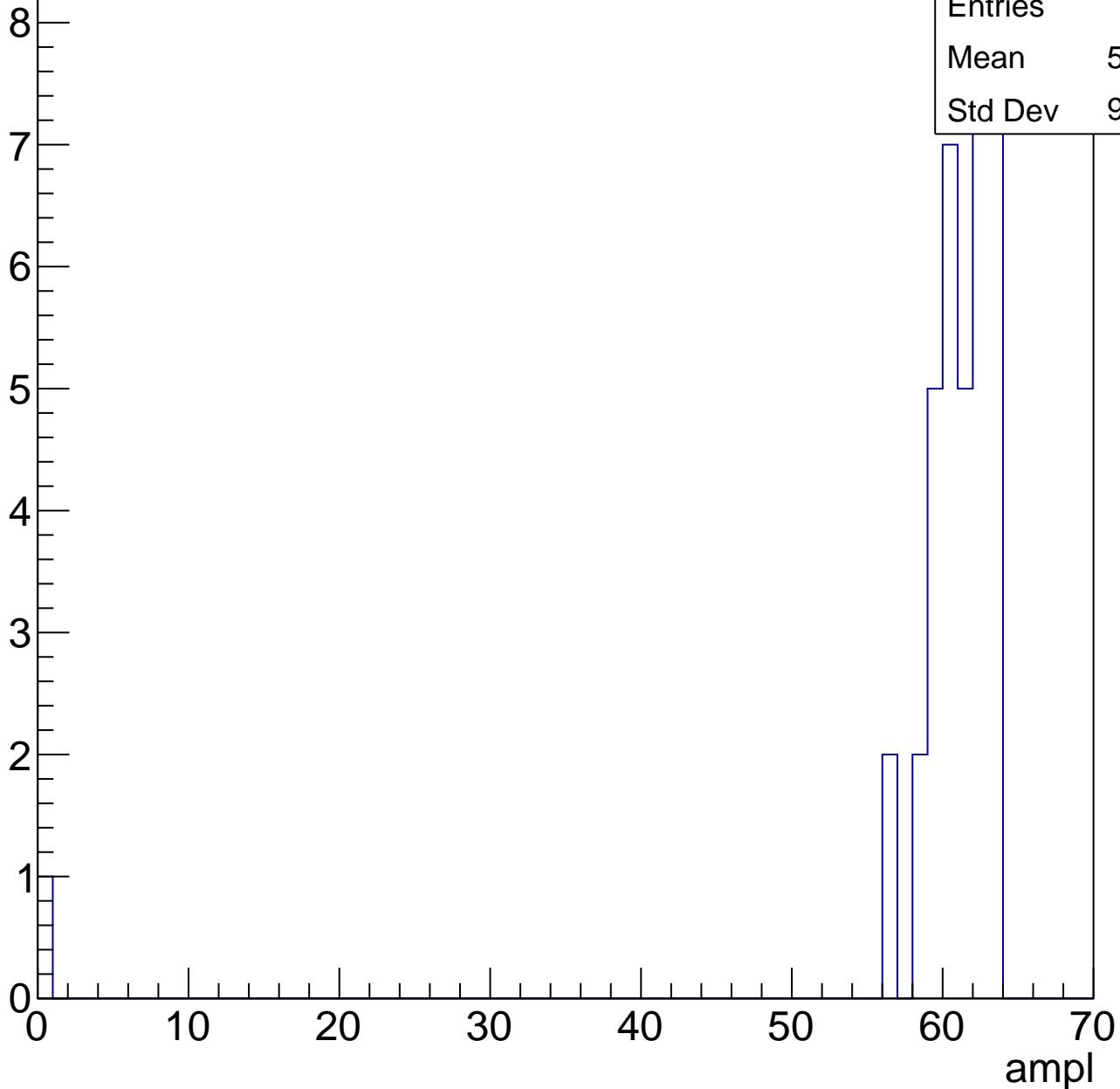


B0L002S, U18-ch30, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	59.16
Std Dev	9.904



B0L002S, U18-ch30, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch30, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch31, adc0

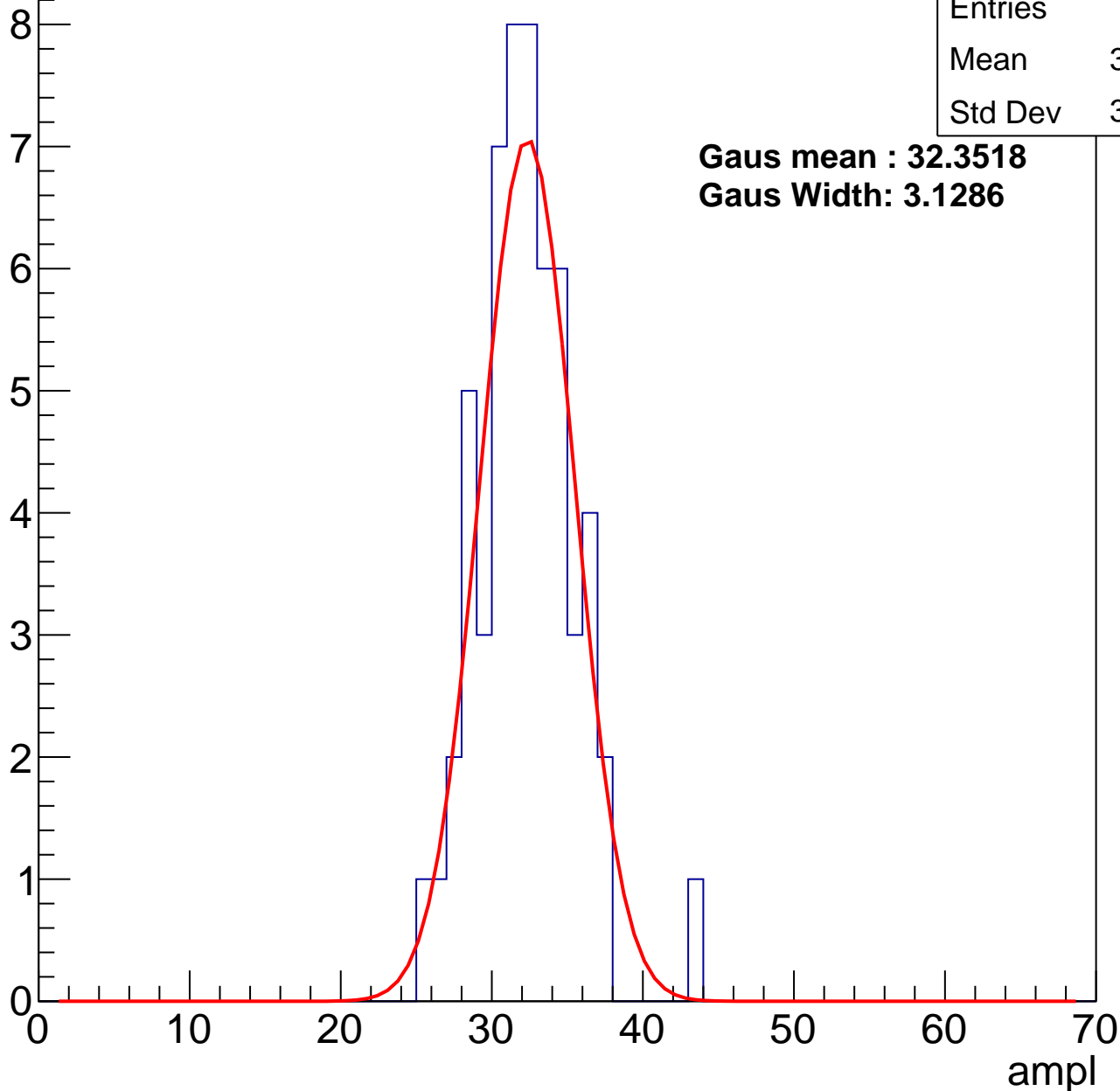
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	31.82
Std Dev	3.157

Gaus mean : 32.3518

Gaus Width: 3.1286



B0L002S, U18-ch31, adc1

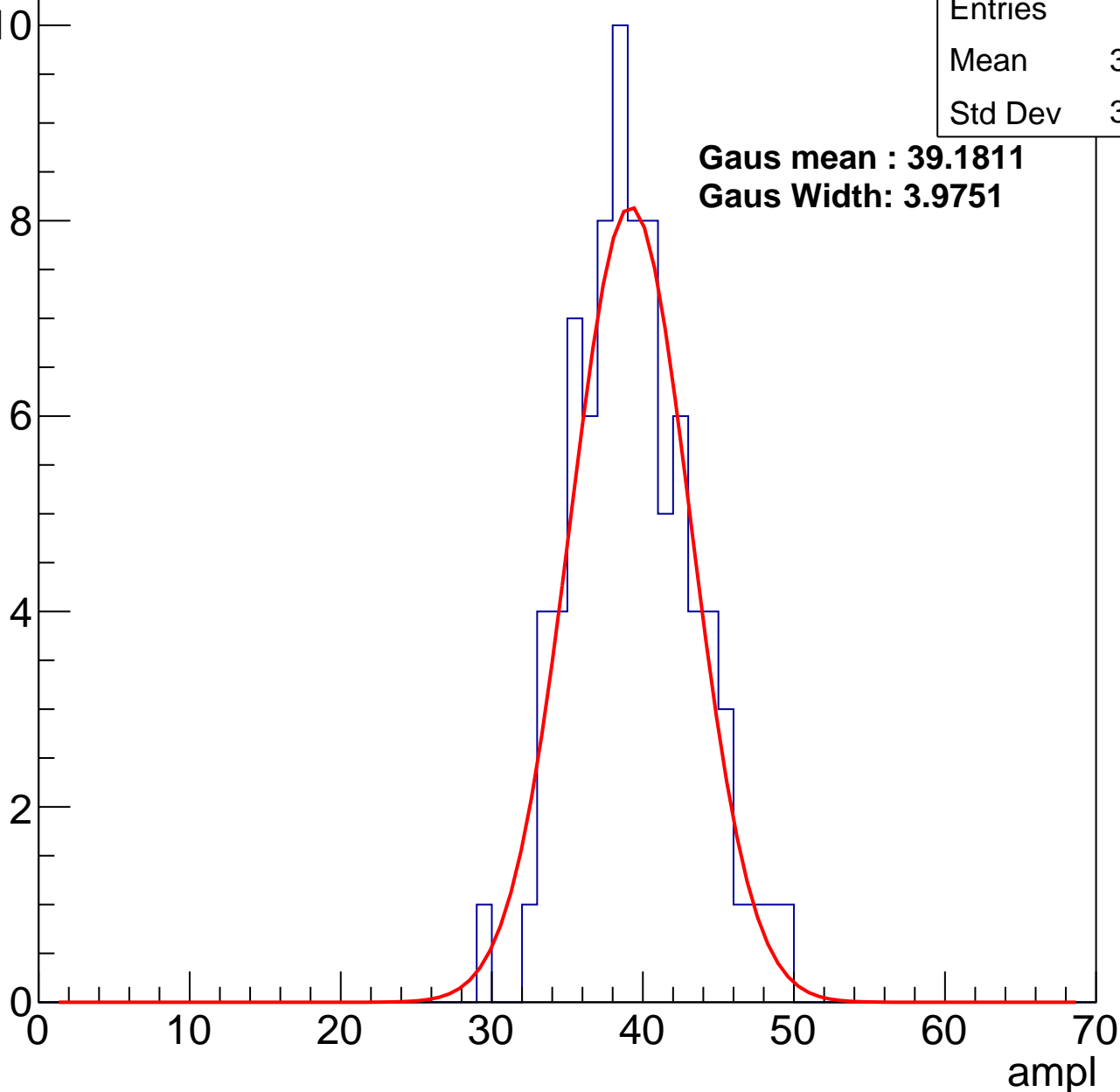
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	38.89
Std Dev	3.878

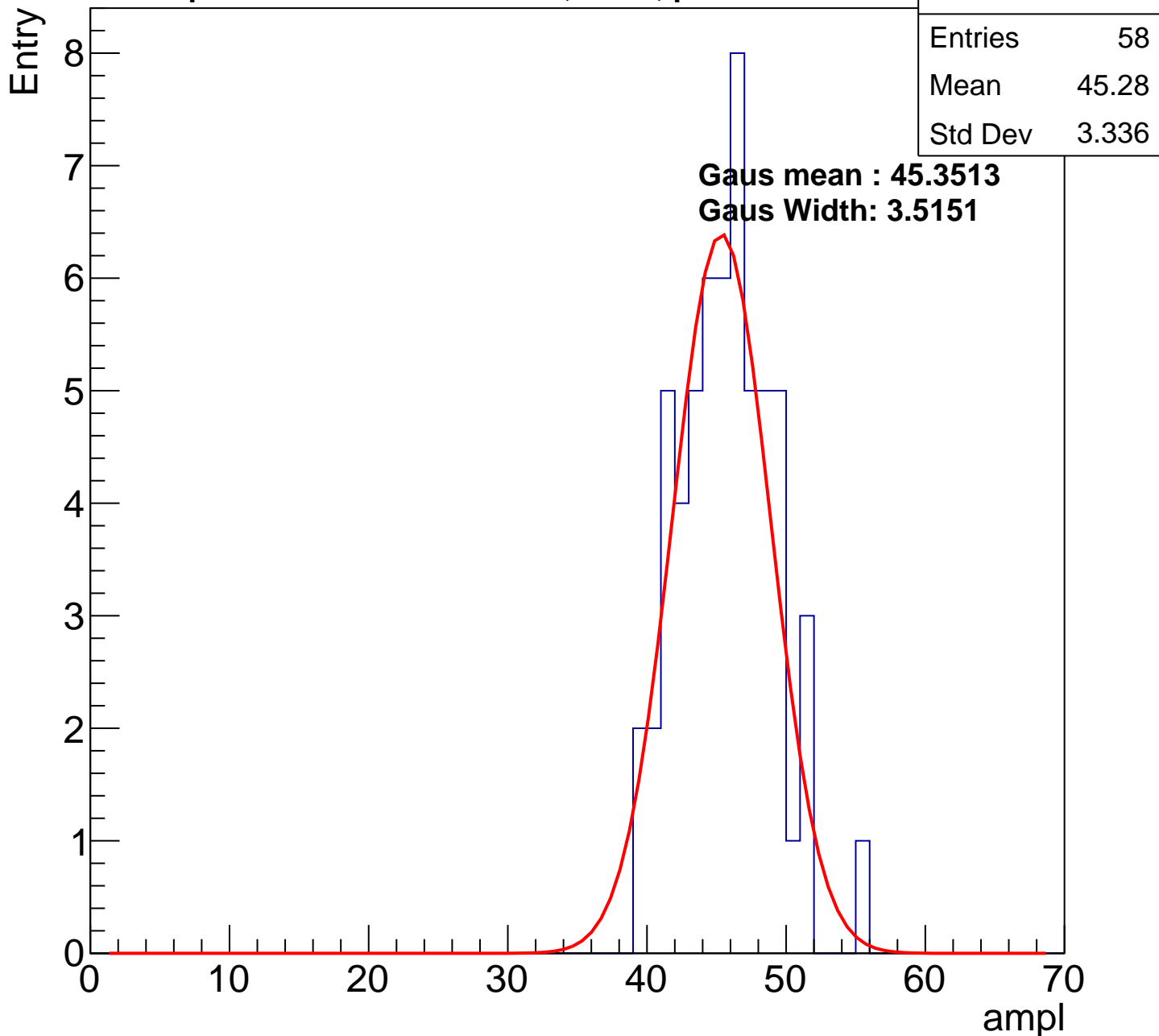
Gaus mean : 39.1811

Gaus Width: 3.9751



B0L002S, U18-ch31, adc2

calib_packv5_042523_0143.root, FC#8, port C1

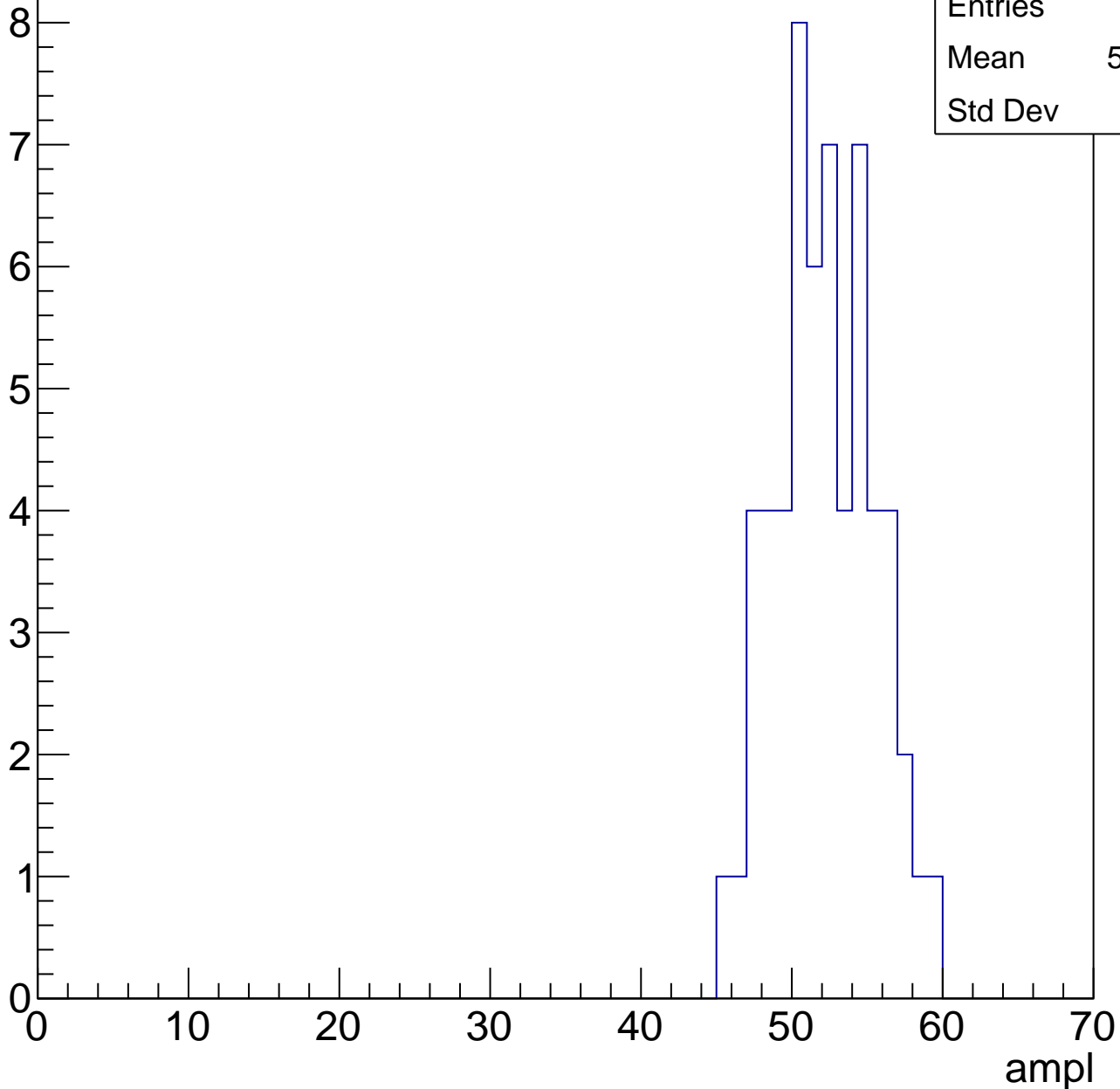


B0L002S, U18-ch31, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	51.76
Std Dev	3.18

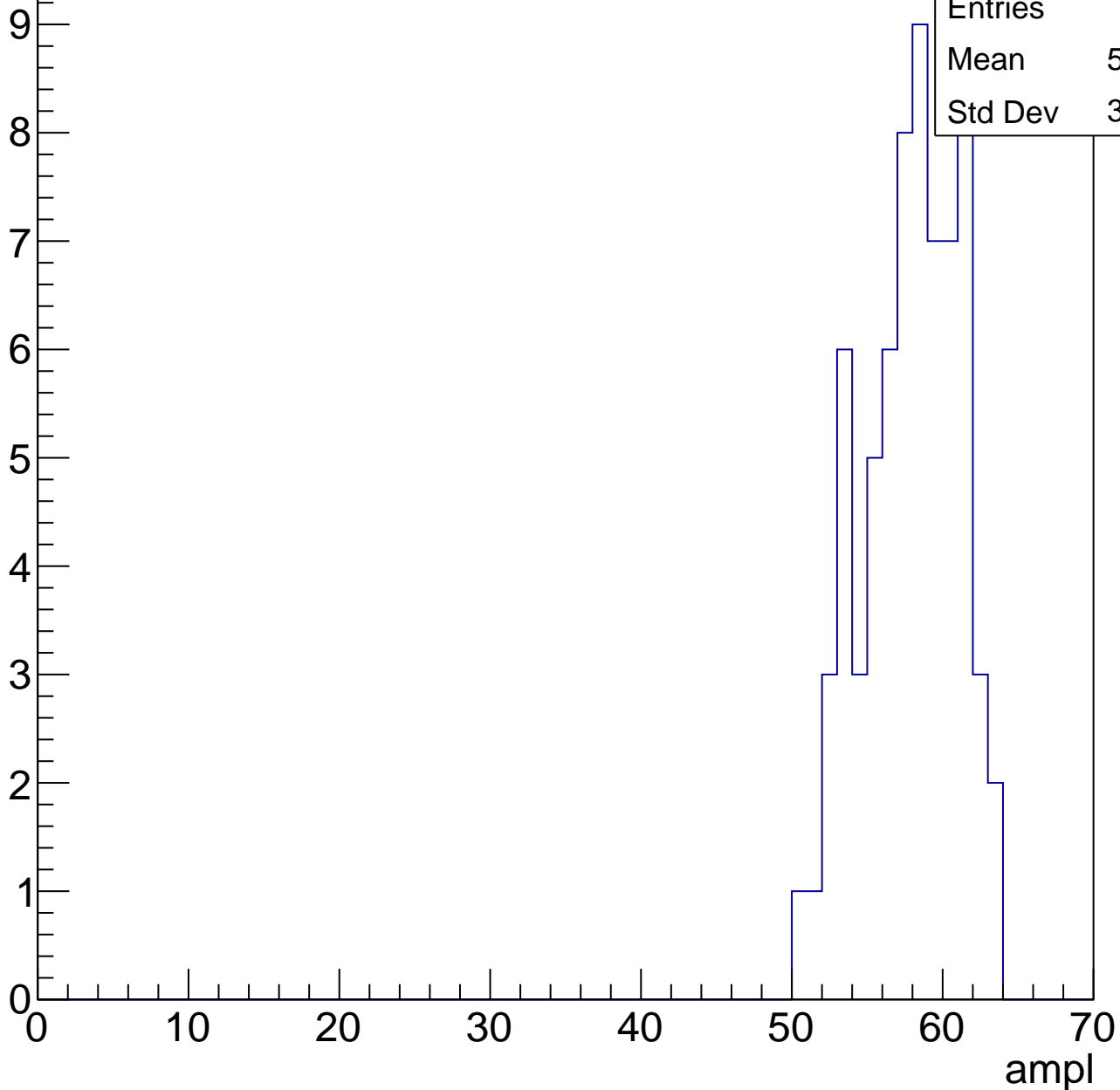


B0L002S, U18-ch31, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	57.38
Std Dev	3.126

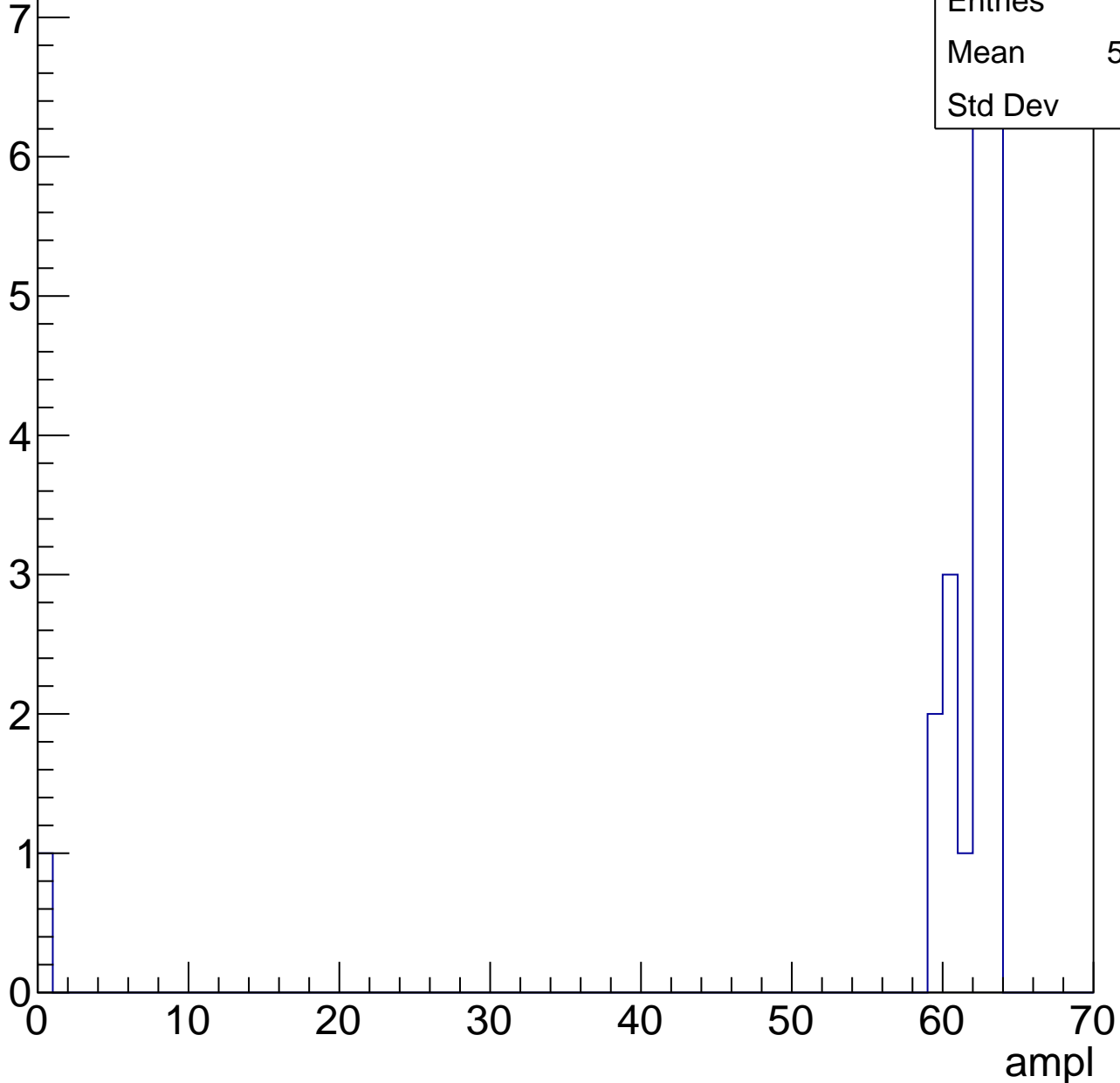


B0L002S, U18-ch31, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	21
Mean	58.76
Std Dev	13.2



B0L002S, U18-ch31, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch31, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch32, adc0

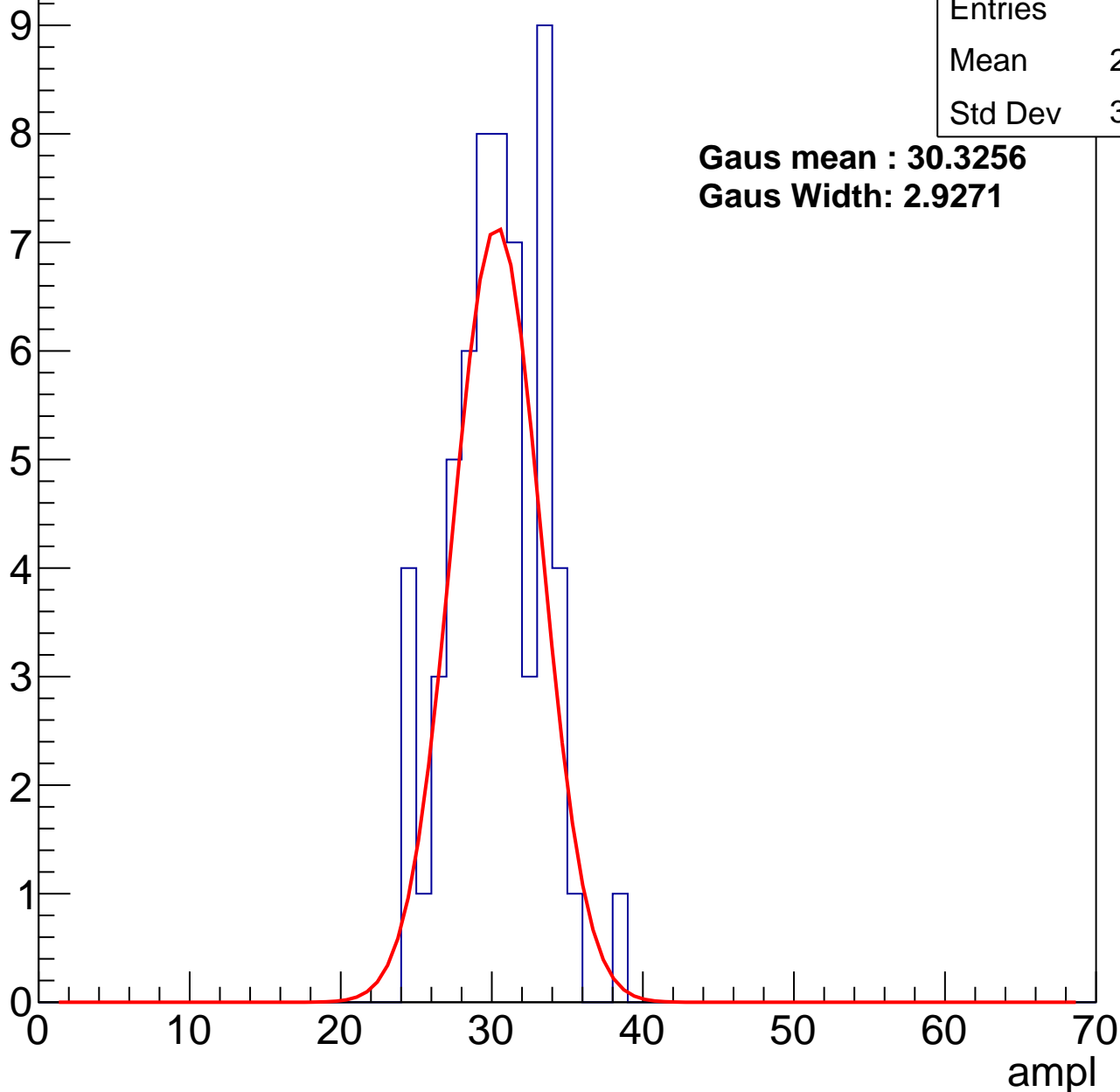
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.88
Std Dev	3.017

Gaus mean : 30.3256

Gaus Width: 2.9271



B0L002S, U18-ch32, adc1

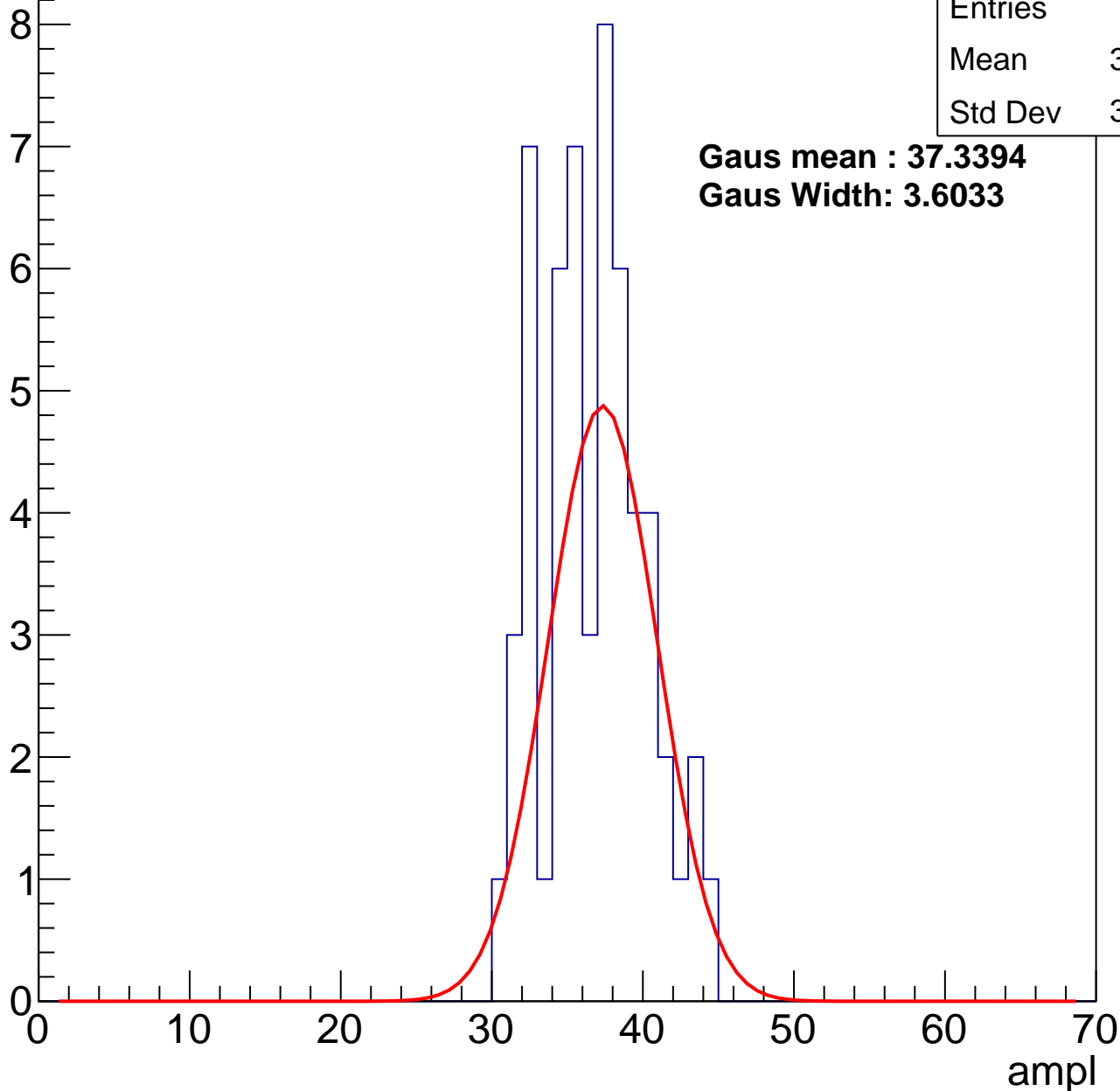
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	36.27
Std Dev	3.378

Gaus mean : 37.3394

Gaus Width: 3.6033



B0L002S, U18-ch32, adc2

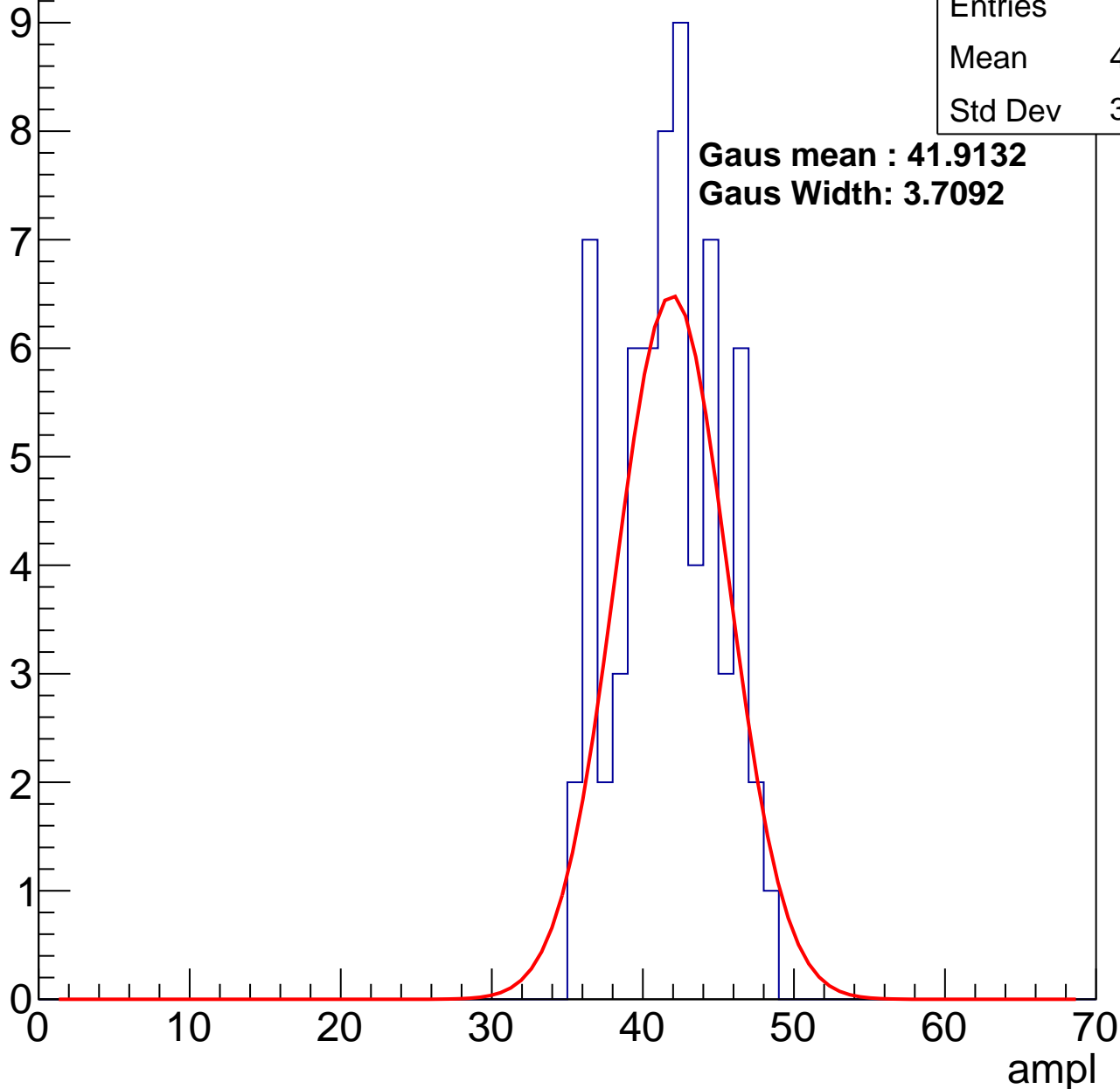
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	41.26
Std Dev	3.345

Gaus mean : 41.9132

Gaus Width: 3.7092

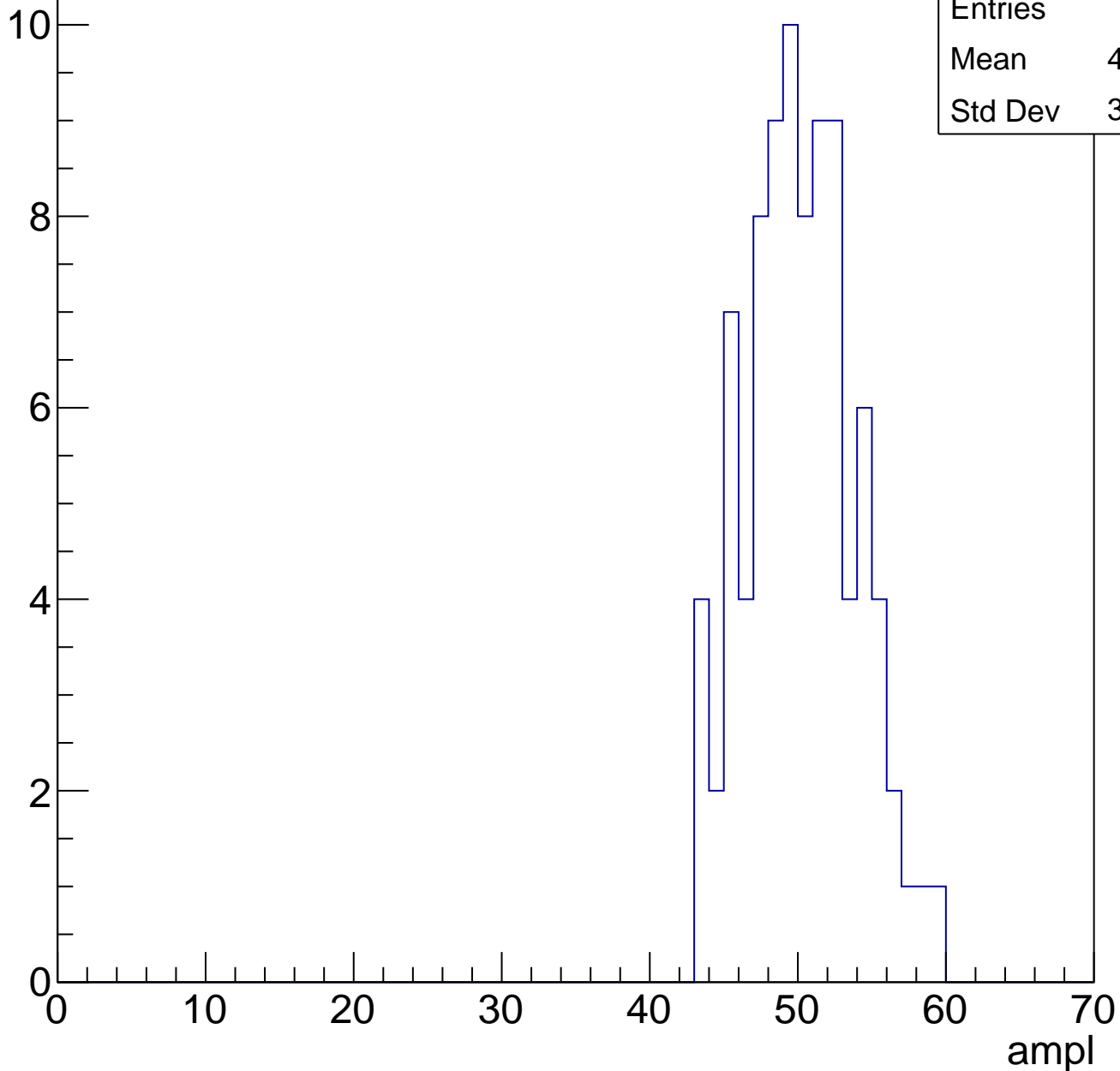


B0L002S, U18-ch32, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	49.73
Std Dev	3.613

Entry

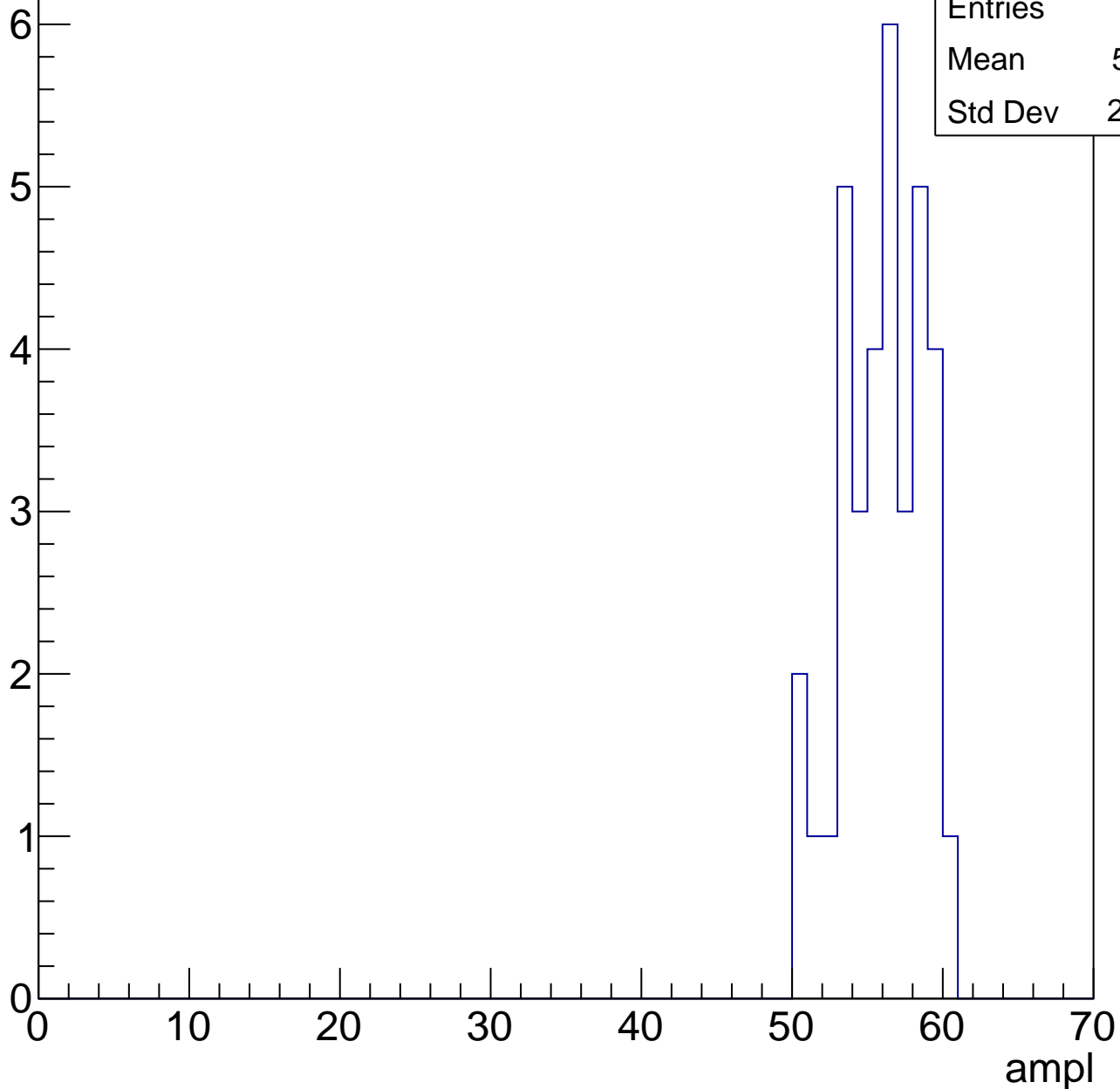


B0L002S, U18-ch32, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	55.51
Std Dev	2.623

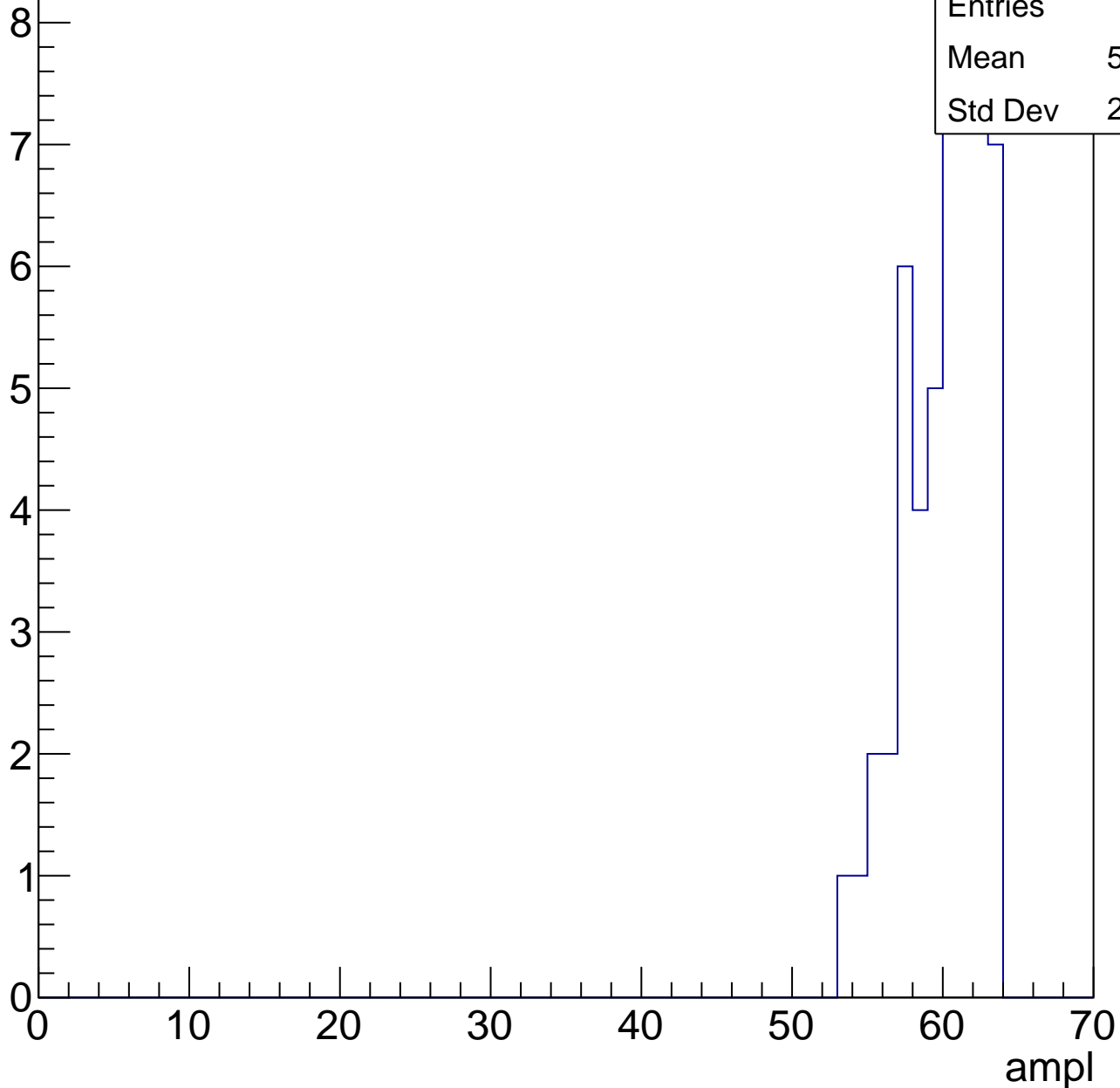


B0L002S, U18-ch32, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

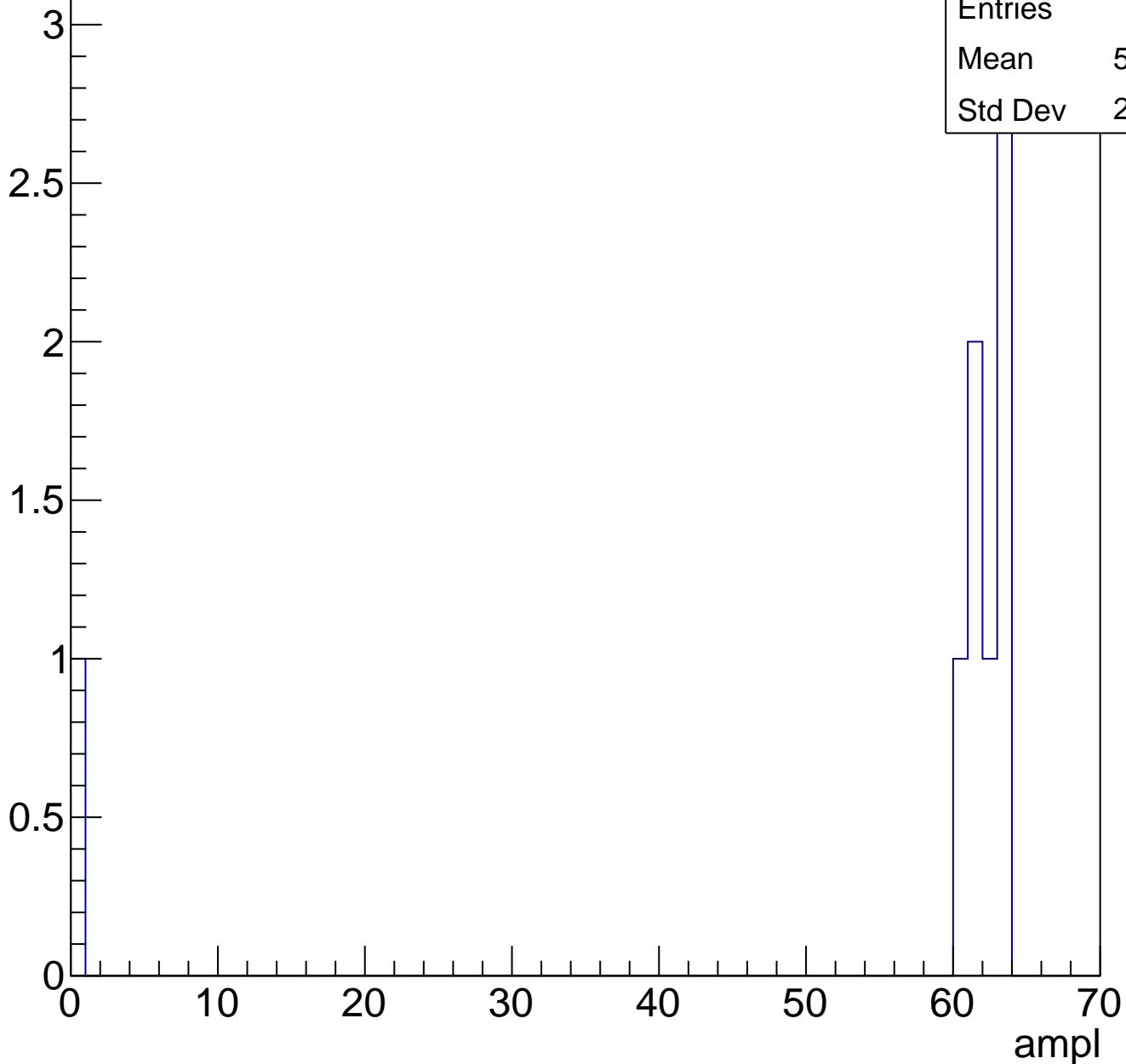
Entries	52
Mean	59.67
Std Dev	2.555



B0L002S, U18-ch32, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch32, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L002S, U18-ch33, adc0

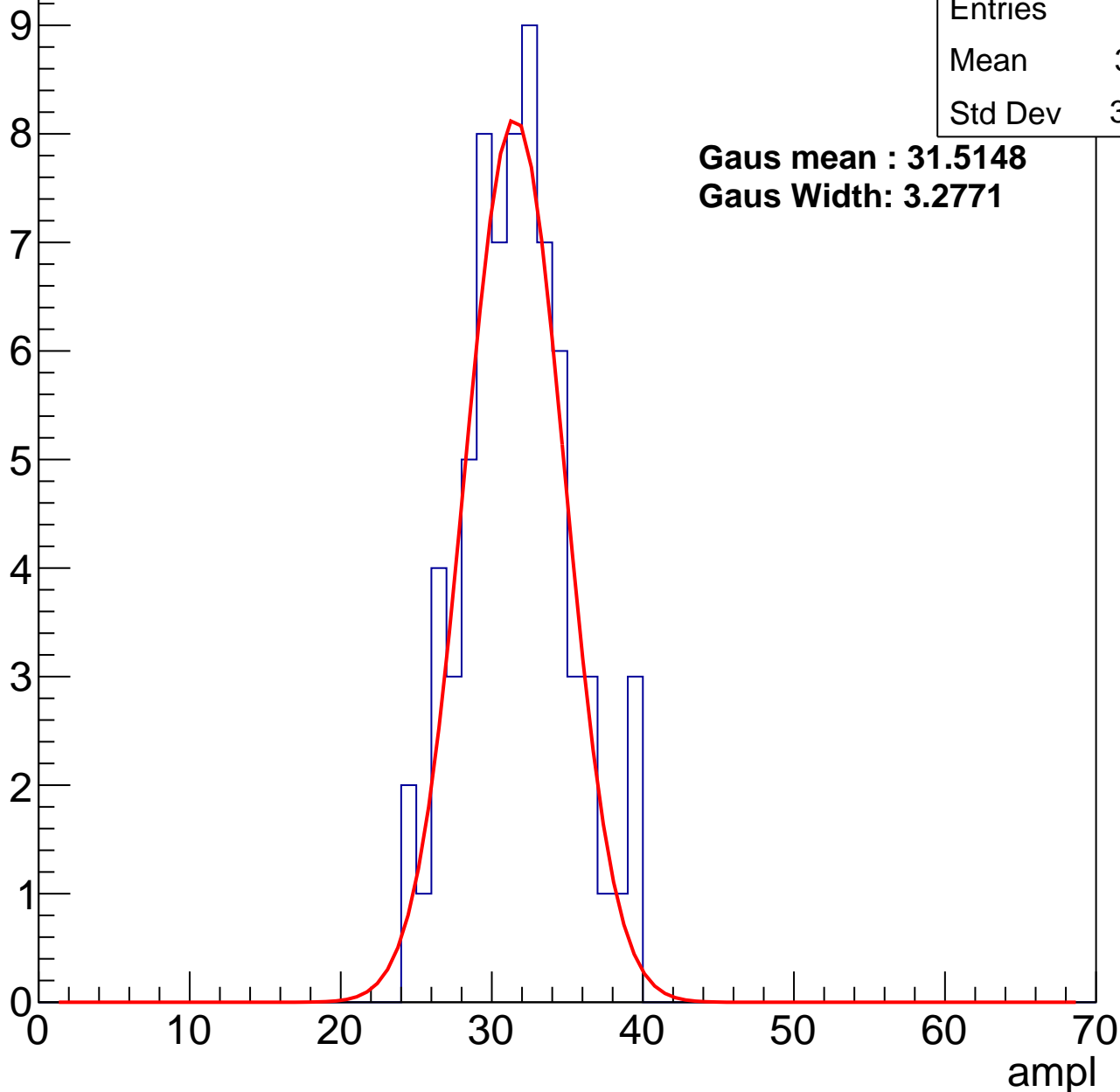
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	31.21
Std Dev	3.468

Gaus mean : 31.5148

Gaus Width: 3.2771



B0L002S, U18-ch33, adc1

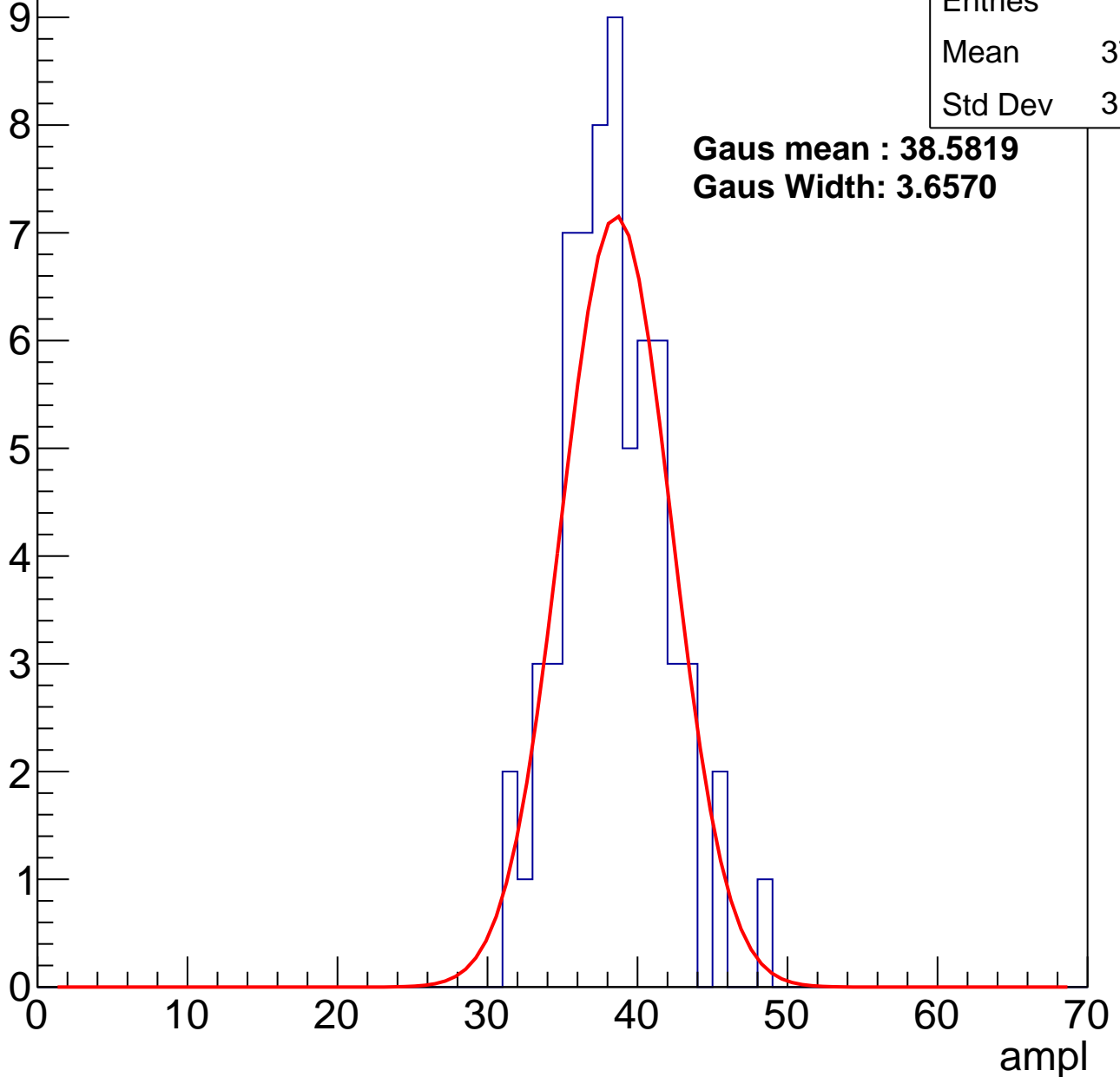
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	37.94
Std Dev	3.393

Gaus mean : 38.5819

Gaus Width: 3.6570



B0L002S, U18-ch33, adc2

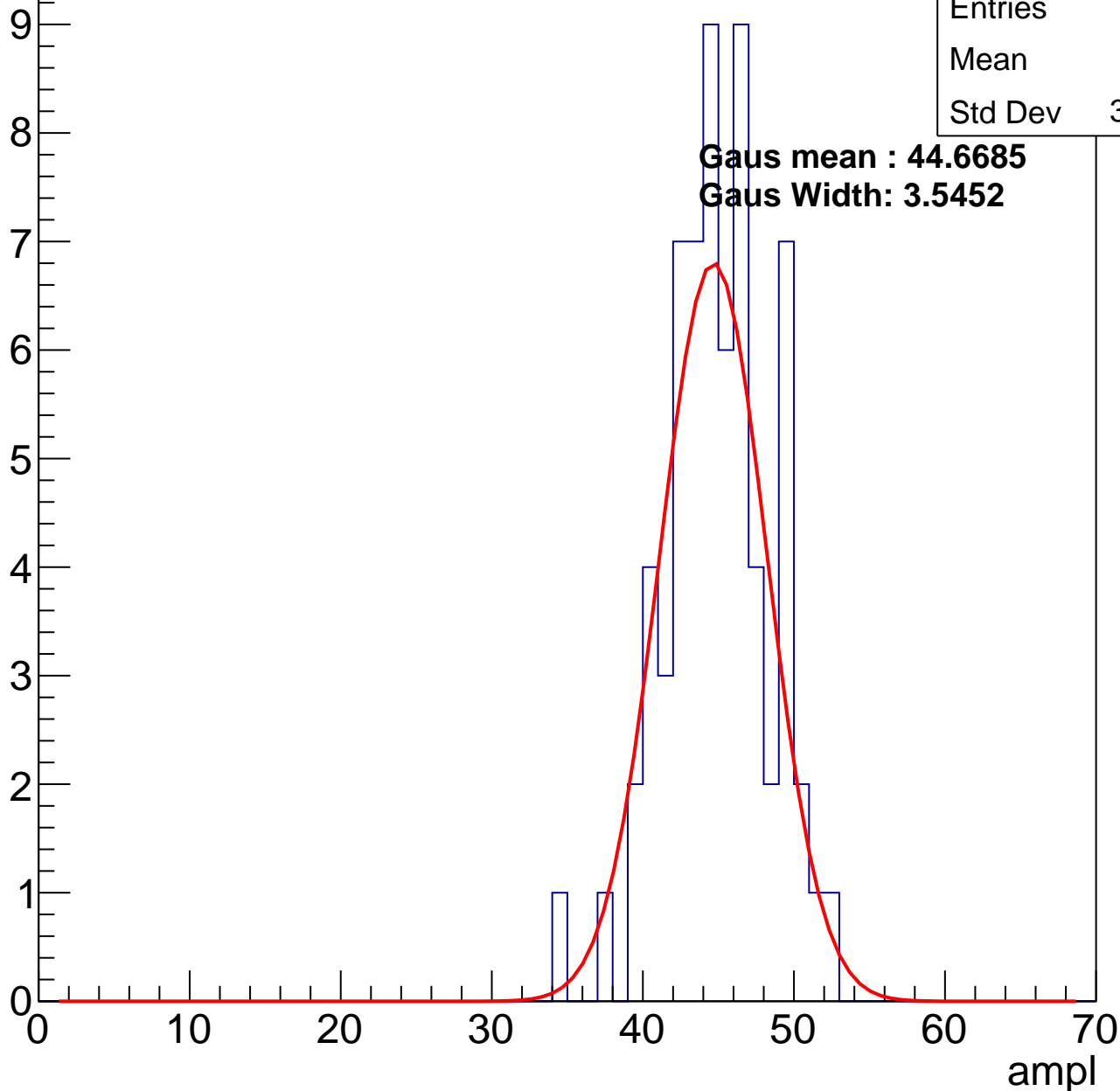
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	44.5
Std Dev	3.434

Gaus mean : 44.6685

Gaus Width: 3.5452

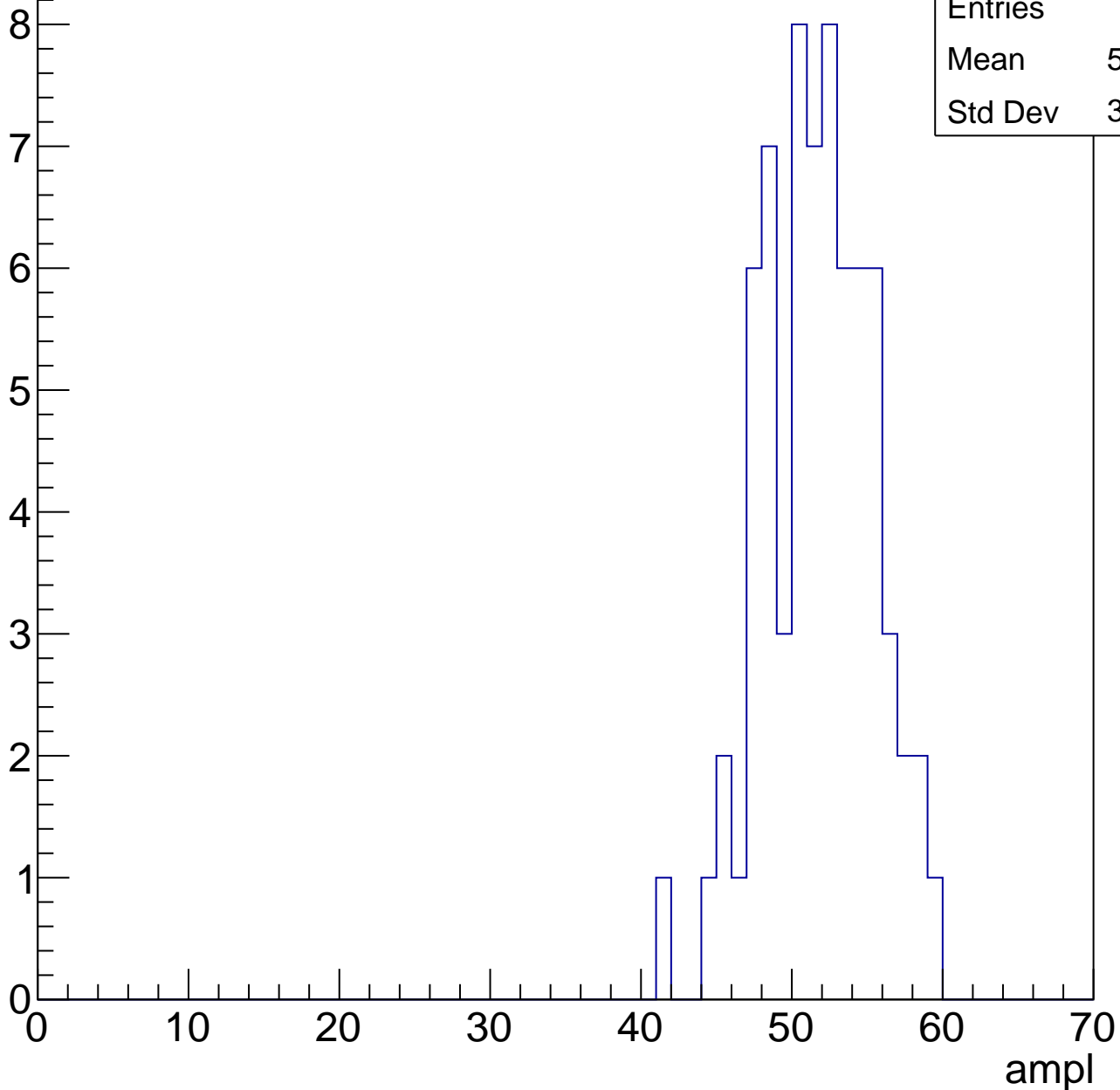


B0L002S, U18-ch33, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	51.26
Std Dev	3.608

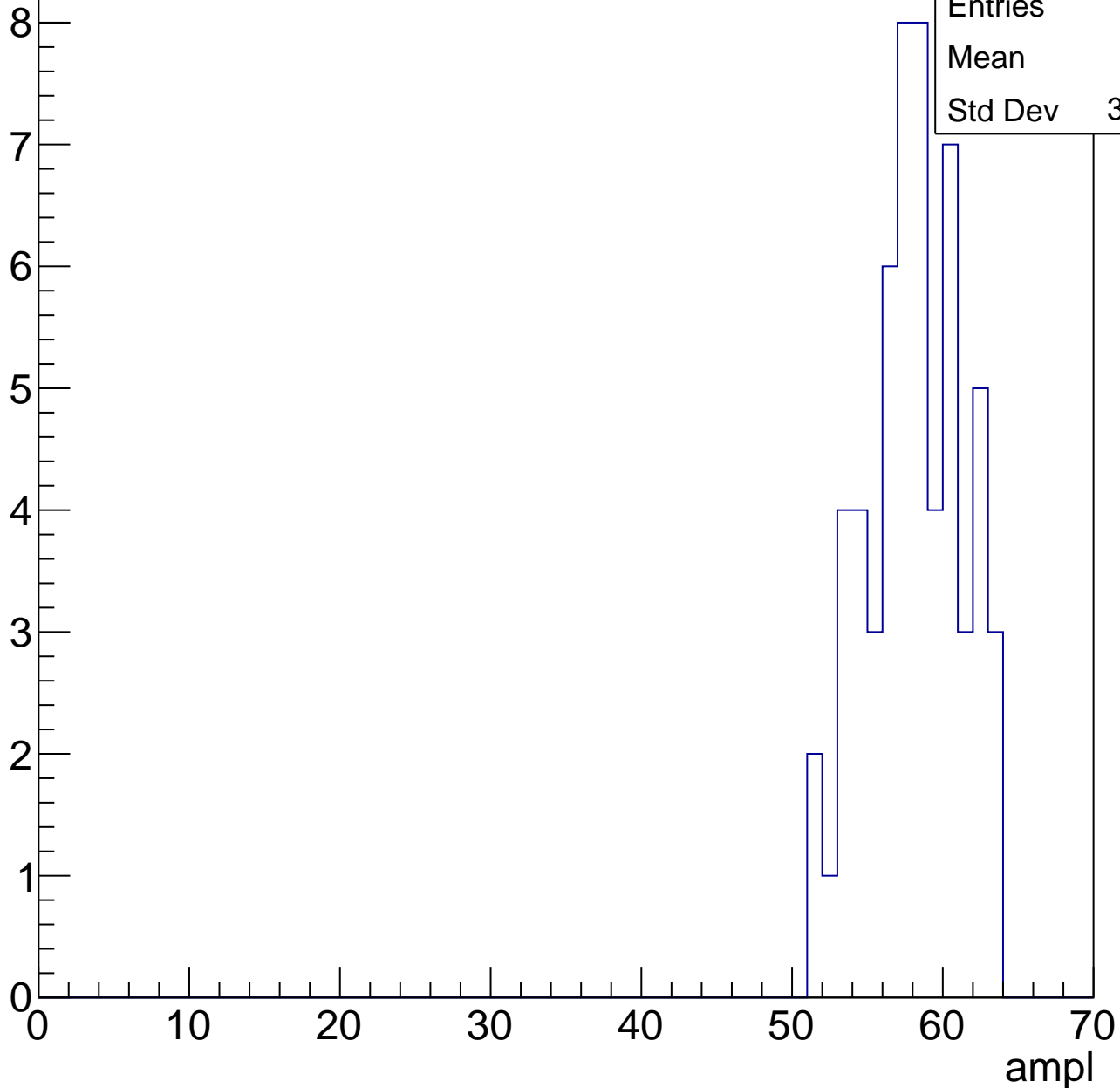


B0L002S, U18-ch33, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.6
Std Dev	3.112

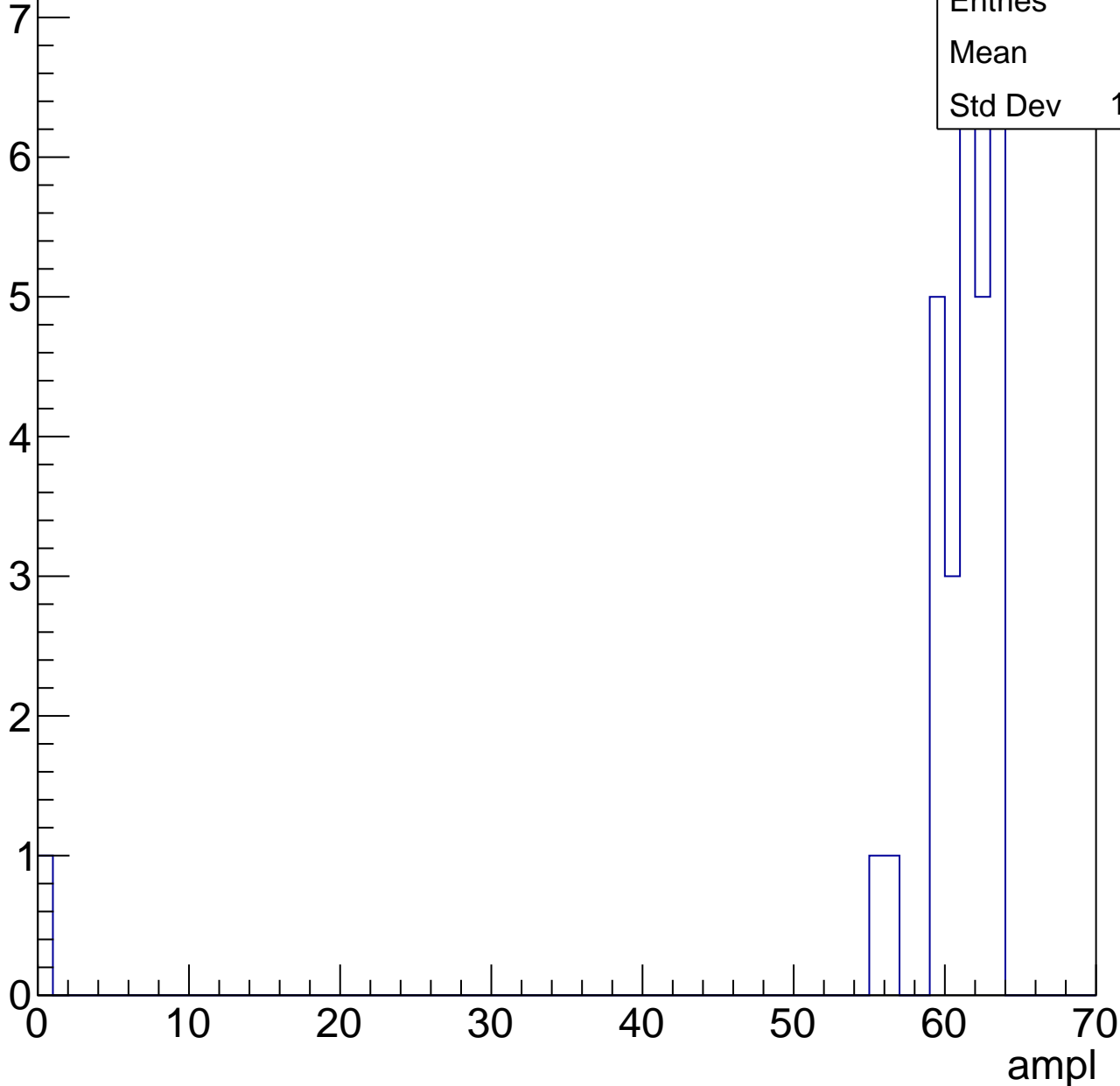


B0L002S, U18-ch33, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	58.8
Std Dev	11.09



B0L002S, U18-ch33, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



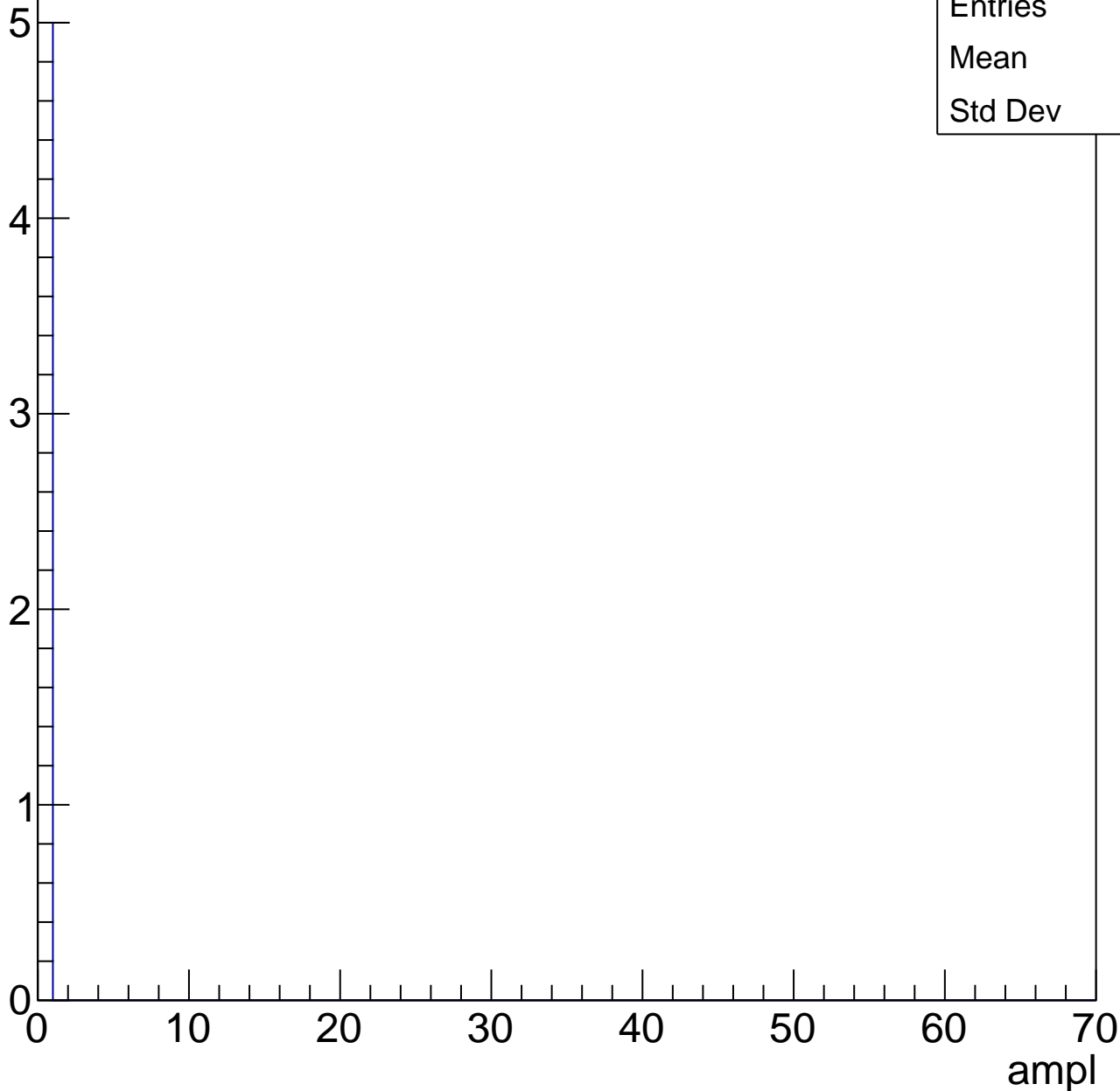
Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch33, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	5
Mean	0
Std Dev	0



B0L002S, U18-ch34, adc0

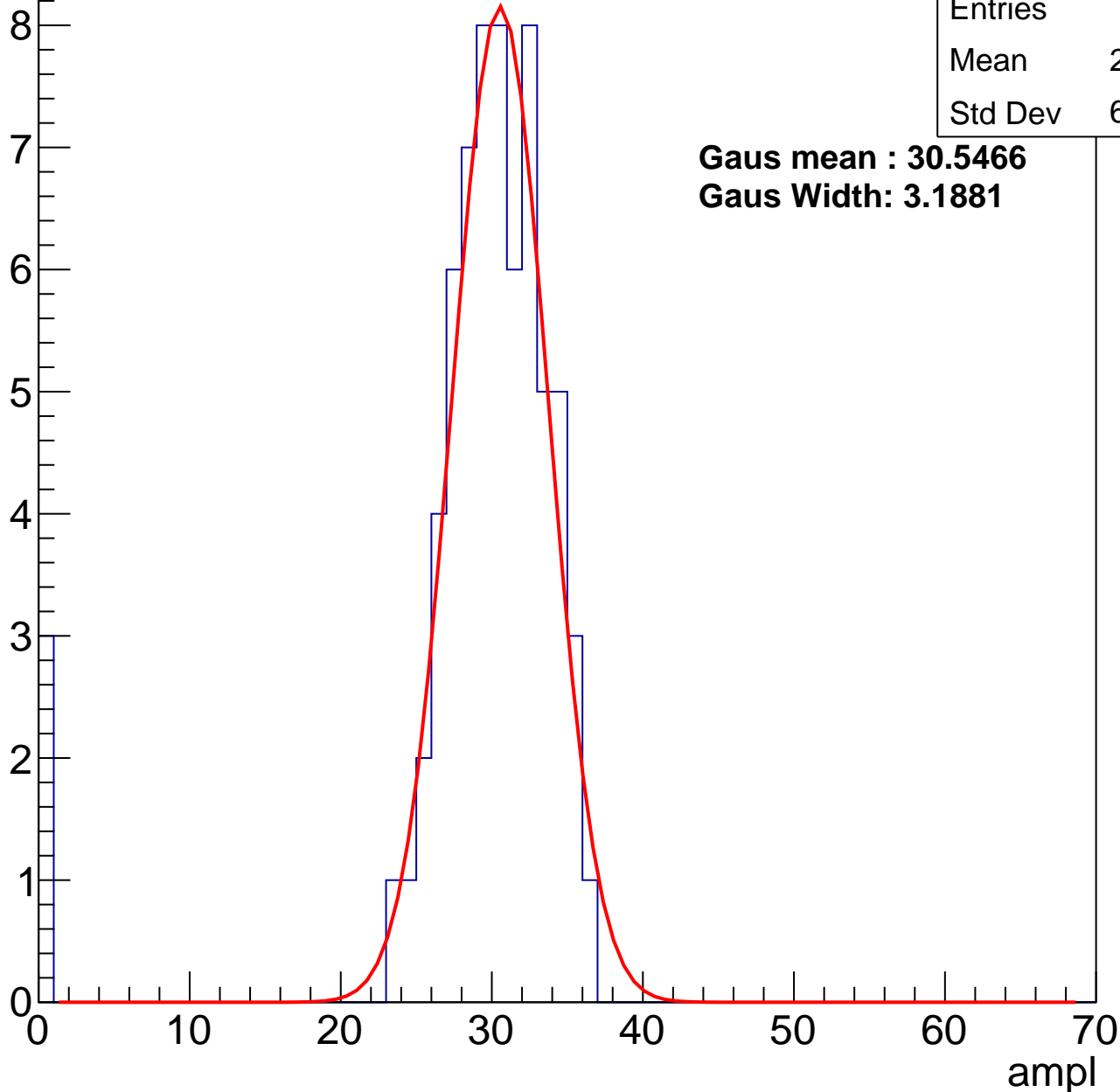
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	28.66
Std Dev	6.797

Gaus mean : 30.5466

Gaus Width: 3.1881



B0L002S, U18-ch34, adc1

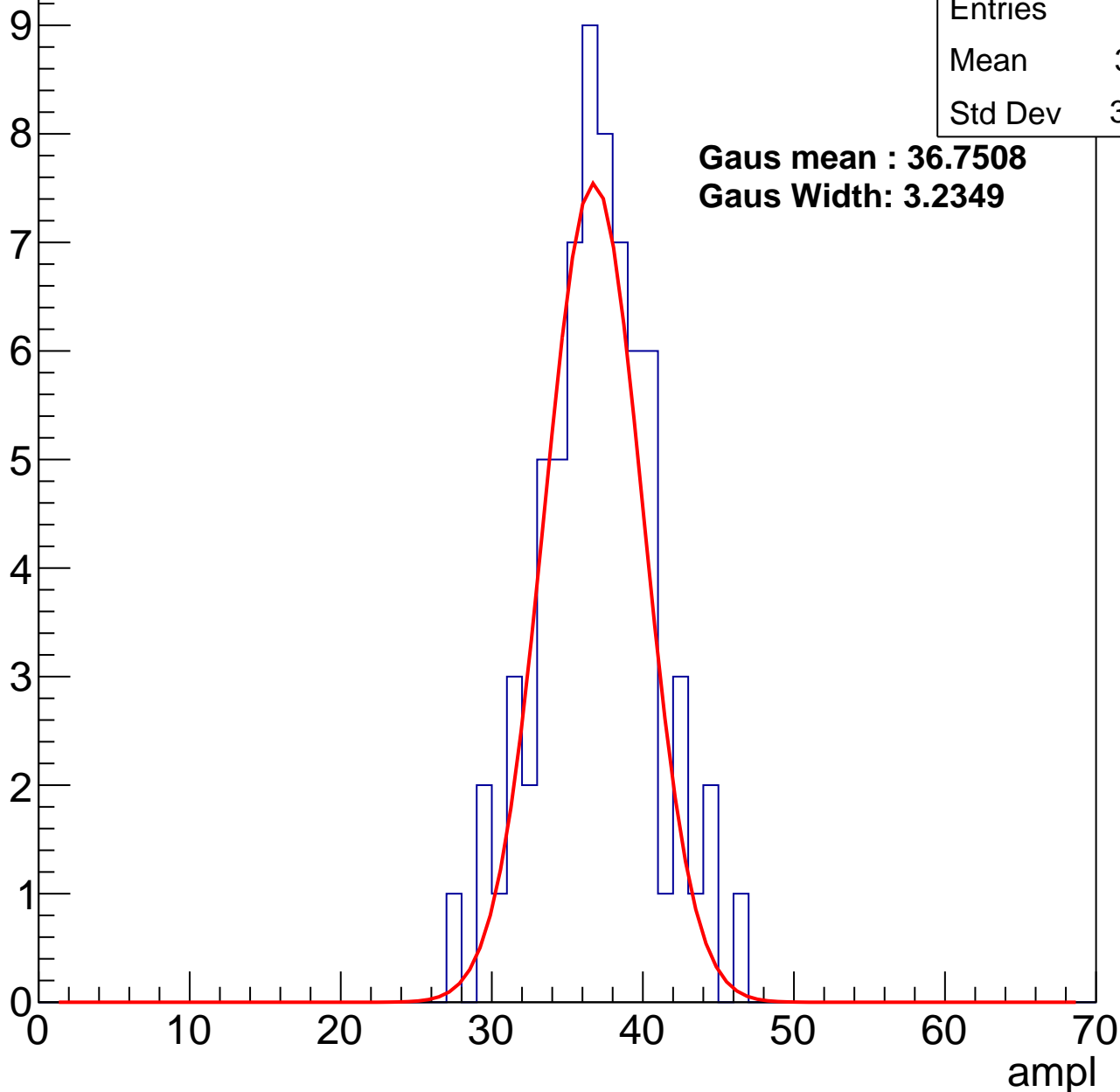
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	36.51
Std Dev	3.725

Gaus mean : 36.7508

Gaus Width: 3.2349



B0L002S, U18-ch34, adc2

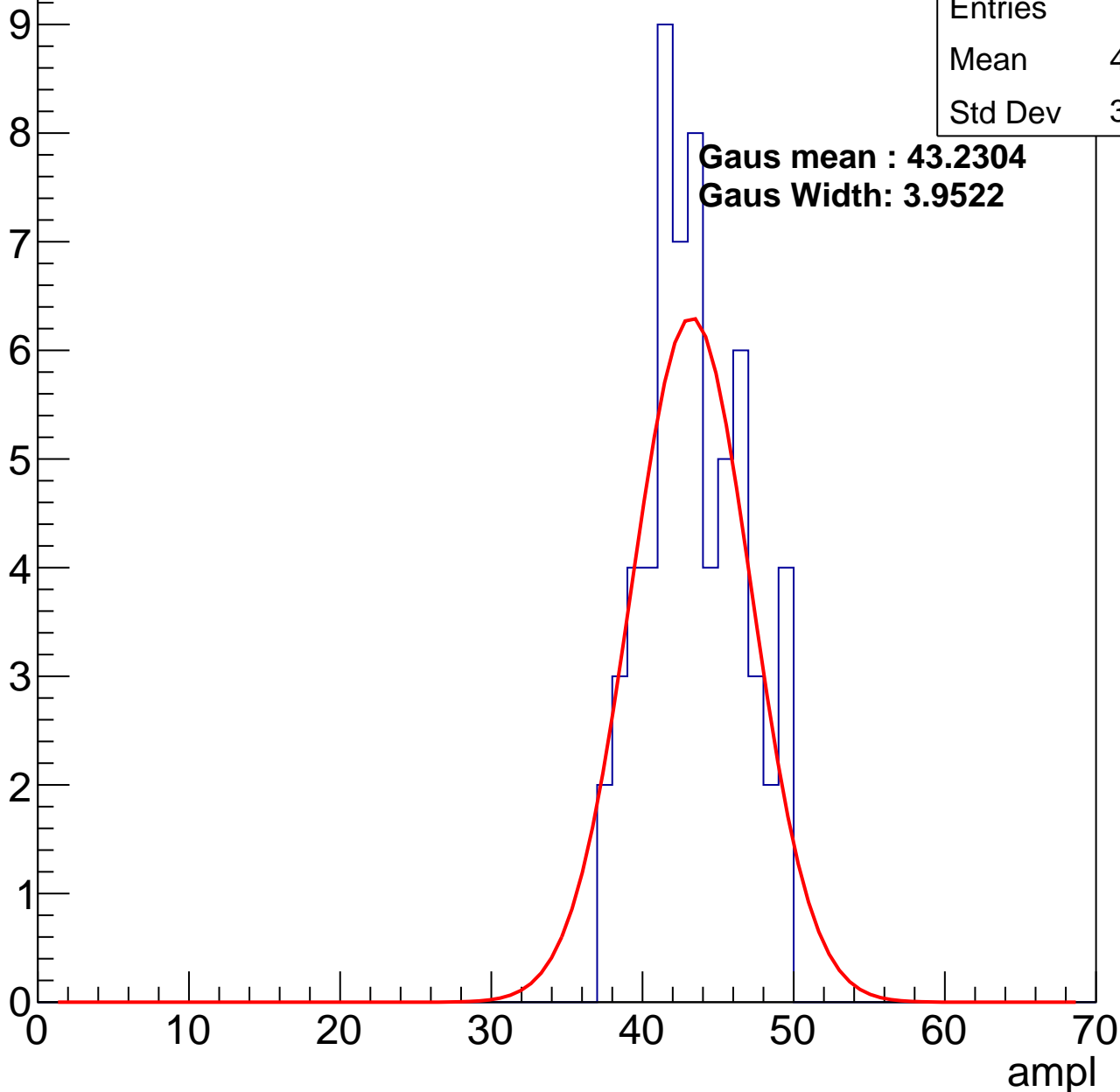
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.97
Std Dev	3.162

Gaus mean : 43.2304

Gaus Width: 3.9522



B0L002S, U18-ch34, adc3

calib_packv5_042523_0143.root, FC#8, port C1

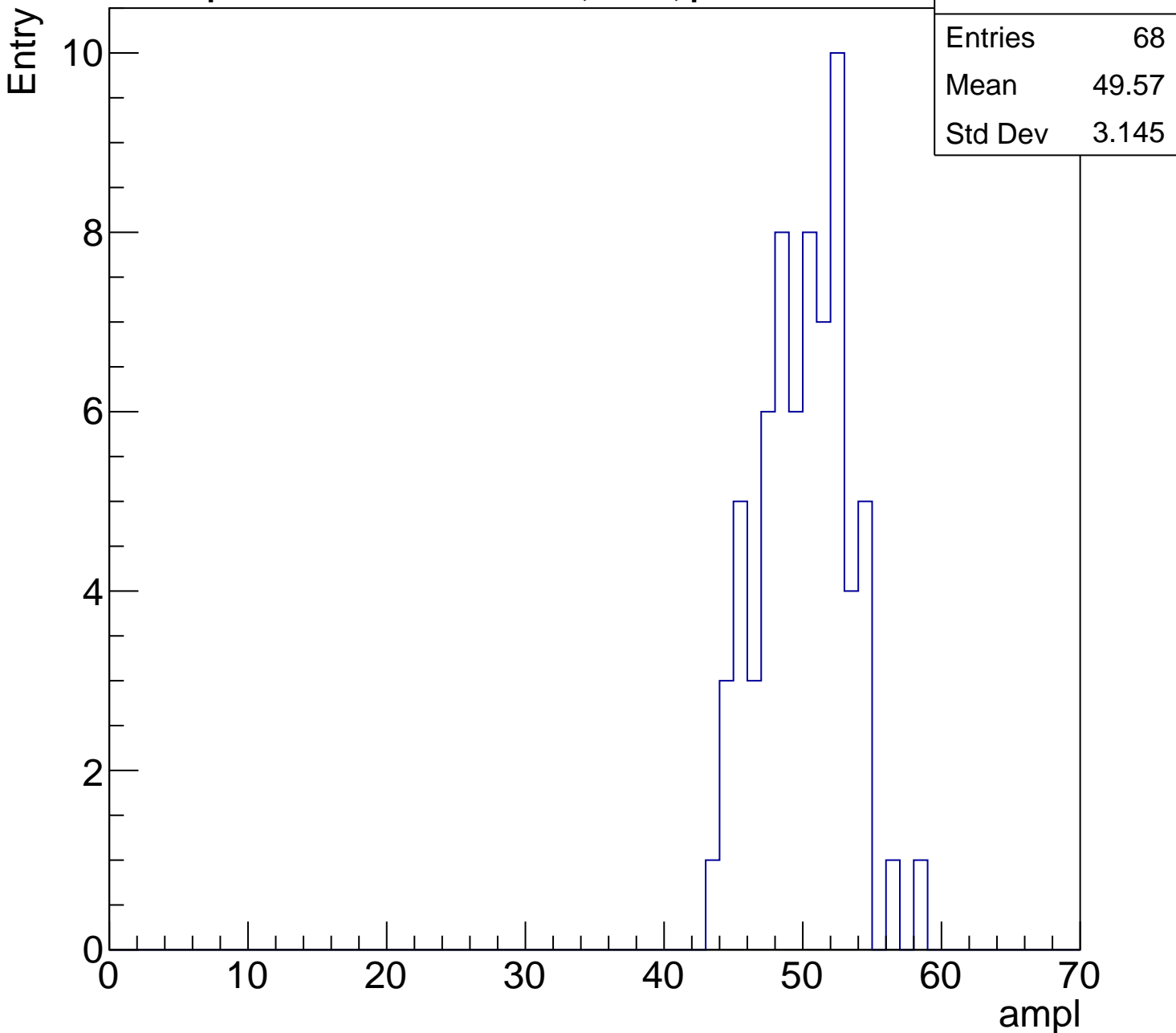
Entries	68
Mean	49.57
Std Dev	3.145

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

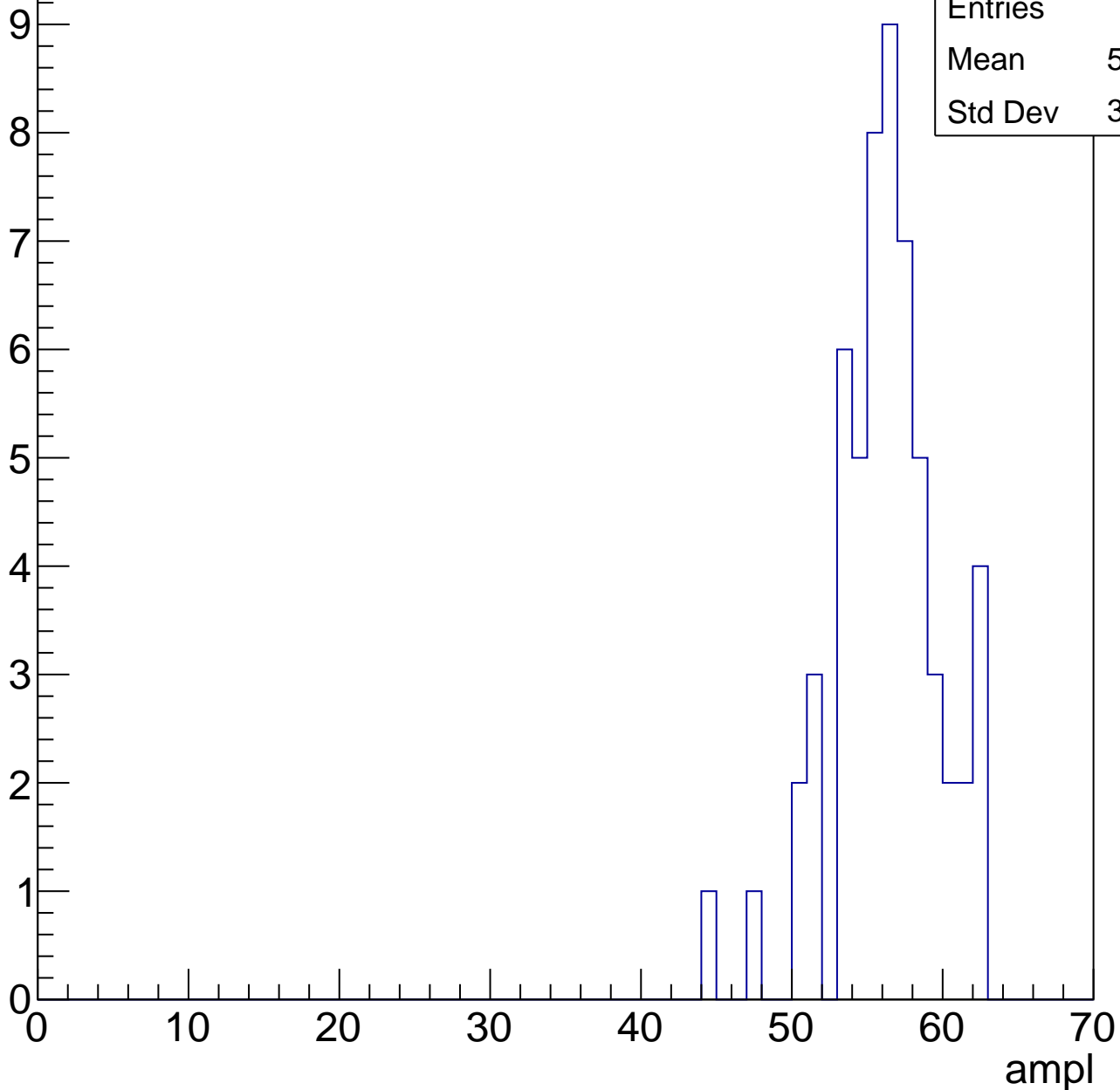


B0L002S, U18-ch34, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

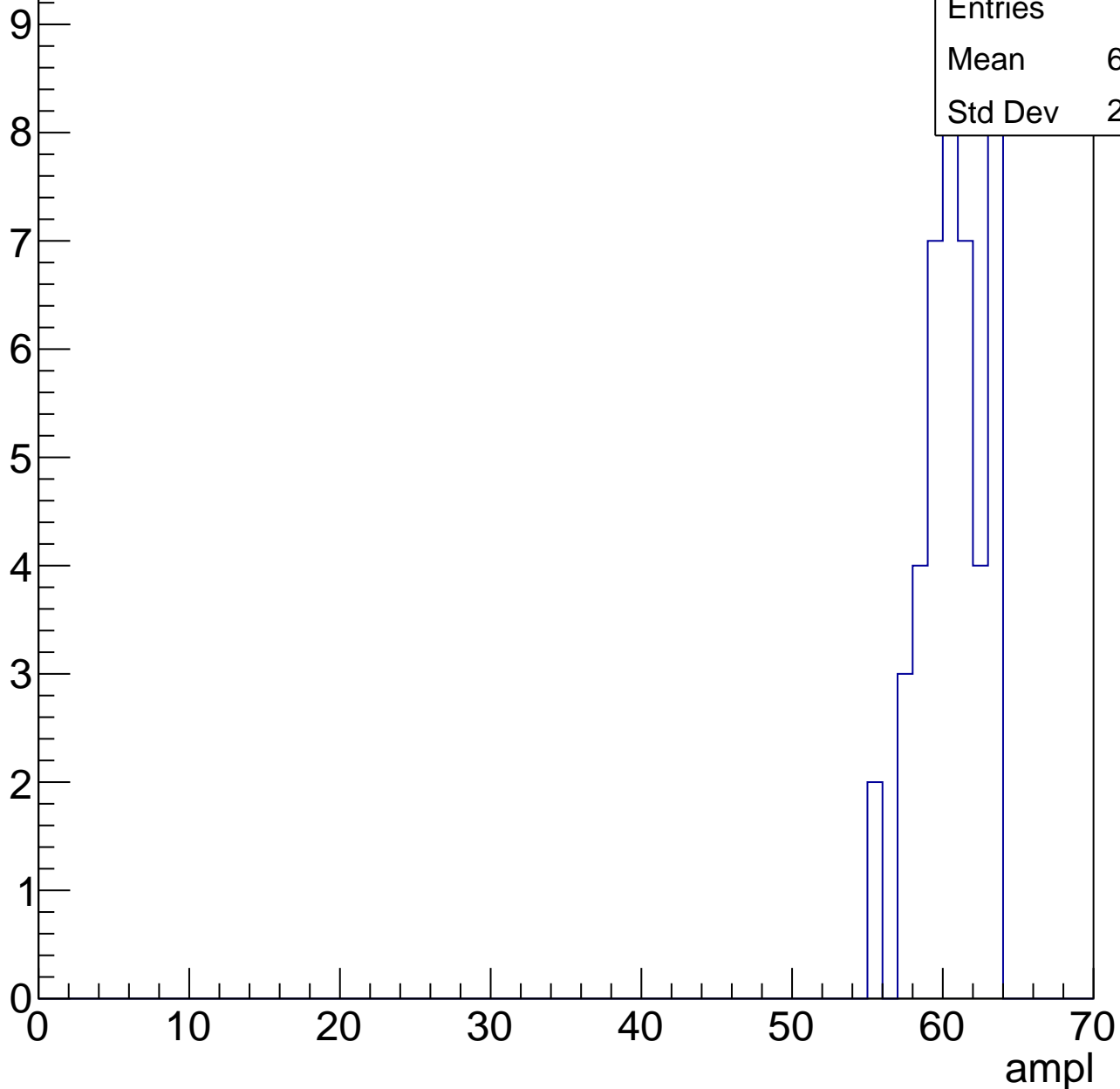
Entries	58
Mean	55.72
Std Dev	3.547



B0L002S, U18-ch34, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch34, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch34, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch35, adc0

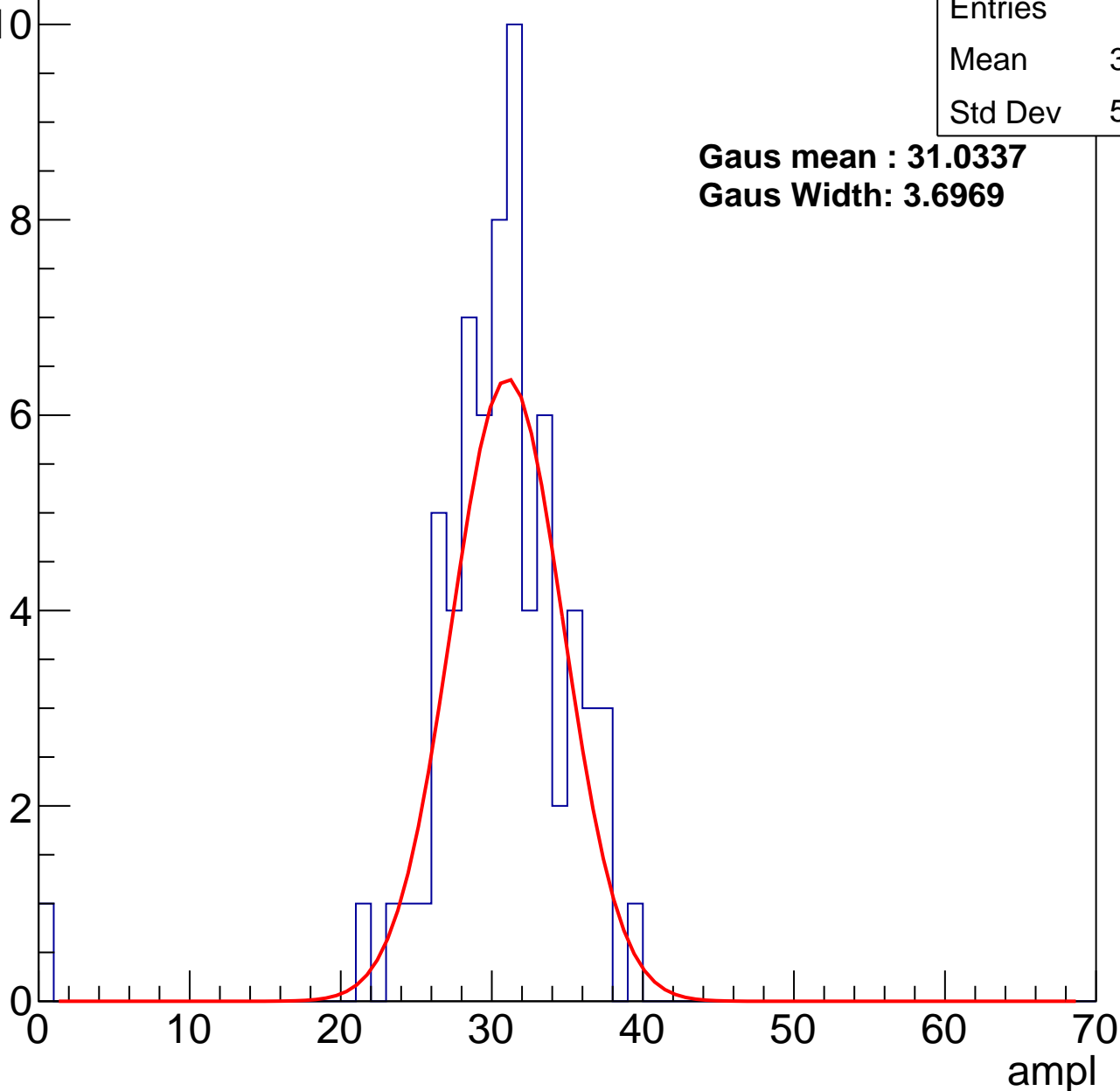
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	30.04
Std Dev	5.129

Gaus mean : 31.0337

Gaus Width: 3.6969



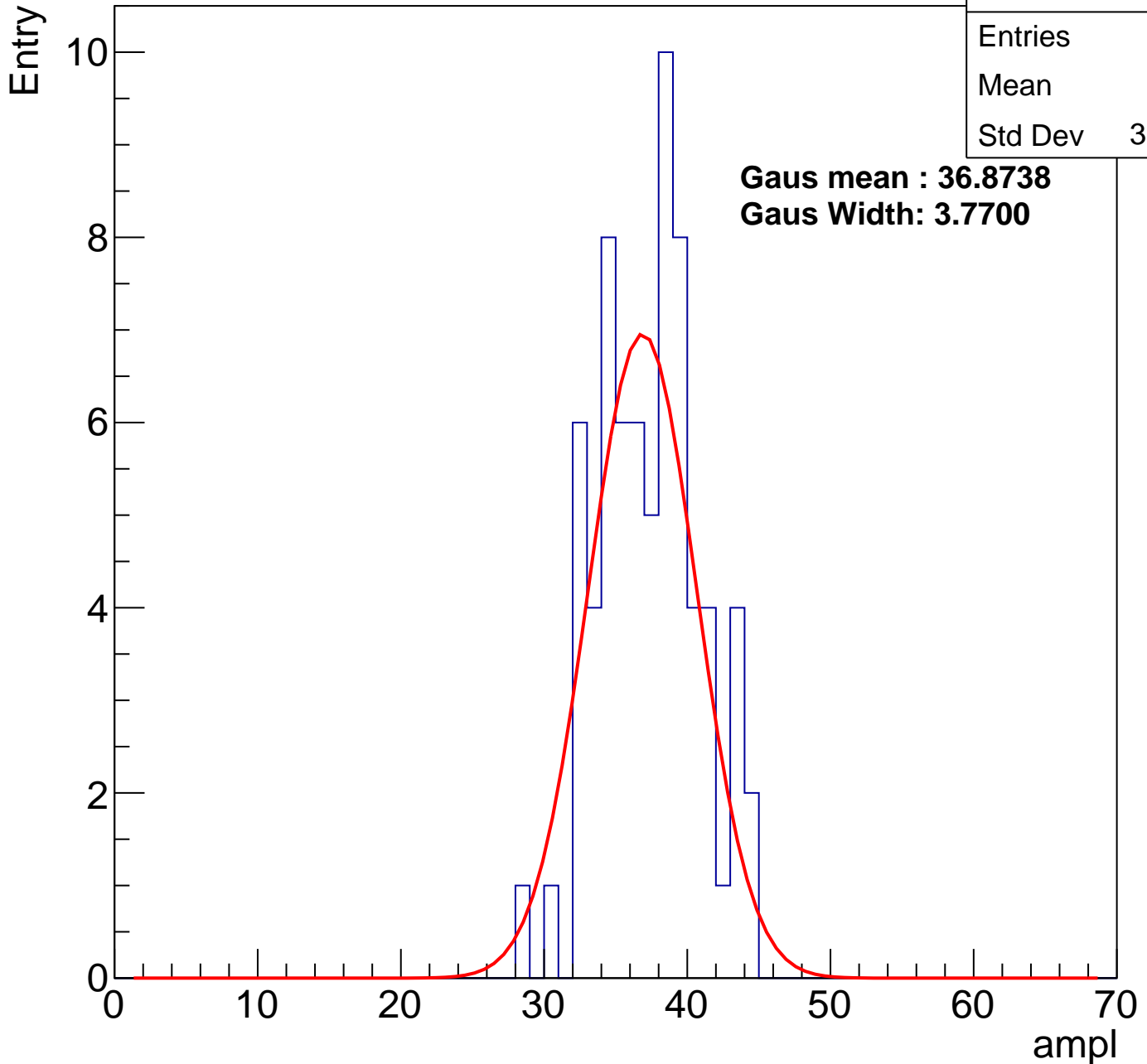
B0L002S, U18-ch35, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	36.9
Std Dev	3.493

Gaus mean : 36.8738

Gaus Width: 3.7700



B0L002S, U18-ch35, adc2

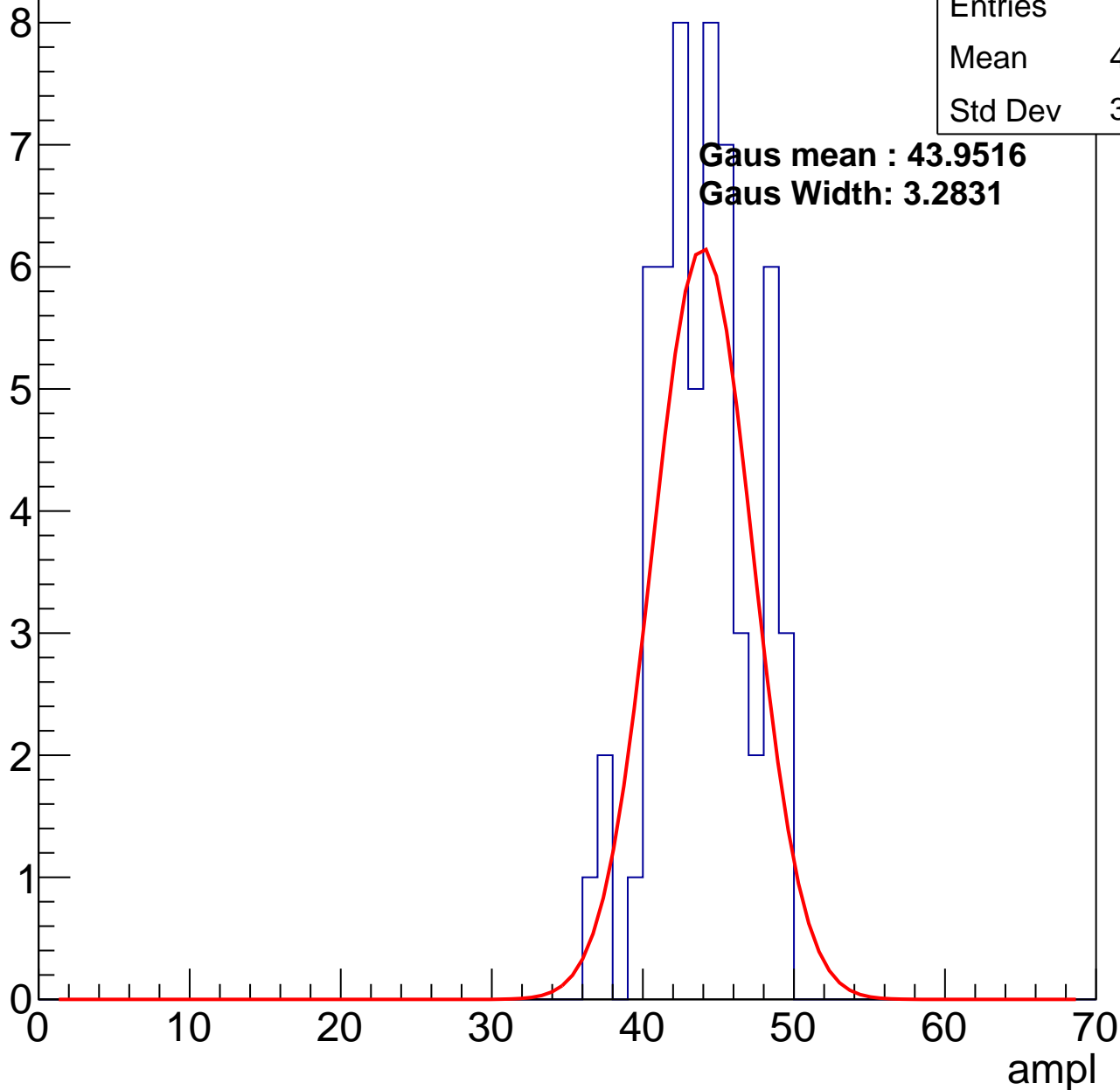
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.45
Std Dev	3.119

Gaus mean : 43.9516

Gaus Width: 3.2831

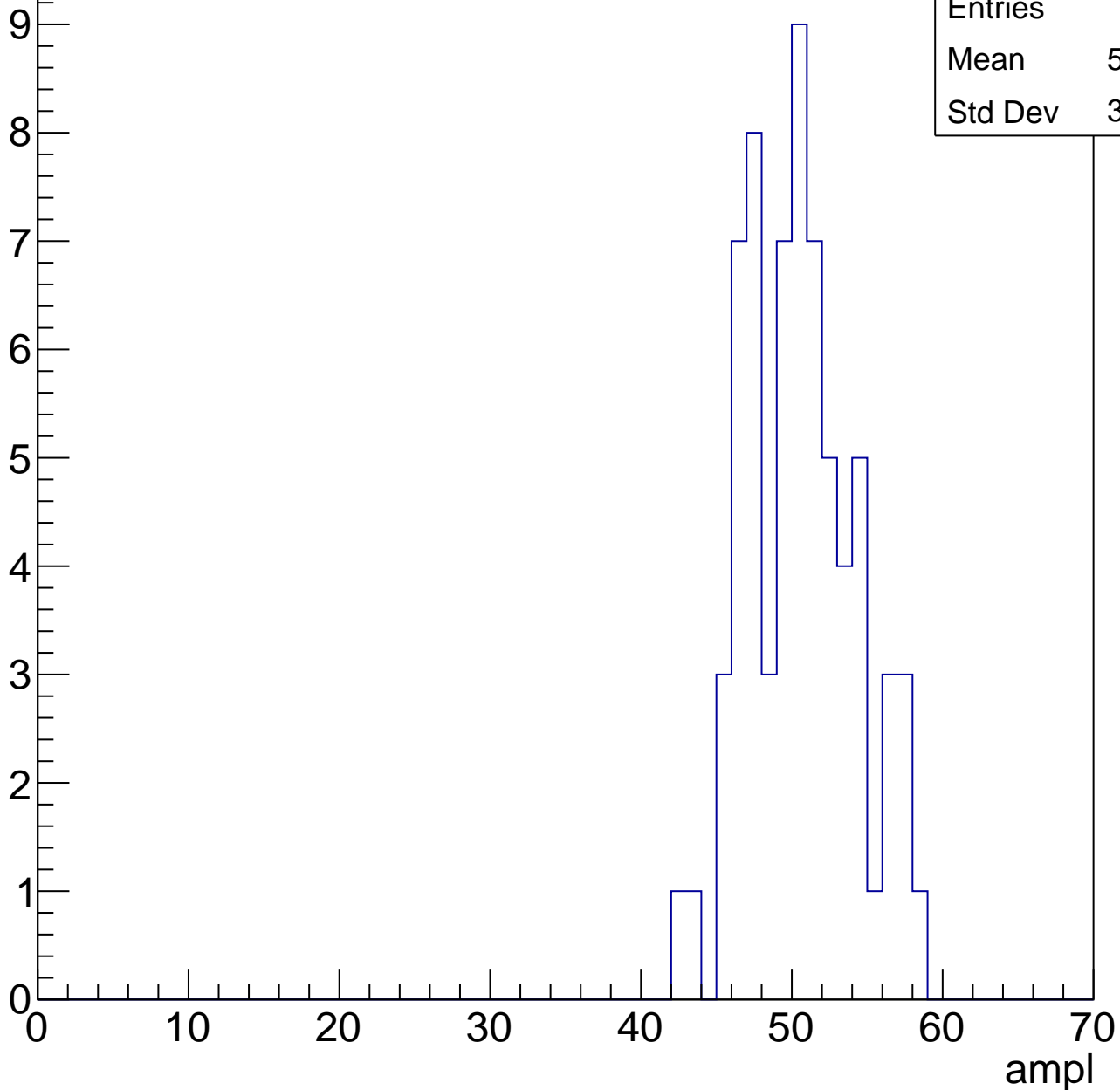


B0L002S, U18-ch35, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

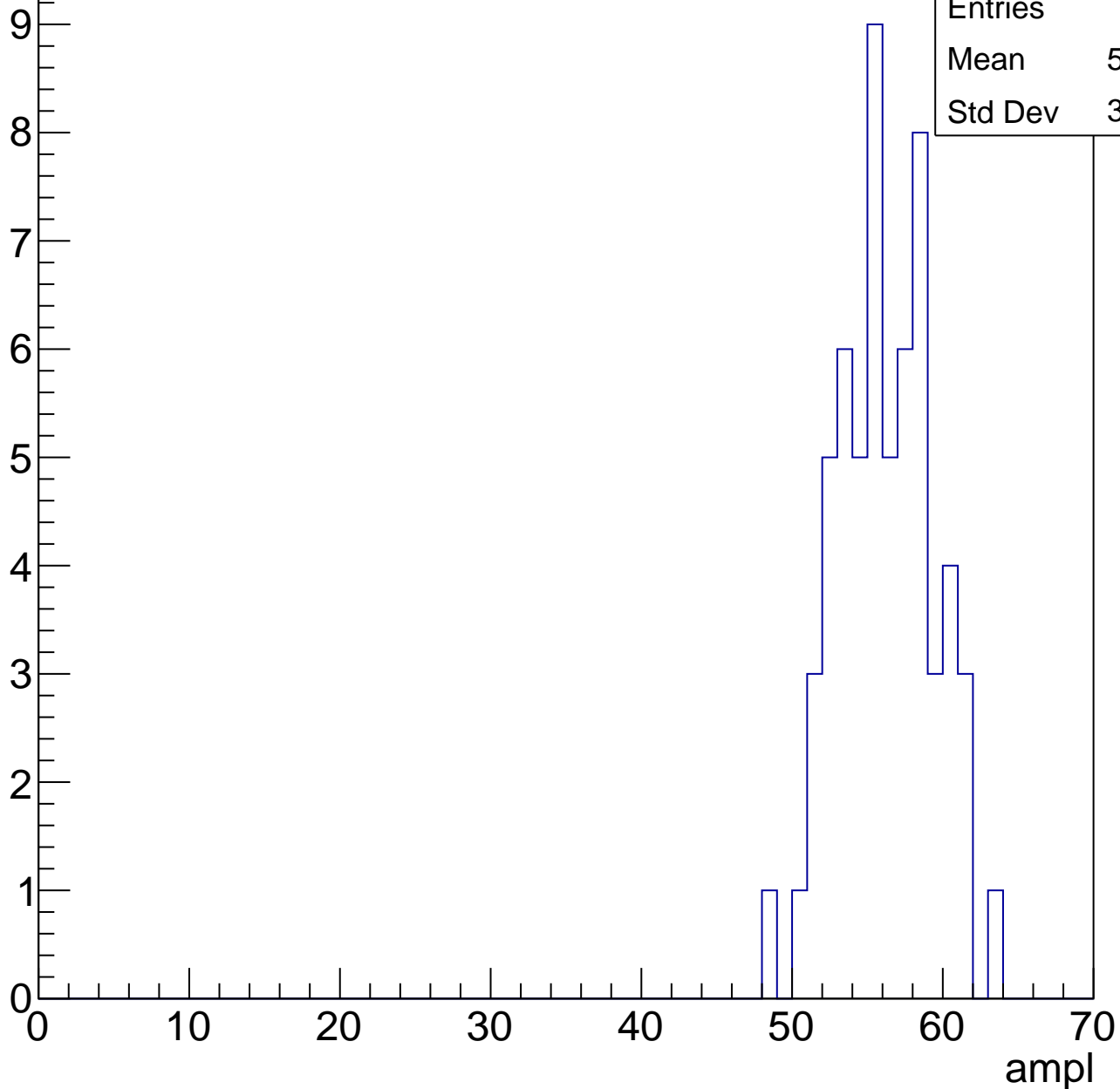
Entries	68
Mean	50.09
Std Dev	3.592



B0L002S, U18-ch35, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

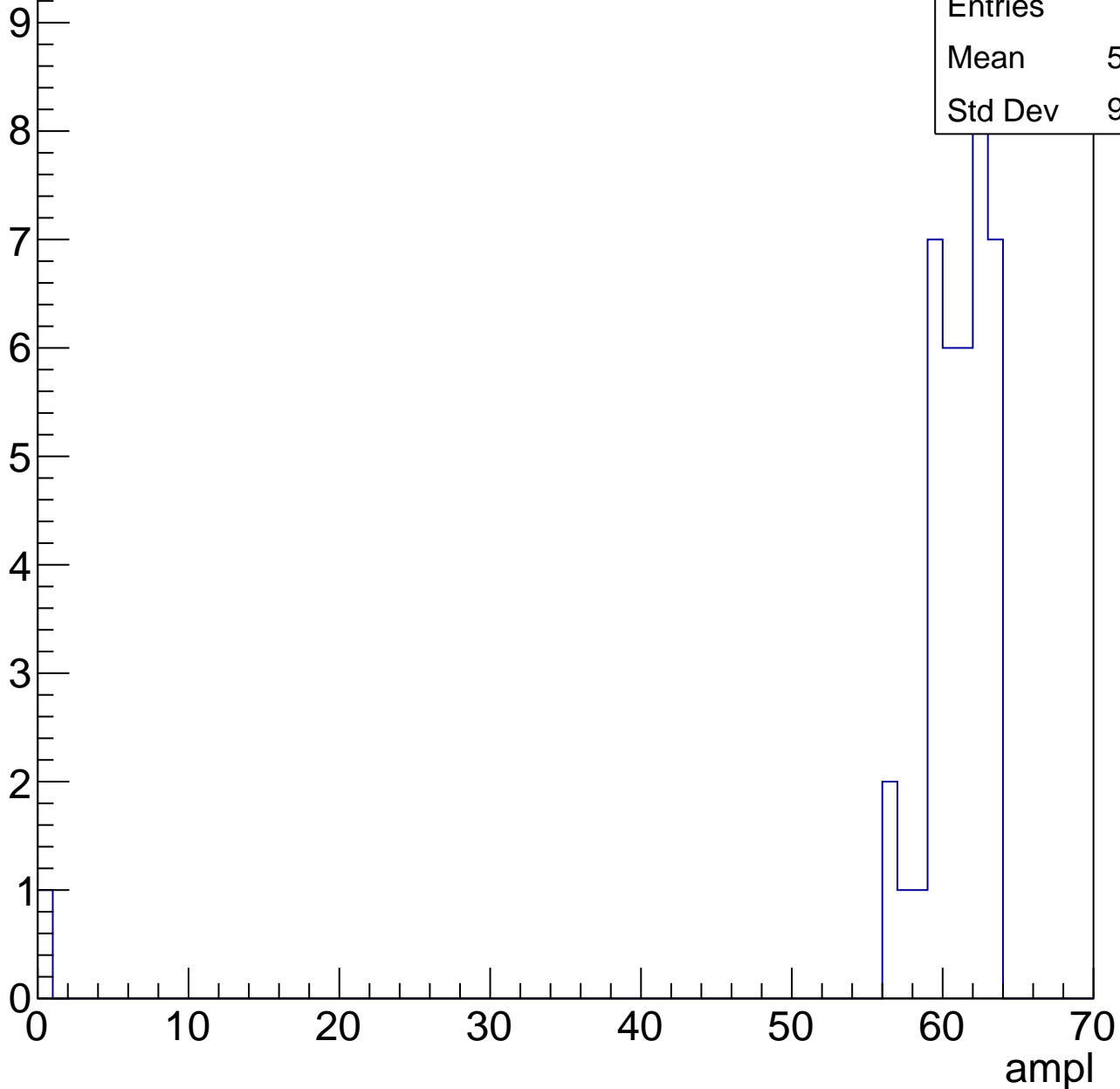


B0L002S, U18-ch35, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	59.12
Std Dev	9.652



B0L002S, U18-ch35, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62.25
Std Dev	0.8292

ampl

0 10 20 30 40 50 60 70

B0L002S, U18-ch35, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch36, adc0

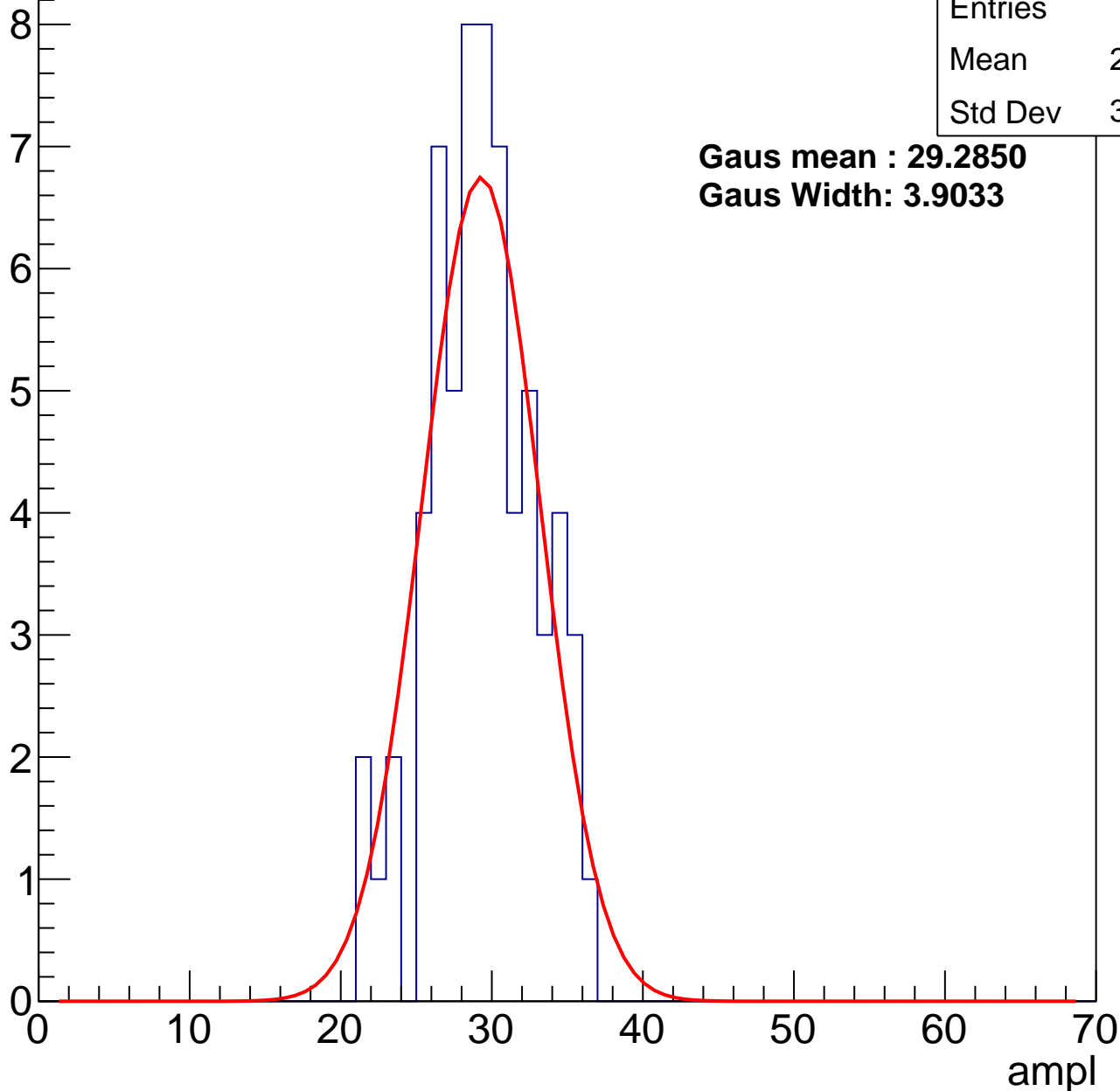
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	28.95
Std Dev	3.484

Gaus mean : 29.2850

Gaus Width: 3.9033



B0L002S, U18-ch36, adc1

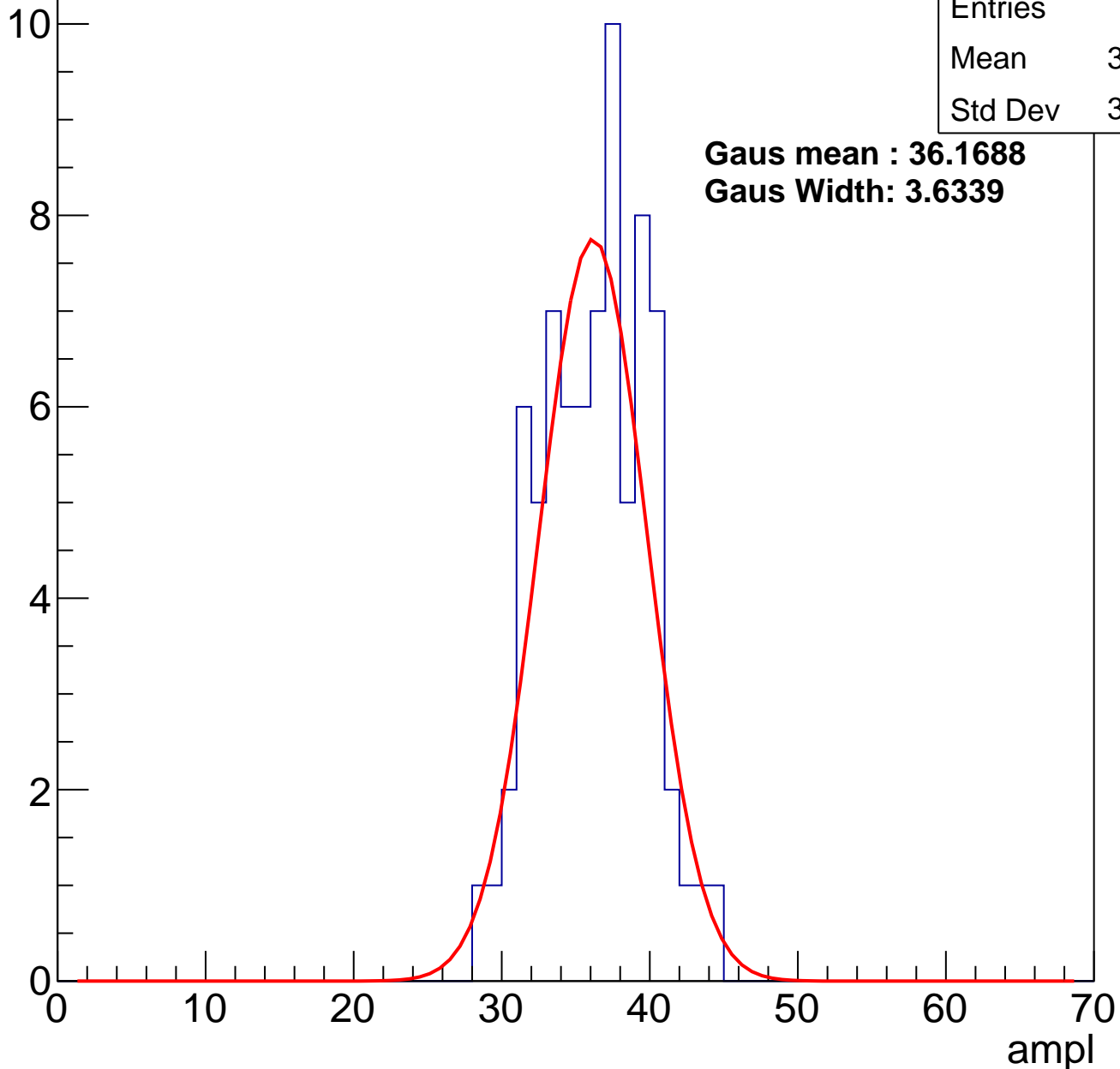
calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	35.83
Std Dev	3.477

Gaus mean : 36.1688

Gaus Width: 3.6339

Entry



B0L002S, U18-ch36, adc2

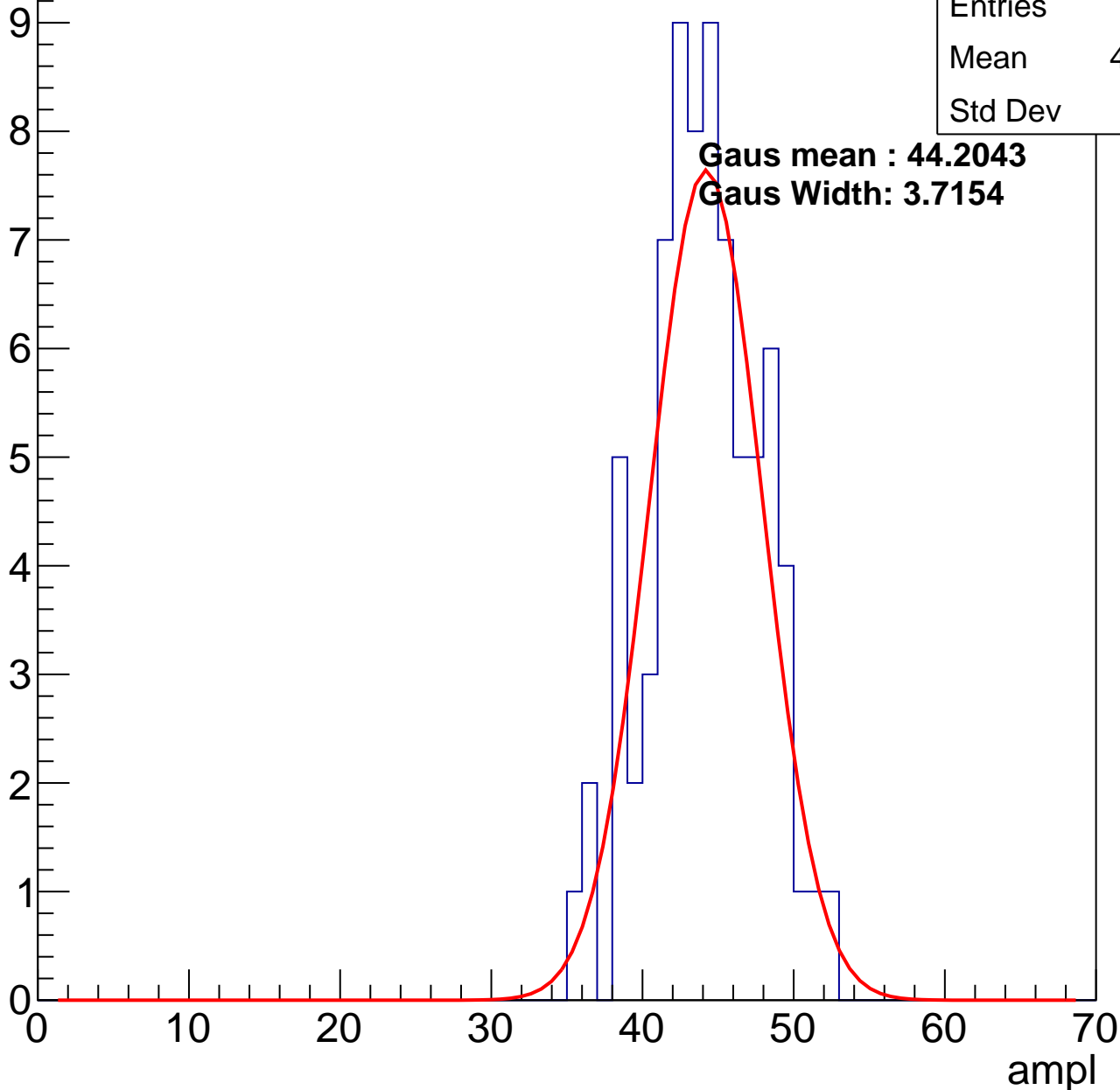
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	43.64
Std Dev	3.63

Gaus mean : 44.2043

Gaus Width: 3.7154

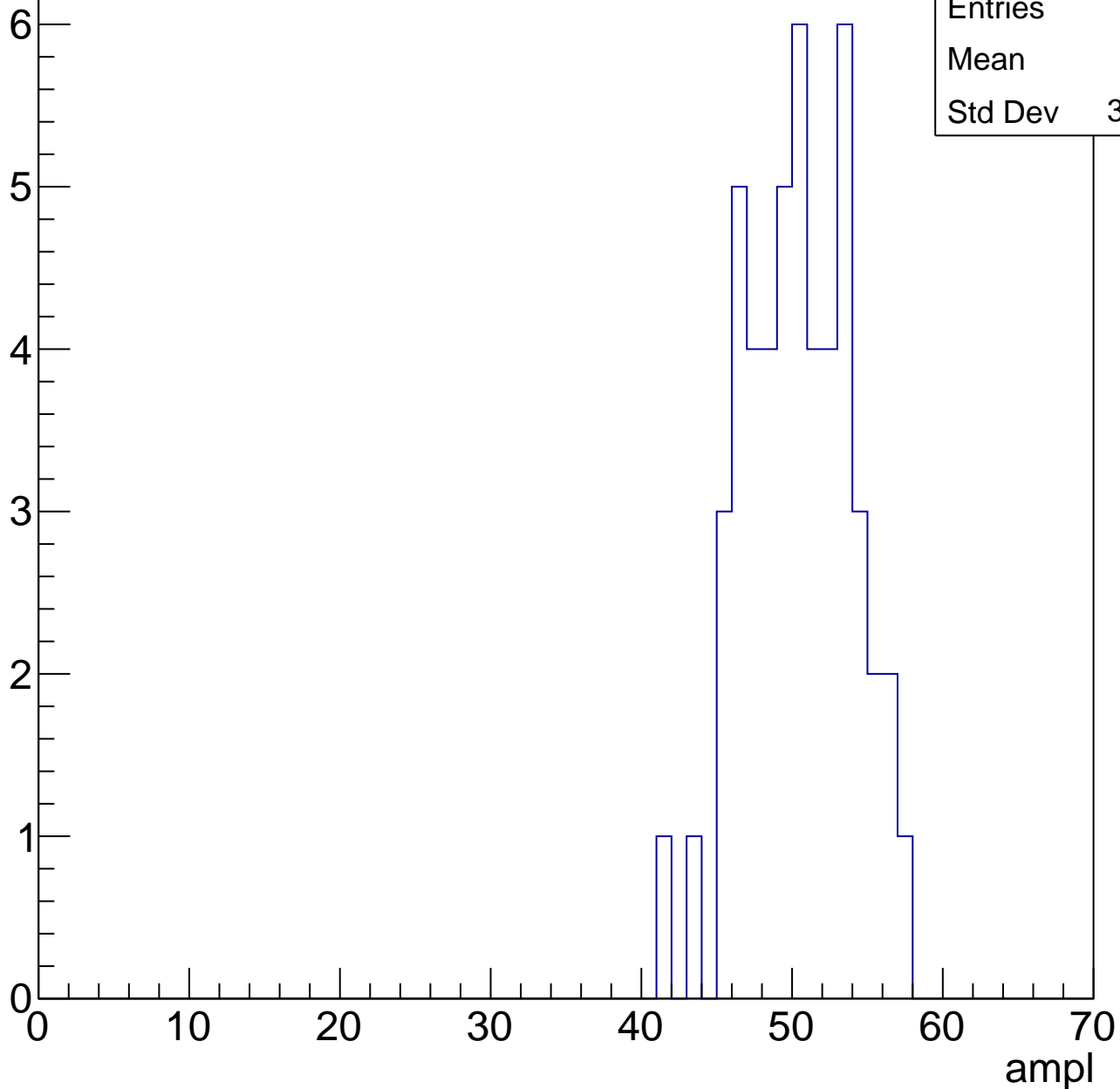


B0L002S, U18-ch36, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	49.9
Std Dev	3.527

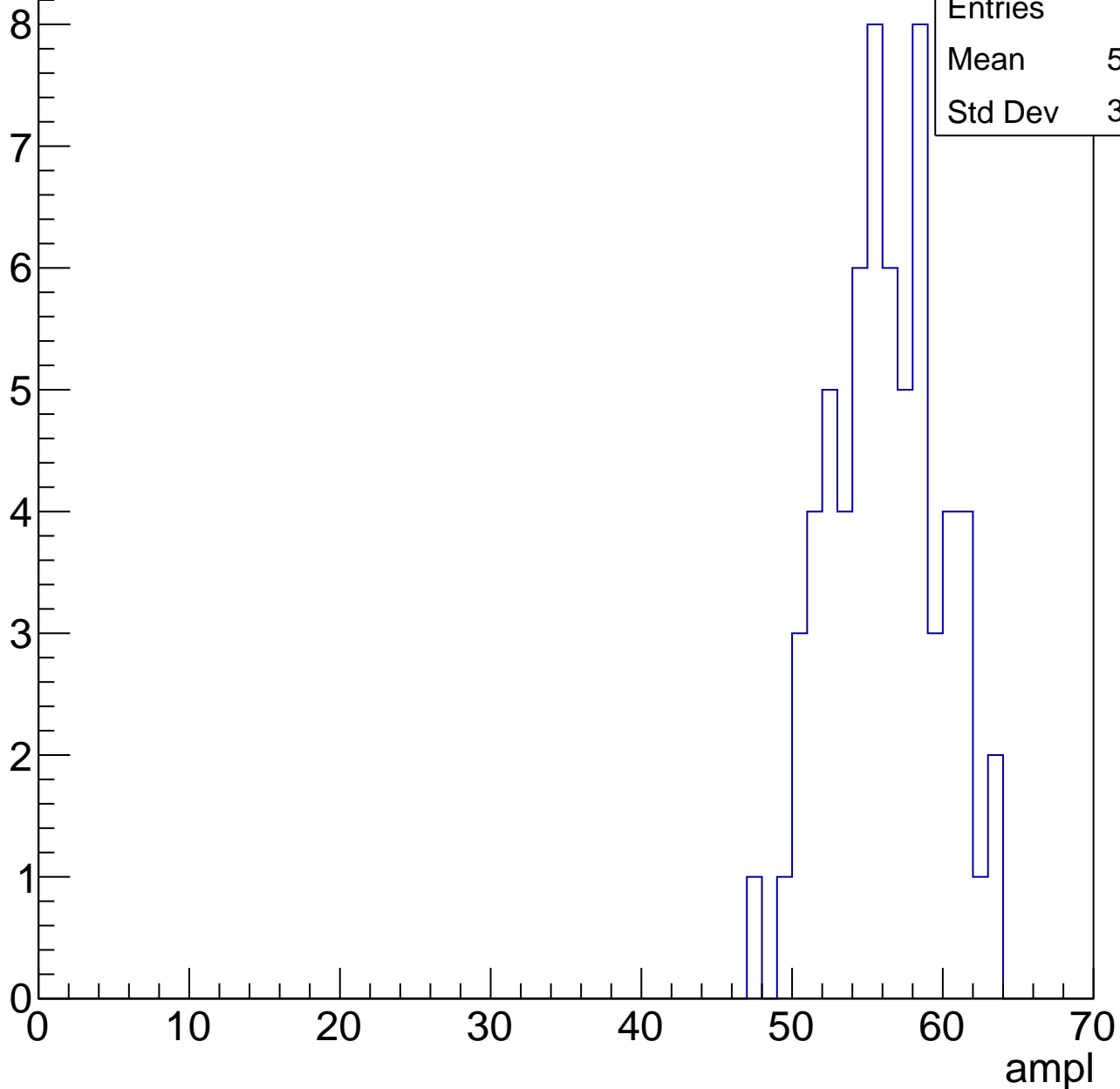


B0L002S, U18-ch36, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

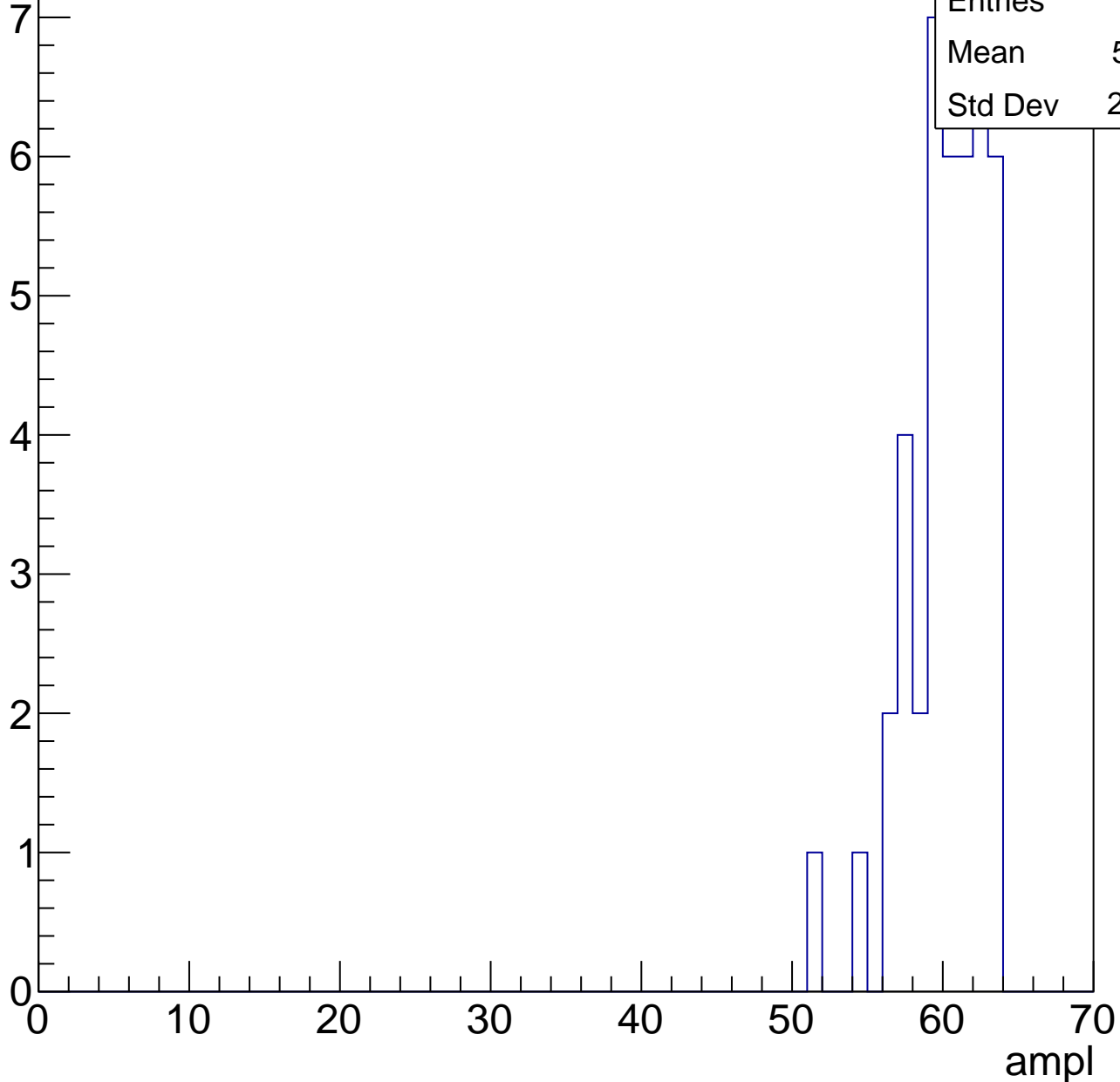
Entries	65
Mean	55.69
Std Dev	3.599



B0L002S, U18-ch36, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch36, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch36, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch37, adc0

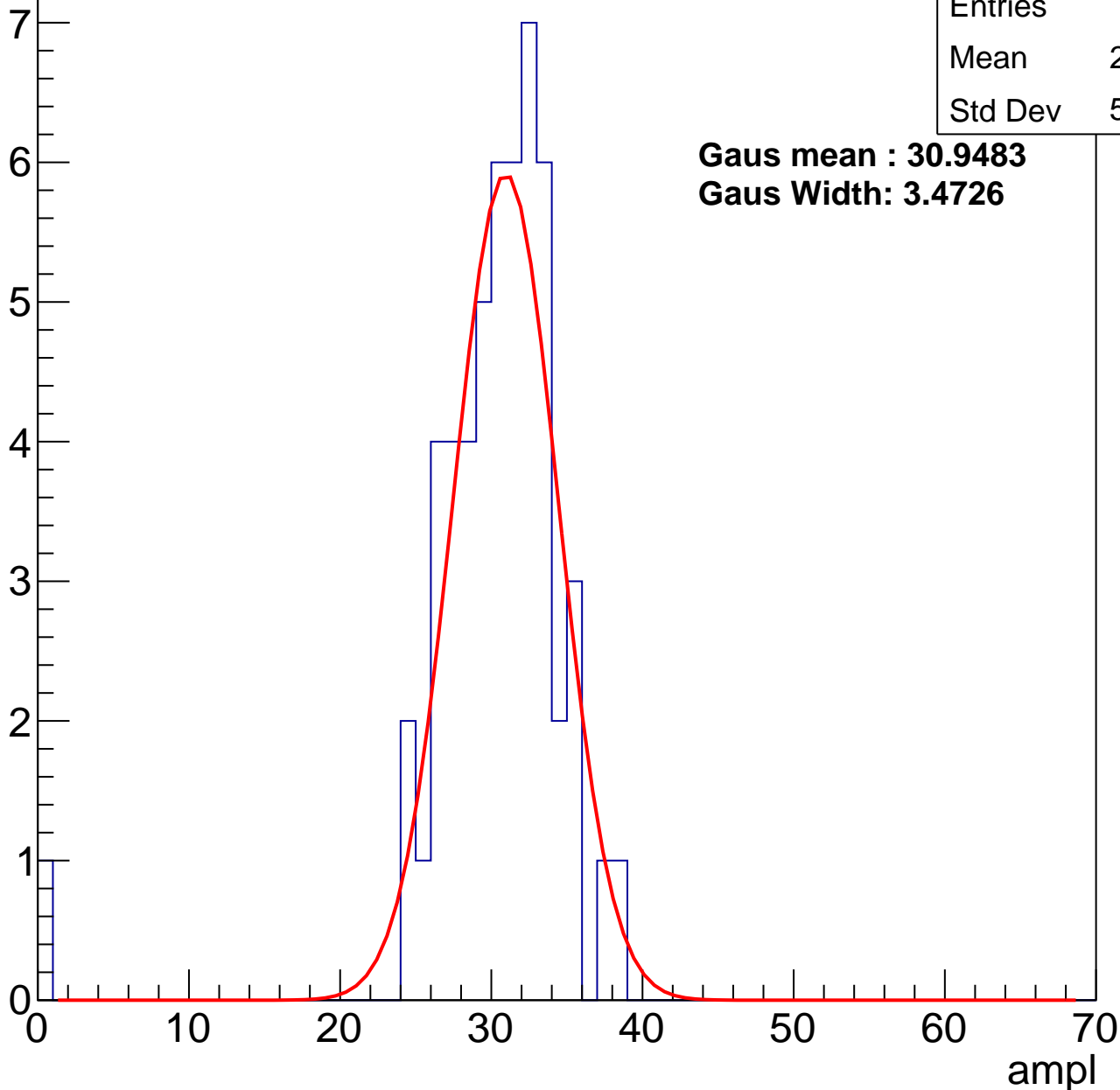
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	29.77
Std Dev	5.182

Gaus mean : 30.9483

Gaus Width: 3.4726



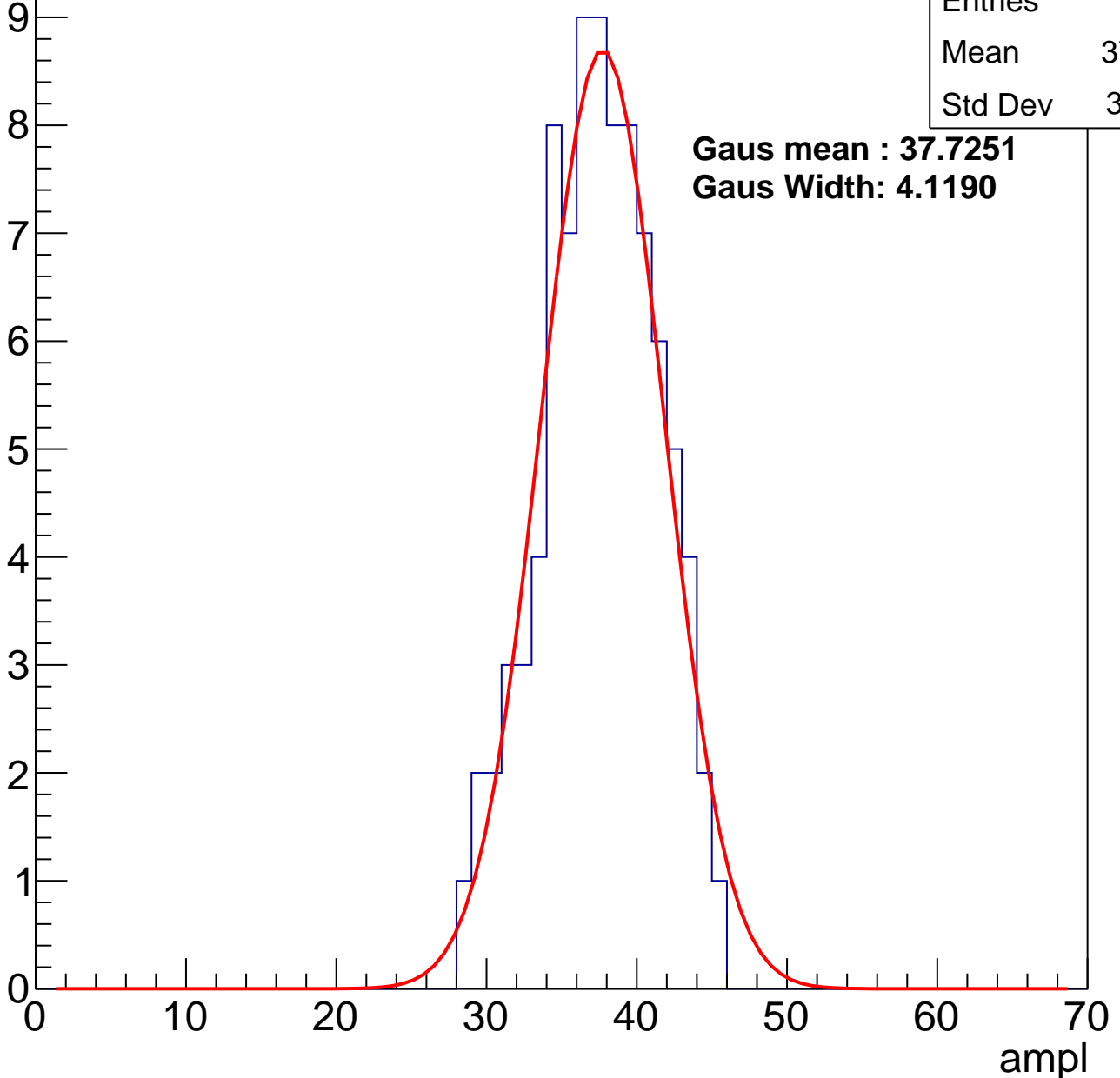
B0L002S, U18-ch37, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	89
Mean	37.06
Std Dev	3.811

Gaus mean : 37.7251
Gaus Width: 4.1190



B0L002S, U18-ch37, adc2

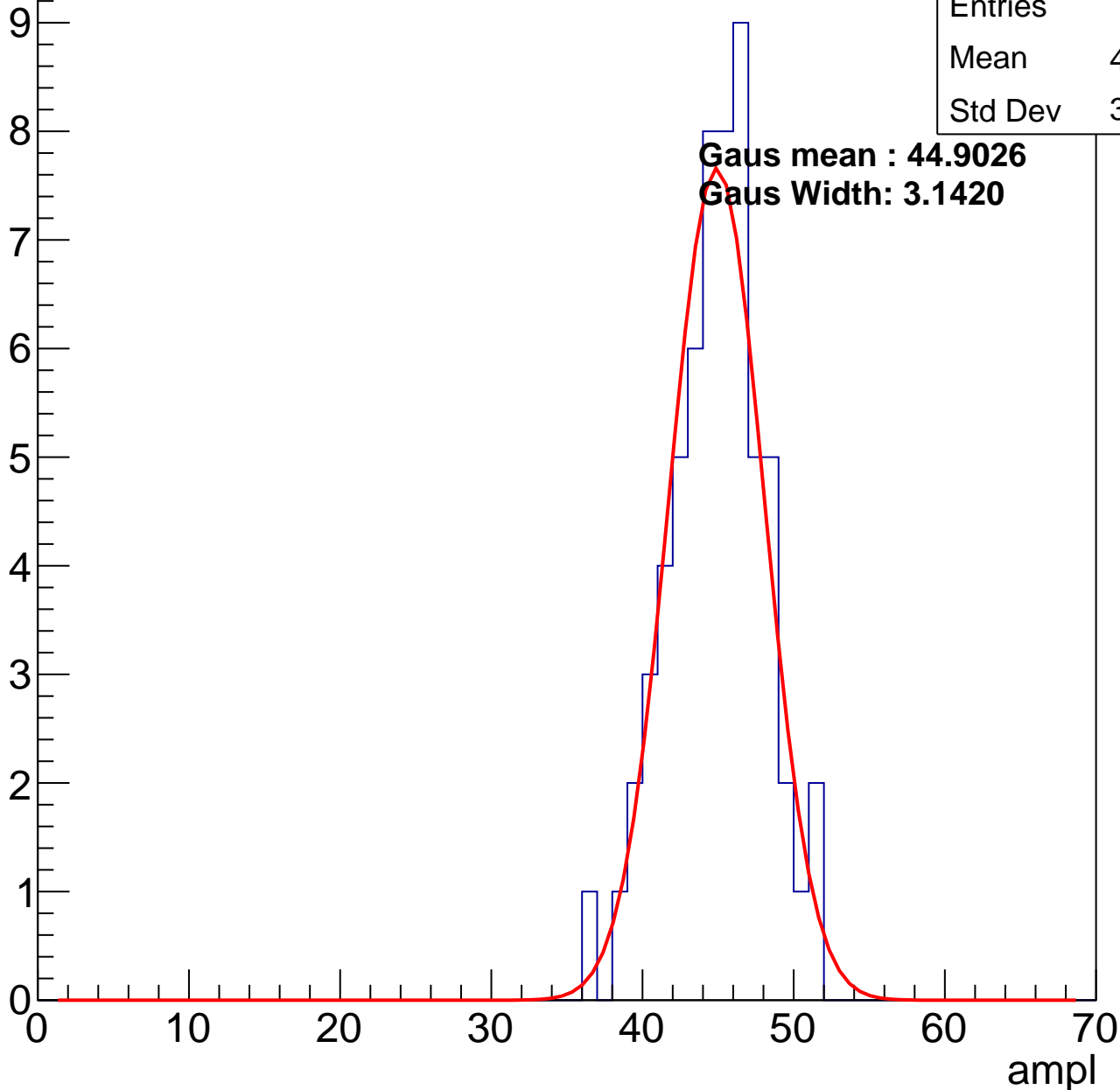
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	44.44
Std Dev	3.114

Gaus mean : 44.9026

Gaus Width: 3.1420

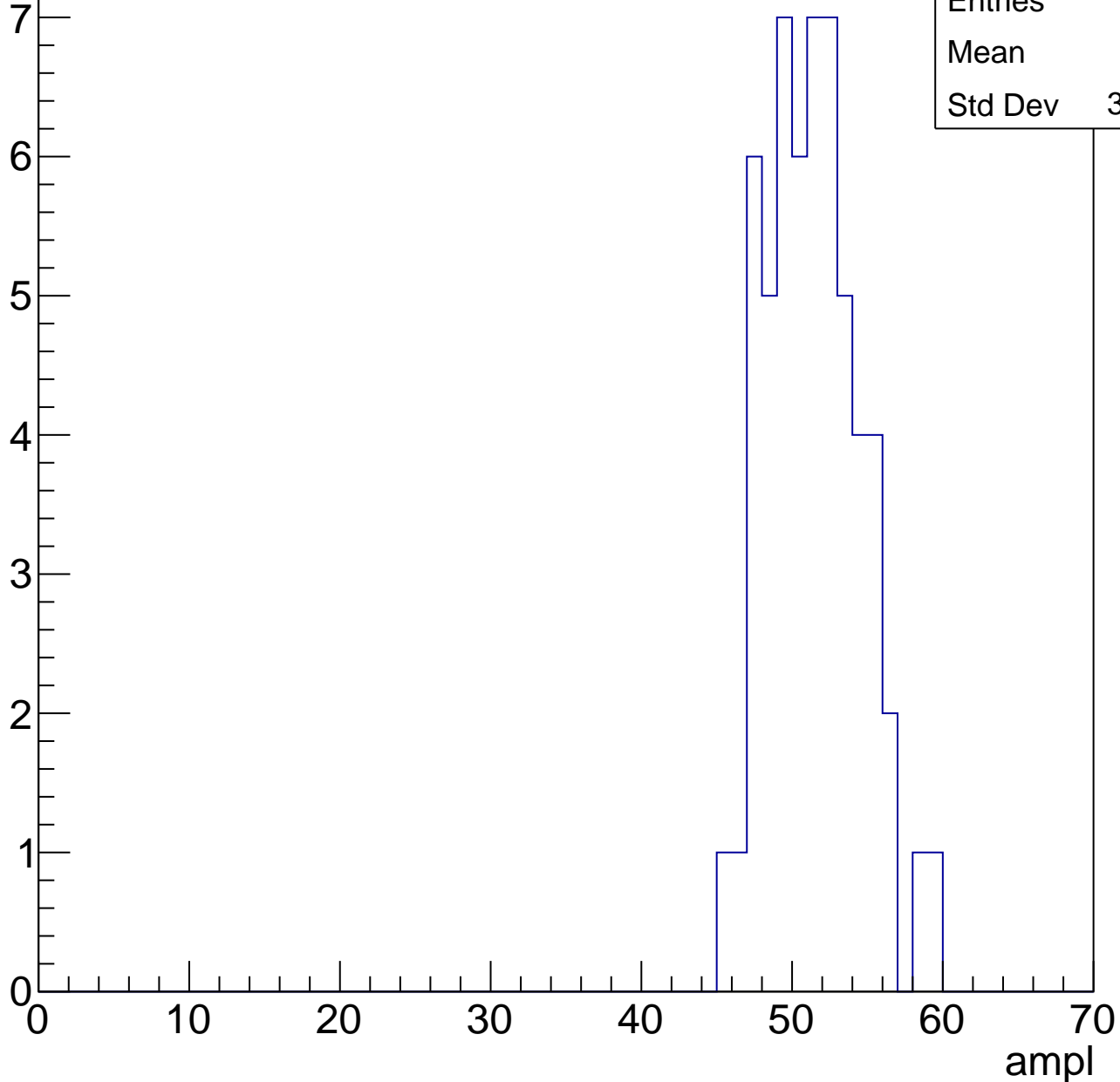


B0L002S, U18-ch37, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	51
Std Dev	3.038



B0L002S, U18-ch37, adc4

calib_packv5_042523_0143.root, FC#8, port C1

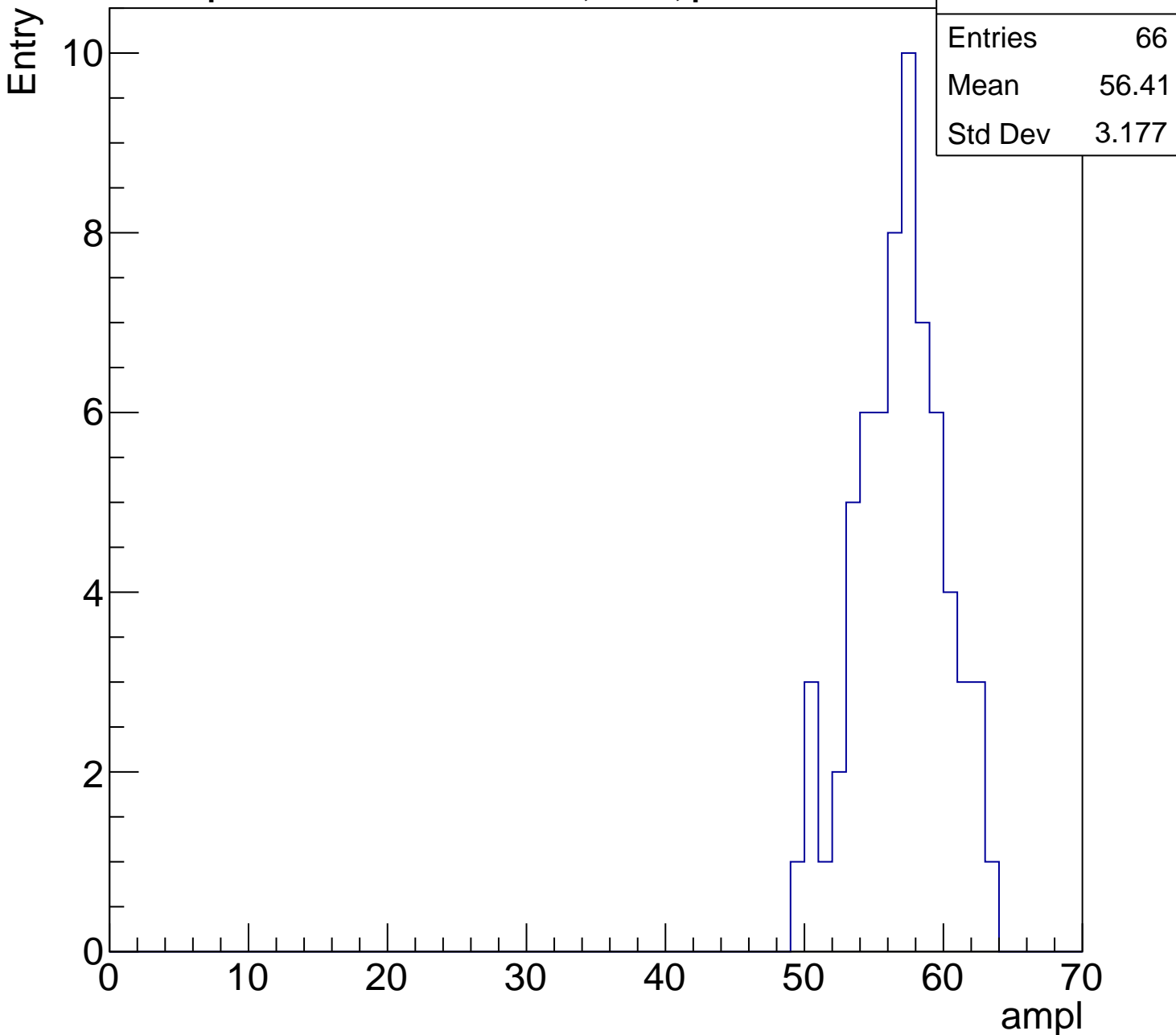
Entries	66
Mean	56.41
Std Dev	3.177

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

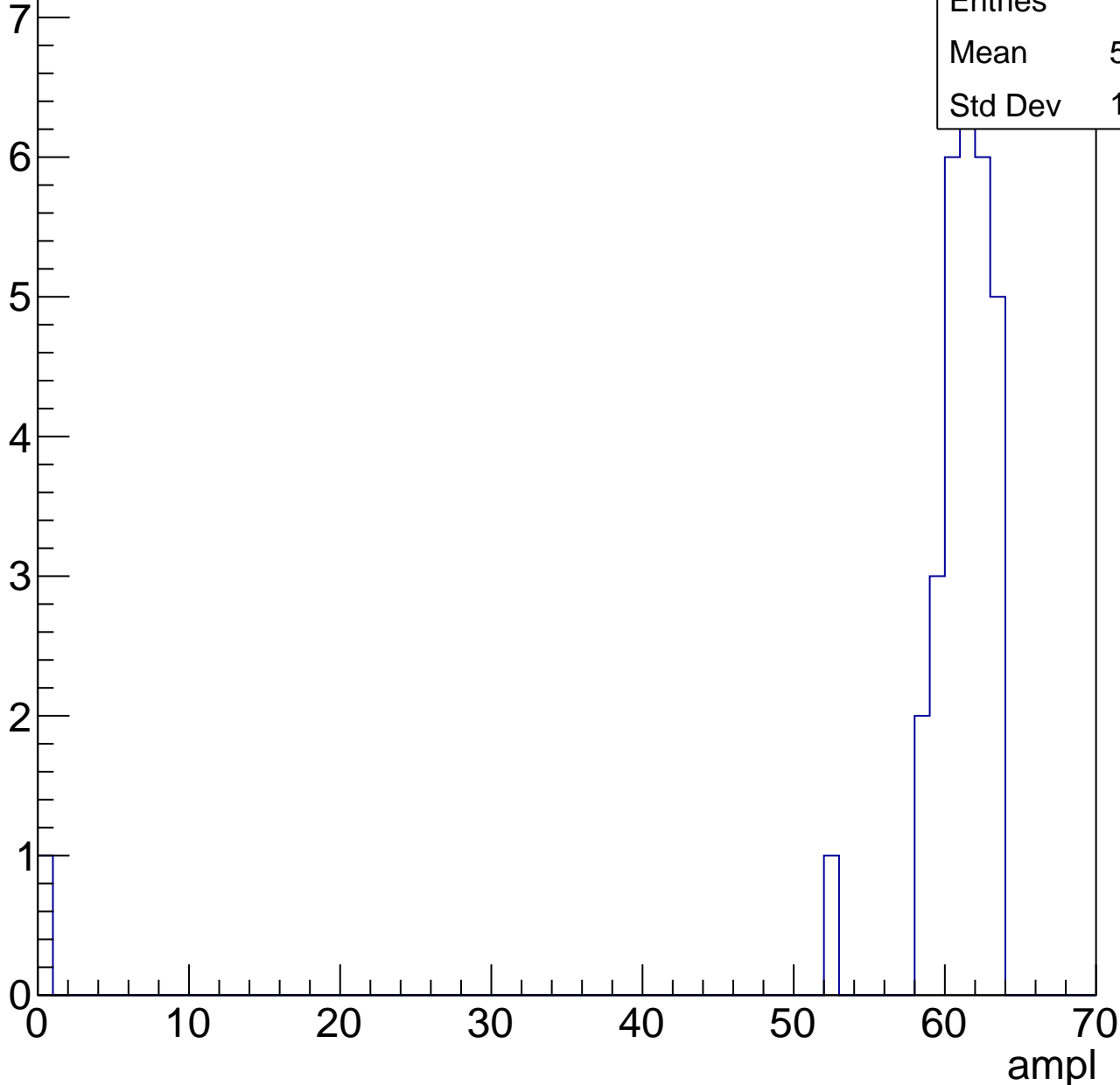


B0L002S, U18-ch37, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

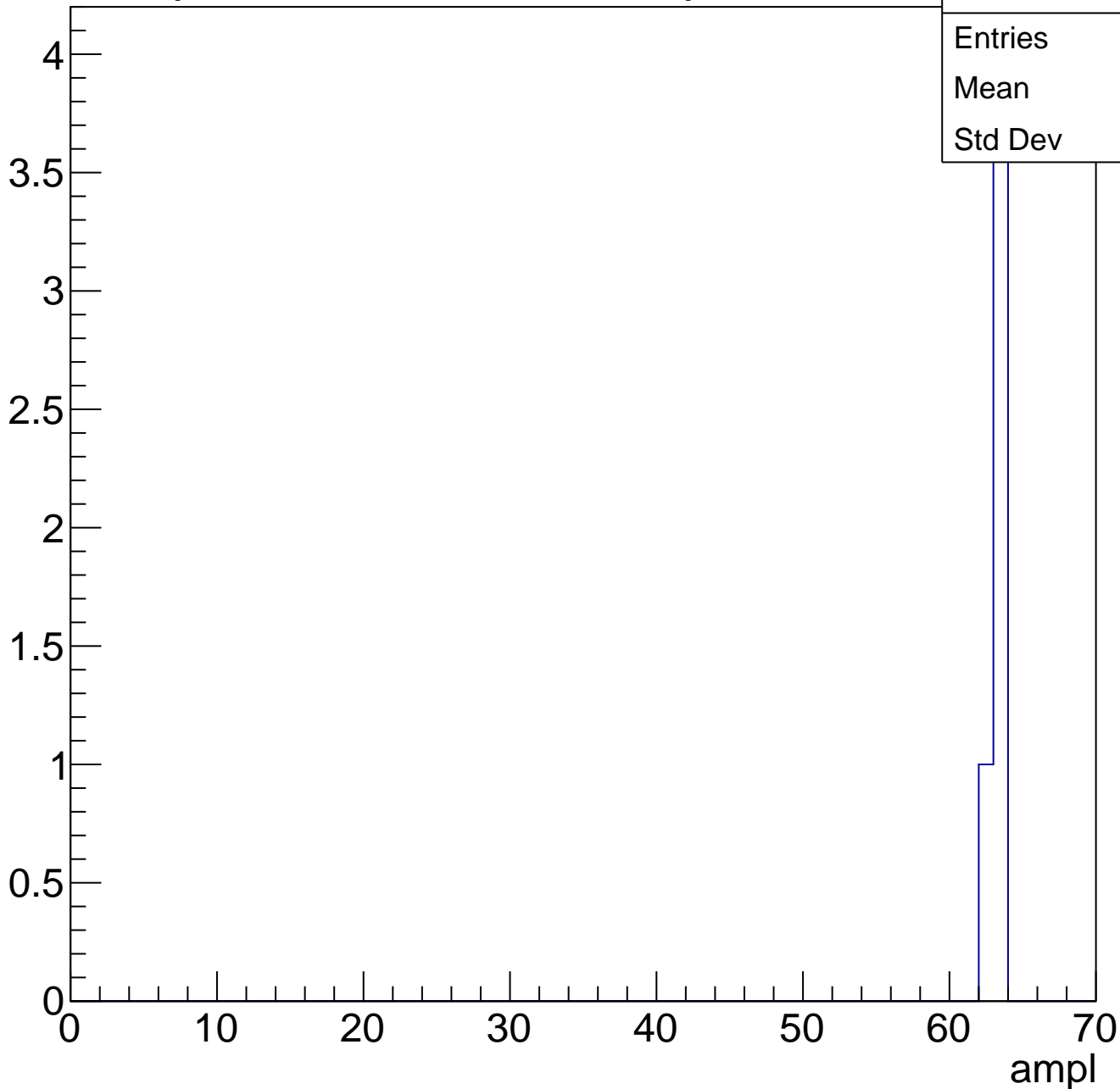
Entries	31
Mean	58.68
Std Dev	10.92



B0L002S, U18-ch37, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch37, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6.333
Std Dev	8.957

B0L002S, U18-ch38, adc0

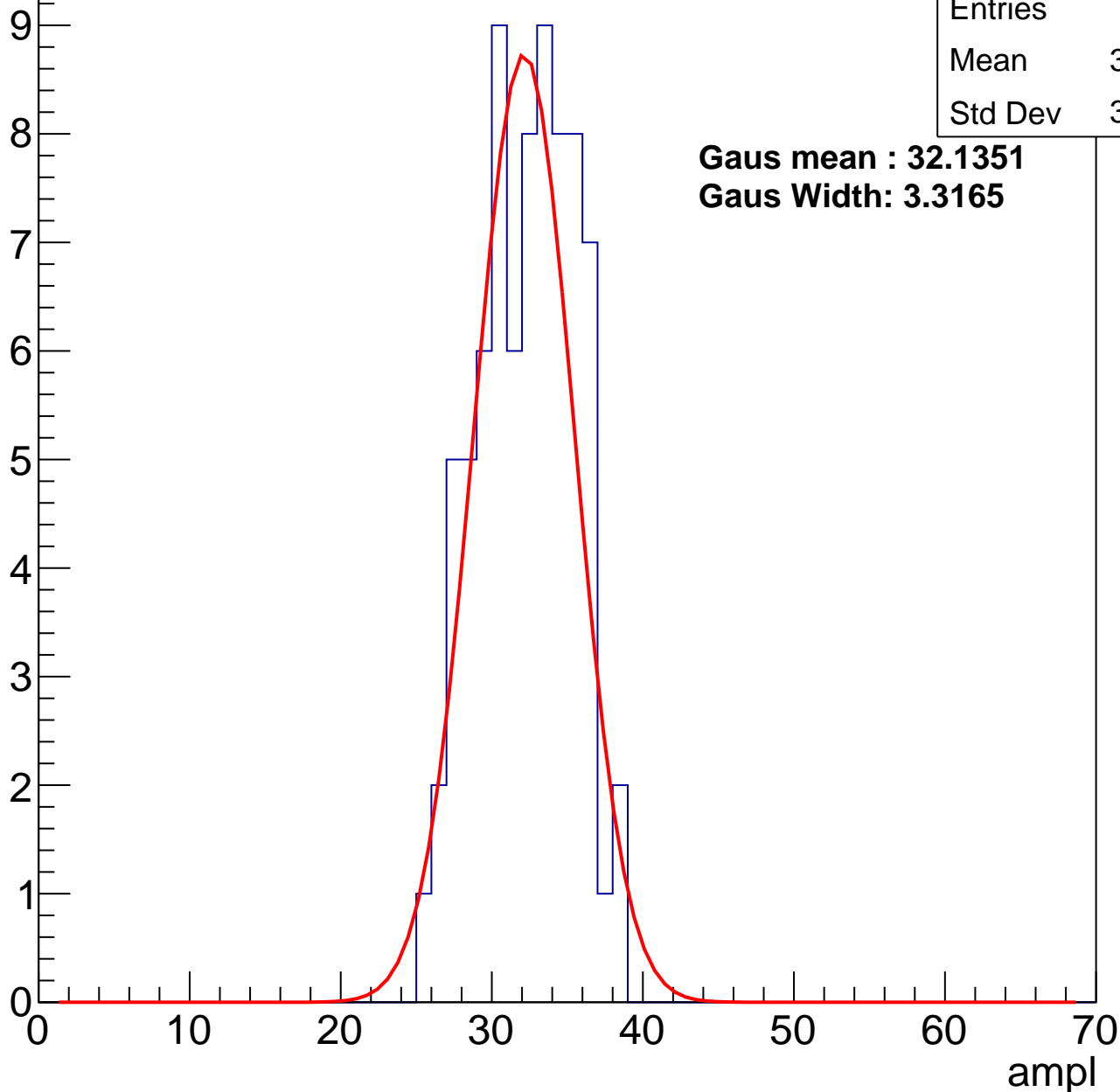
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	31.84
Std Dev	3.105

Gaus mean : 32.1351

Gaus Width: 3.3165



B0L002S, U18-ch38, adc1

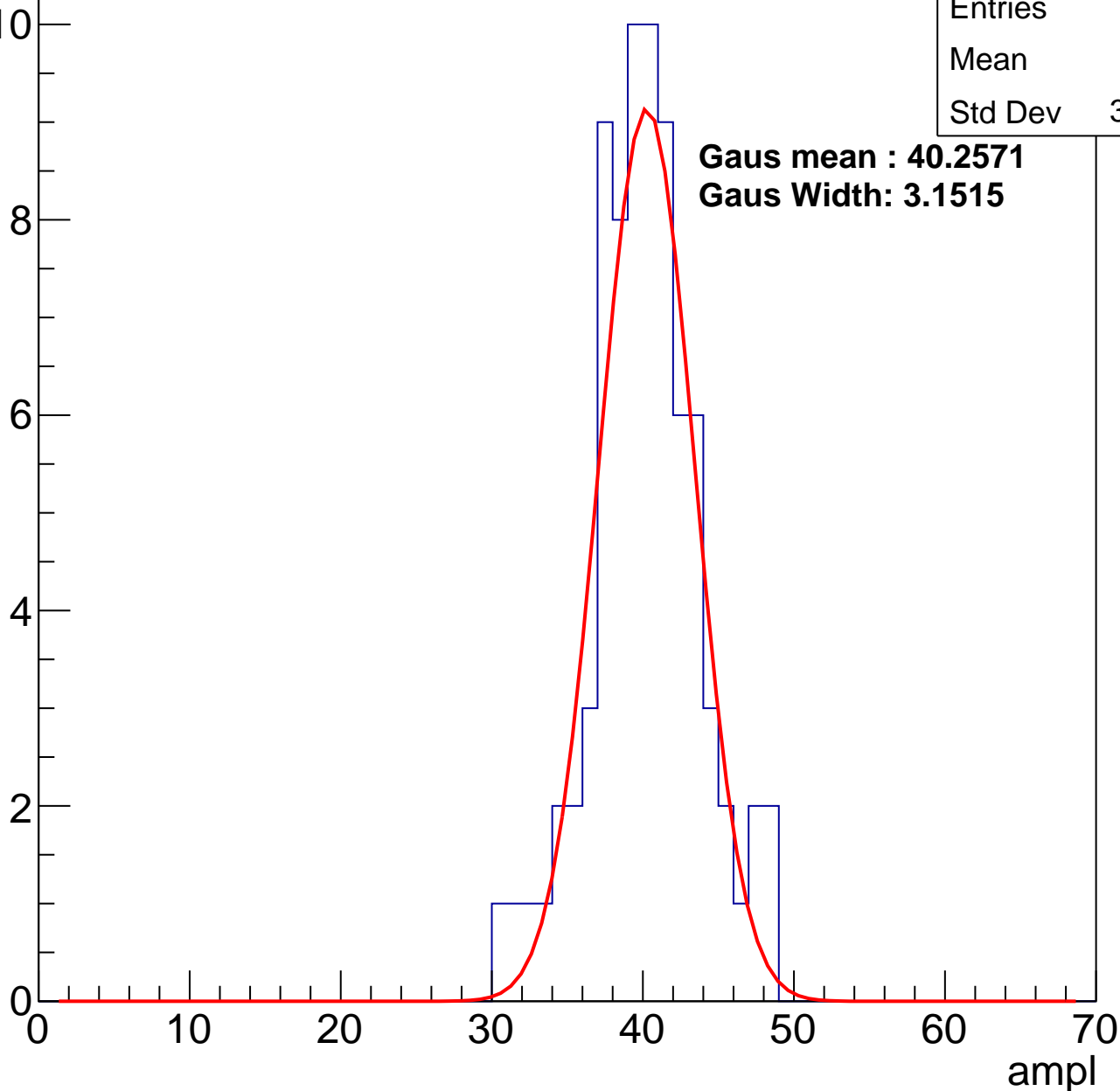
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	39.7
Std Dev	3.605

Gaus mean : 40.2571

Gaus Width: 3.1515



B0L002S, U18-ch38, adc2

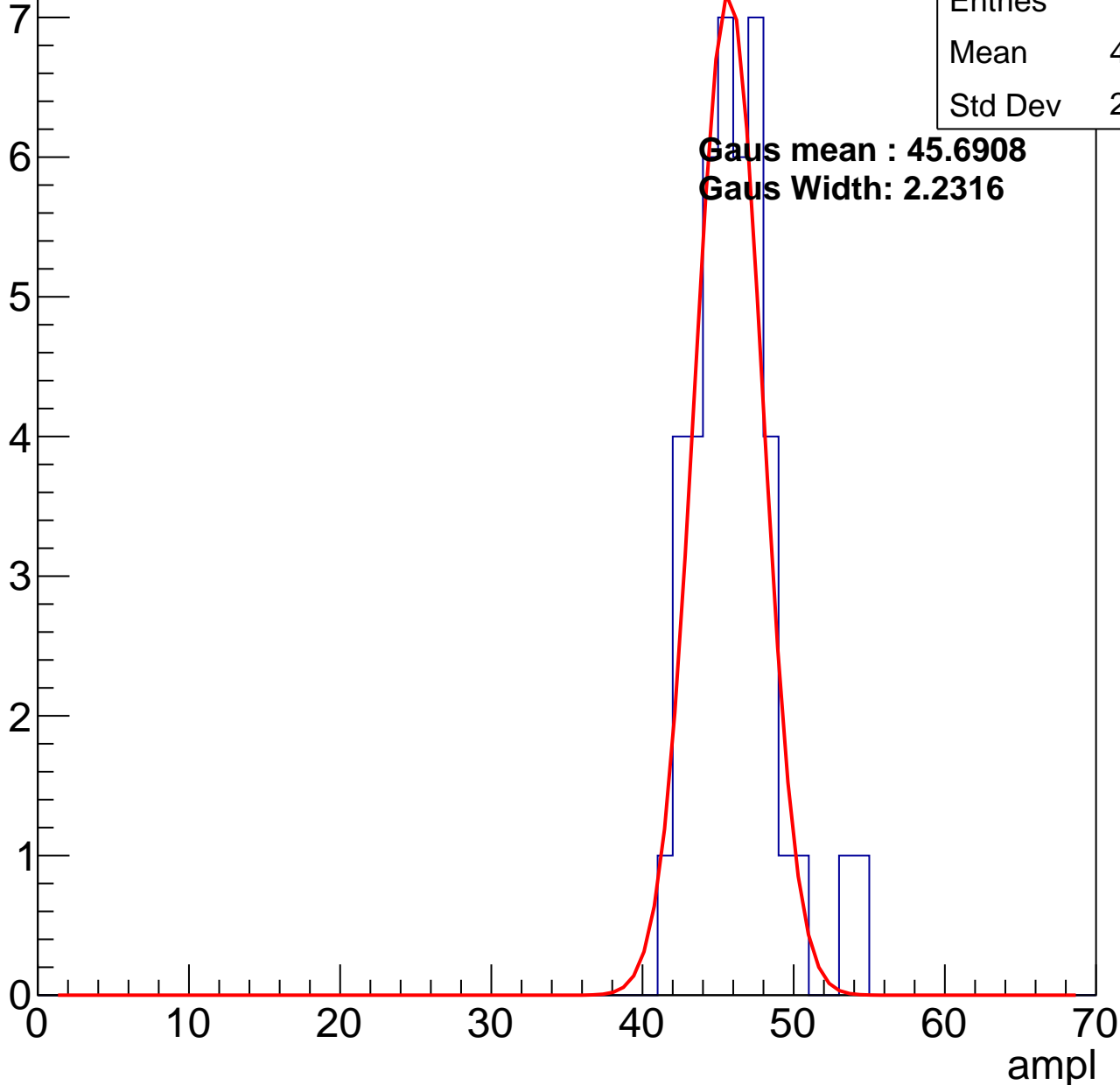
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	45.65
Std Dev	2.693

Gaus mean : 45.6908

Gaus Width: 2.2316



B0L002S, U18-ch38, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	67
Mean	51.28
Std Dev	3.08

Entry

10

8

6

4

2

0

0

10

20

30

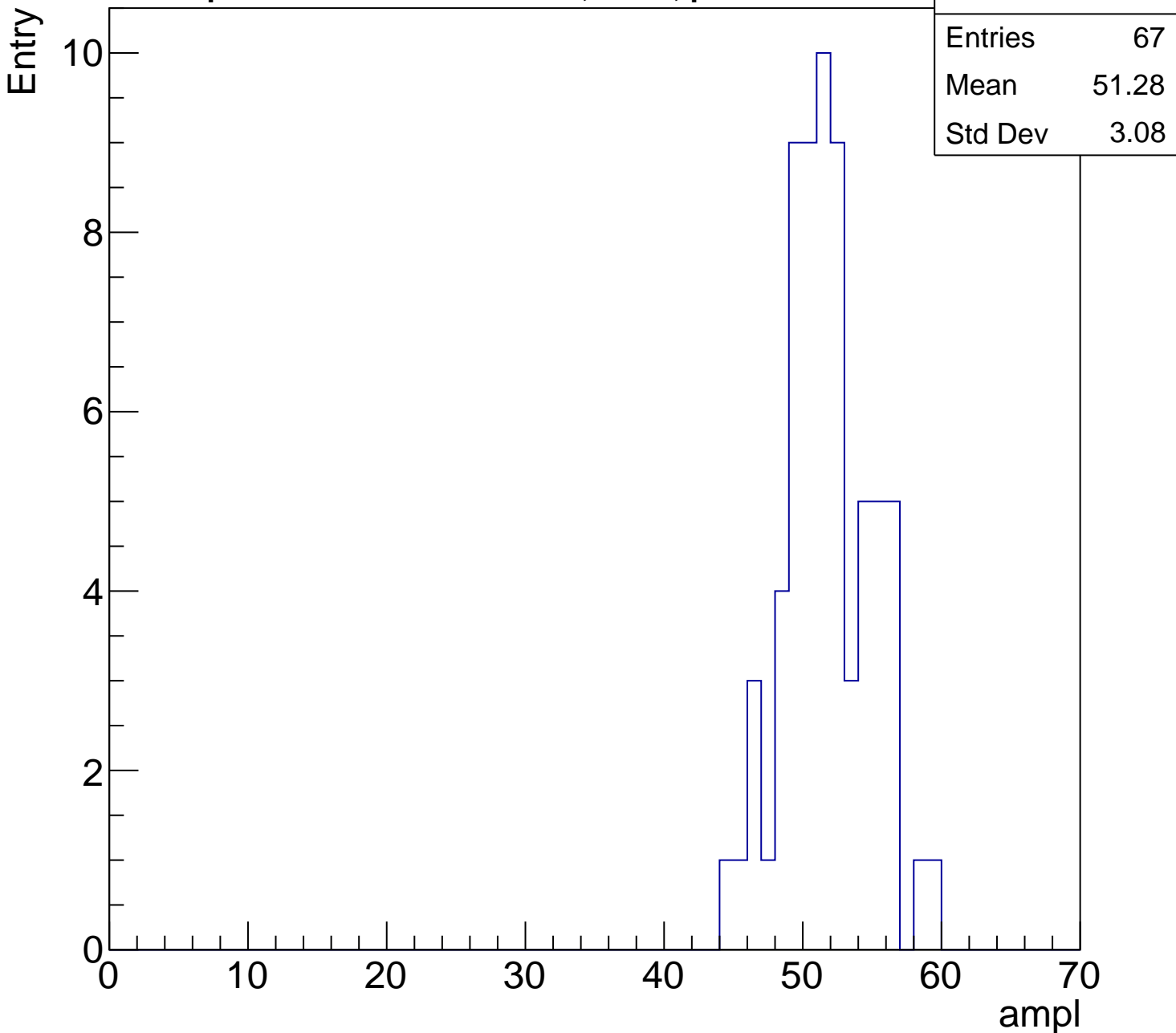
40

50

60

70

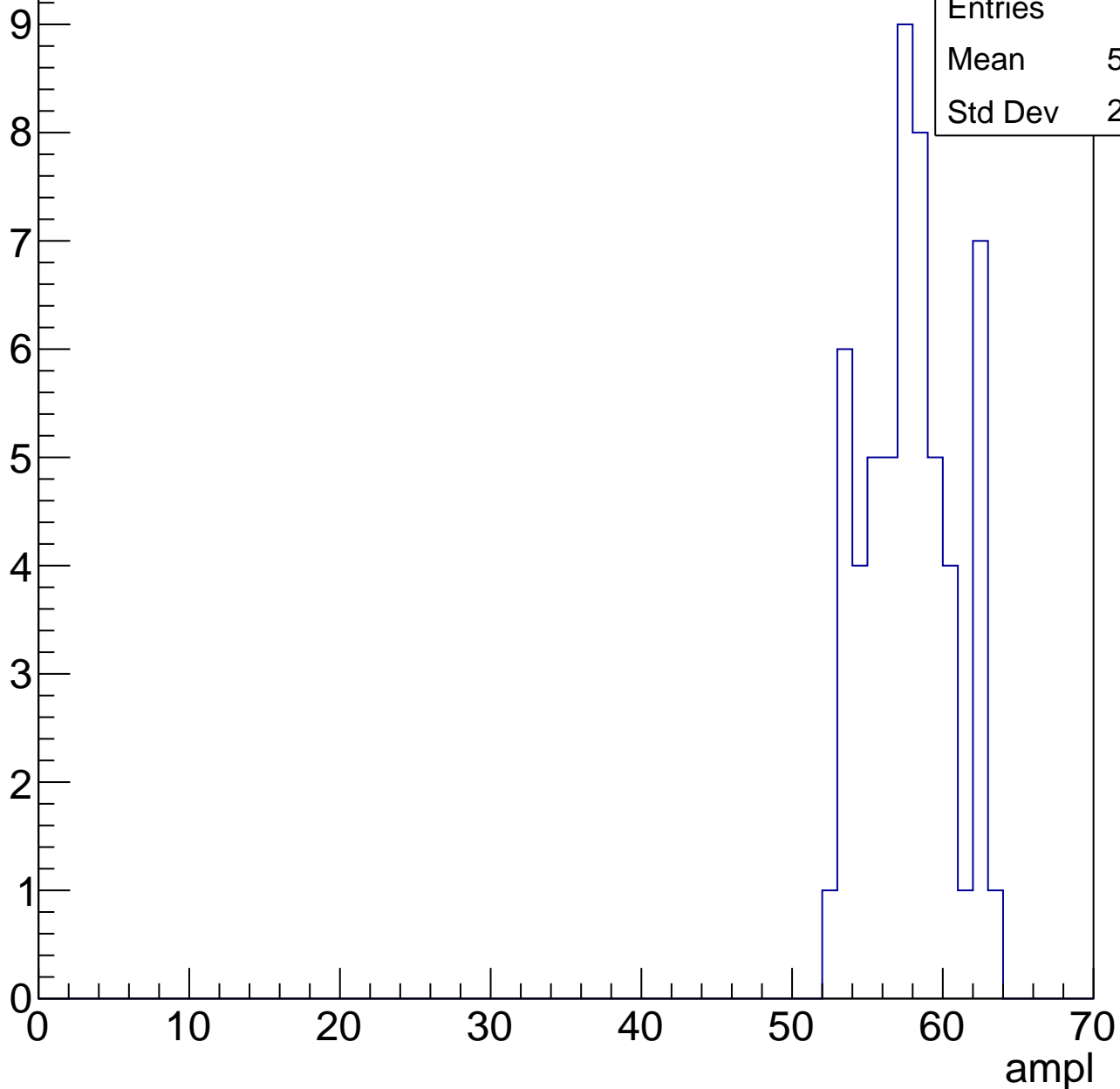
ampl



B0L002S, U18-ch38, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	56
Mean	57.34
Std Dev	2.886

B0L002S, U18-ch38, adc5

calib_packv5_042523_0143.root, FC#8, port C1

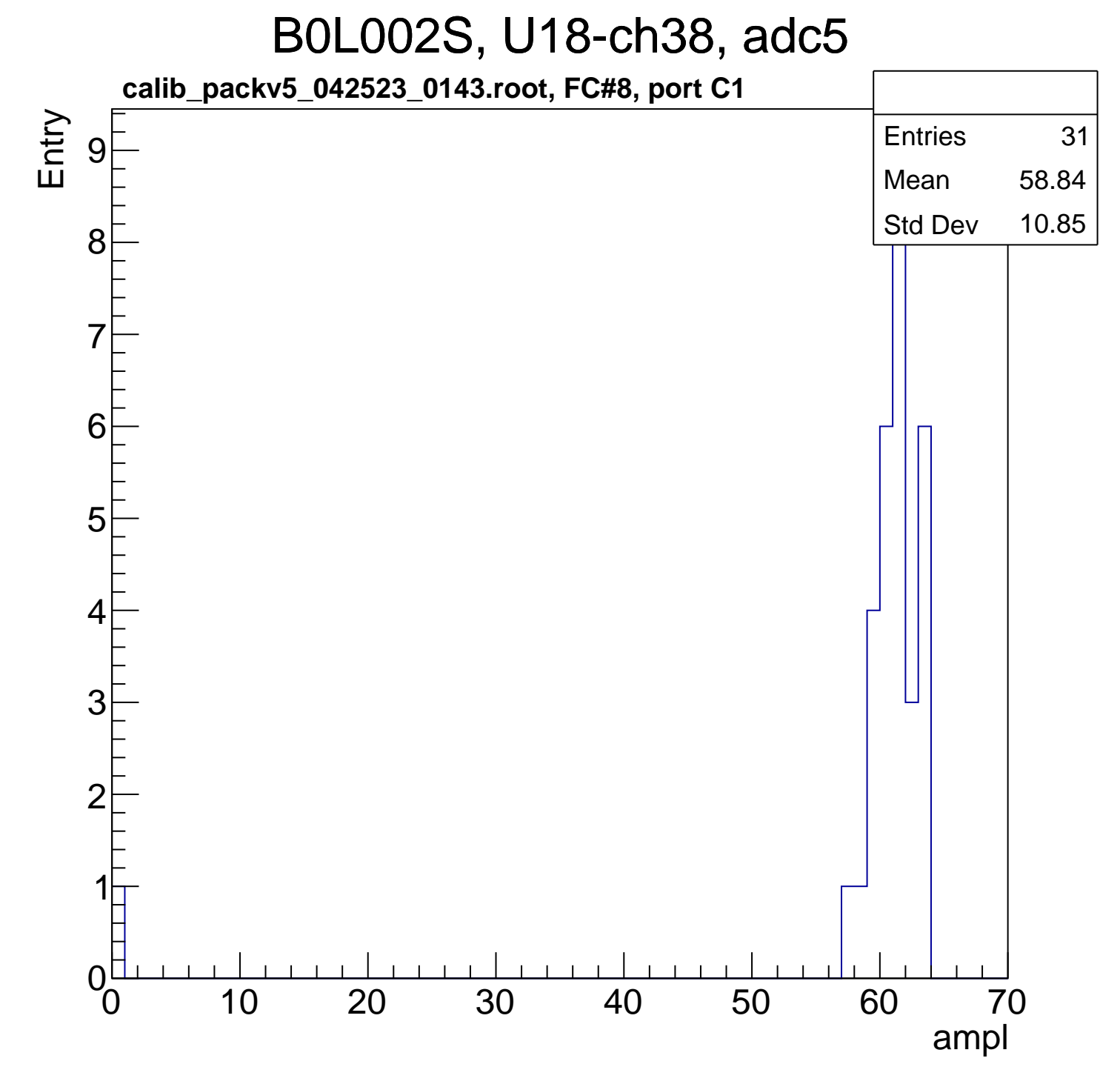
Entry

9
8
7
6
5
4
3
2
1
0

Entries	31
Mean	58.84
Std Dev	10.85

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch38, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch38, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch39, adc0

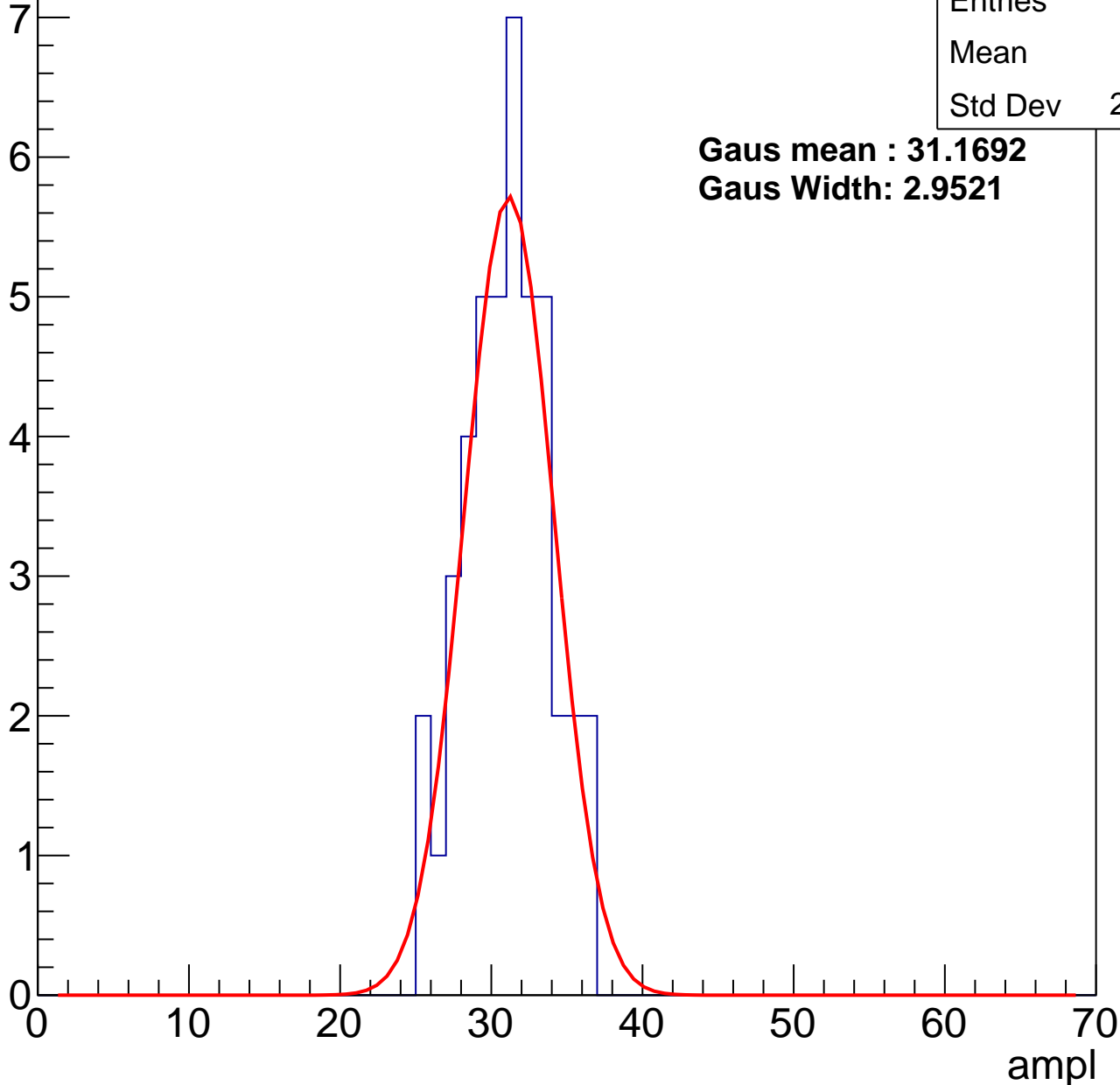
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	30.6
Std Dev	2.746

Gaus mean : 31.1692

Gaus Width: 2.9521



B0L002S, U18-ch39, adc1

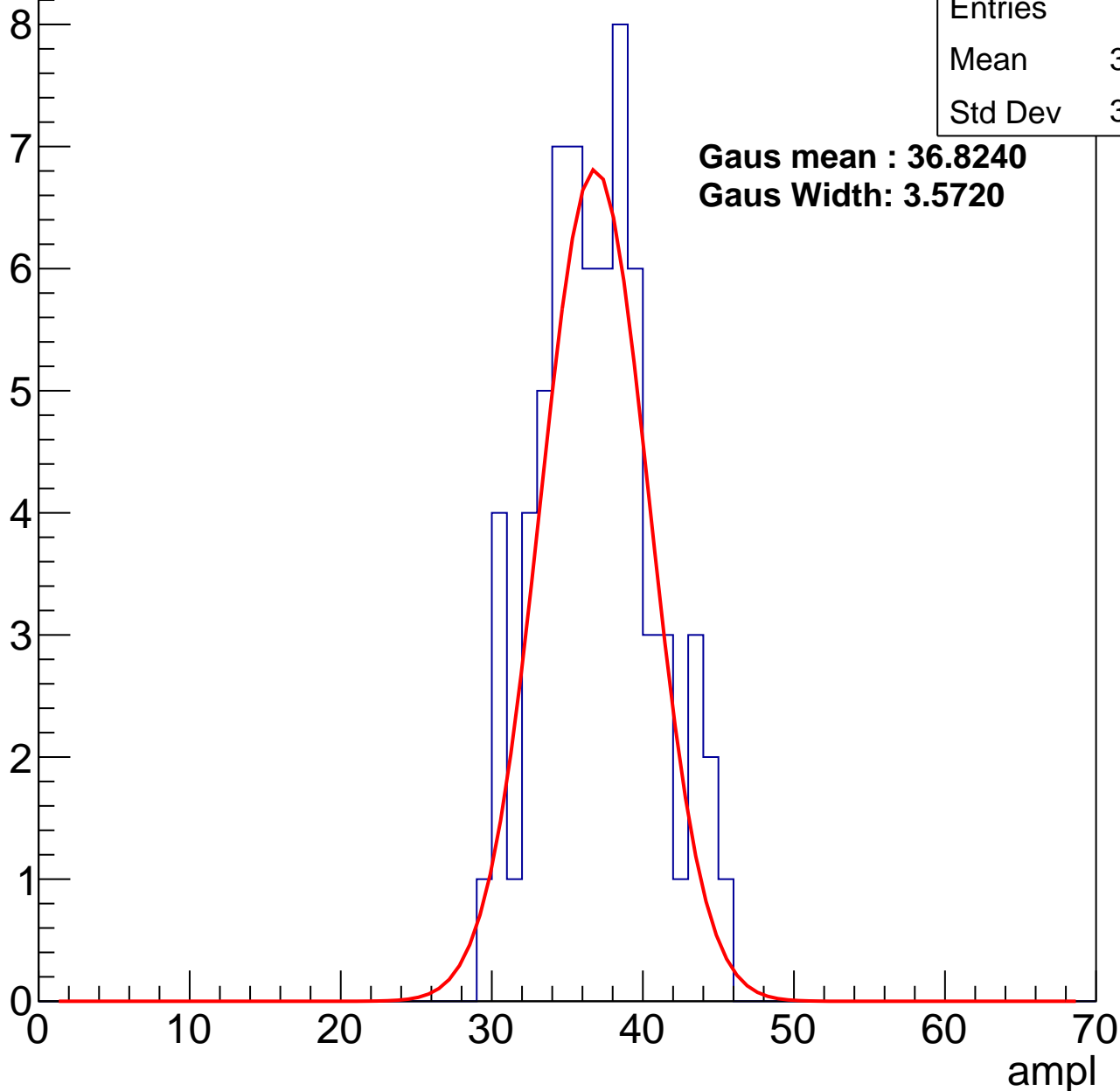
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.46
Std Dev	3.747

Gaus mean : 36.8240

Gaus Width: 3.5720



B0L002S, U18-ch39, adc2

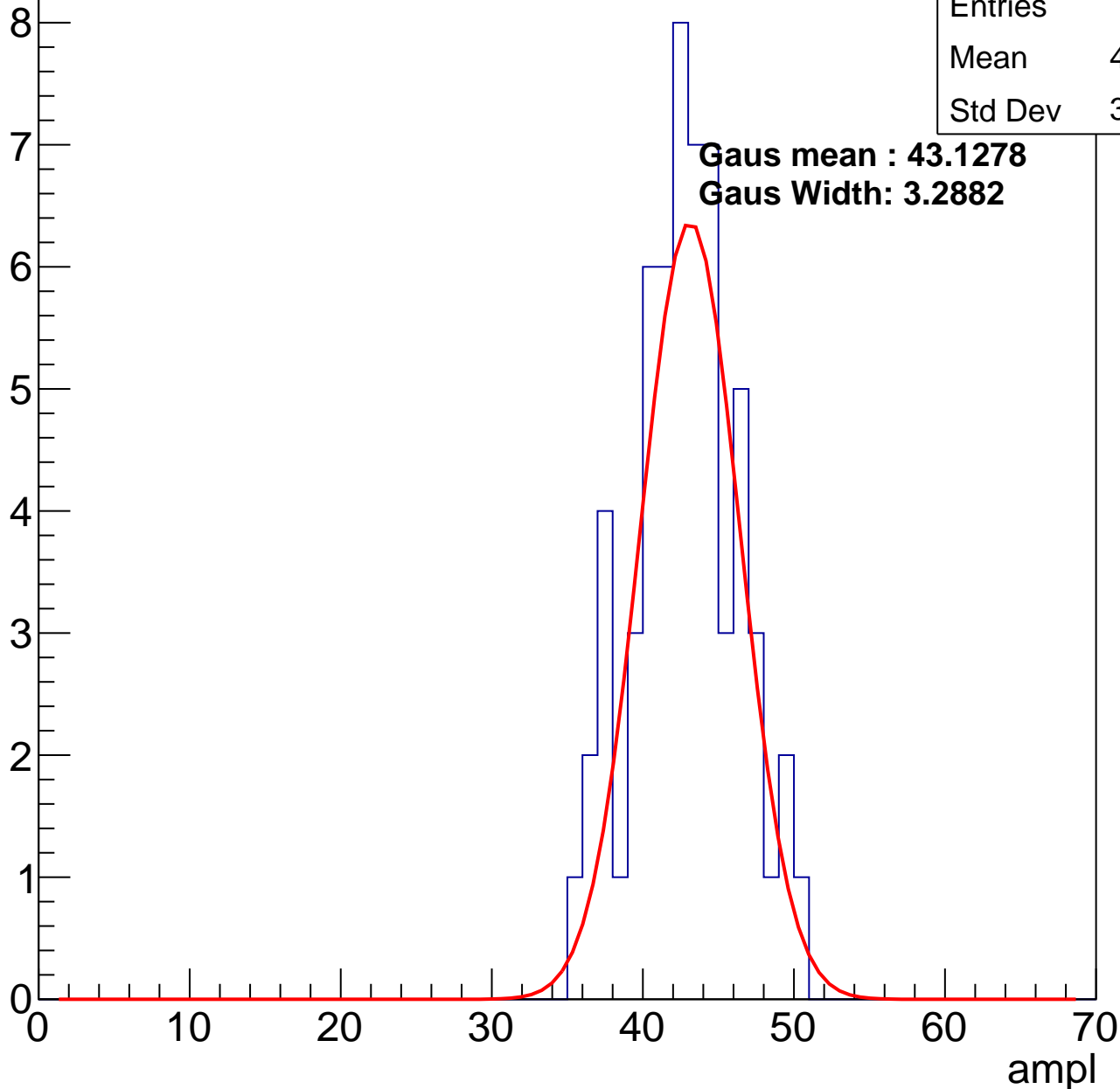
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	42.38
Std Dev	3.416

Gaus mean : 43.1278

Gaus Width: 3.2882

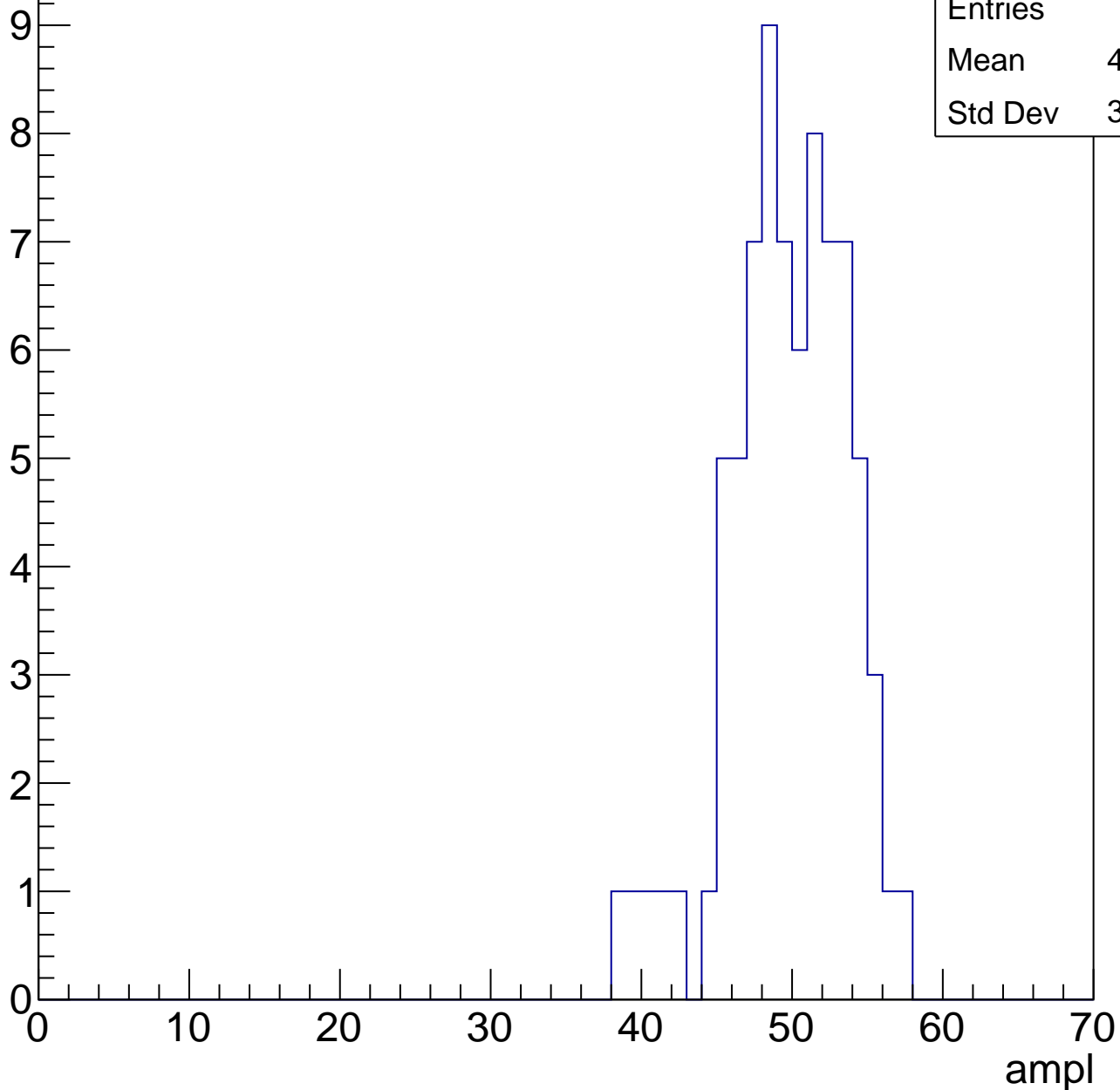


B0L002S, U18-ch39, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	49.27
Std Dev	3.873

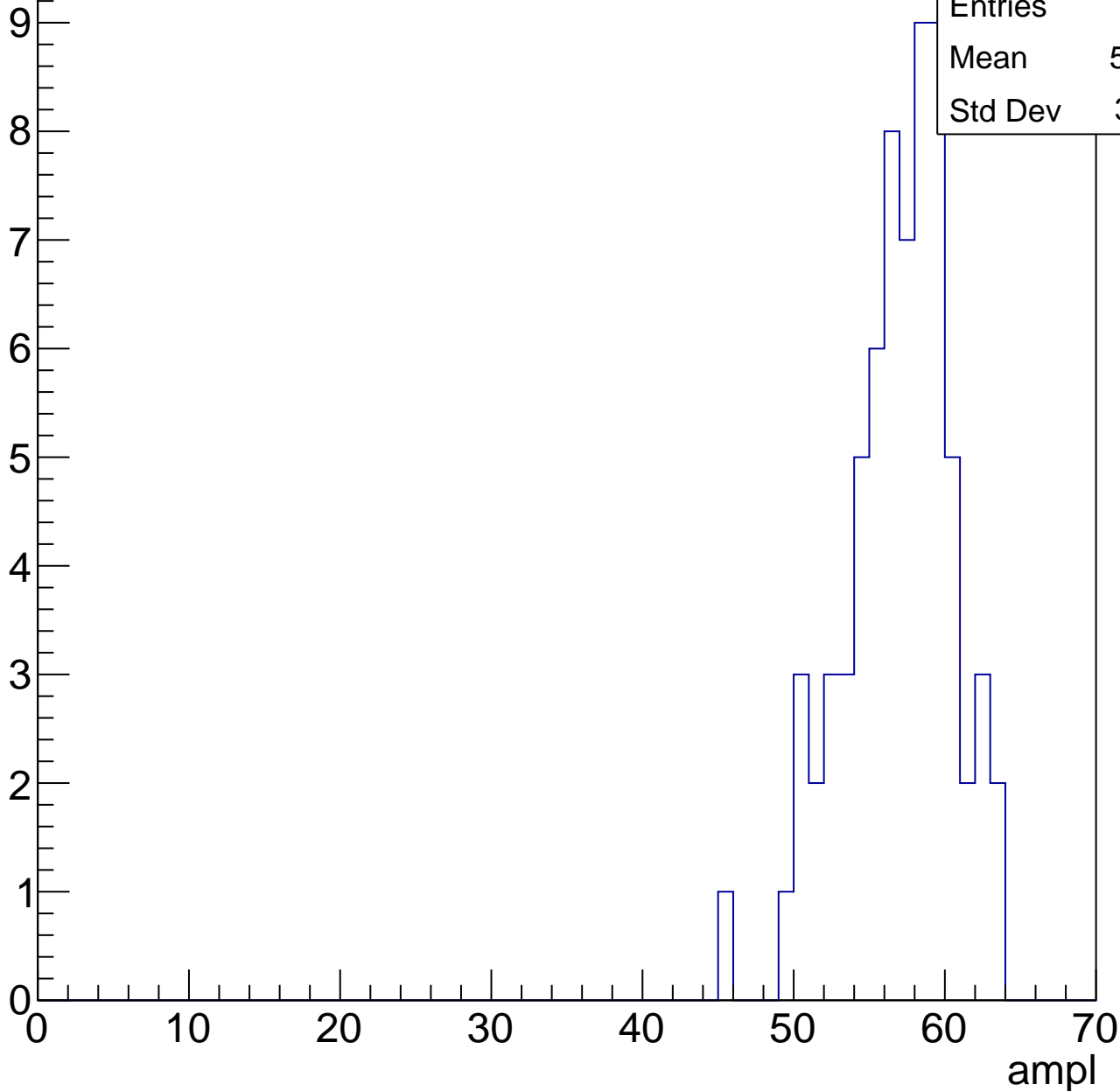


B0L002S, U18-ch39, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	56.45
Std Dev	3.561

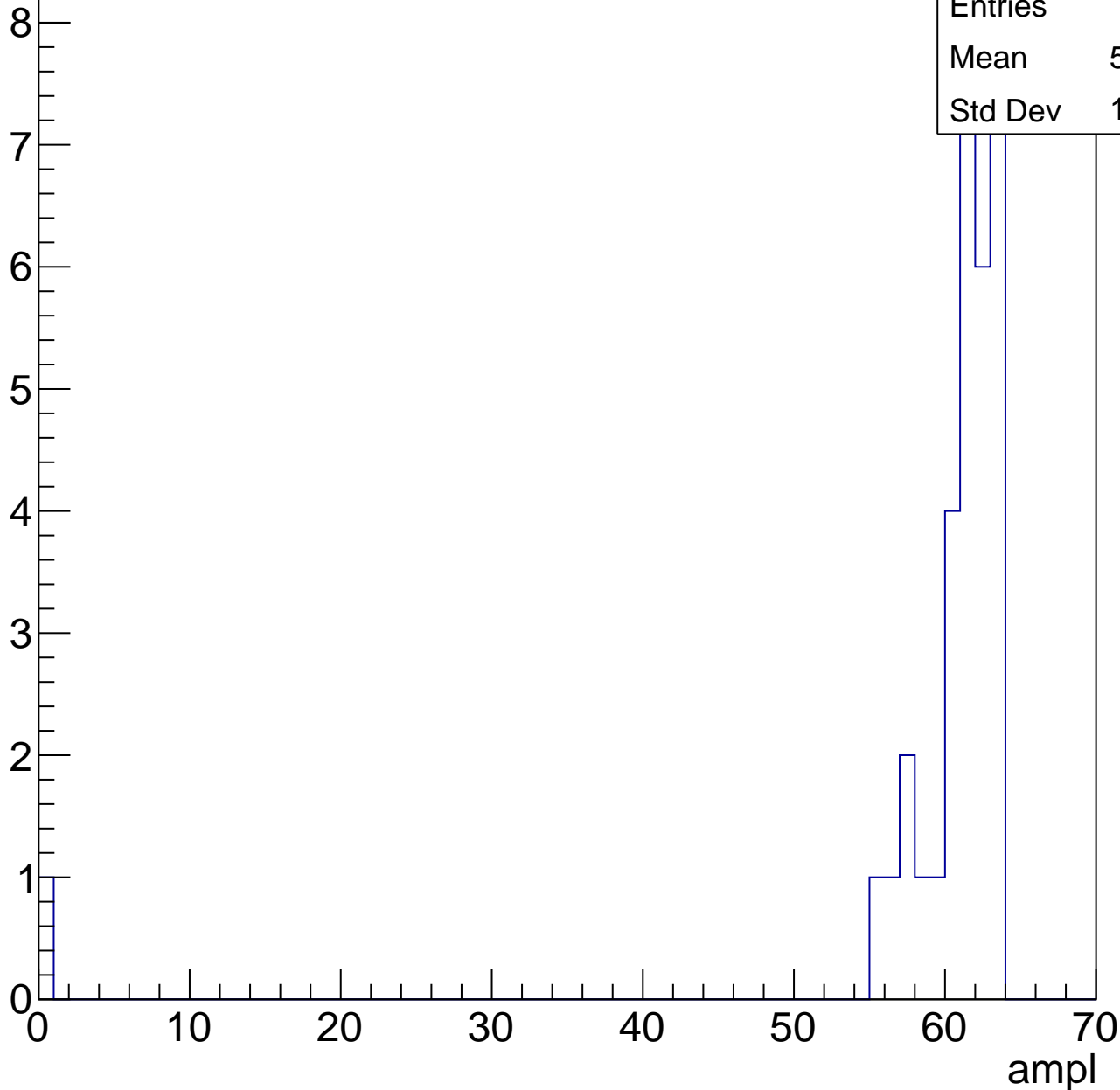


B0L002S, U18-ch39, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.97
Std Dev	10.64



B0L002S, U18-ch39, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch39, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch40, adc0

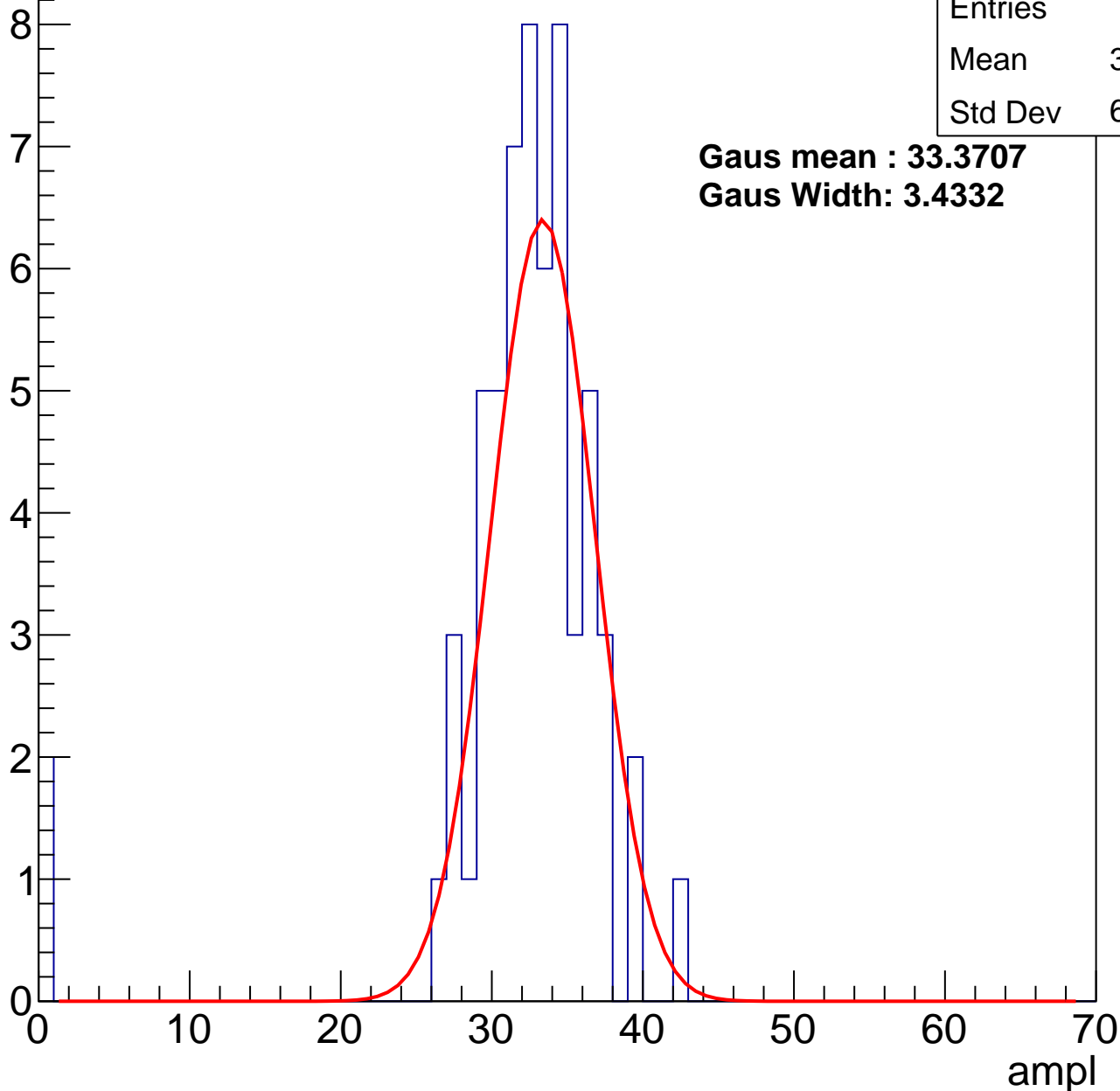
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	31.48
Std Dev	6.645

Gaus mean : 33.3707

Gaus Width: 3.4332



B0L002S, U18-ch40, adc1

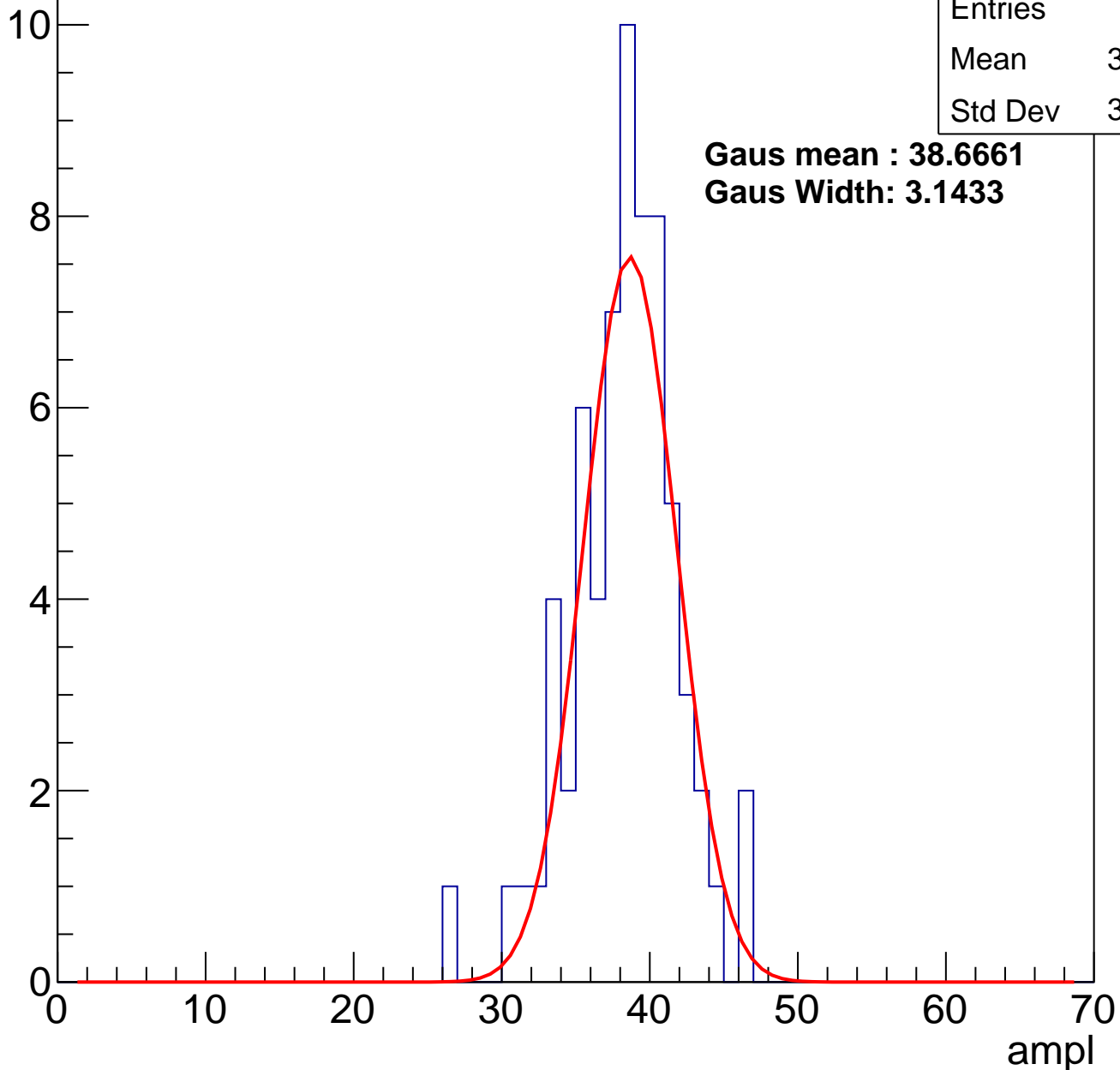
calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	37.83
Std Dev	3.583

Gaus mean : 38.6661

Gaus Width: 3.1433

Entry



B0L002S, U18-ch40, adc2

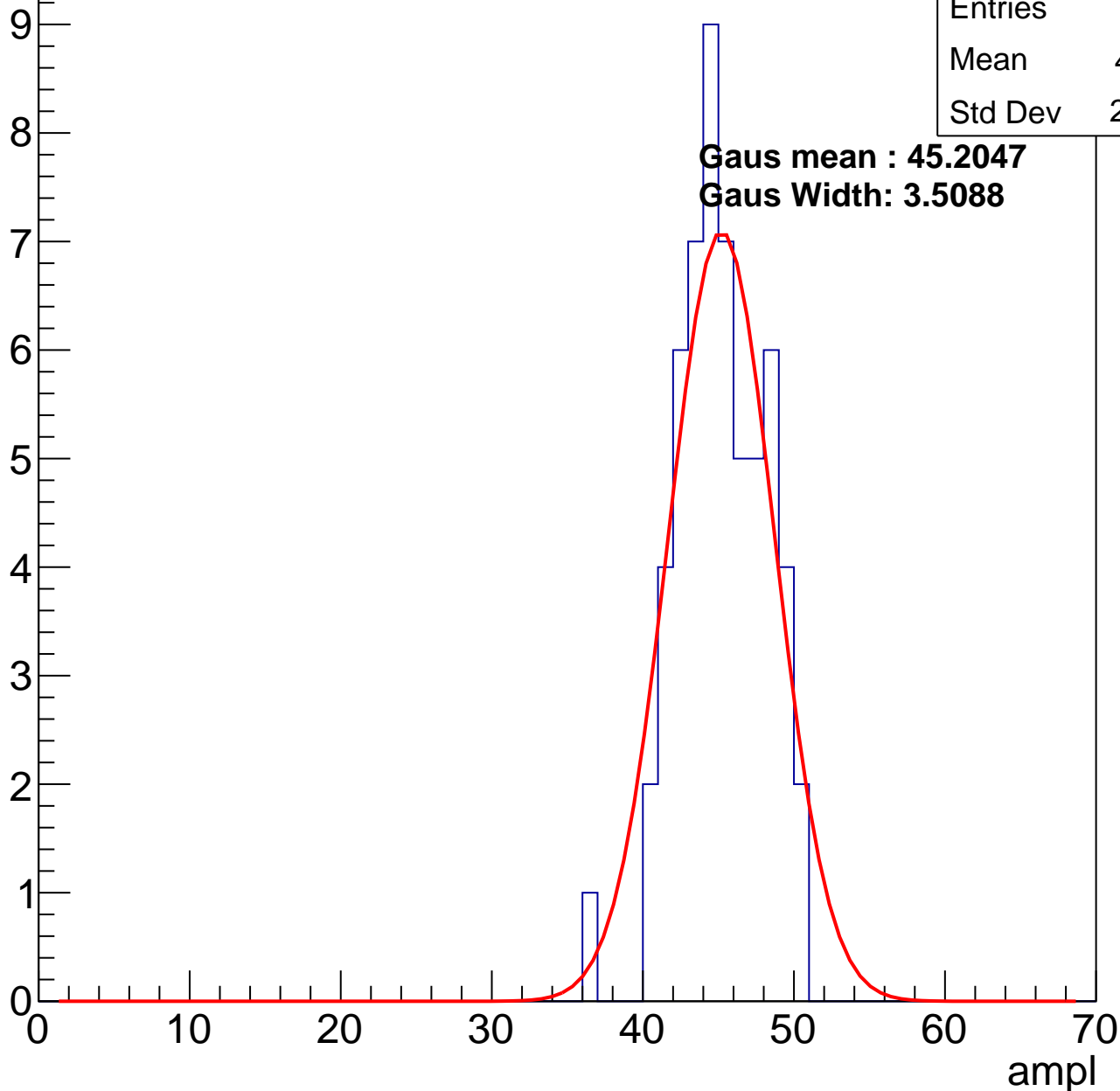
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.71
Std Dev	2.859

Gaus mean : 45.2047

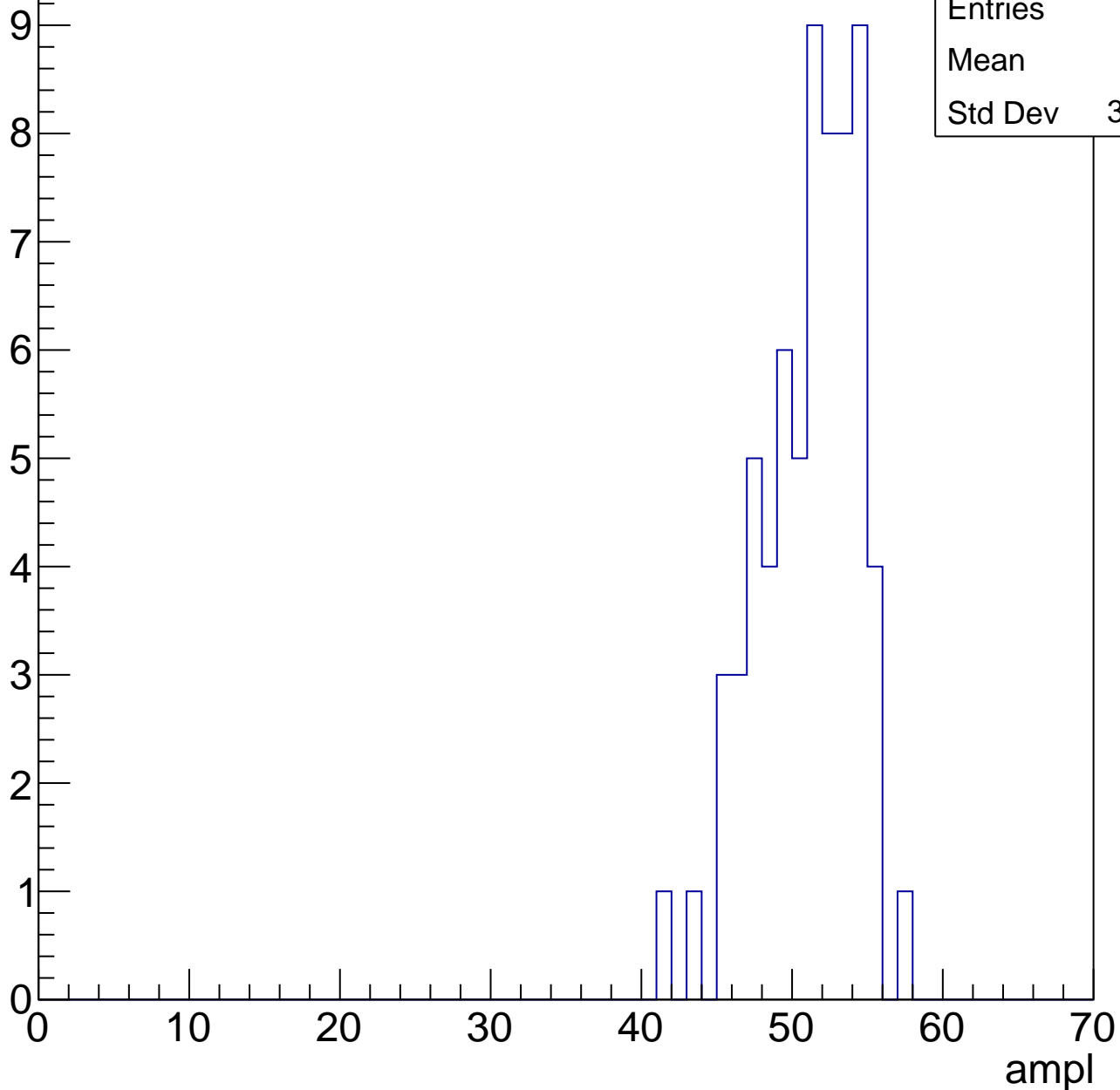
Gaus Width: 3.5088



B0L002S, U18-ch40, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

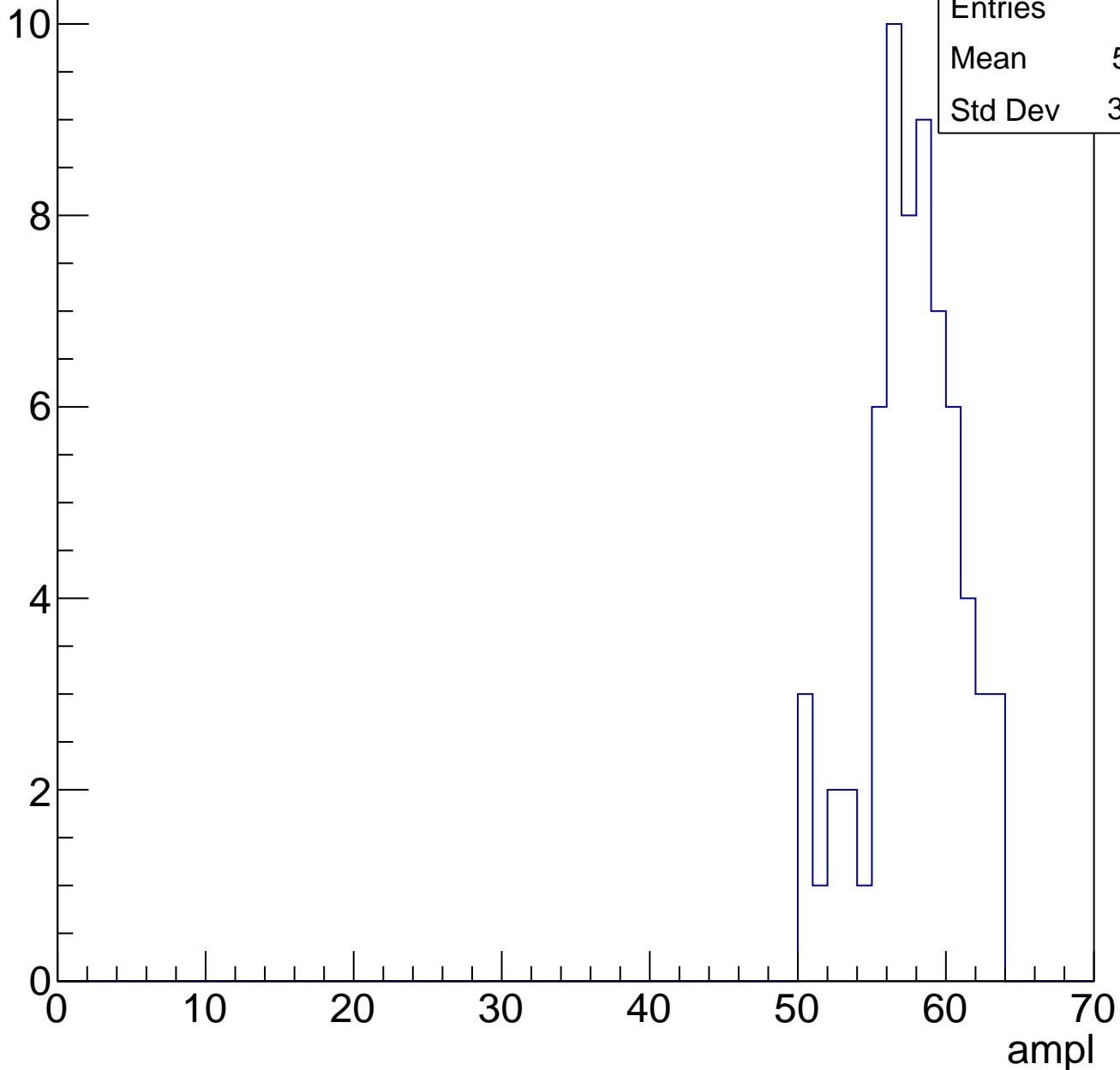


B0L002S, U18-ch40, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	65
Mean	57.31
Std Dev	3.137

Entry

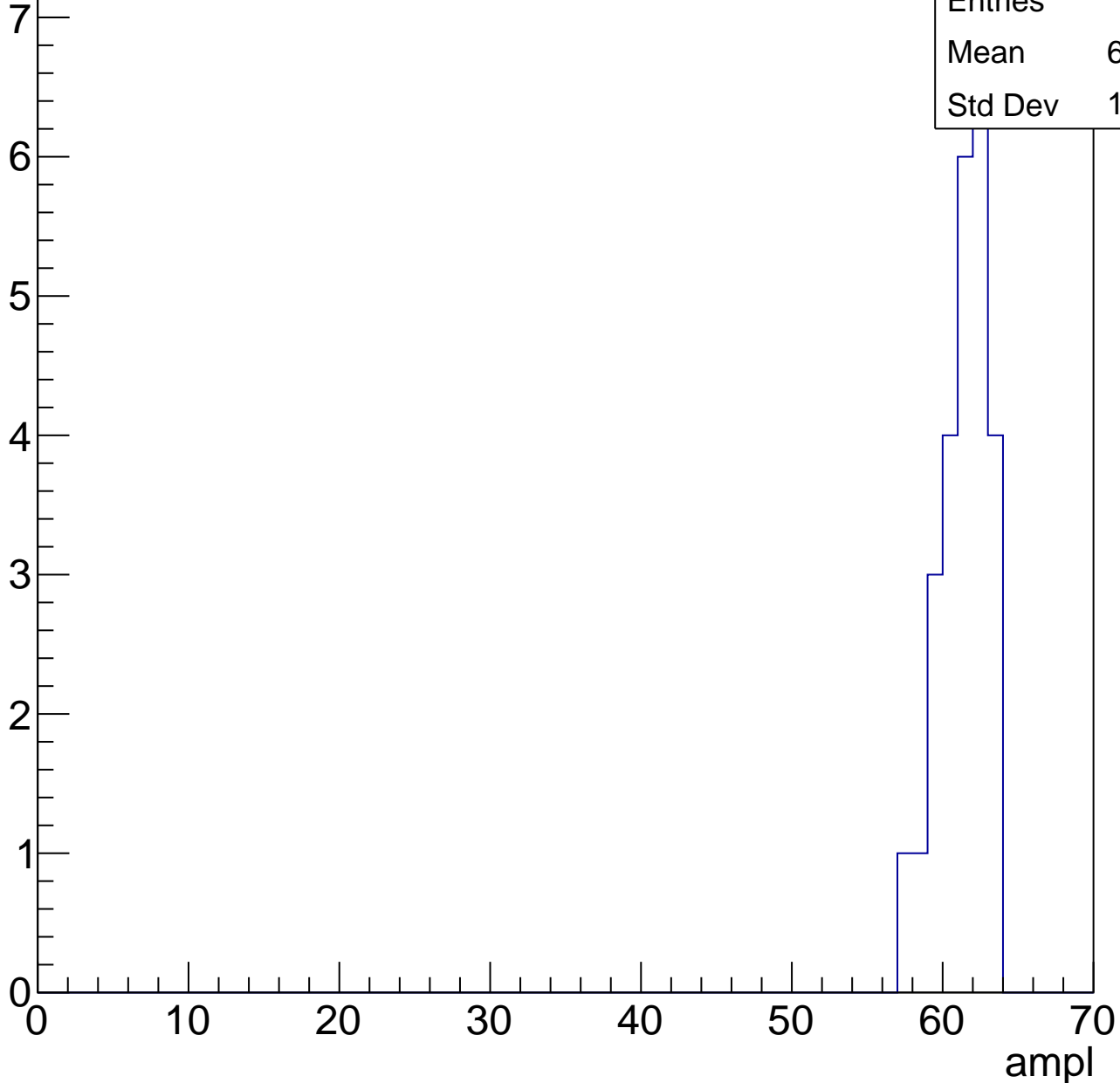


B0L002S, U18-ch40, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	26
Mean	60.92
Std Dev	1.567



B0L002S, U18-ch40, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch40, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch41, adc0

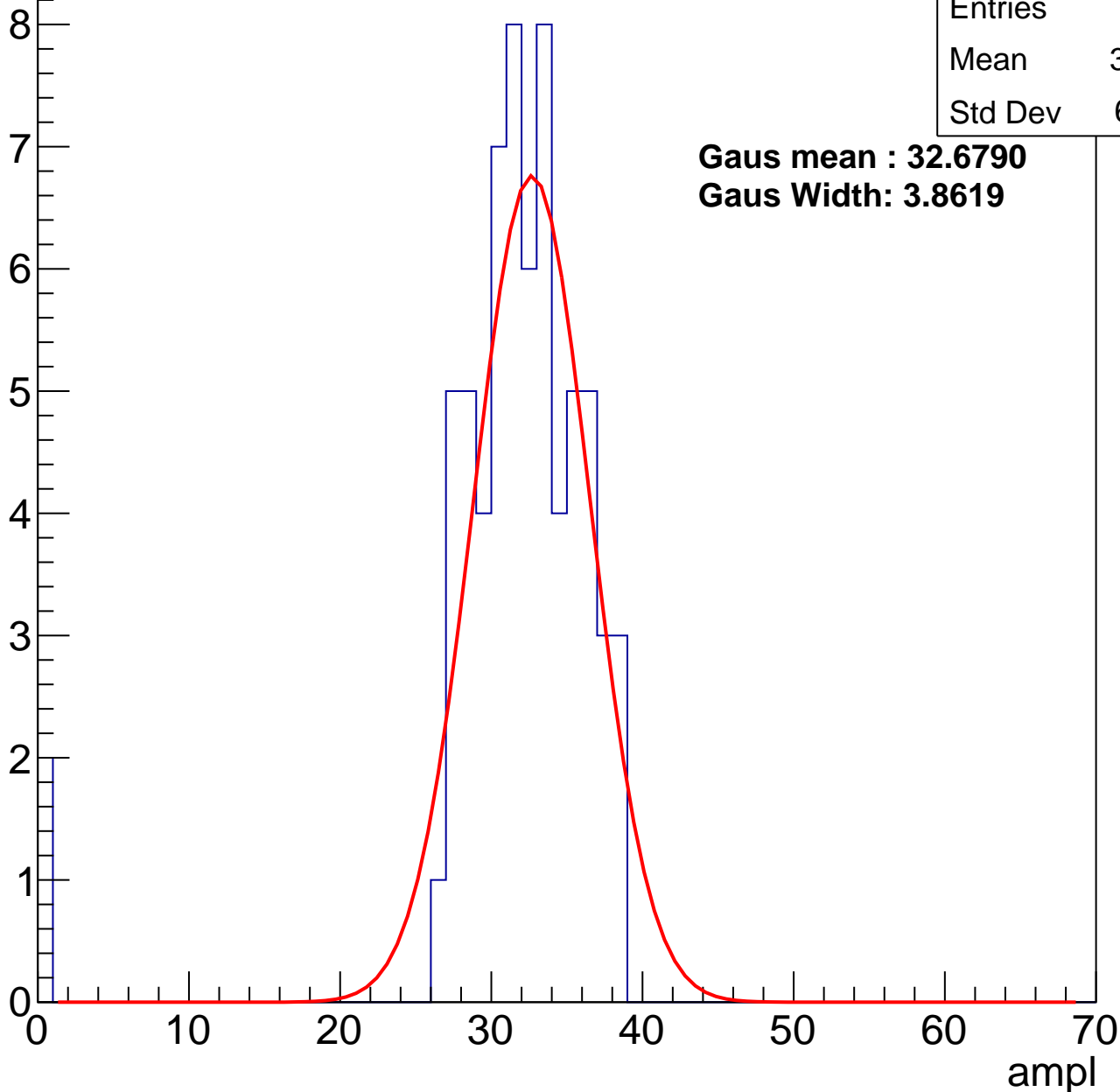
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	31.02
Std Dev	6.311

Gaus mean : 32.6790

Gaus Width: 3.8619



B0L002S, U18-ch41, adc1

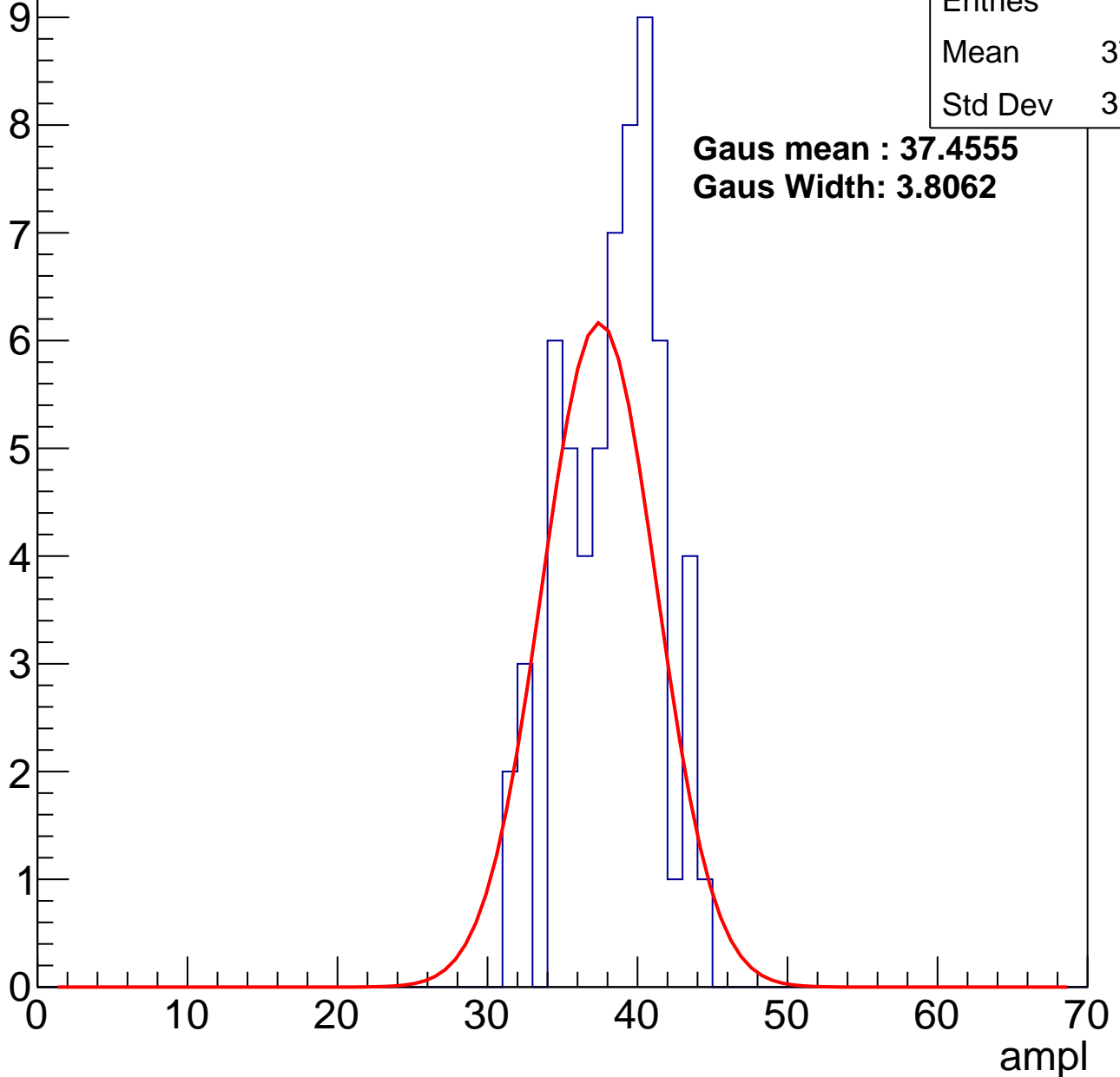
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.84
Std Dev	3.179

Gaus mean : 37.4555

Gaus Width: 3.8062



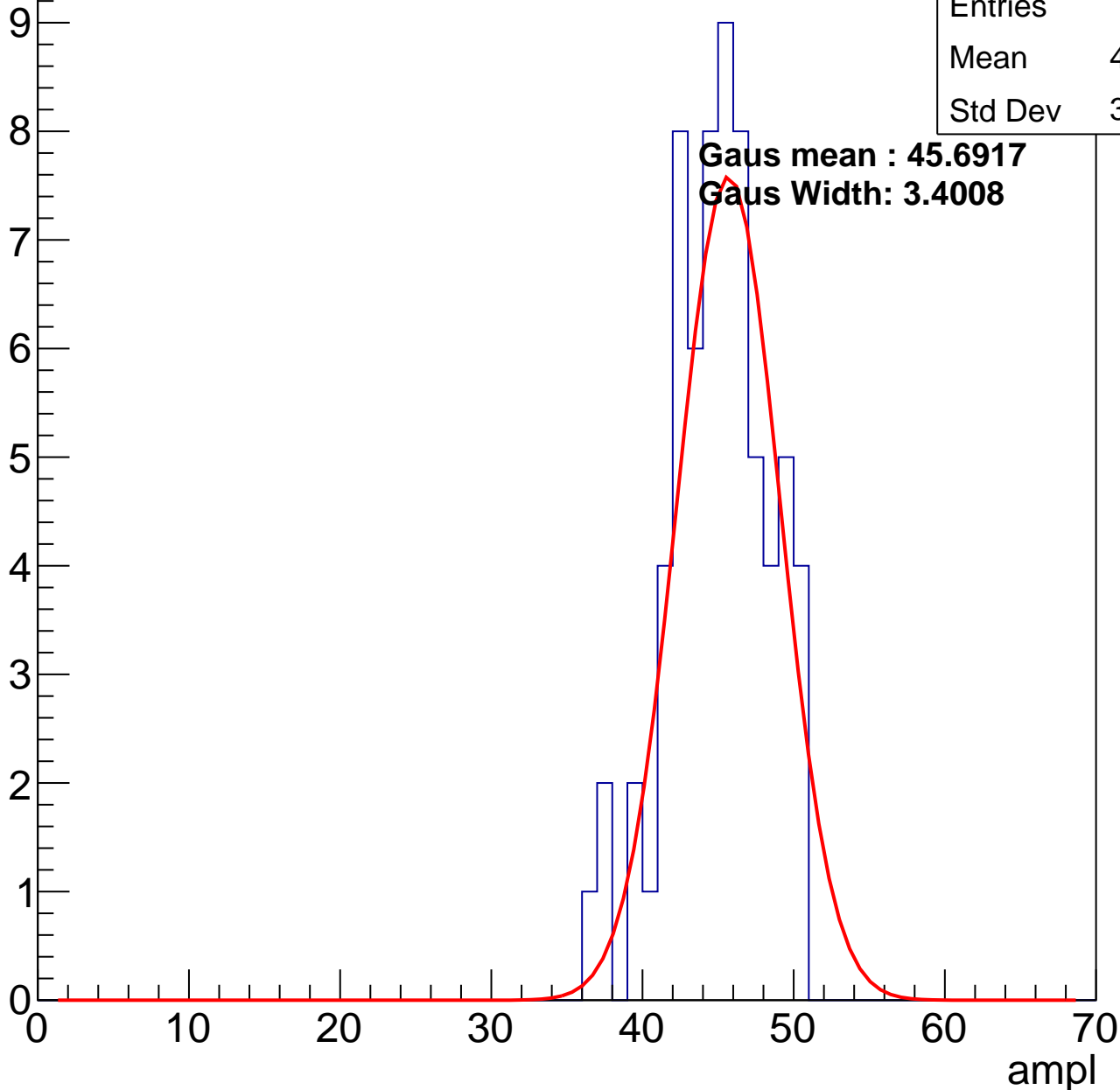
B0L002S, U18-ch41, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

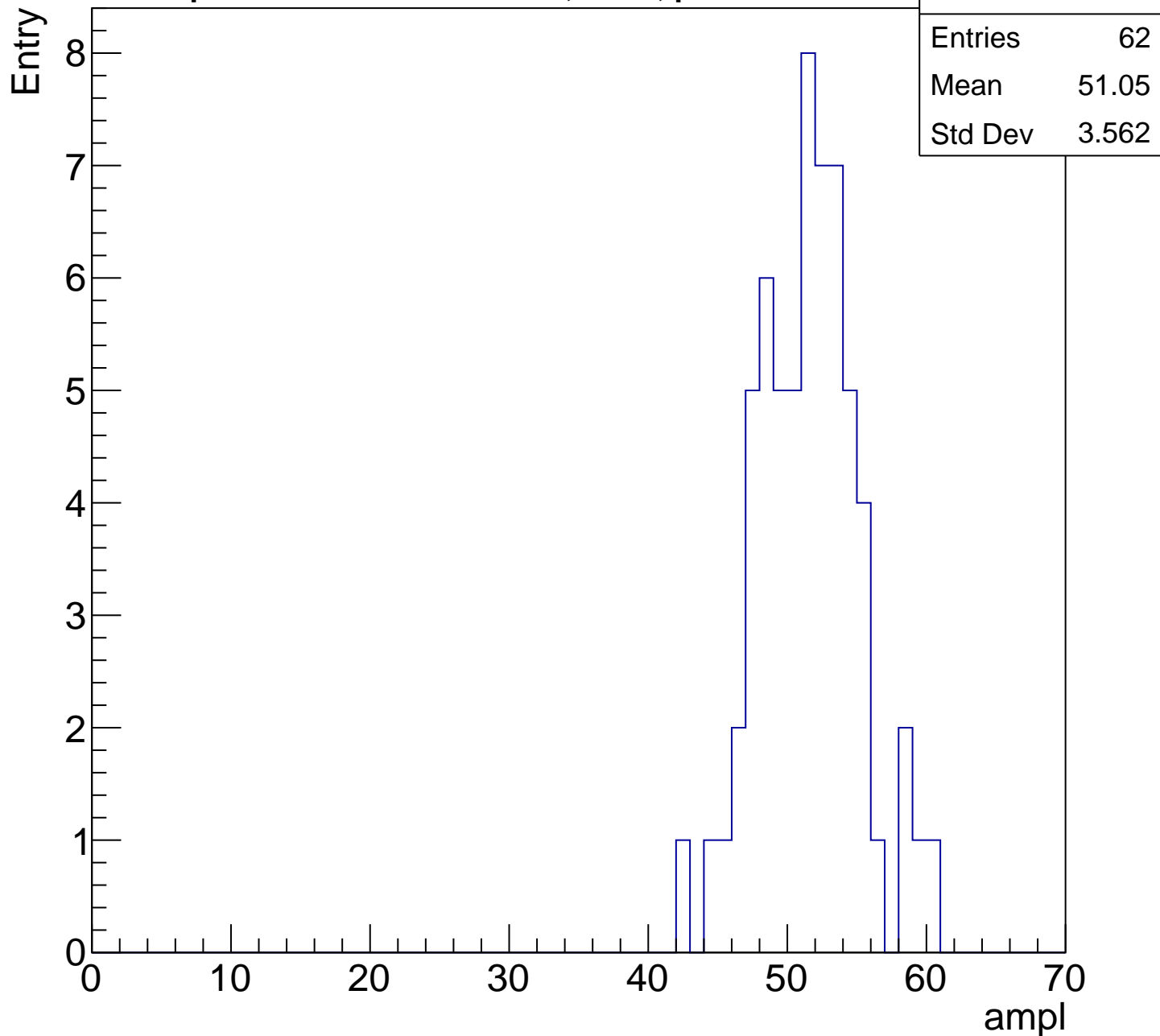
Entries	67
Mean	44.52
Std Dev	3.239

Gaus mean : 45.6917
Gaus Width: 3.4008



B0L002S, U18-ch41, adc3

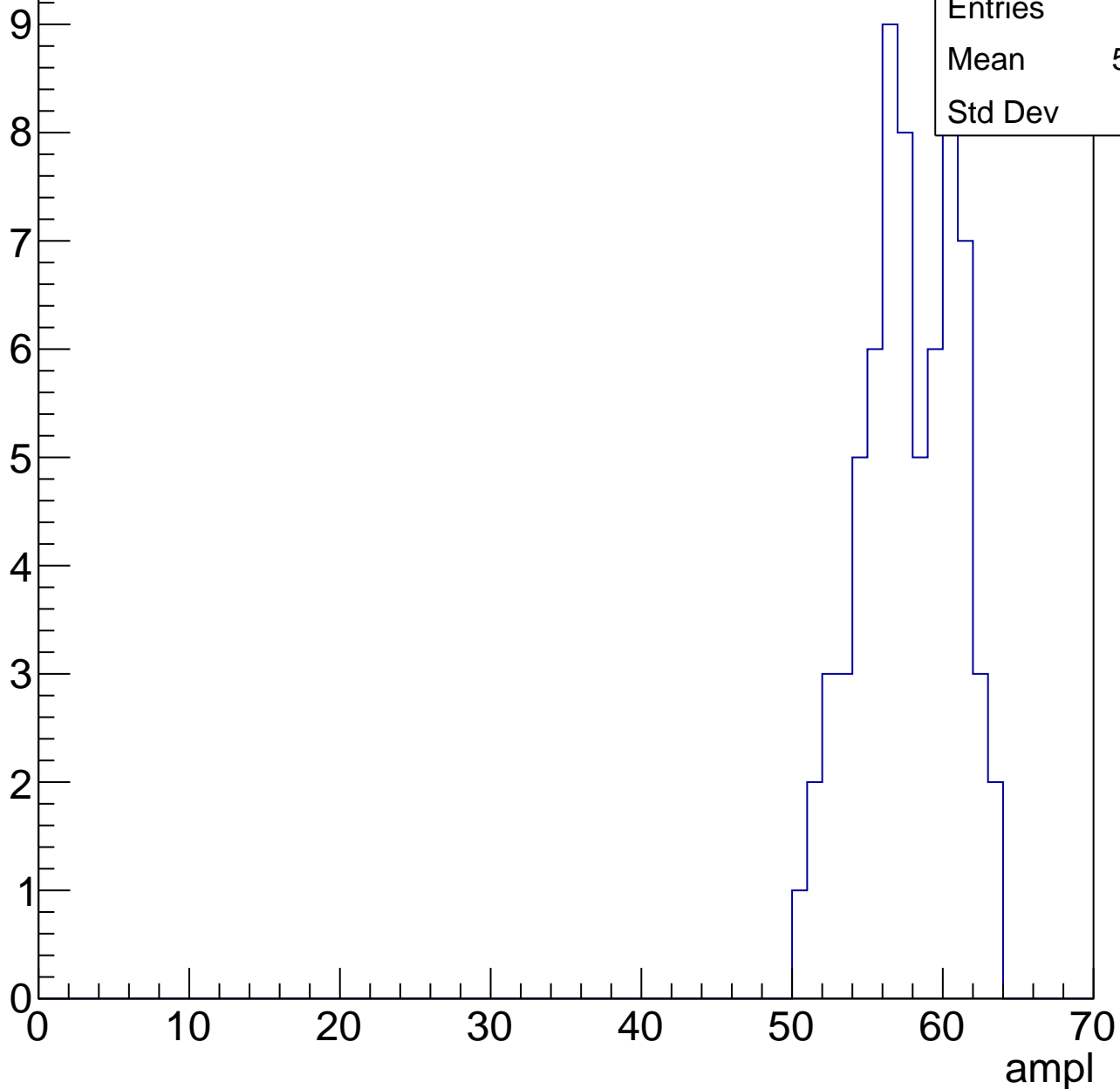
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U18-ch41, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



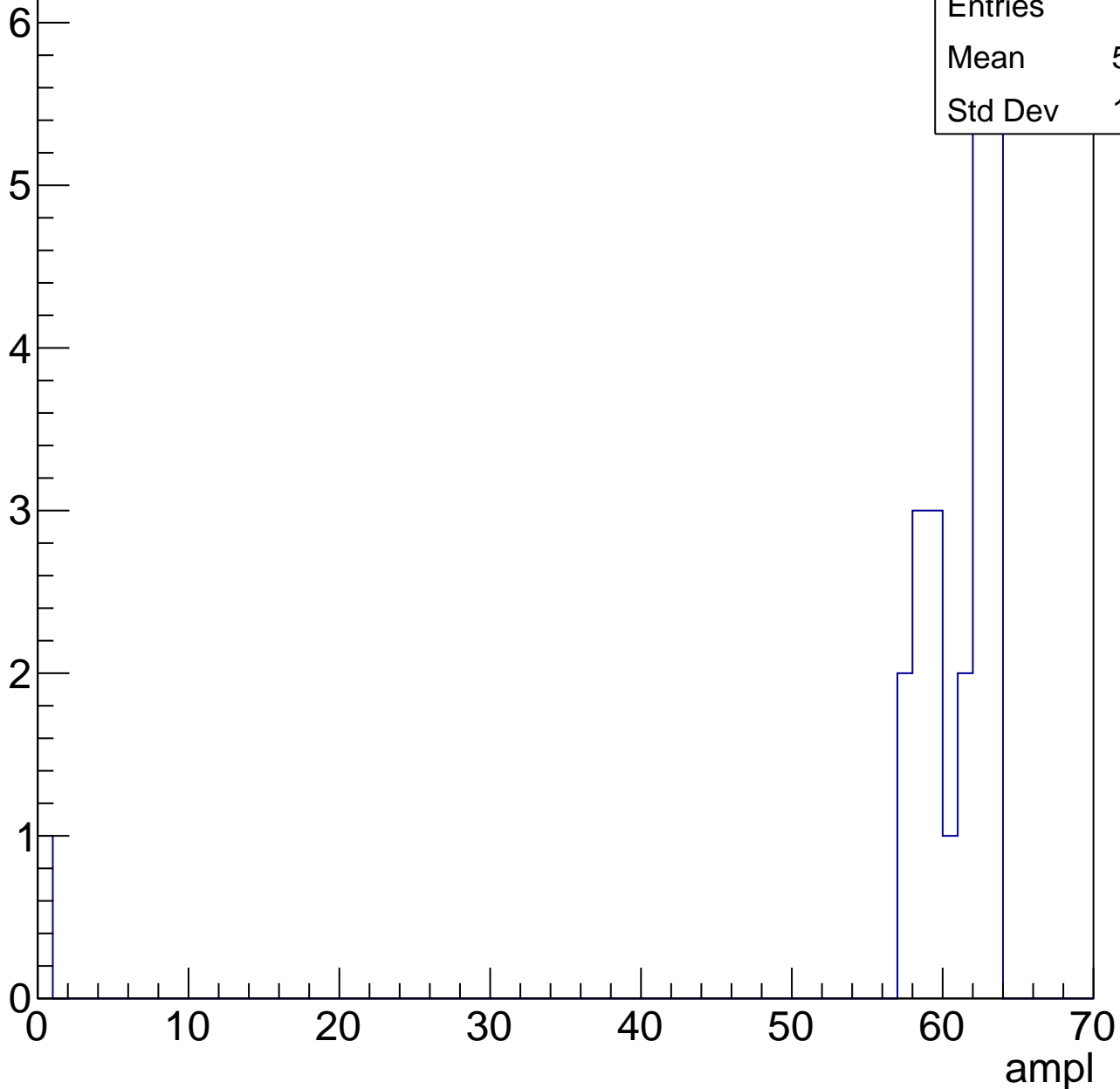
Entries	68
Mean	57.21
Std Dev	3.16

B0L002S, U18-ch41, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	24
Mean	58.21
Std Dev	12.31



B0L002S, U18-ch41, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch41, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch42, adc0

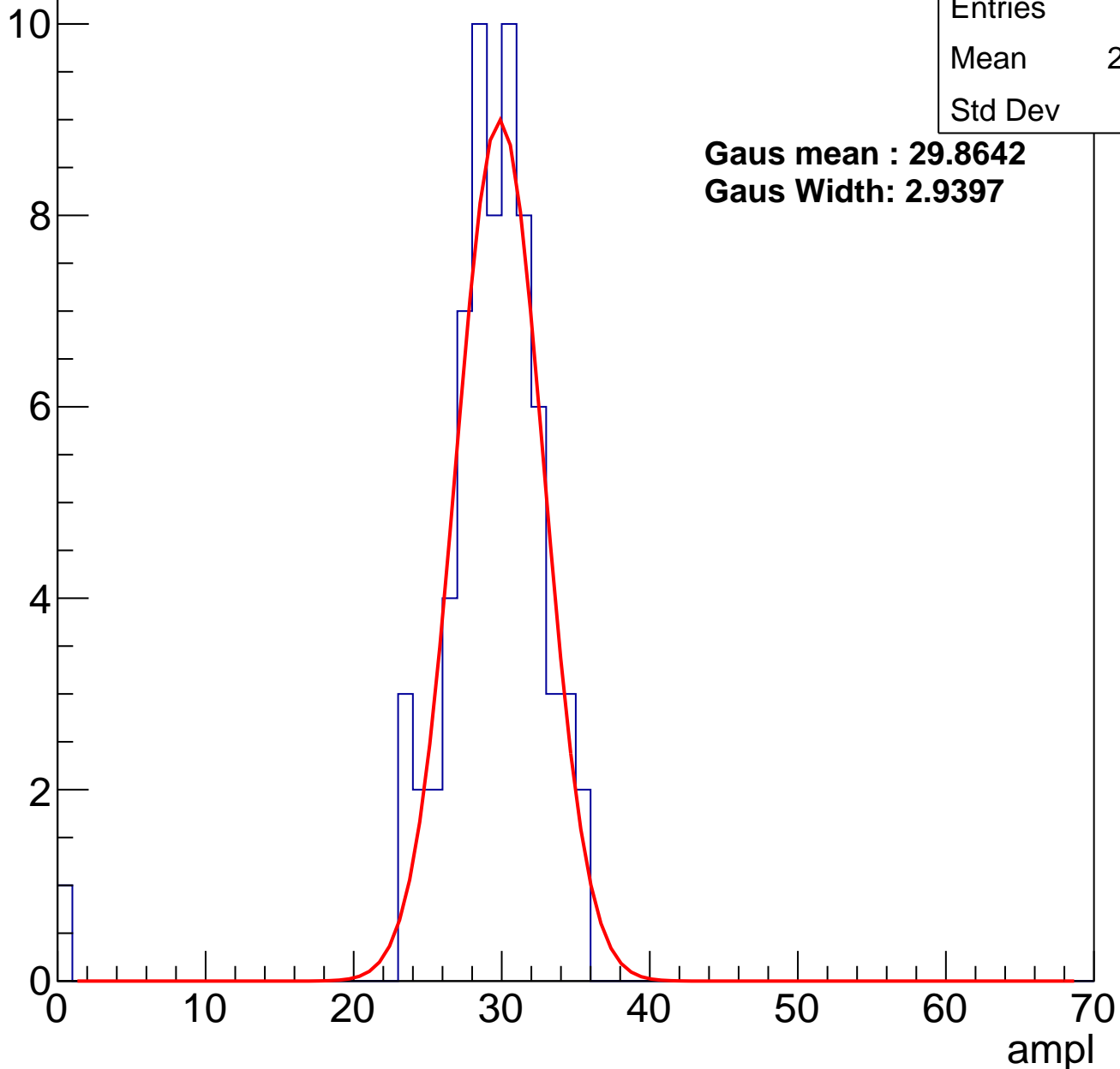
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	28.74
Std Dev	4.49

Gaus mean : 29.8642

Gaus Width: 2.9397

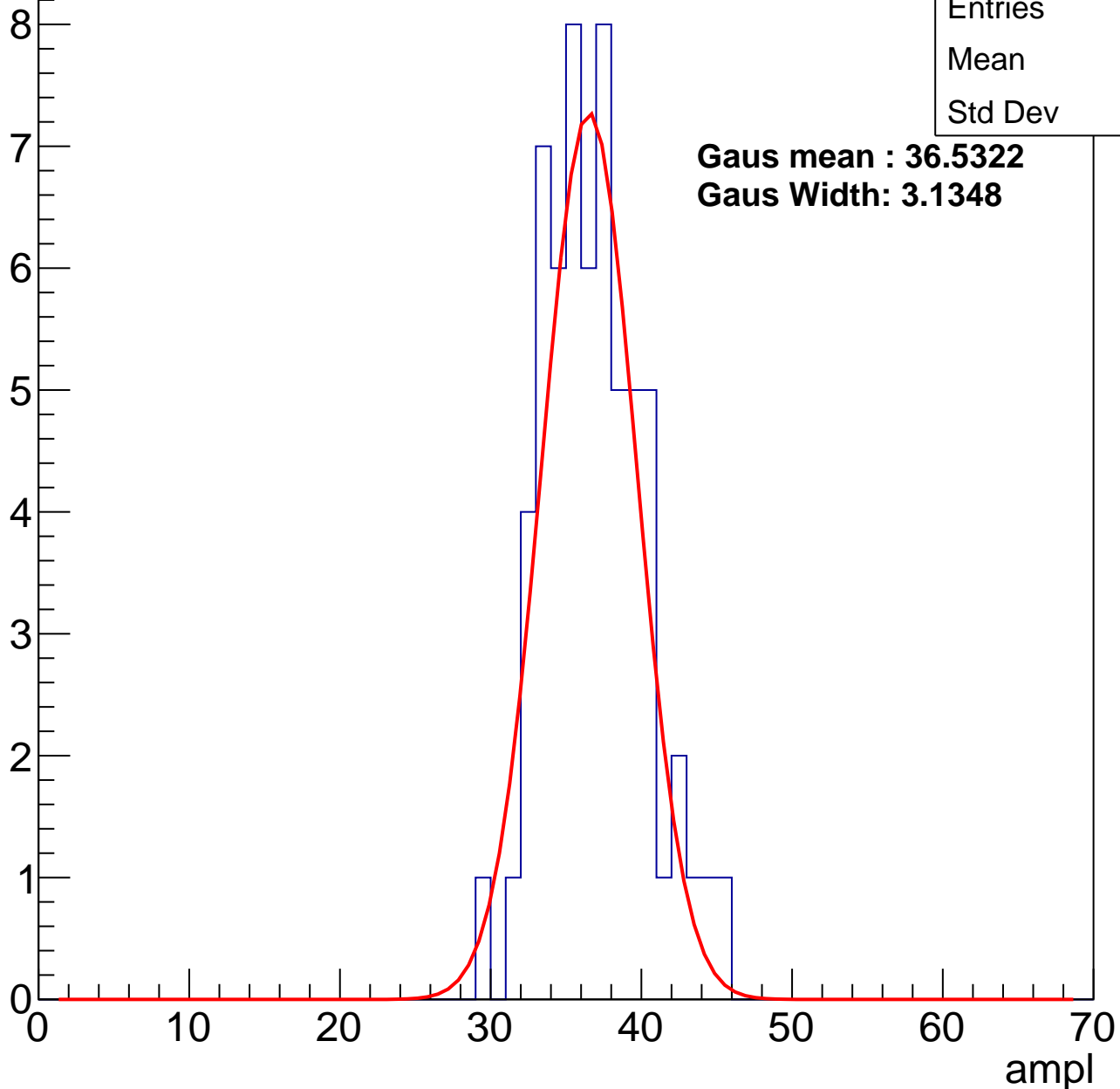
Entry



B0L002S, U18-ch42, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	62
Mean	36.4
Std Dev	3.28

Gaus mean : 36.5322

Gaus Width: 3.1348

B0L002S, U18-ch42, adc2

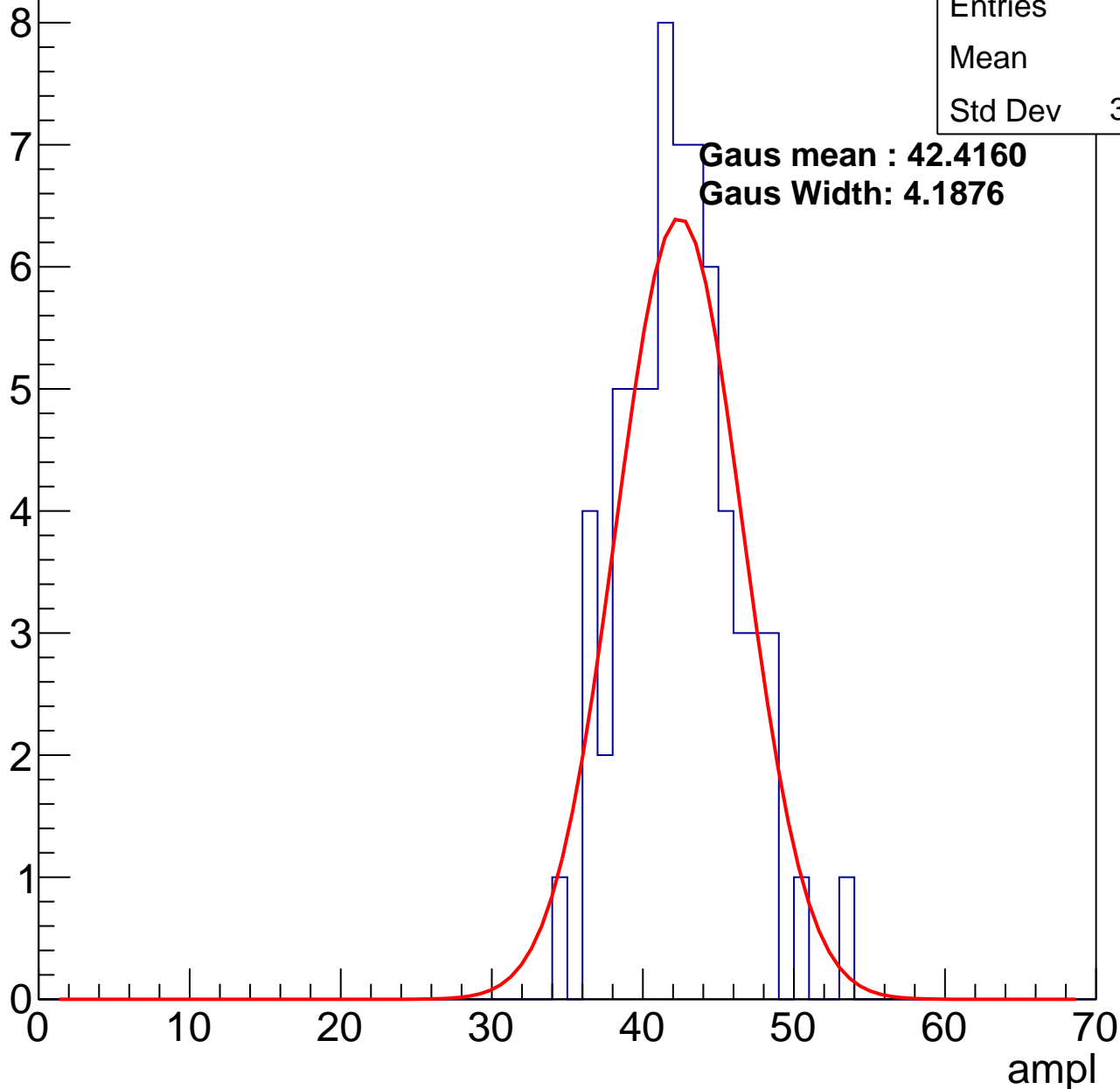
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	42
Std Dev	3.709

Gaus mean : 42.4160

Gaus Width: 4.1876

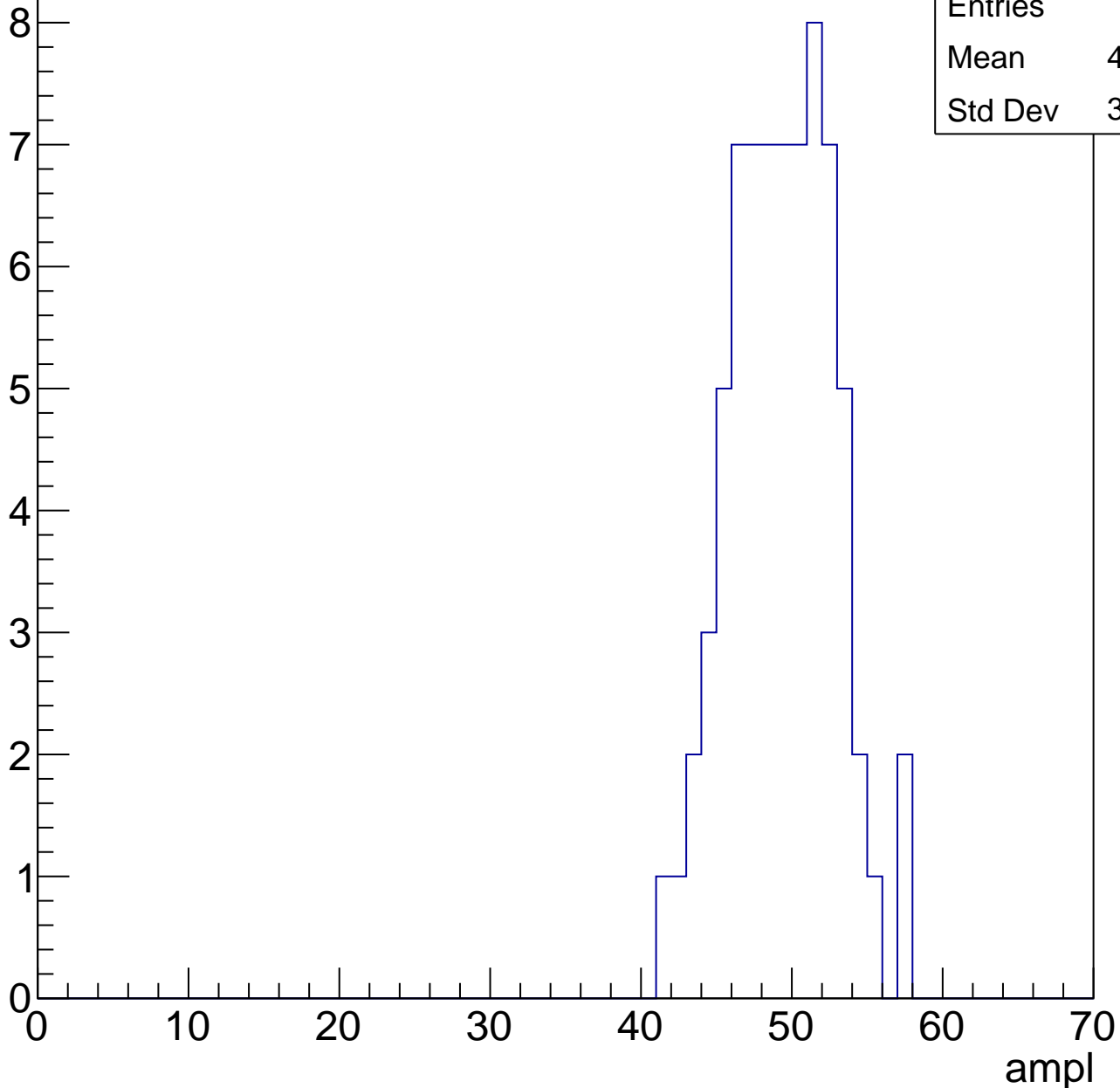


B0L002S, U18-ch42, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	48.89
Std Dev	3.402



B0L002S, U18-ch42, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	71
Mean	56.01
Std Dev	3.325

Entry

10

8

6

4

2

0

0

10

20

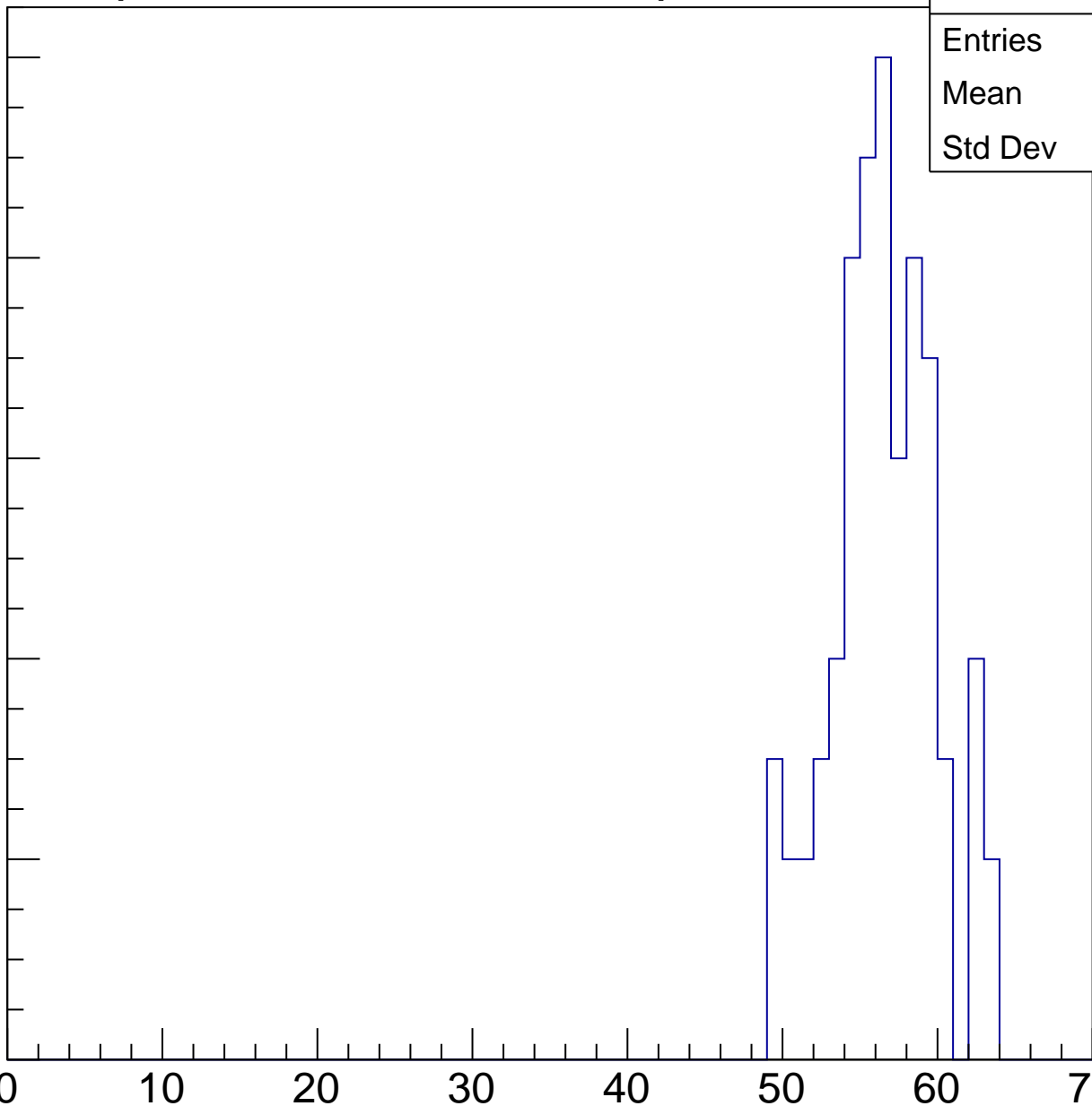
30

40

50

60

ampl

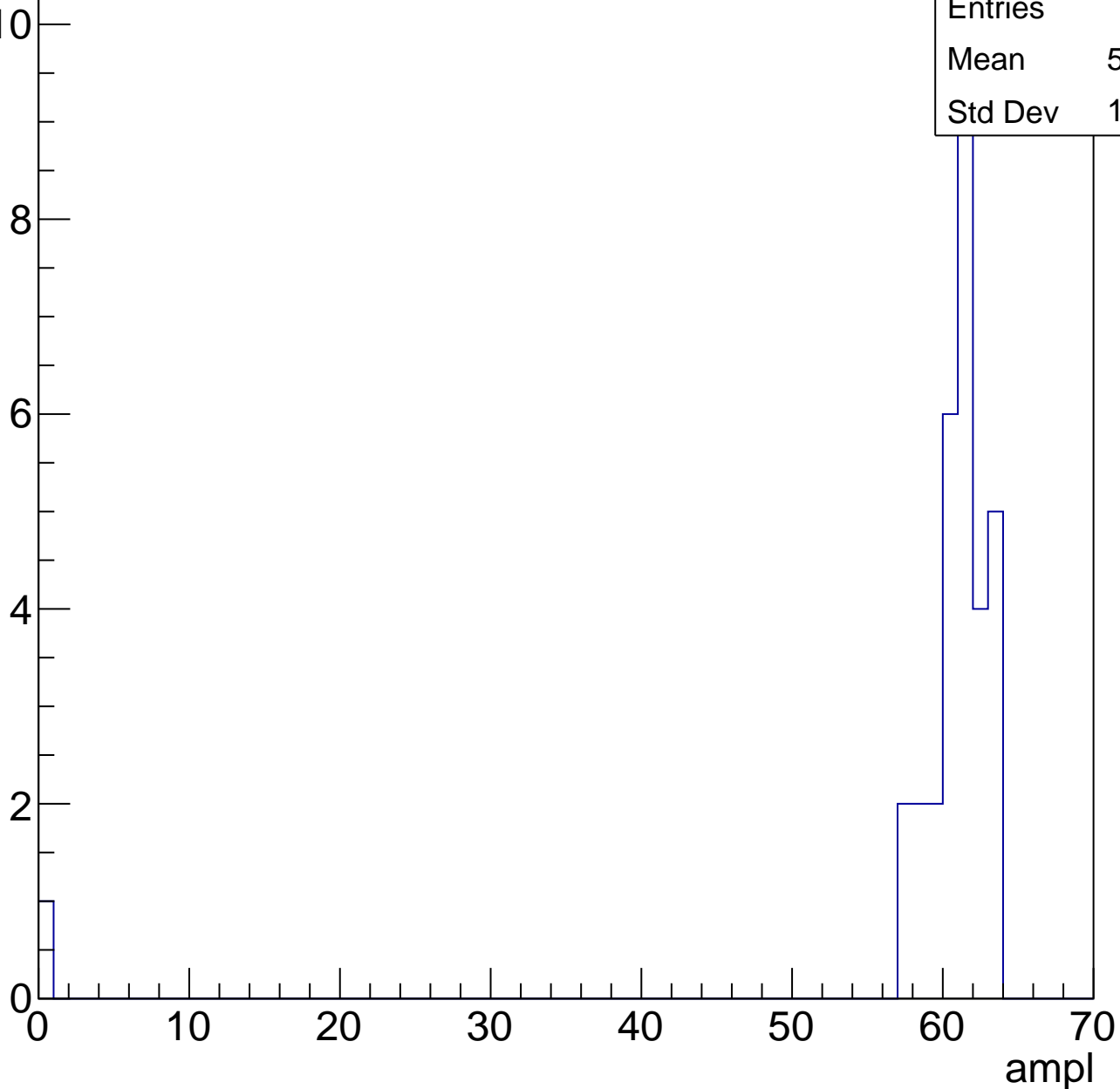


B0L002S, U18-ch42, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

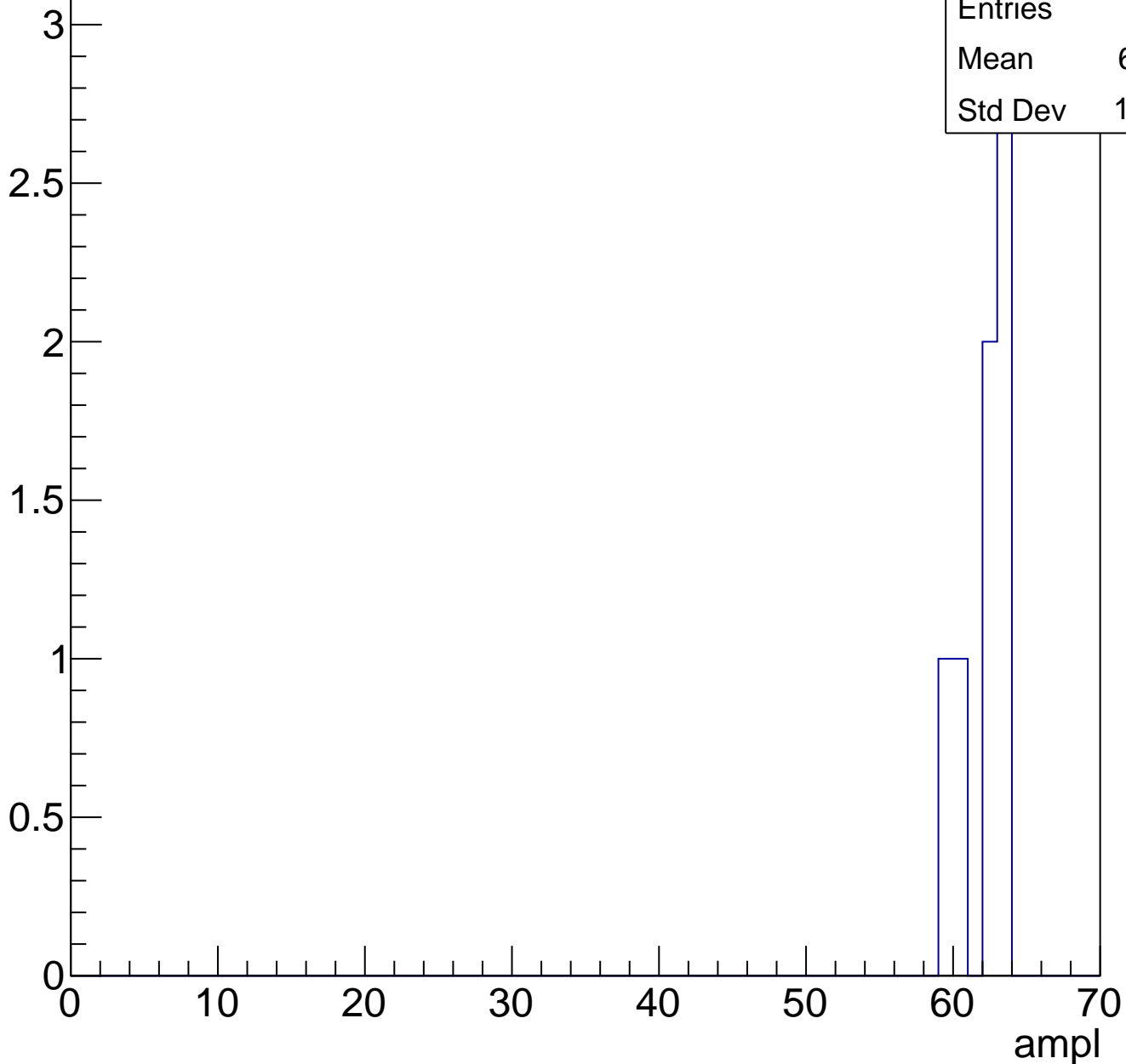
Entries	32
Mean	58.78
Std Dev	10.68



B0L002S, U18-ch42, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch42, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch43, adc0

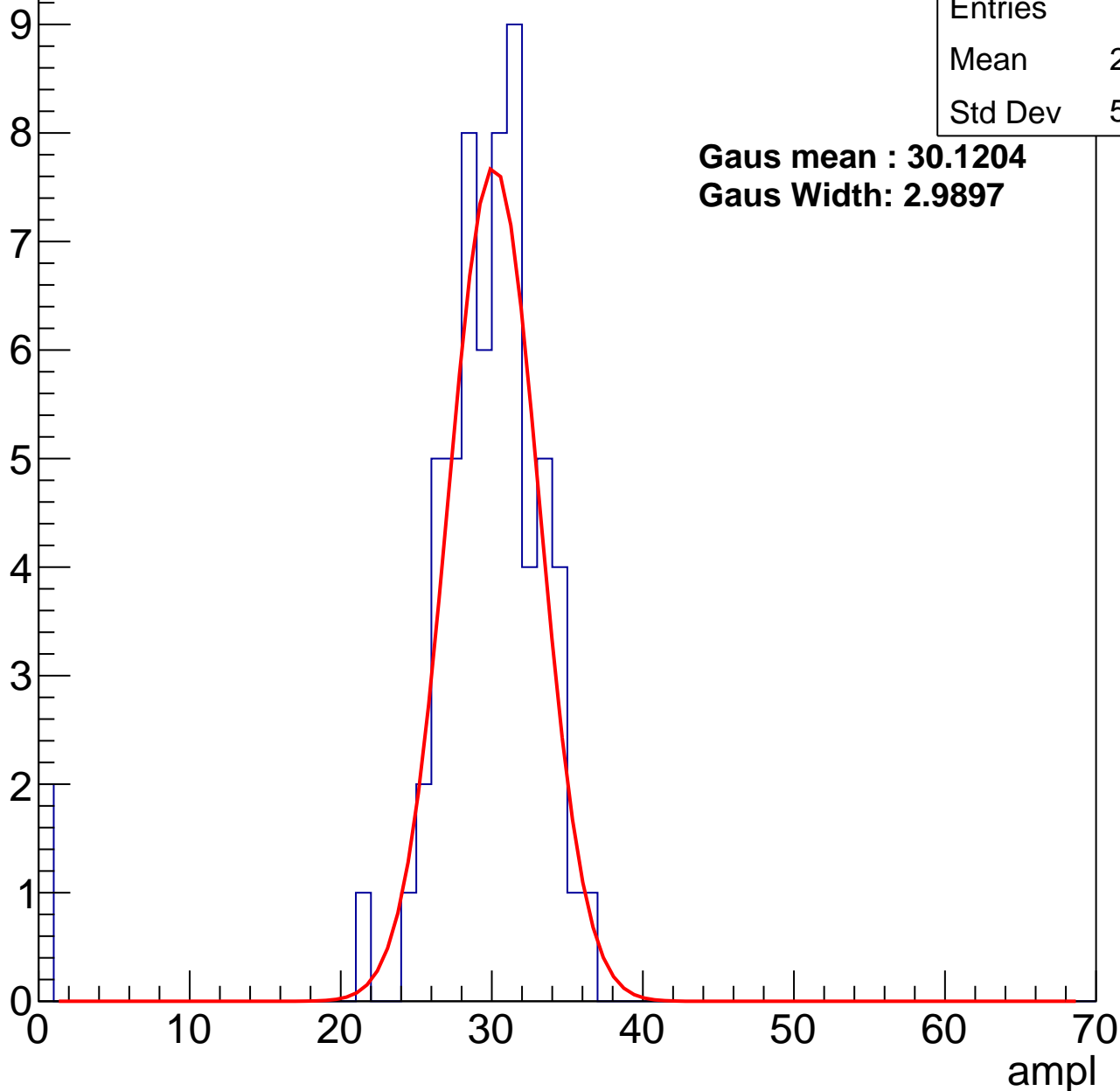
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	28.66
Std Dev	5.976

Gaus mean : 30.1204

Gaus Width: 2.9897



B0L002S, U18-ch43, adc1

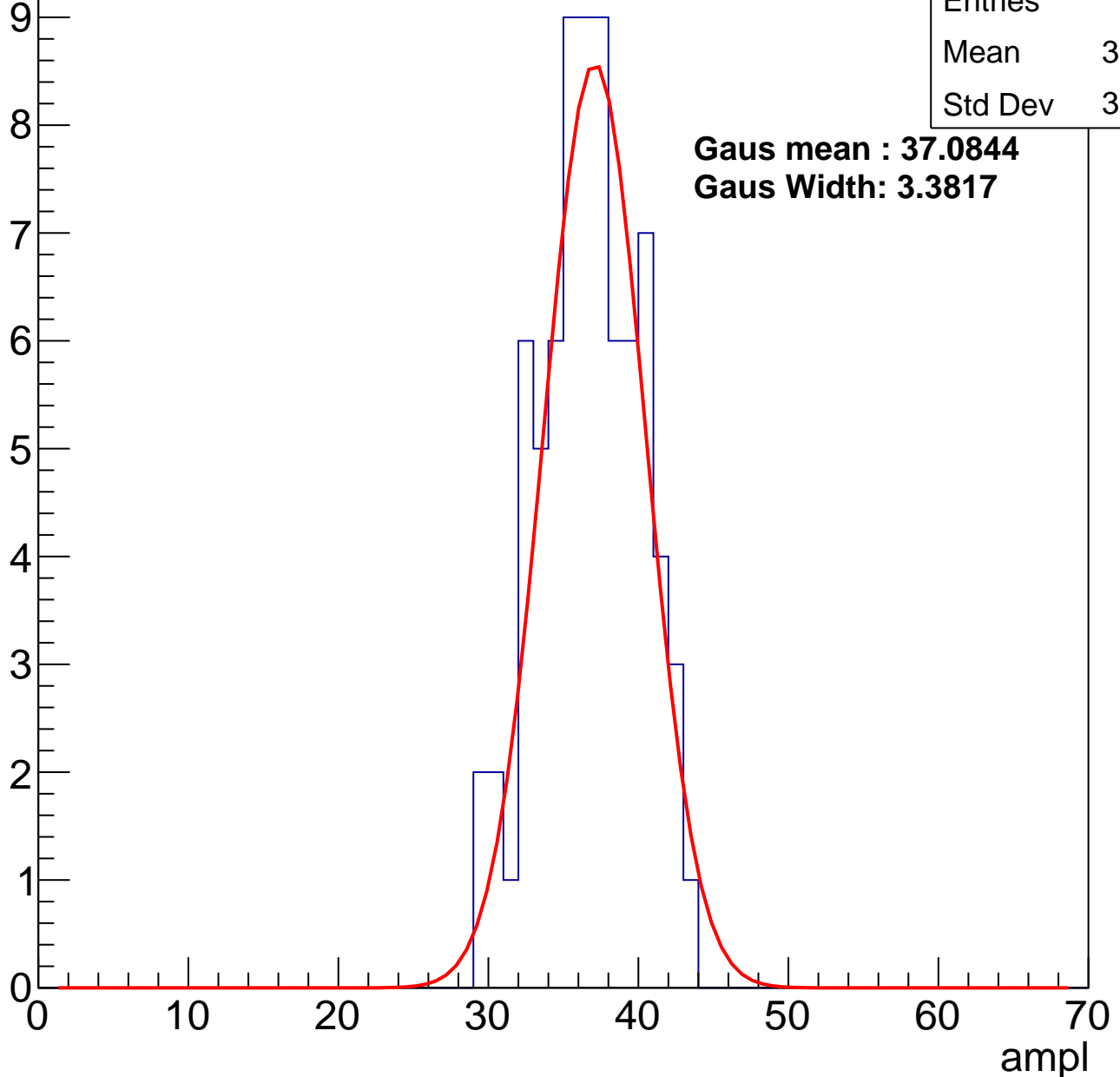
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	36.28
Std Dev	3.283

Gaus mean : 37.0844

Gaus Width: 3.3817



B0L002S, U18-ch43, adc2

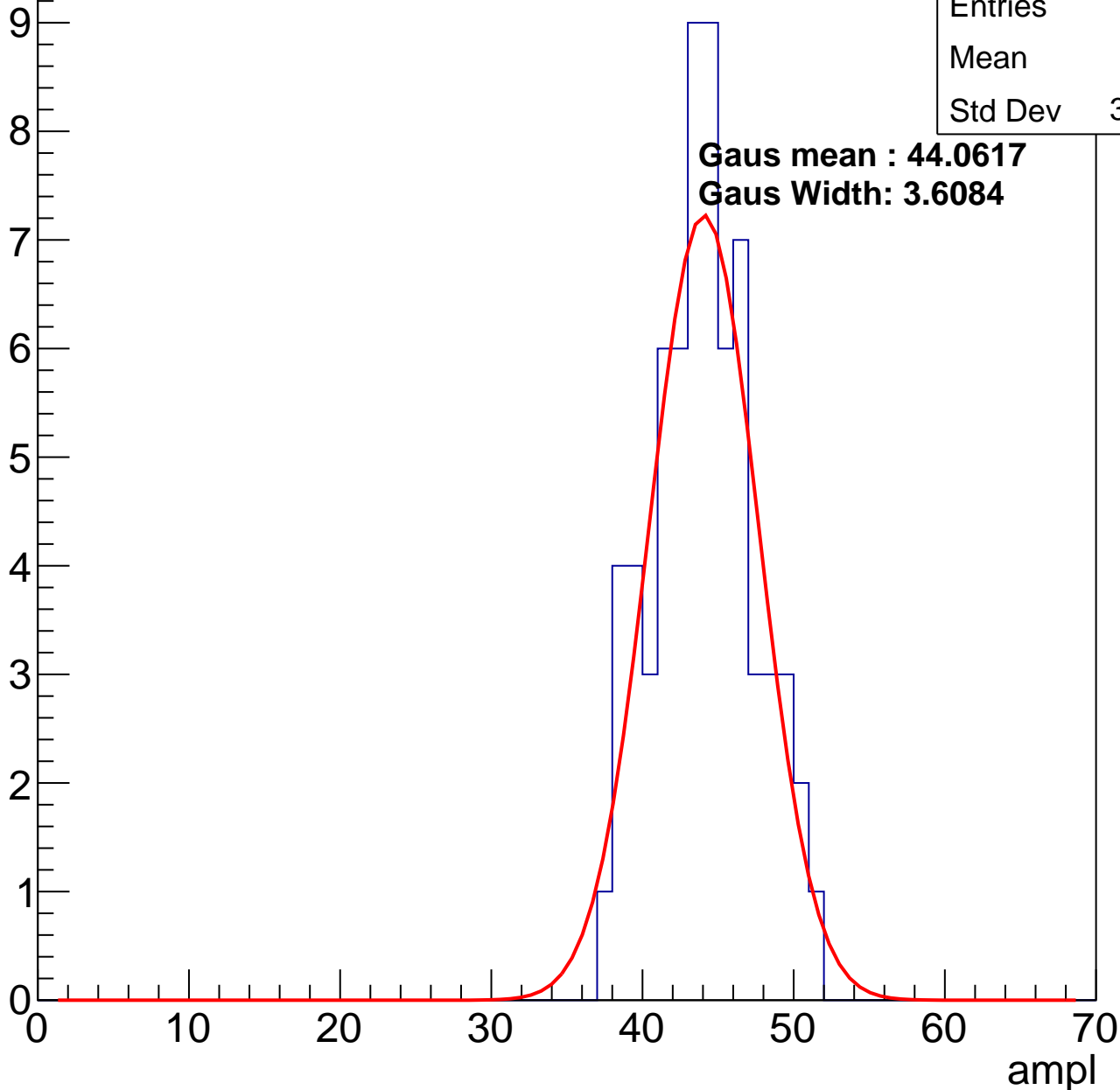
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	43.6
Std Dev	3.283

Gaus mean : 44.0617

Gaus Width: 3.6084

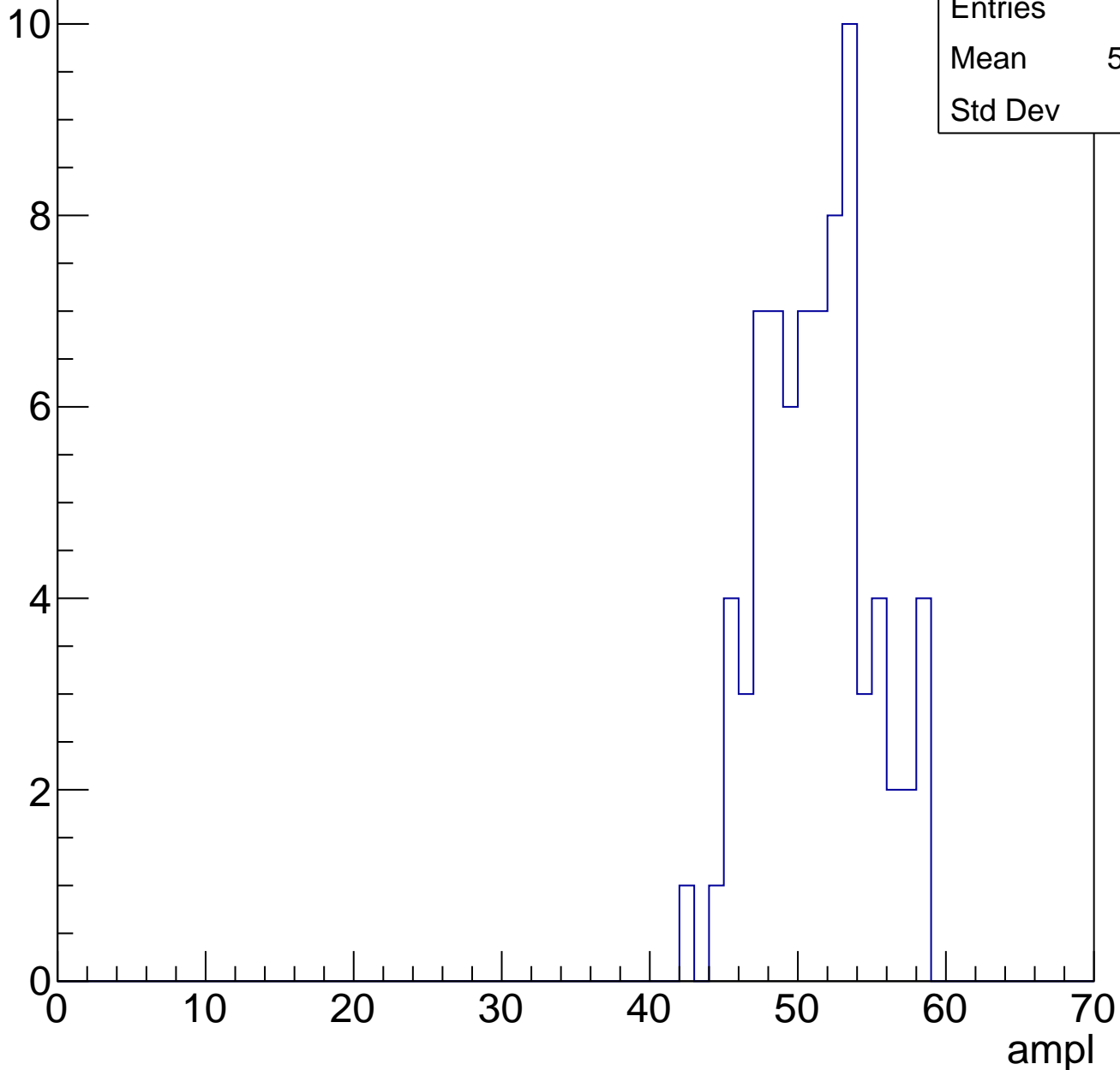


B0L002S, U18-ch43, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	50.74
Std Dev	3.64

Entry

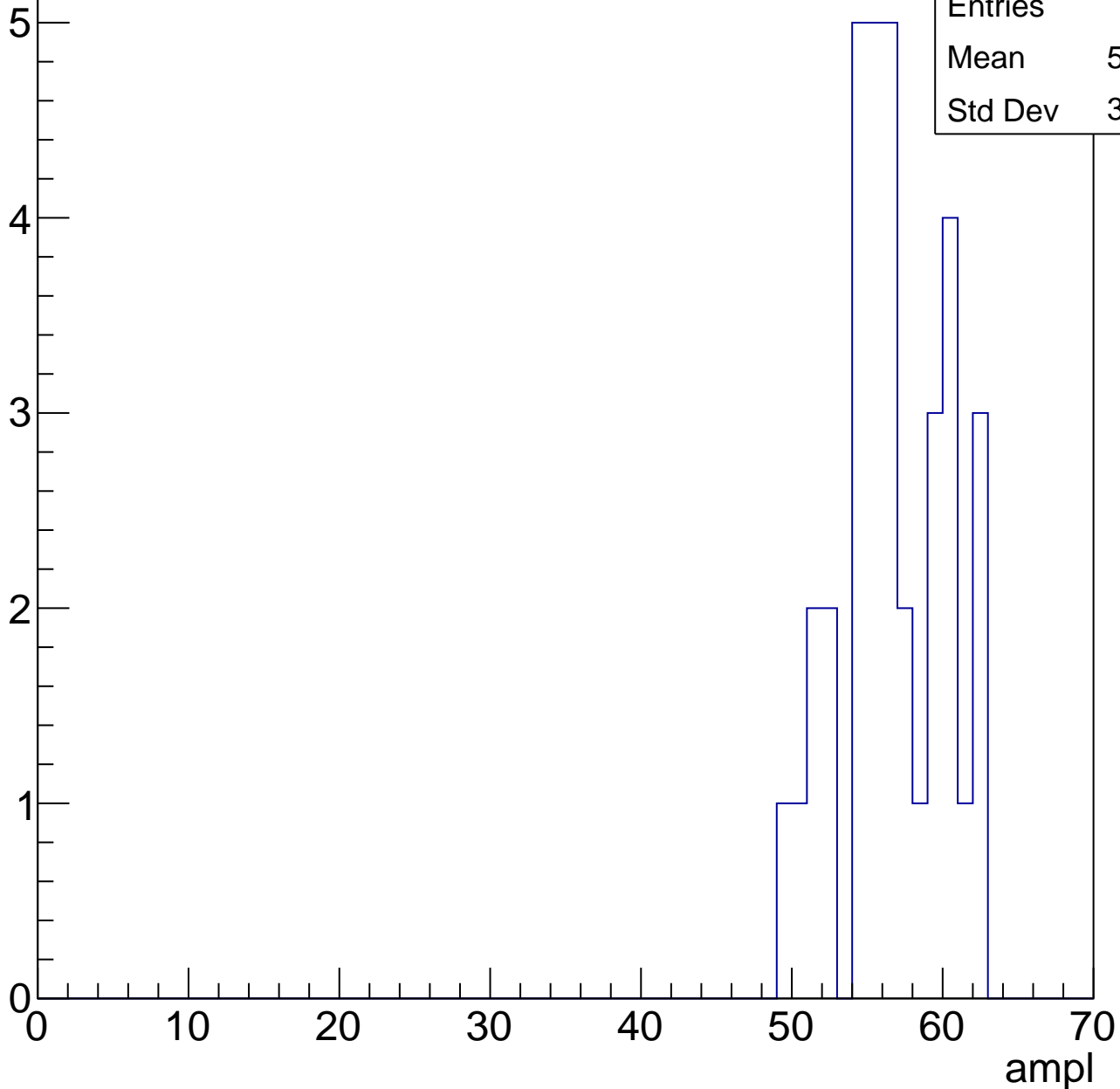


B0L002S, U18-ch43, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	56.17
Std Dev	3.468

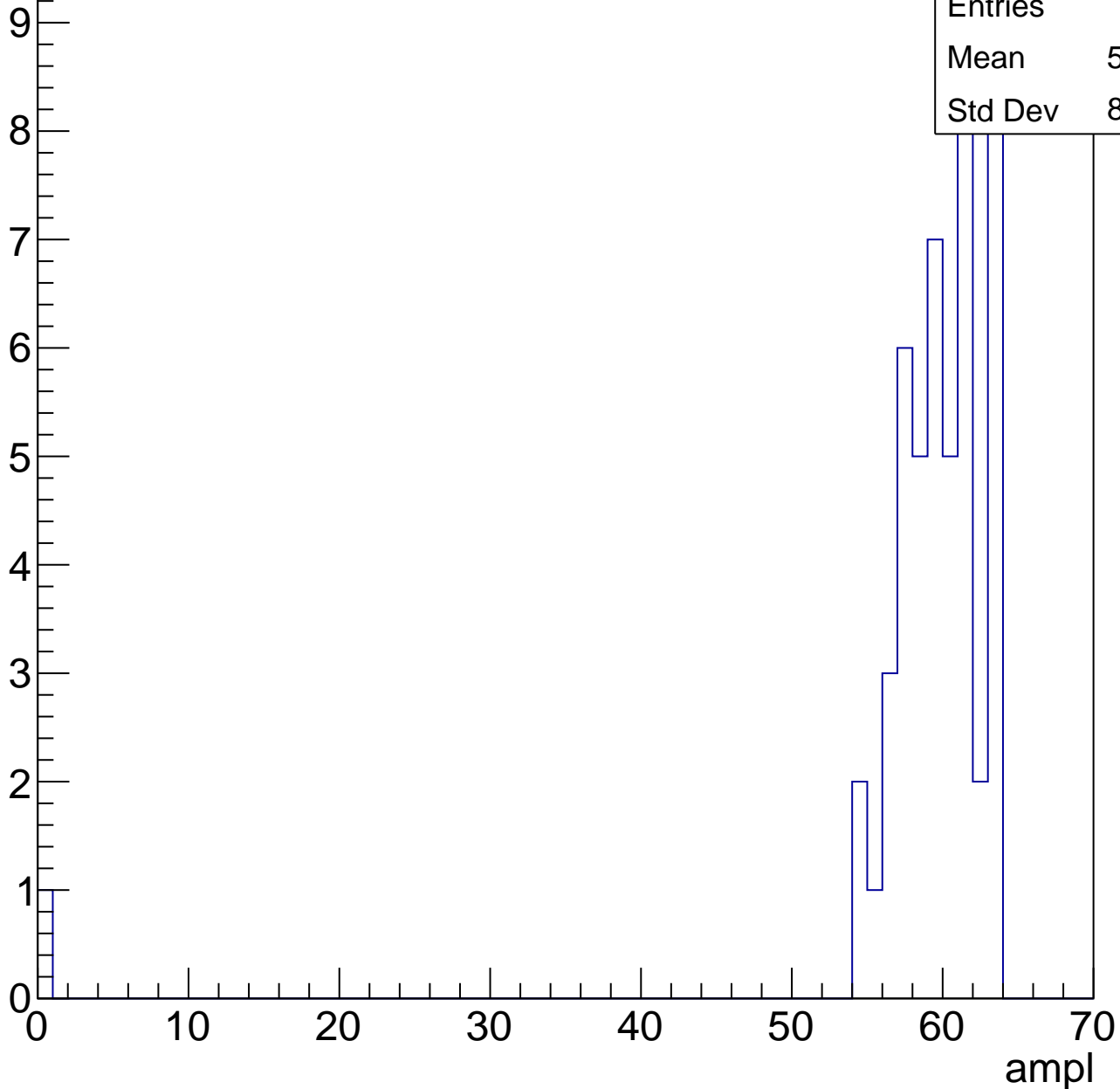


B0L002S, U18-ch43, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.27
Std Dev	8.778

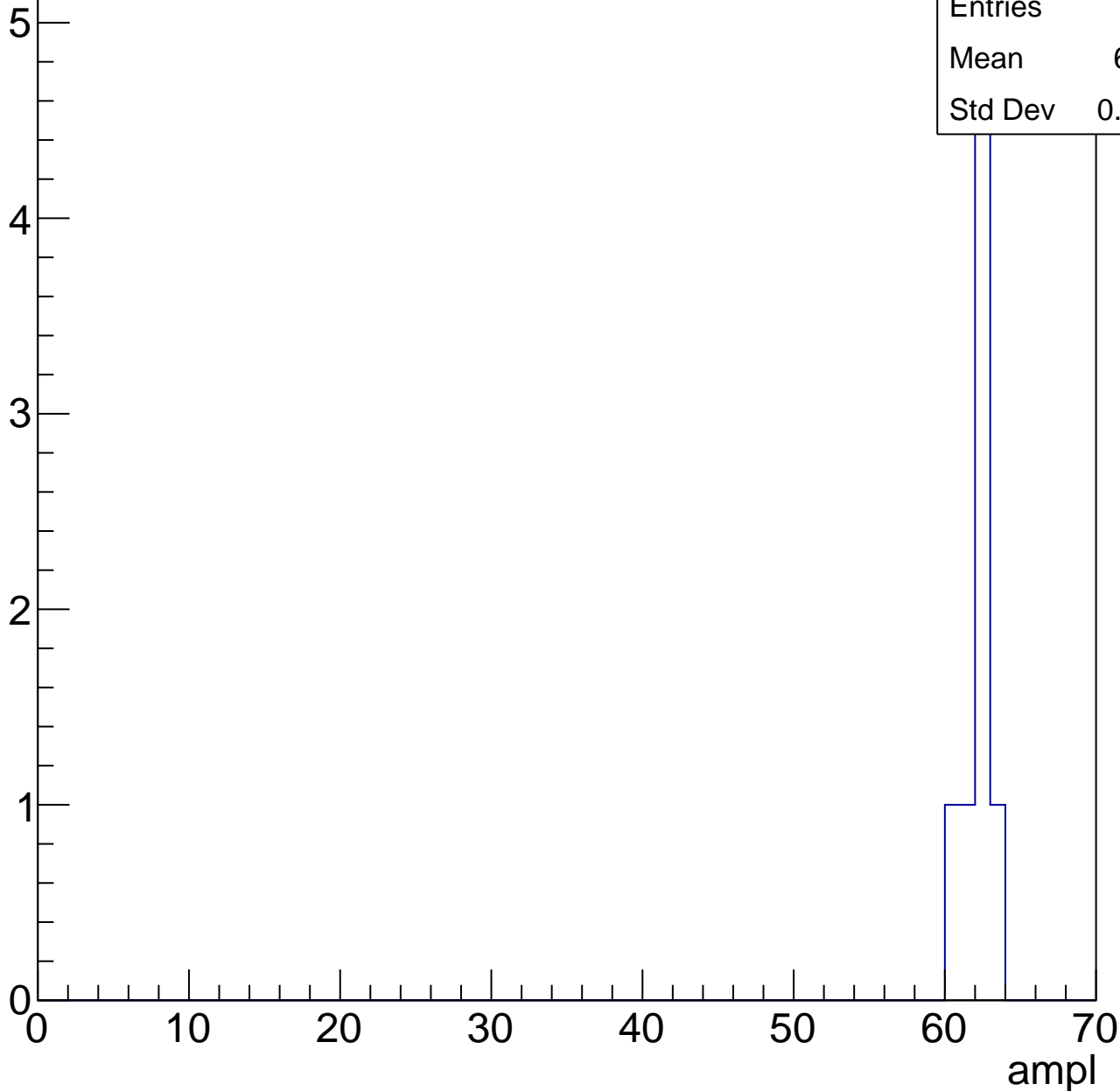


B0L002S, U18-ch43, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	61.75
Std Dev	0.8292



B0L002S, U18-ch43, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6.667
Std Dev	9.428

B0L002S, U18-ch44, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	27.91
Std Dev	6.799

Gaus mean : 29.8269

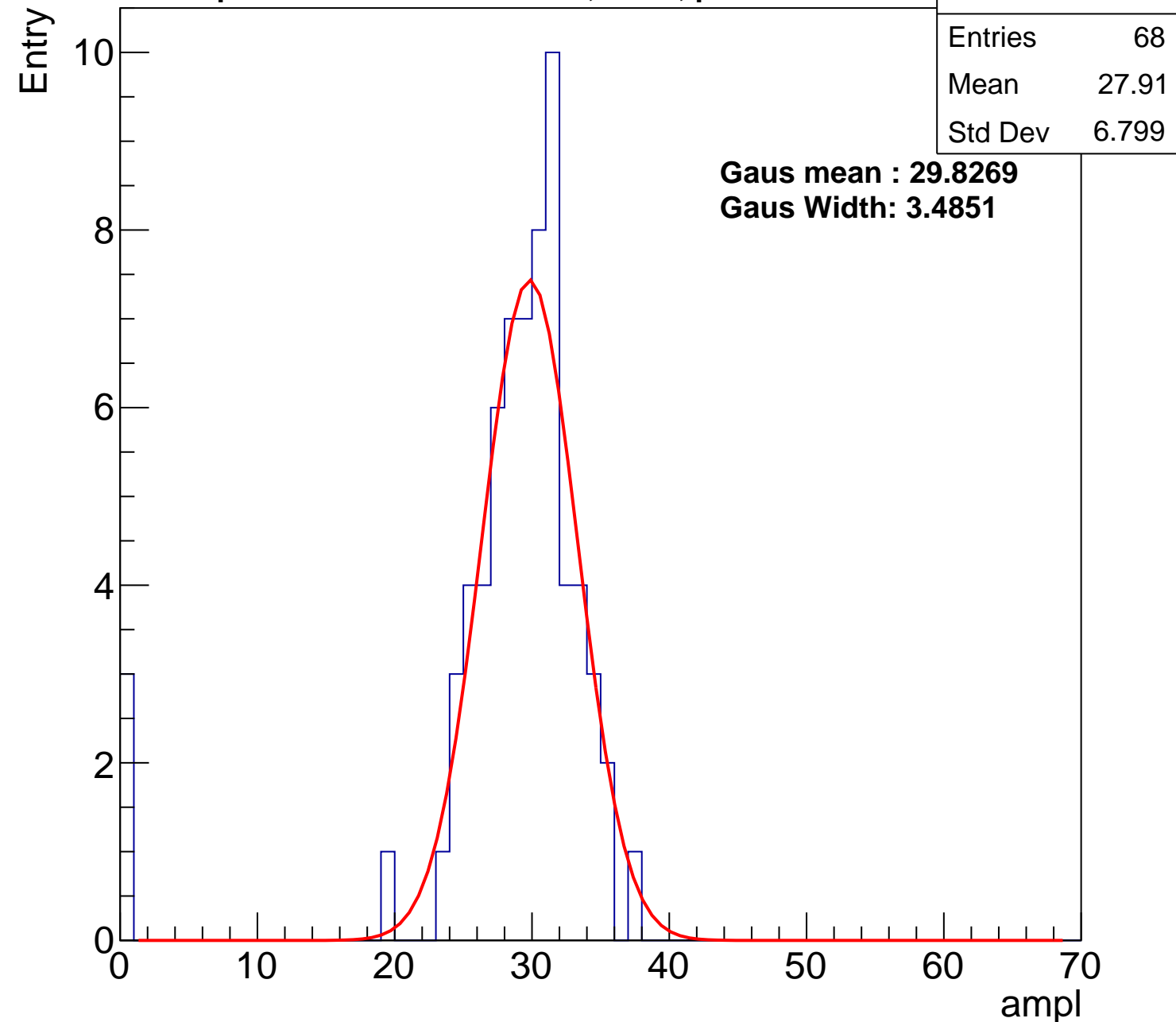
Gaus Width: 3.4851

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch44, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	76
Mean	36.72
Std Dev	3.214

Gaus mean : 36.6819

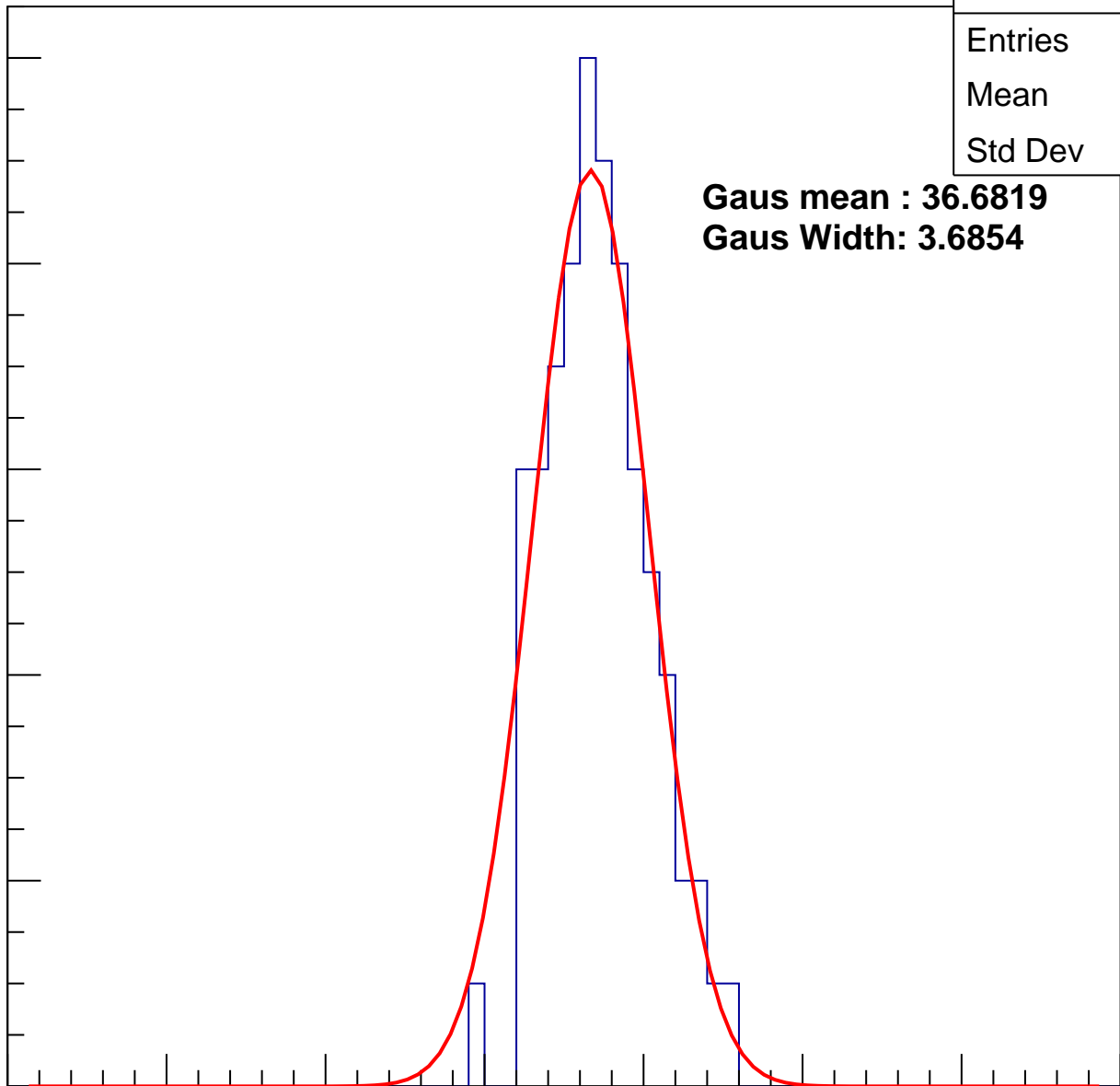
Gaus Width: 3.6854

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch44, adc2

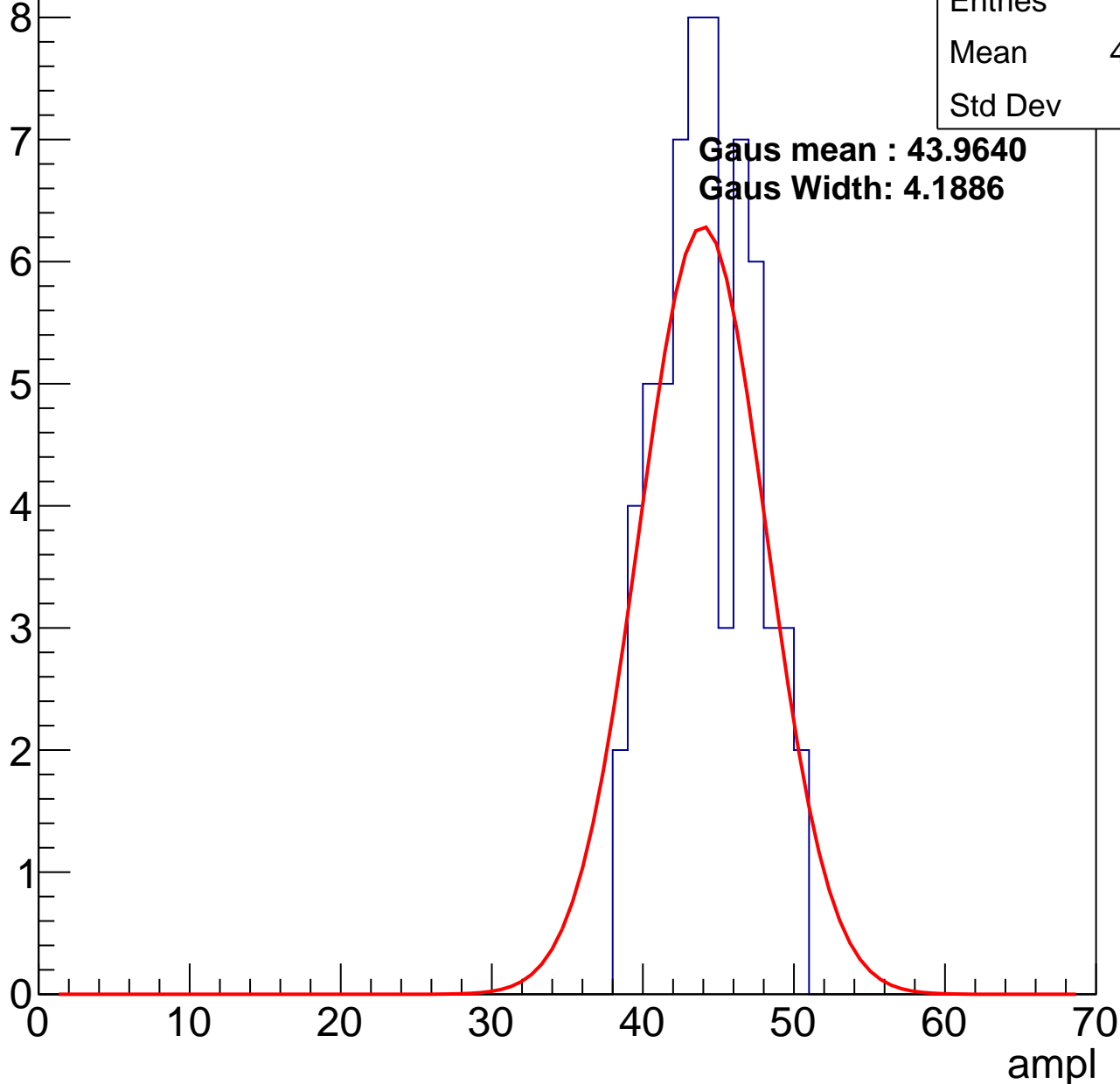
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.76
Std Dev	3.11

Gaus mean : 43.9640

Gaus Width: 4.1886

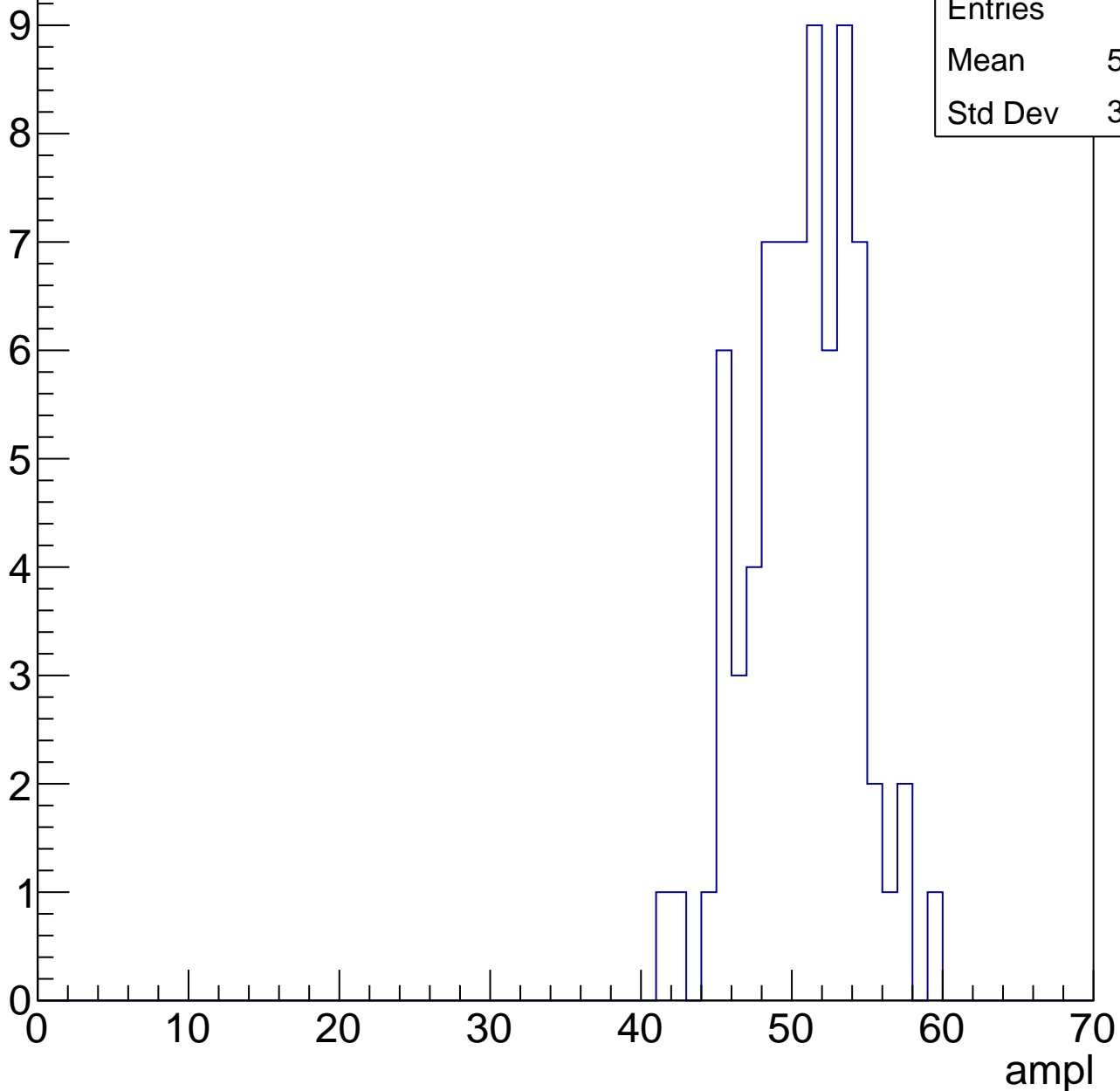


B0L002S, U18-ch44, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.23
Std Dev	3.559

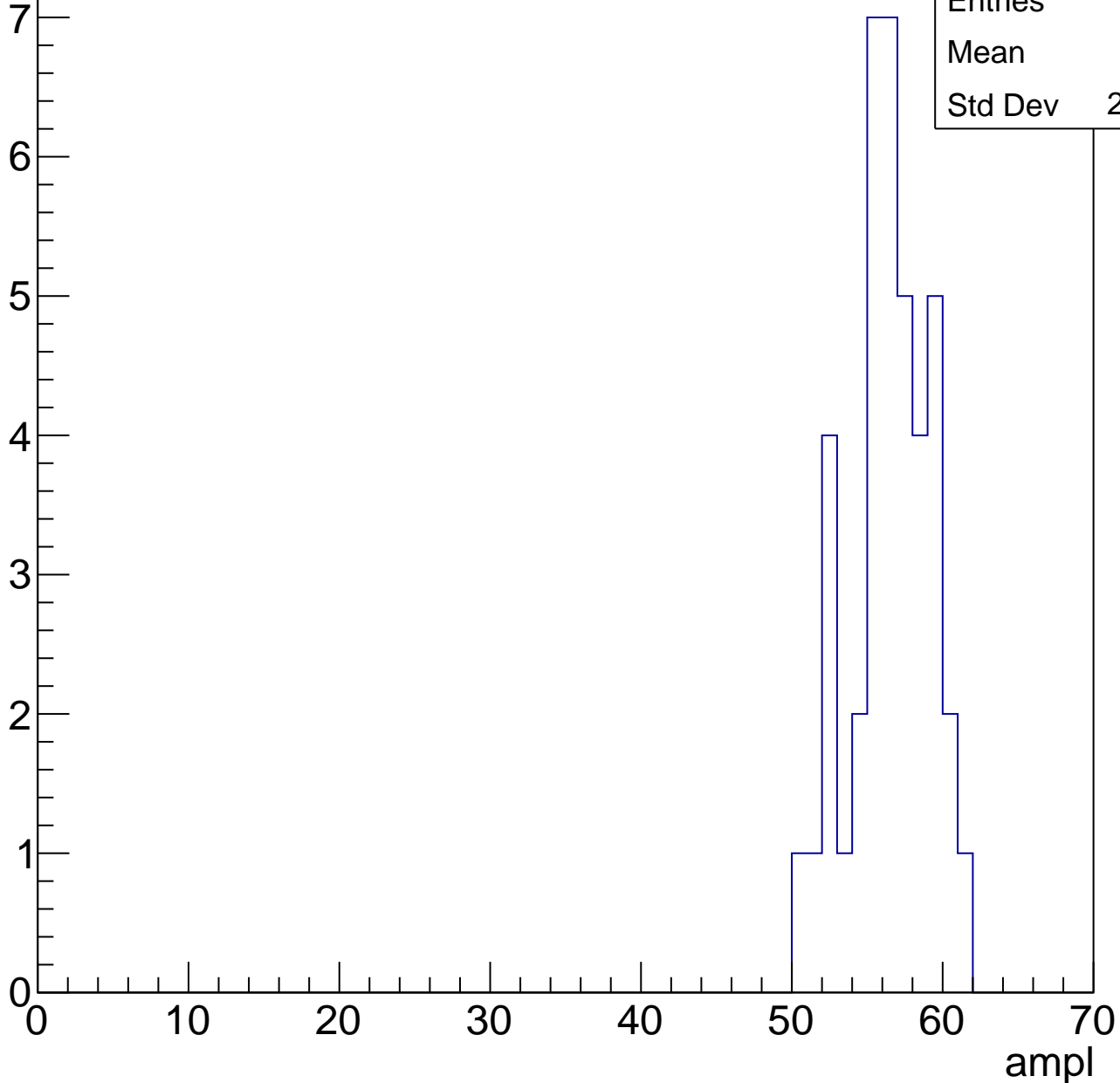


B0L002S, U18-ch44, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

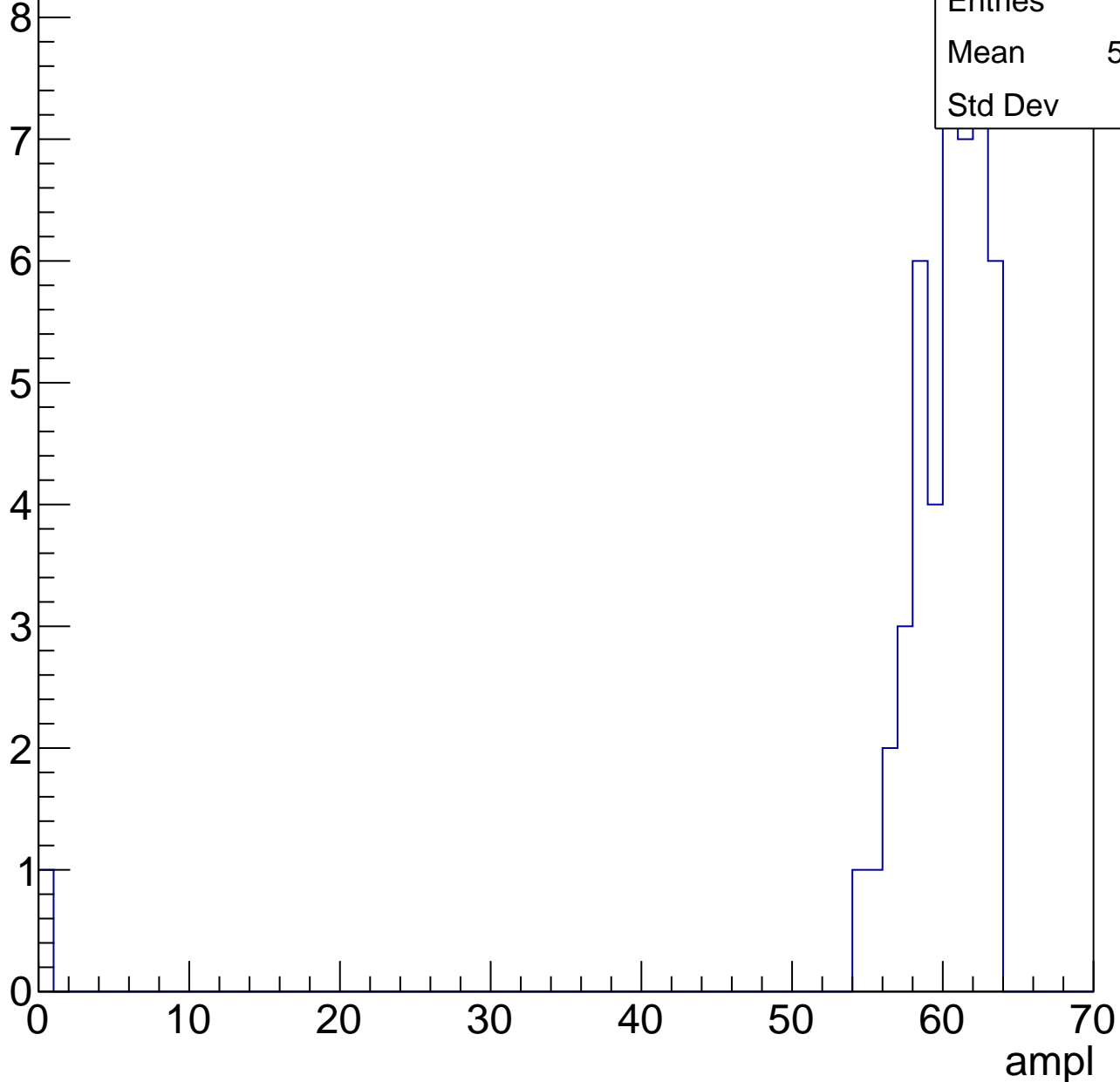
Entries	40
Mean	56
Std Dev	2.608



B0L002S, U18-ch44, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch44, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch44, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch45, adc0

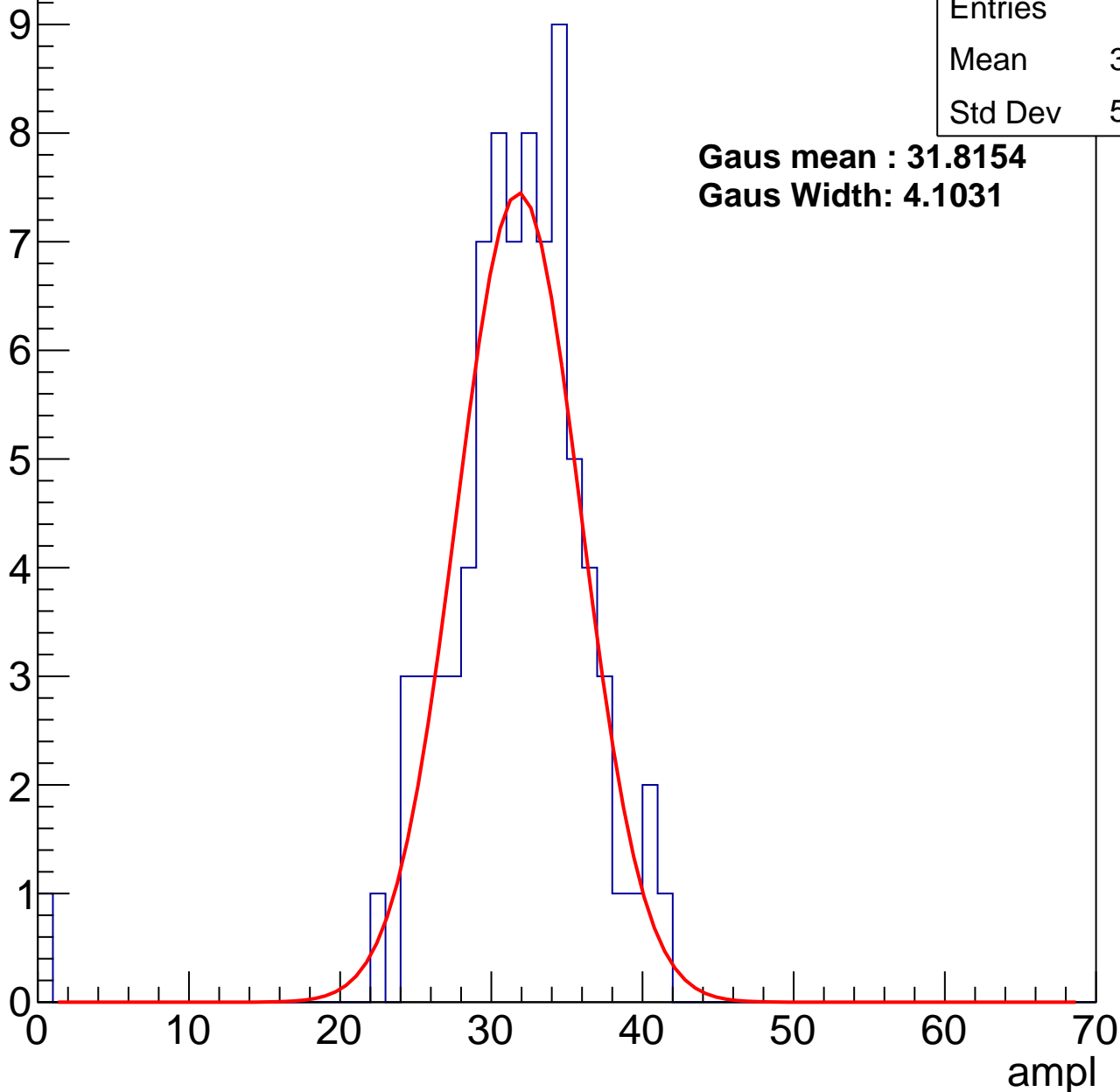
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.12
Std Dev	5.295

Gaus mean : 31.8154

Gaus Width: 4.1031



B0L002S, U18-ch45, adc1

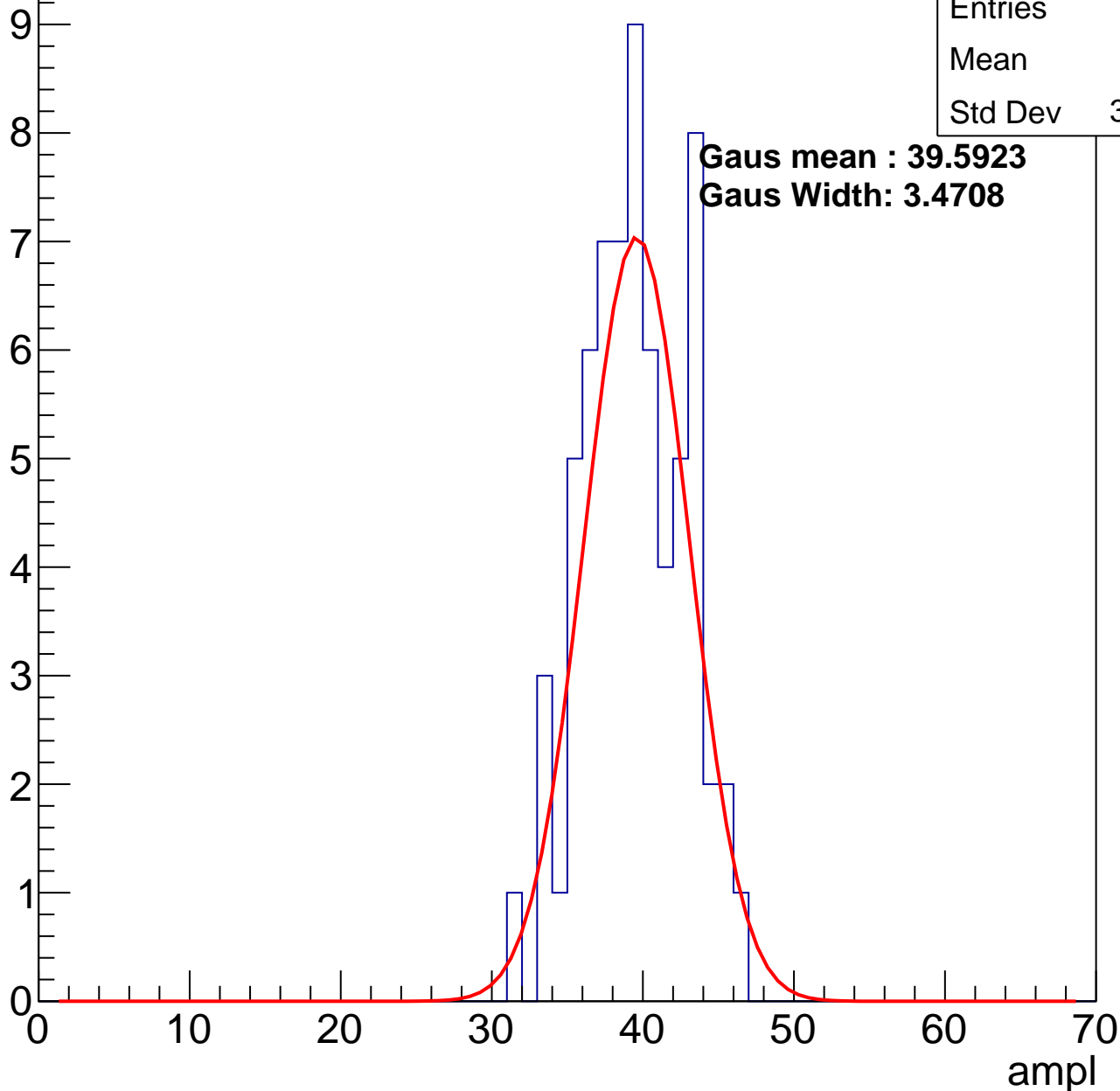
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	39
Std Dev	3.305

Gaus mean : 39.5923

Gaus Width: 3.4708



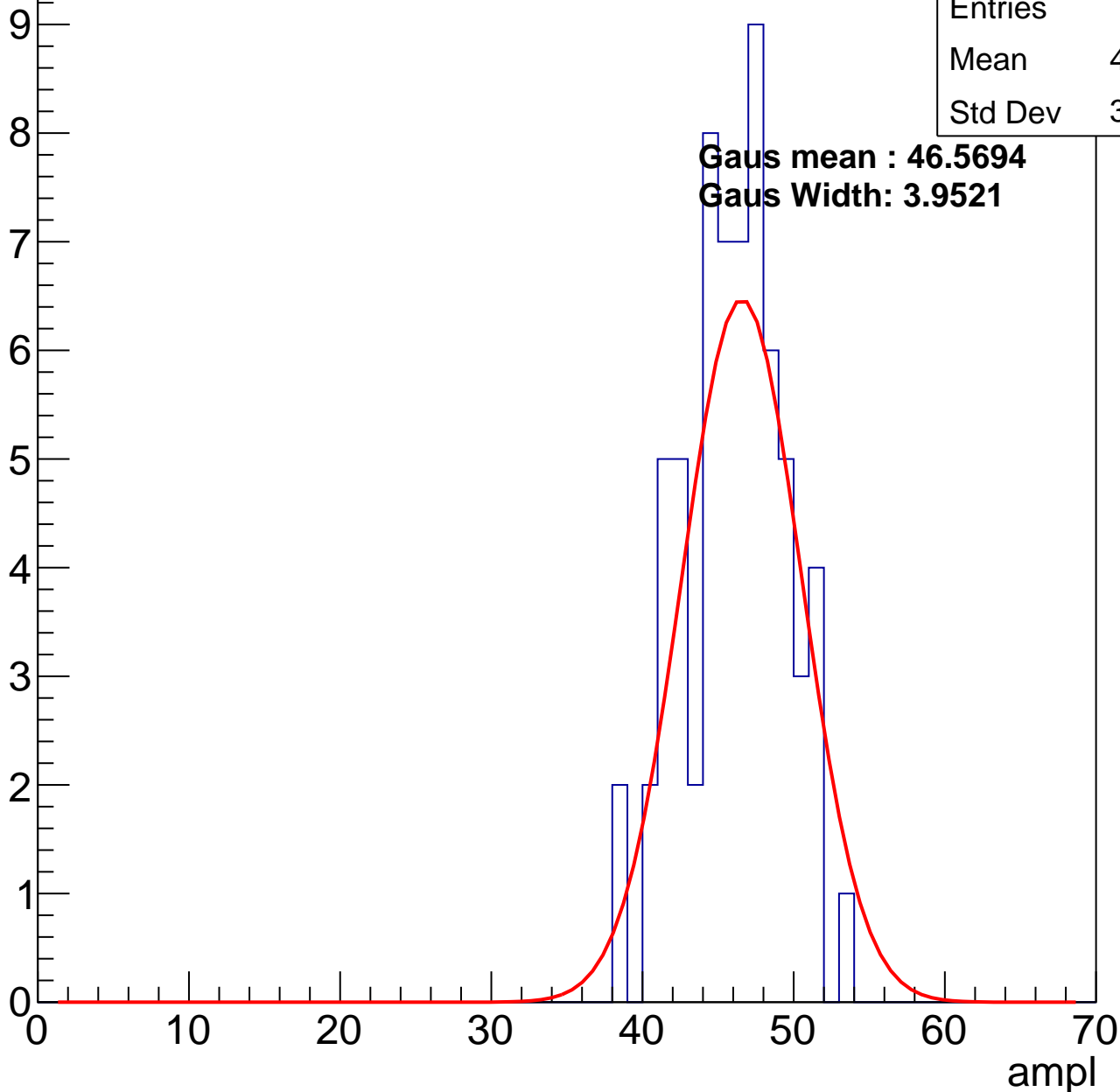
B0L002S, U18-ch45, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	45.59
Std Dev	3.317

Gaus mean : 46.5694
Gaus Width: 3.9521

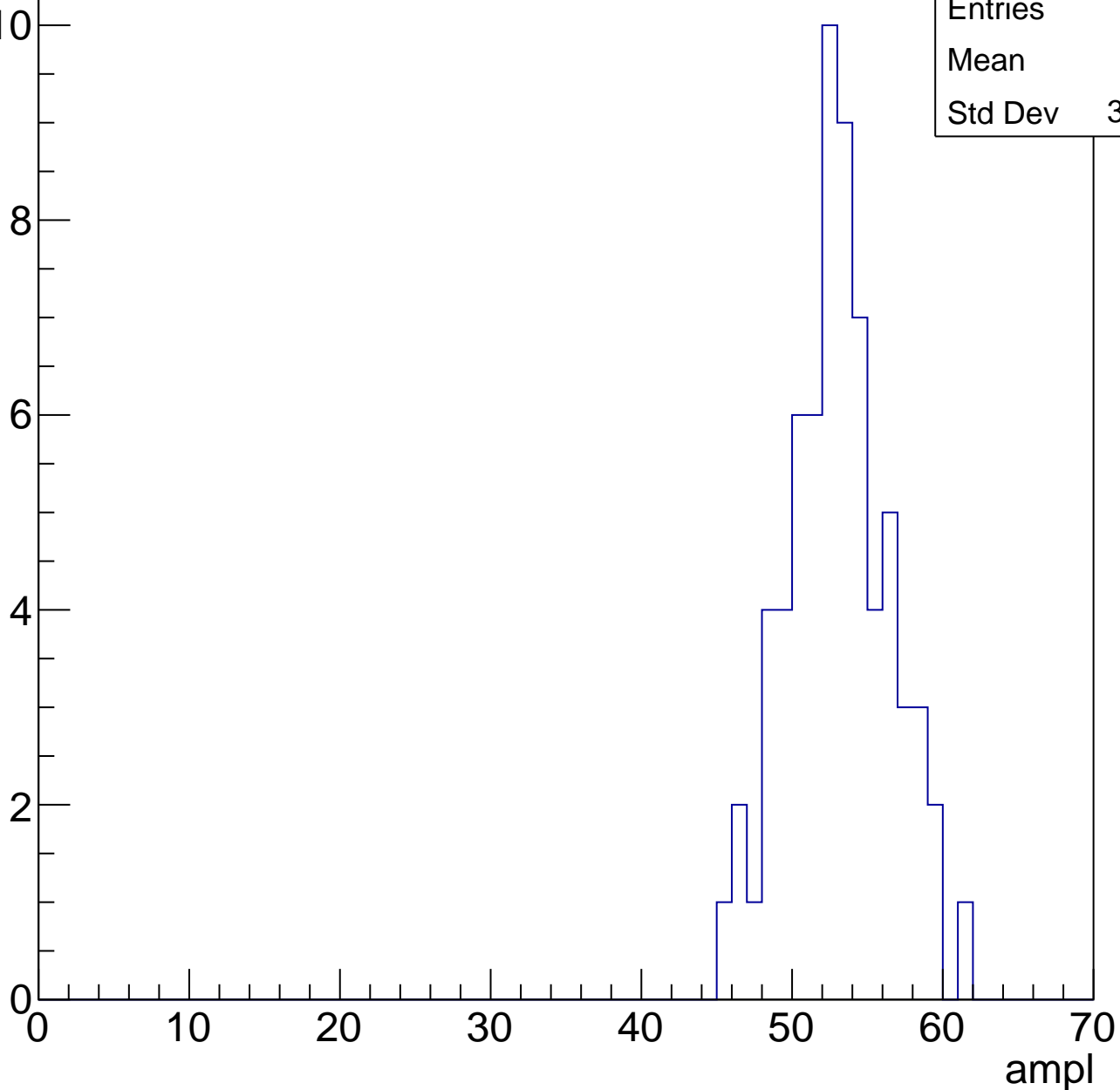


B0L002S, U18-ch45, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	52.6
Std Dev	3.357



B0L002S, U18-ch45, adc4

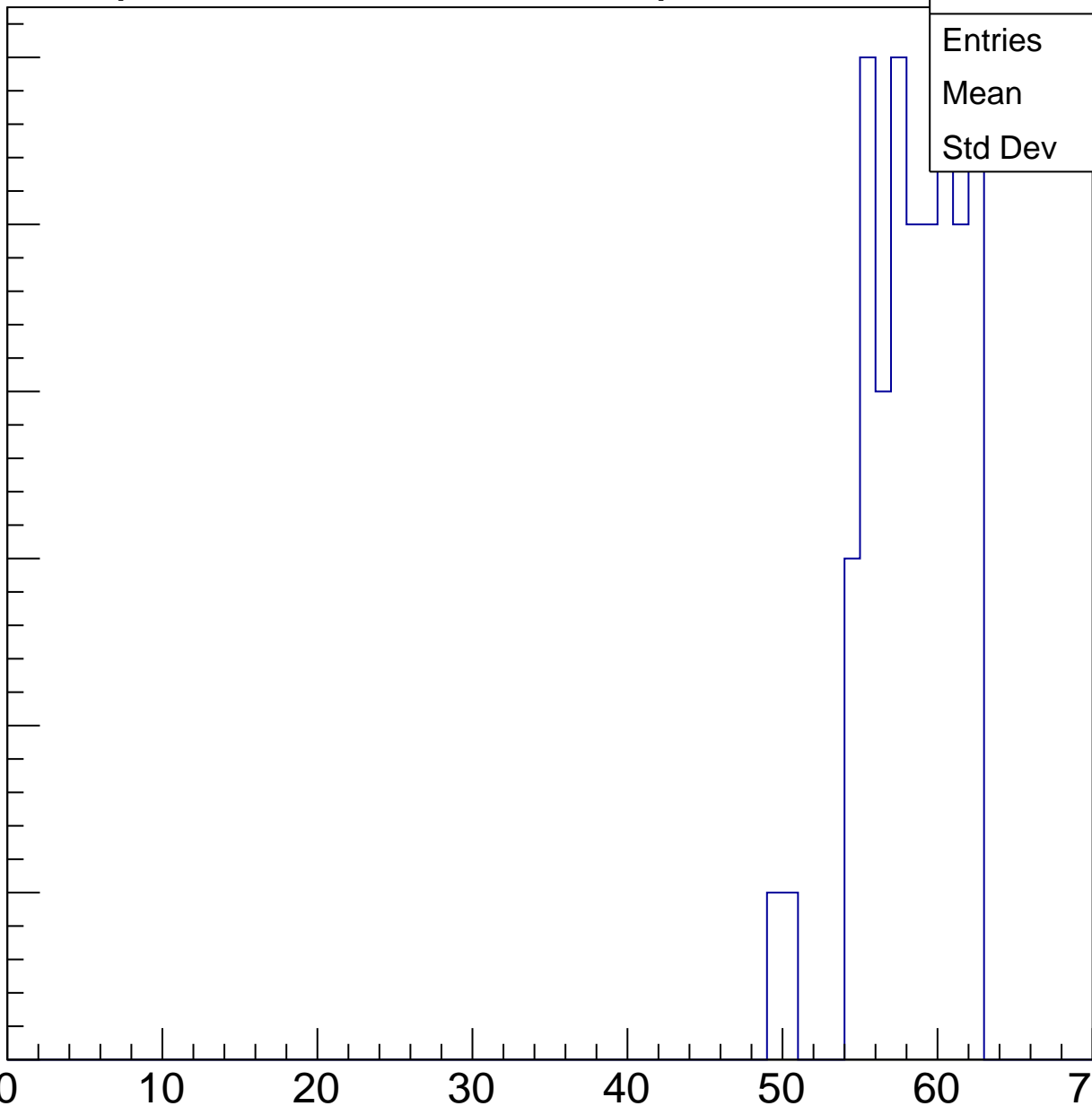
calib_packv5_042523_0143.root, FC#8, port C1

Entry

6
5
4
3
2
1
0

Entries	48
Mean	57.9
Std Dev	3.022

ampl

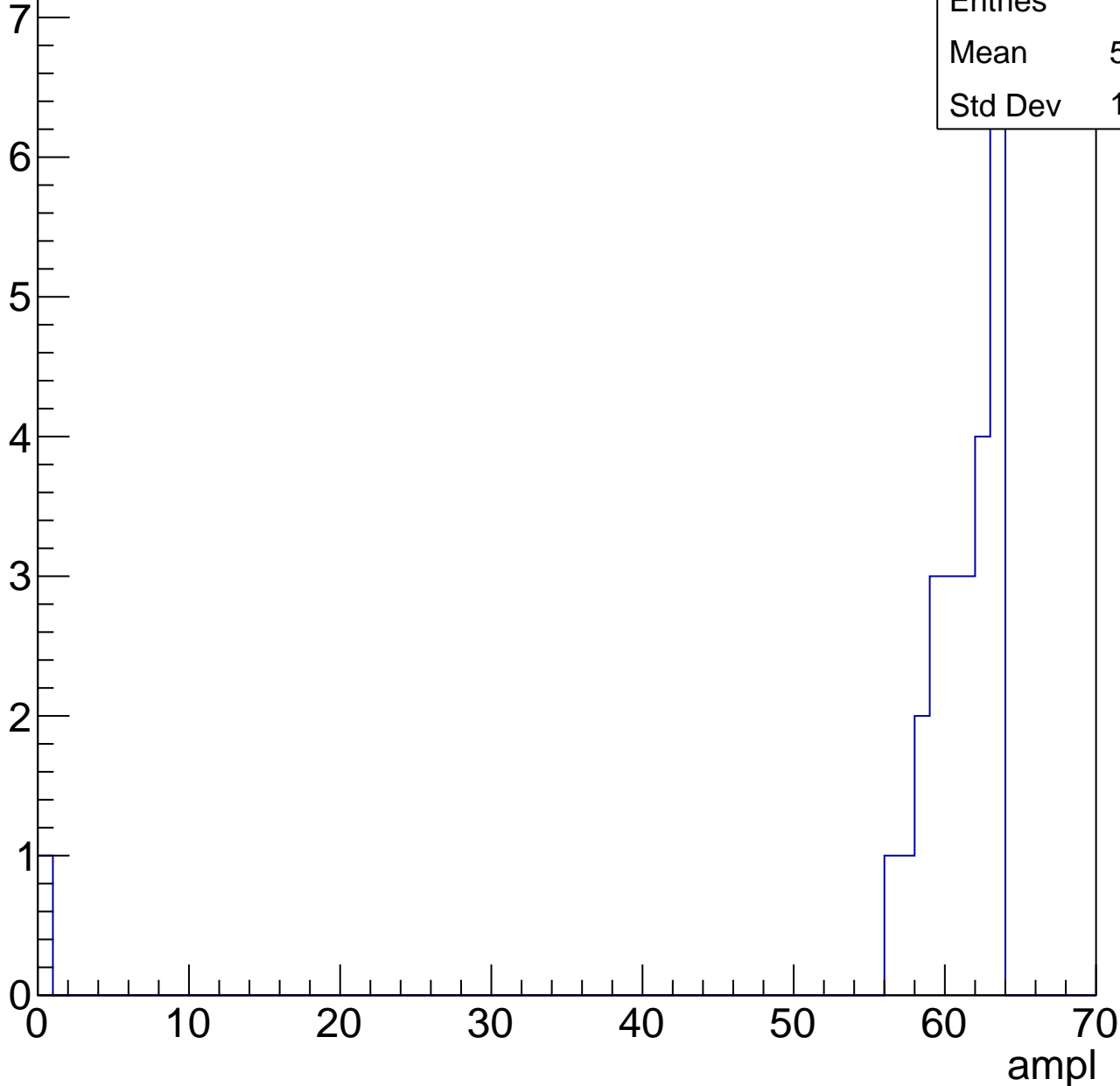


B0L002S, U18-ch45, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	25
Mean	58.32
Std Dev	12.08



B0L002S, U18-ch45, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch45, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	21
Std Dev	29.7

B0L002S, U18-ch46, adc0

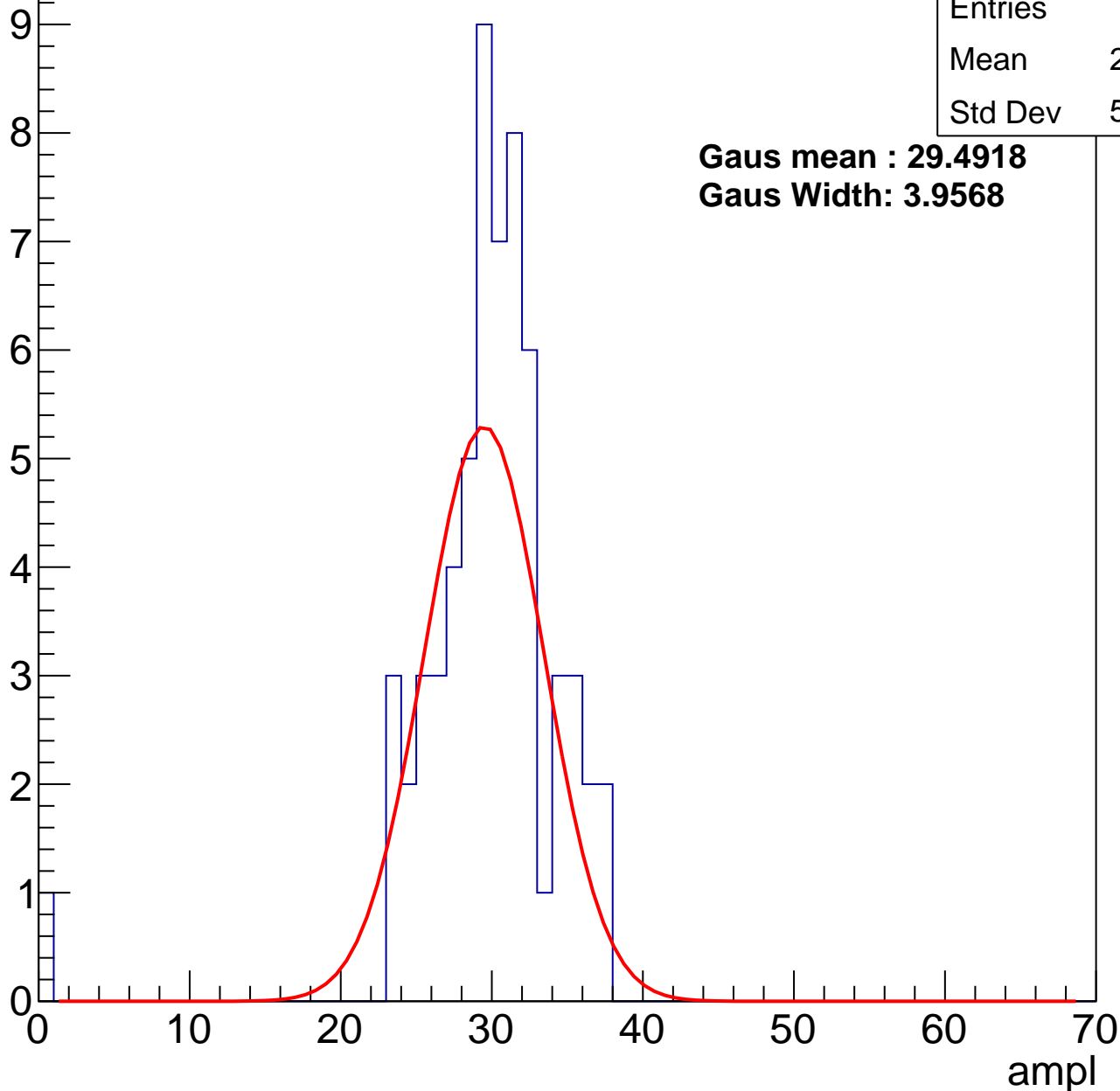
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	29.27
Std Dev	5.093

Gaus mean : 29.4918

Gaus Width: 3.9568



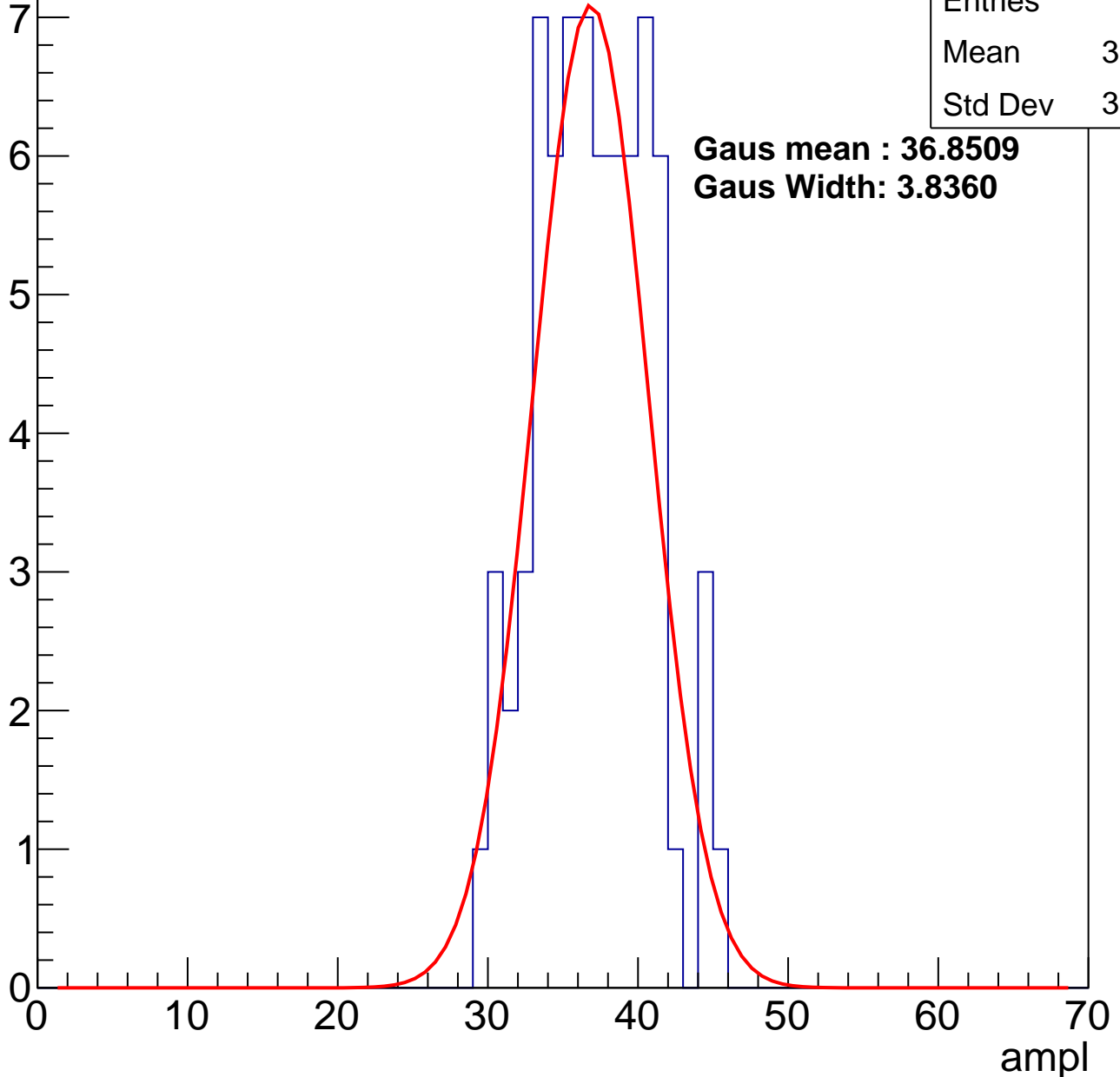
B0L002S, U18-ch46, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.64
Std Dev	3.679

Gaus mean : 36.8509
Gaus Width: 3.8360



B0L002S, U18-ch46, adc2

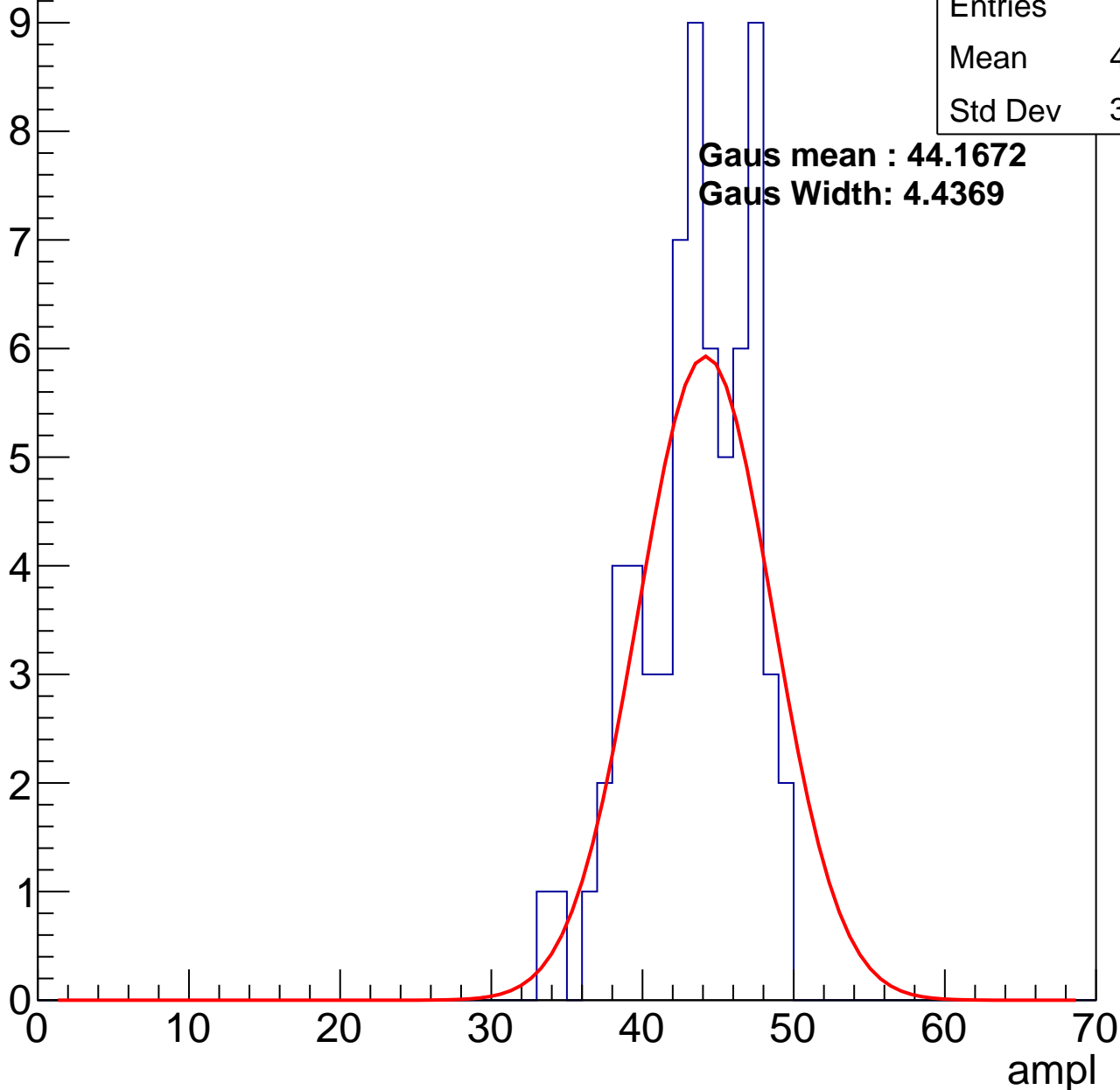
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	43.02
Std Dev	3.658

Gaus mean : 44.1672

Gaus Width: 4.4369

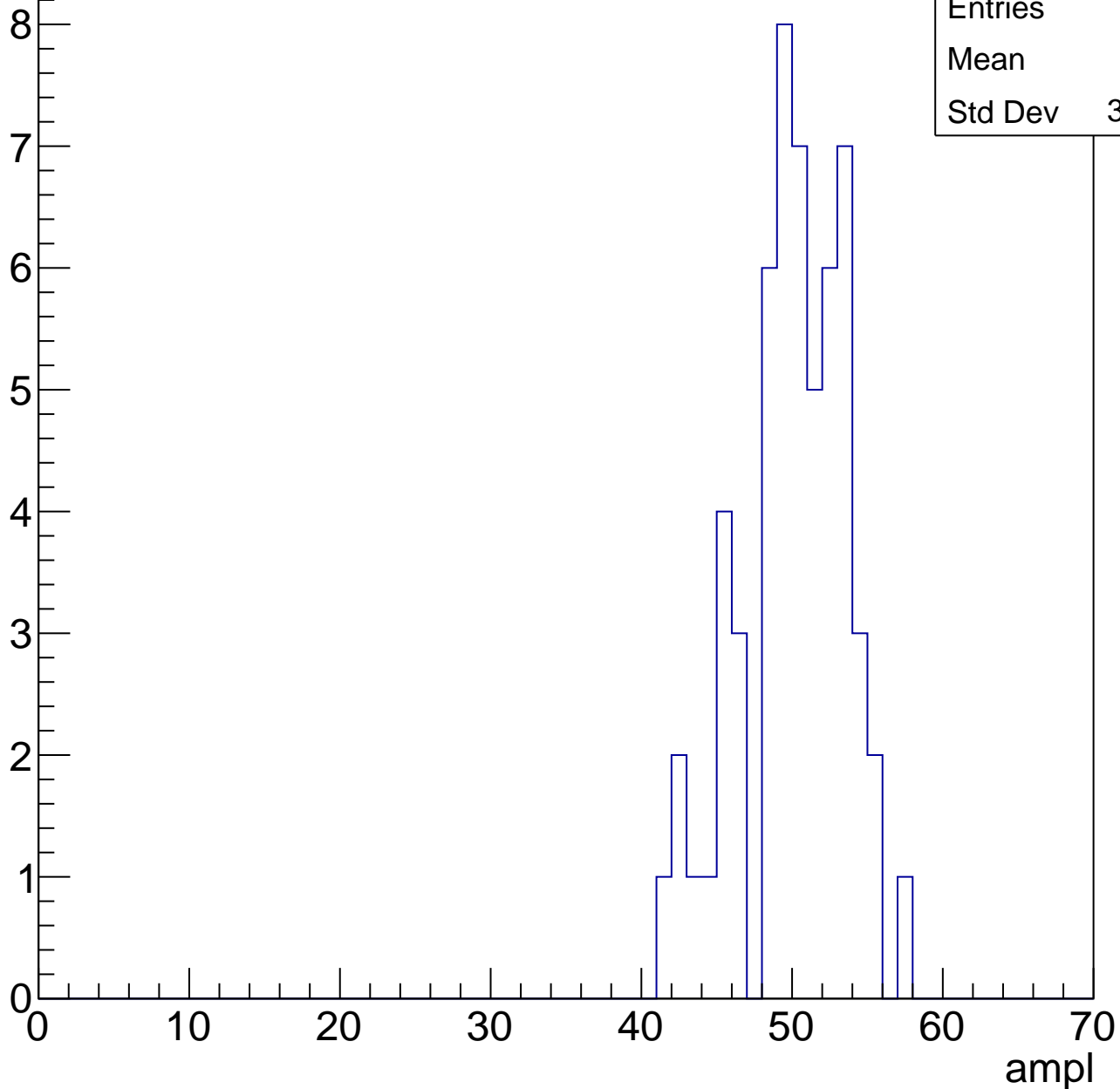


B0L002S, U18-ch46, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

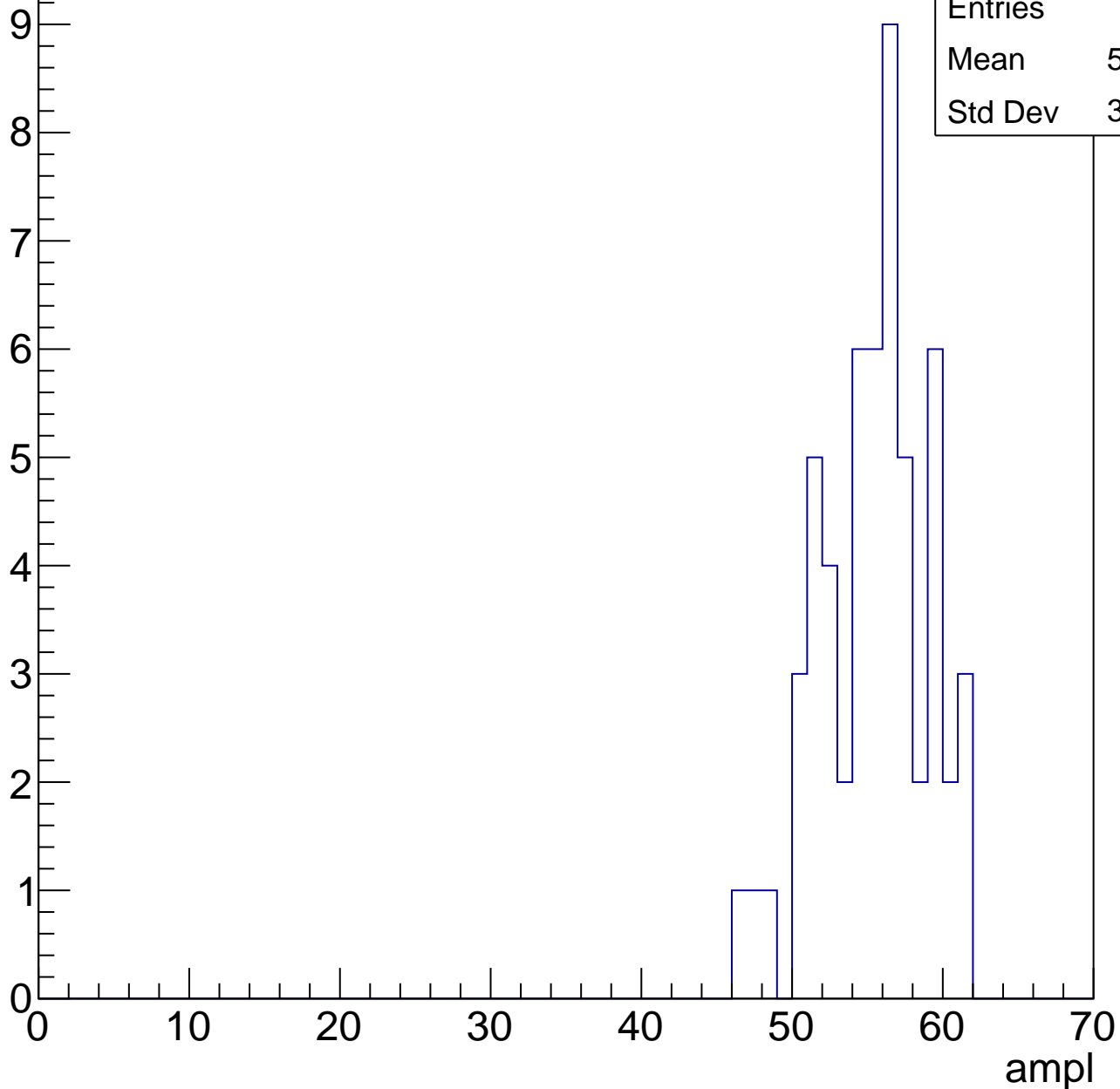
Entries	57
Mean	49.6
Std Dev	3.514



B0L002S, U18-ch46, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

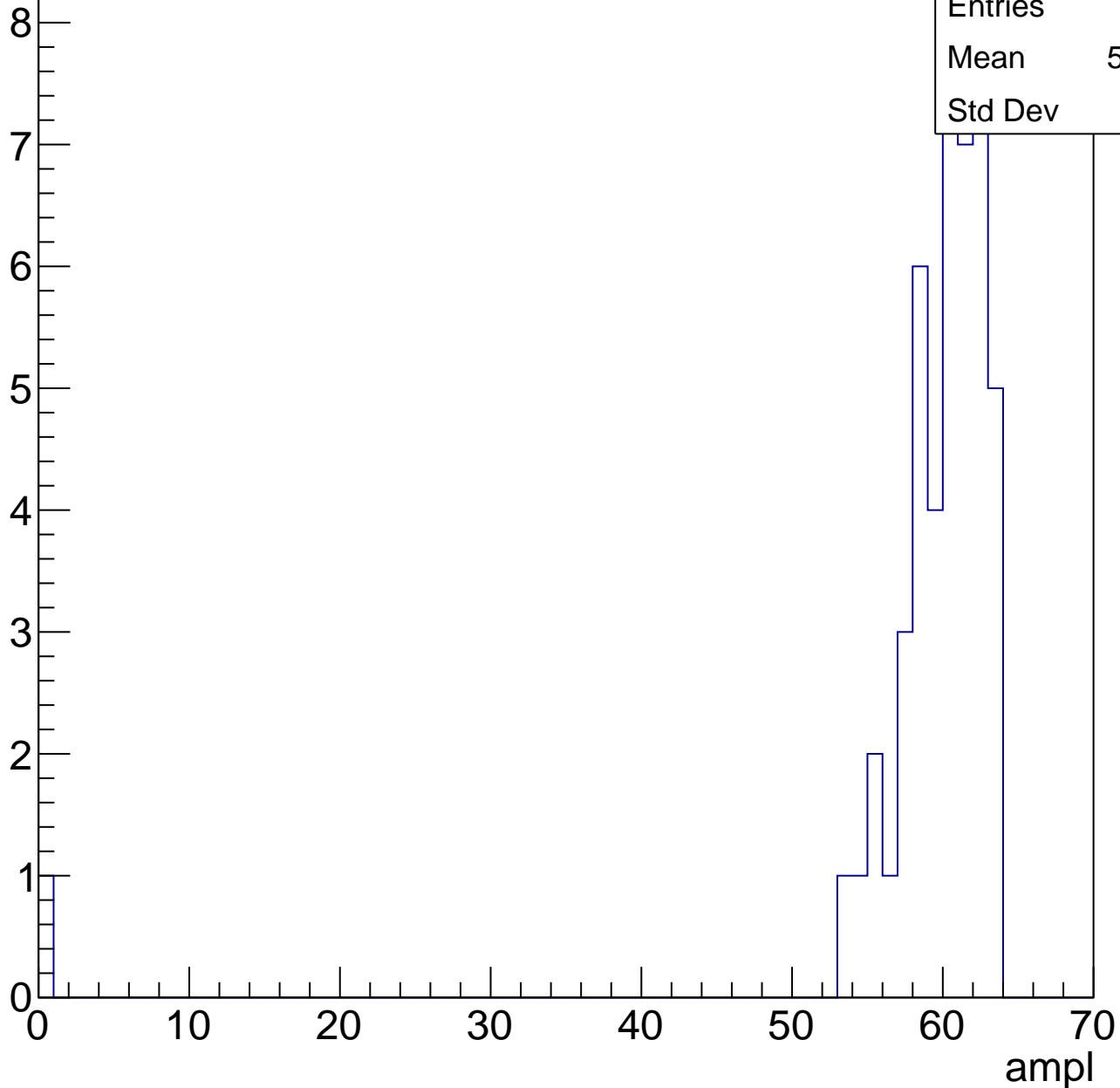


Entries	56
Mean	54.93
Std Dev	3.535

B0L002S, U18-ch46, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

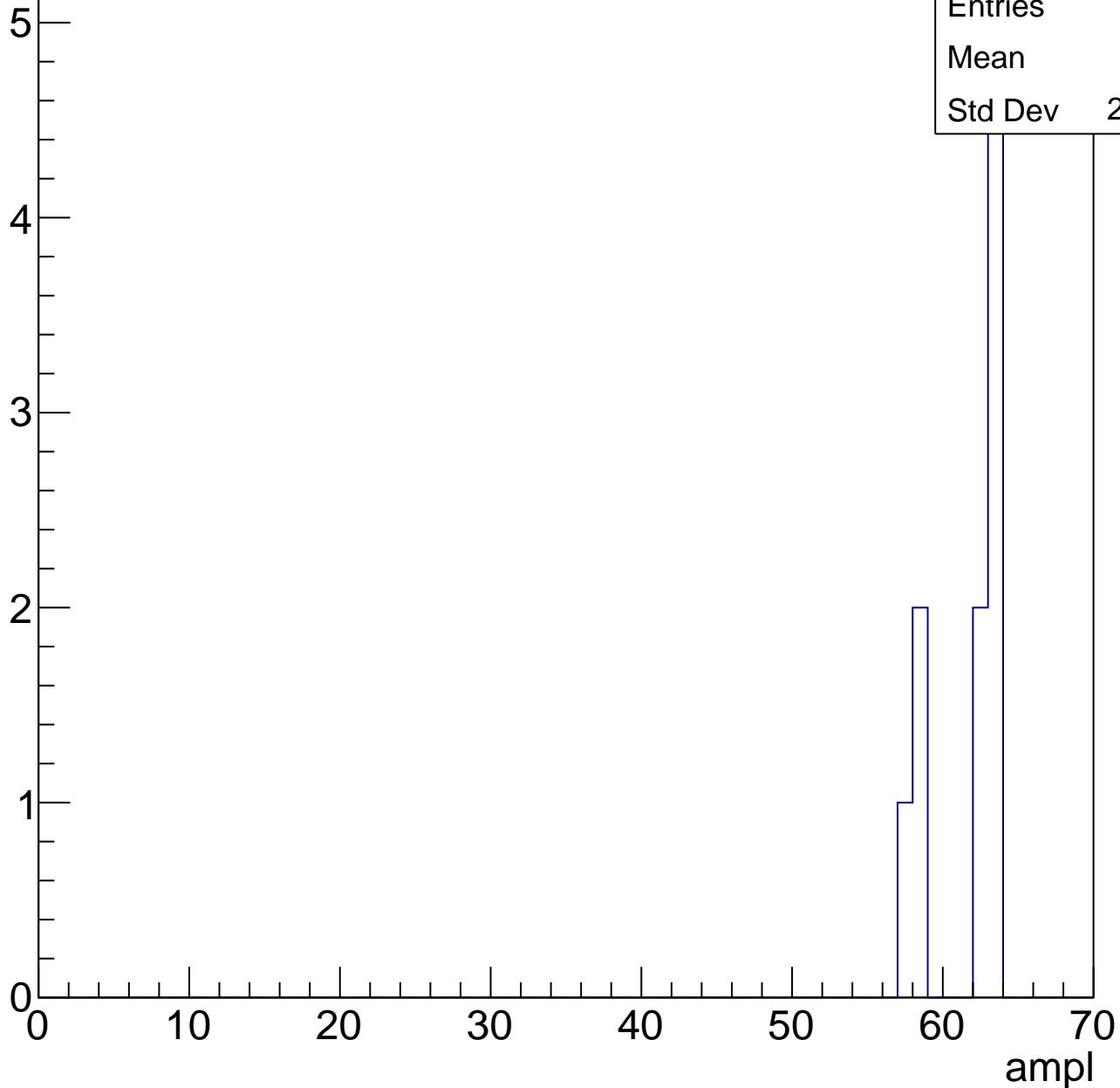


B0L002S, U18-ch46, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	10
Mean	61.2
Std Dev	2.358



B0L002S, U18-ch46, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch47, adc0

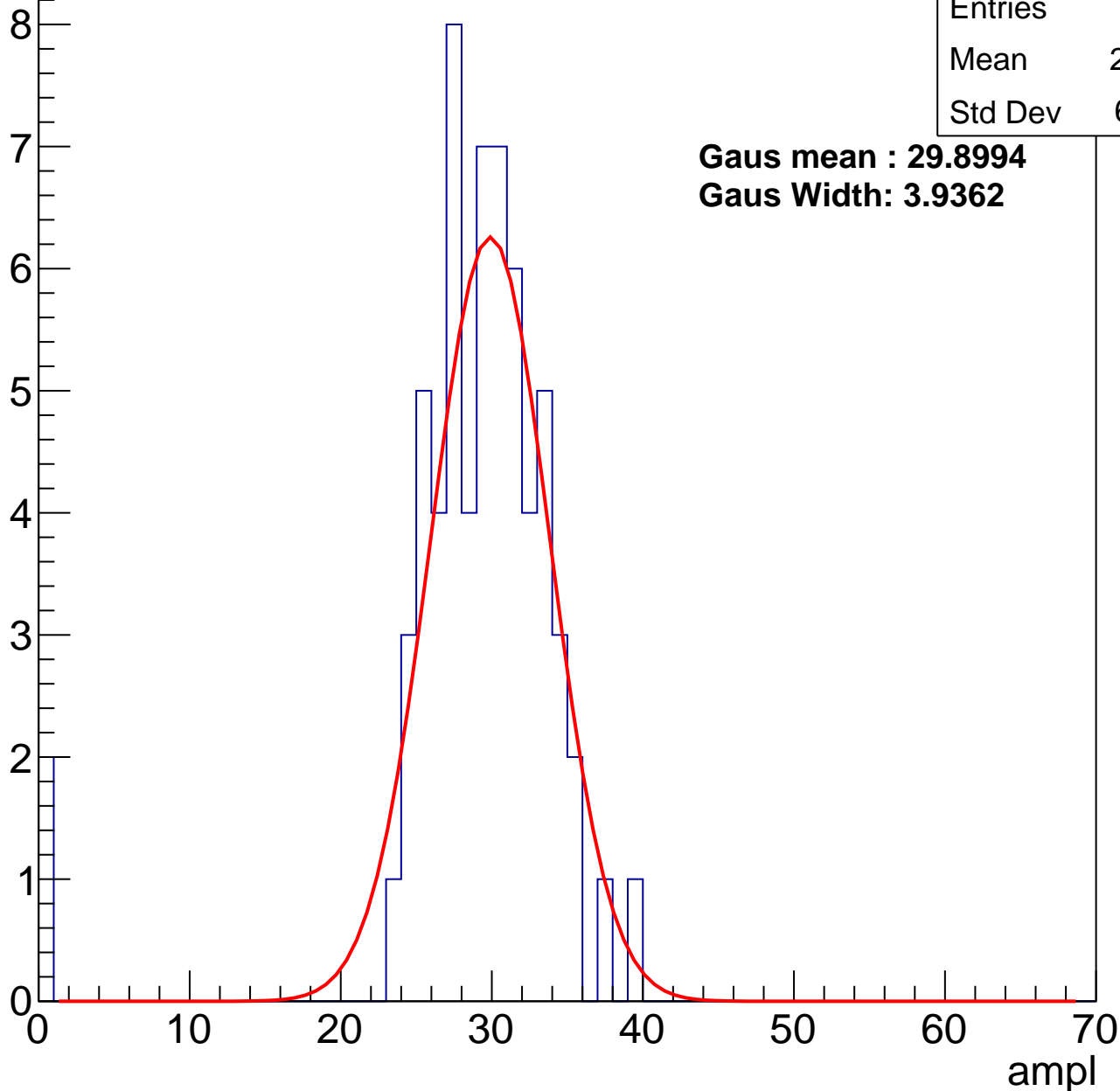
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	28.44
Std Dev	6.151

Gaus mean : 29.8994

Gaus Width: 3.9362



B0L002S, U18-ch47, adc1

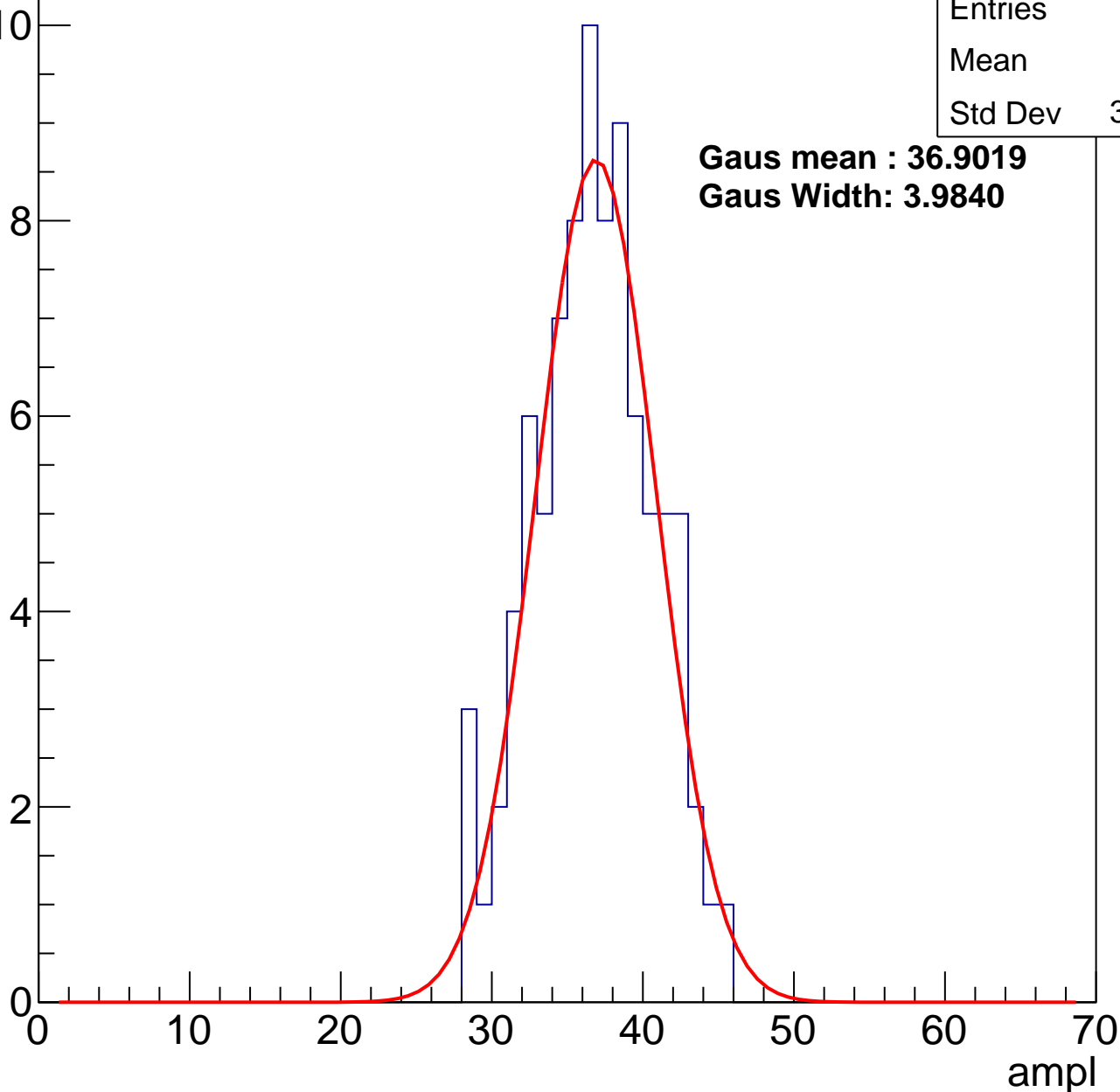
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	88
Mean	36.3
Std Dev	3.856

Gaus mean : 36.9019

Gaus Width: 3.9840



B0L002S, U18-ch47, adc2

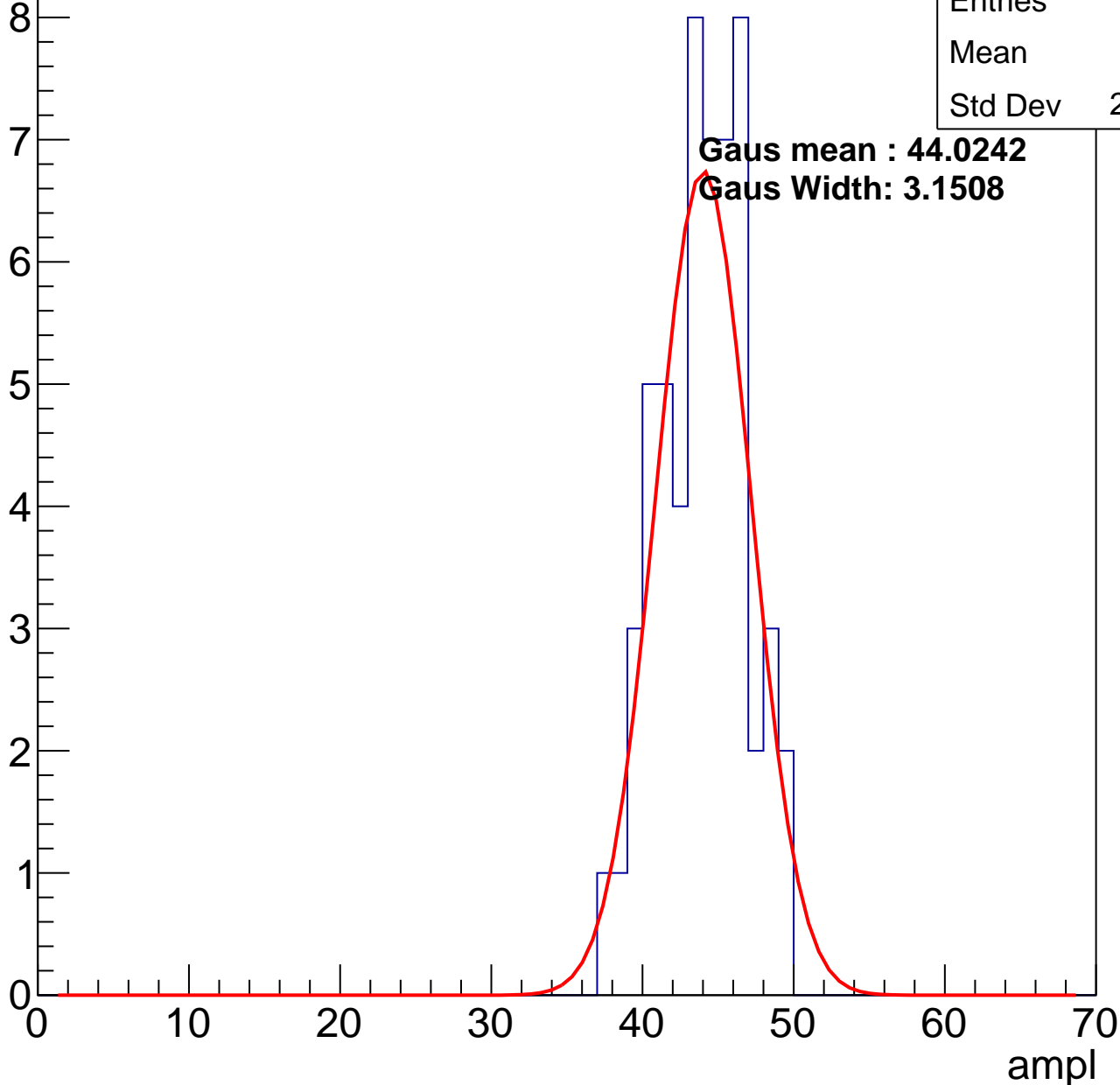
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.5
Std Dev	2.835

Gaus mean : 44.0242

Gaus Width: 3.1508

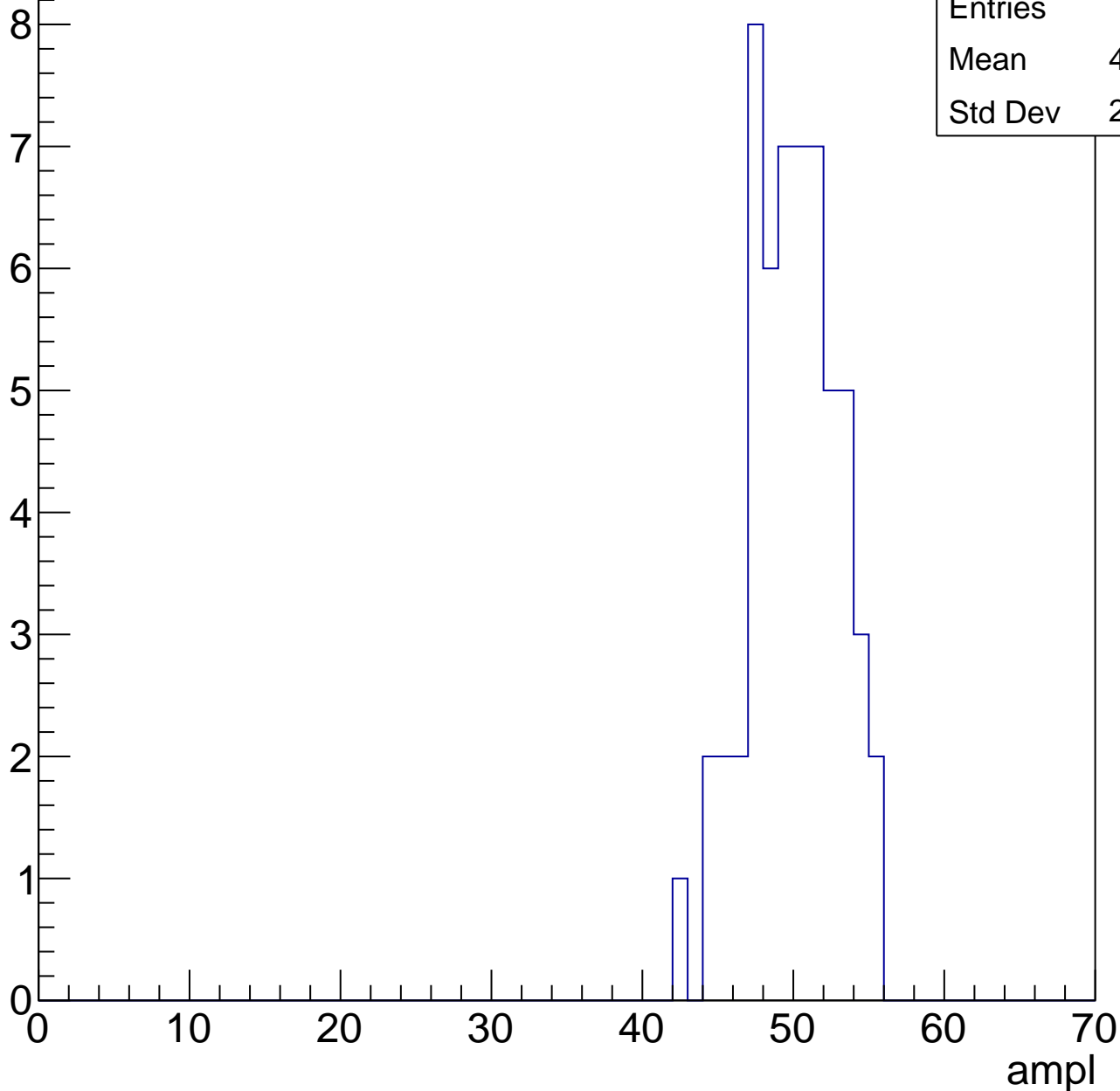


B0L002S, U18-ch47, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.53
Std Dev	2.896

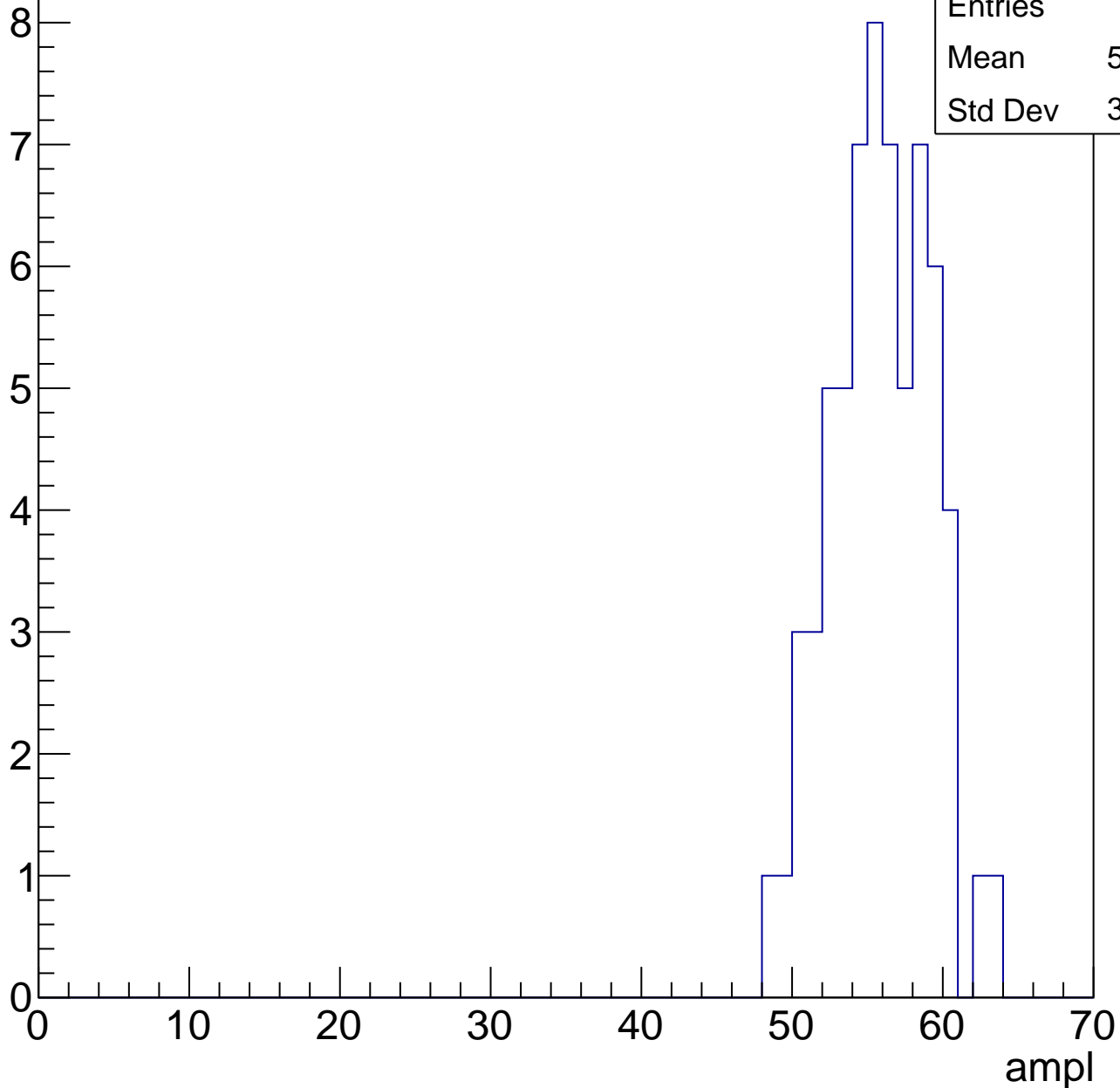


B0L002S, U18-ch47, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	55.39
Std Dev	3.234

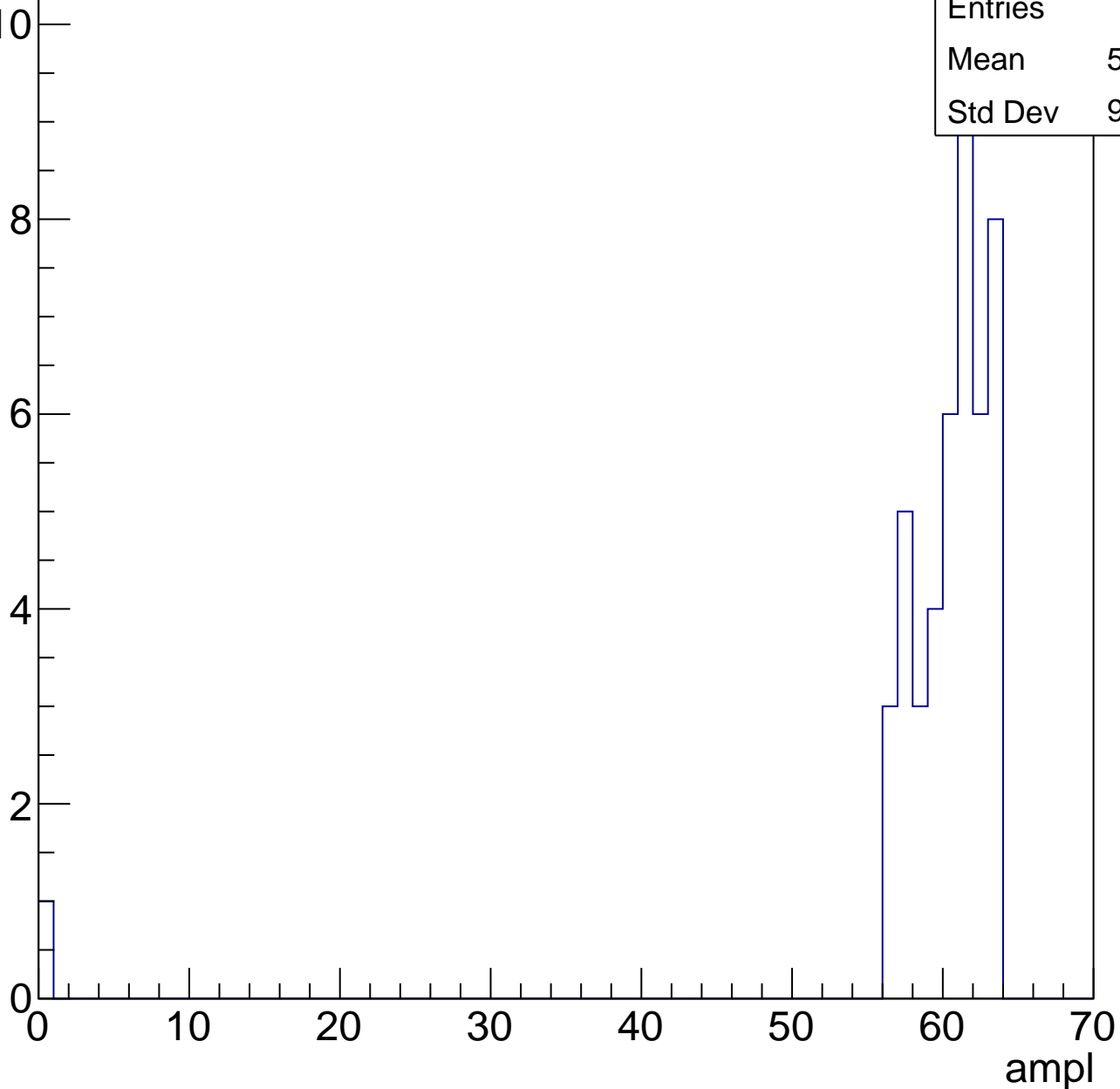


B0L002S, U18-ch47, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	58.89
Std Dev	9.039



B0L002S, U18-ch47, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch47, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch48, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	28.8
Std Dev	4.878

Gaus mean : 29.4158

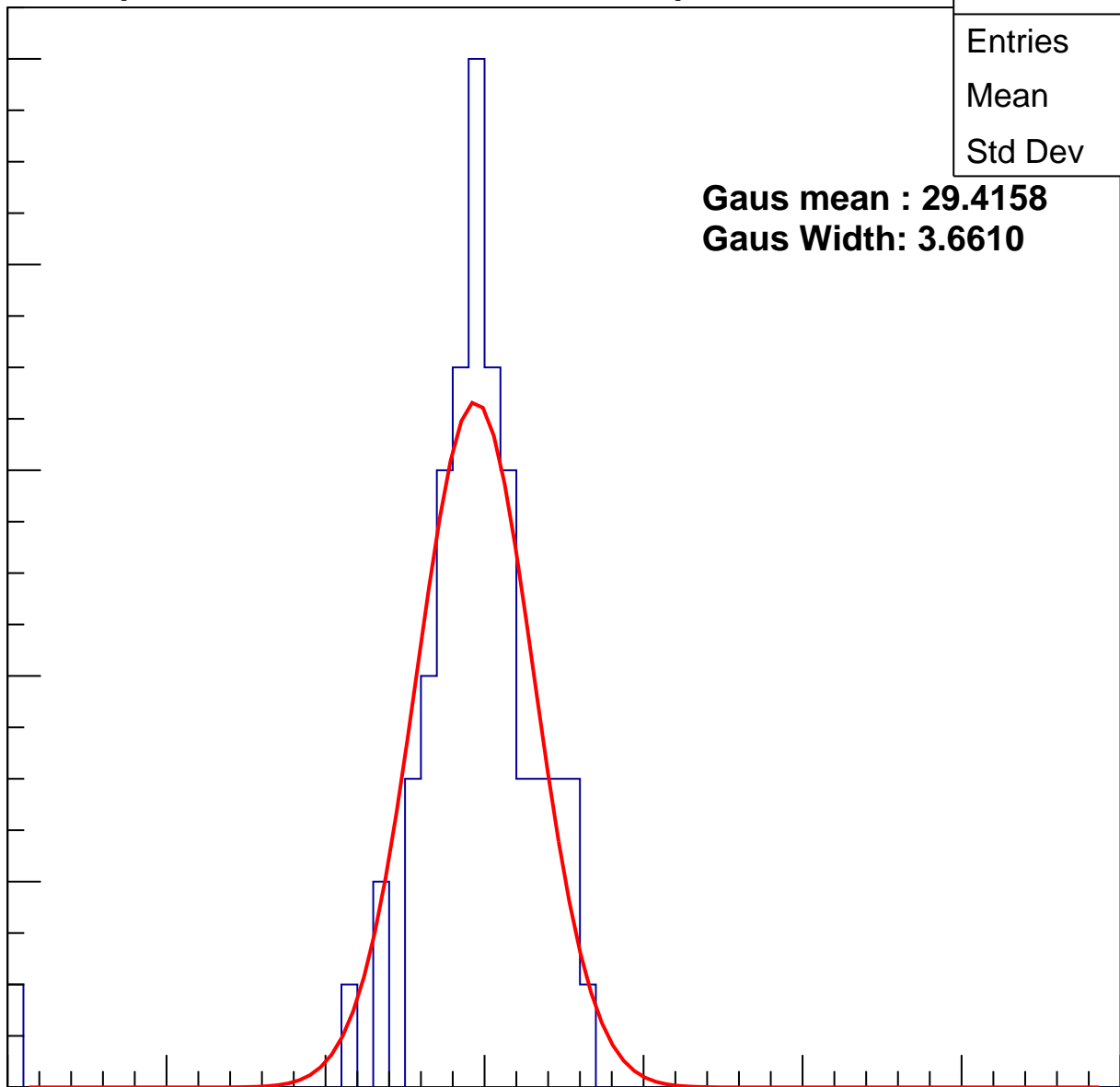
Gaus Width: 3.6610

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch48, adc1

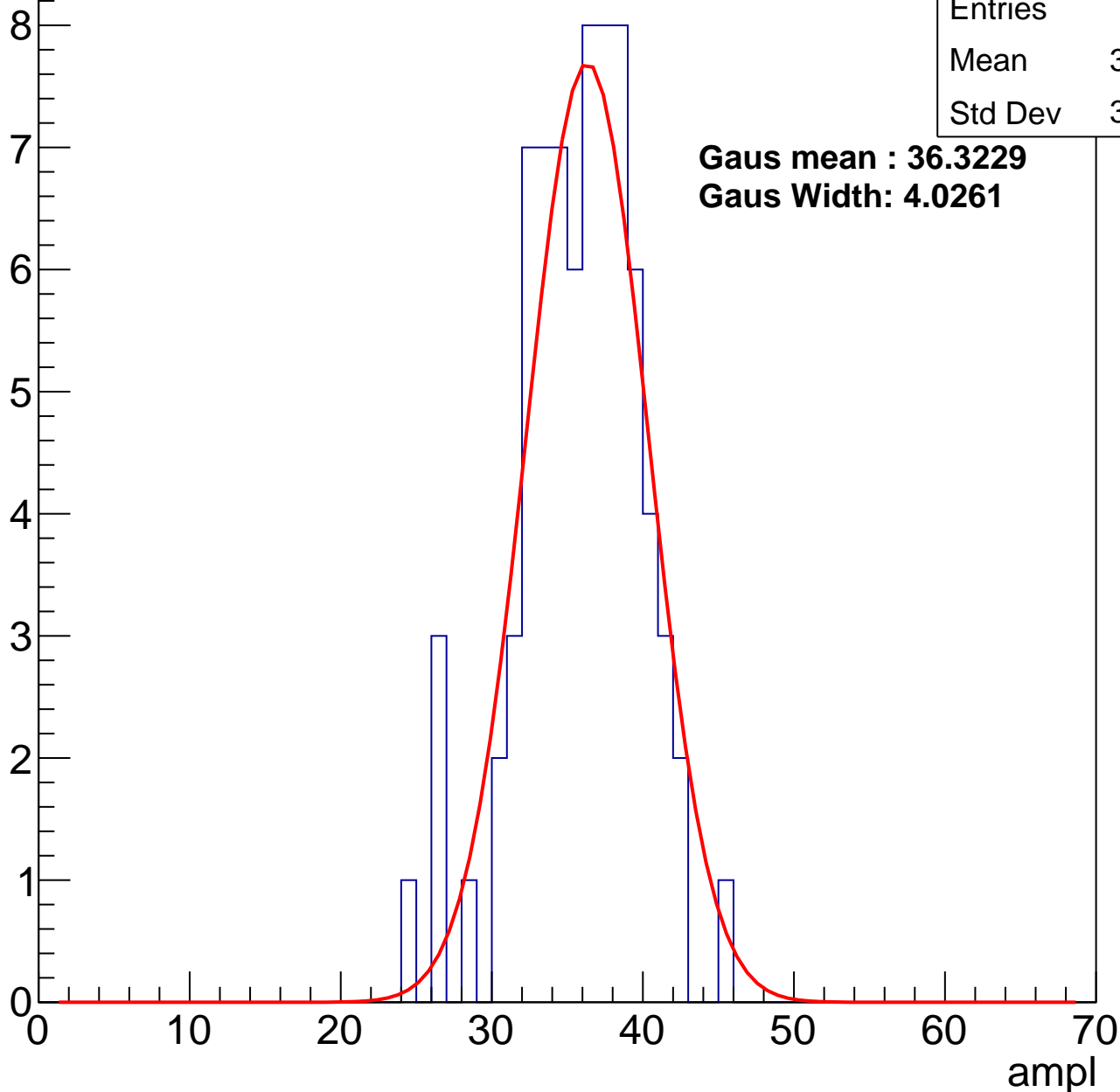
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	35.32
Std Dev	3.972

Gaus mean : 36.3229

Gaus Width: 4.0261



B0L002S, U18-ch48, adc2

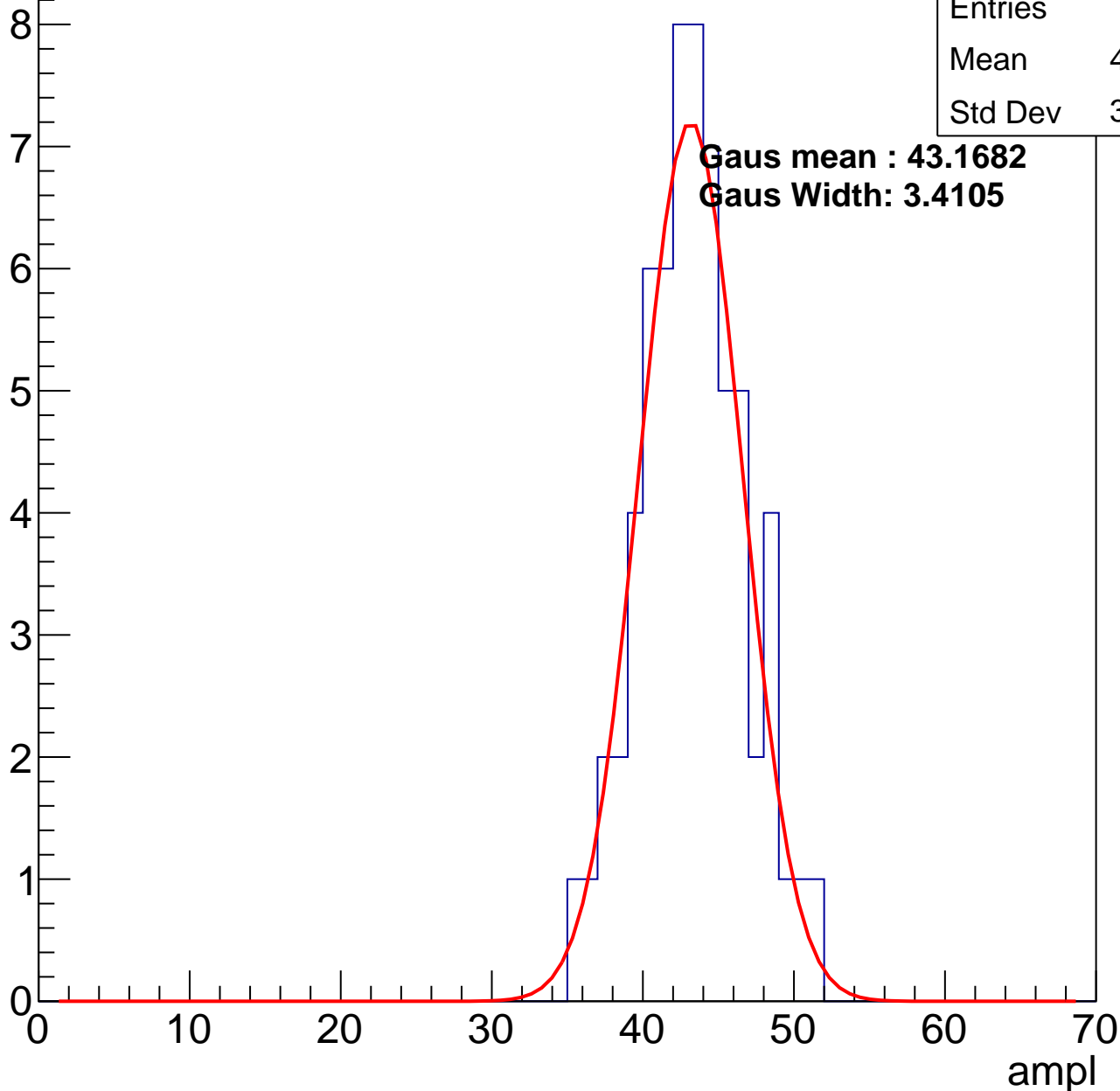
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.84
Std Dev	3.392

Gaus mean : 43.1682

Gaus Width: 3.4105

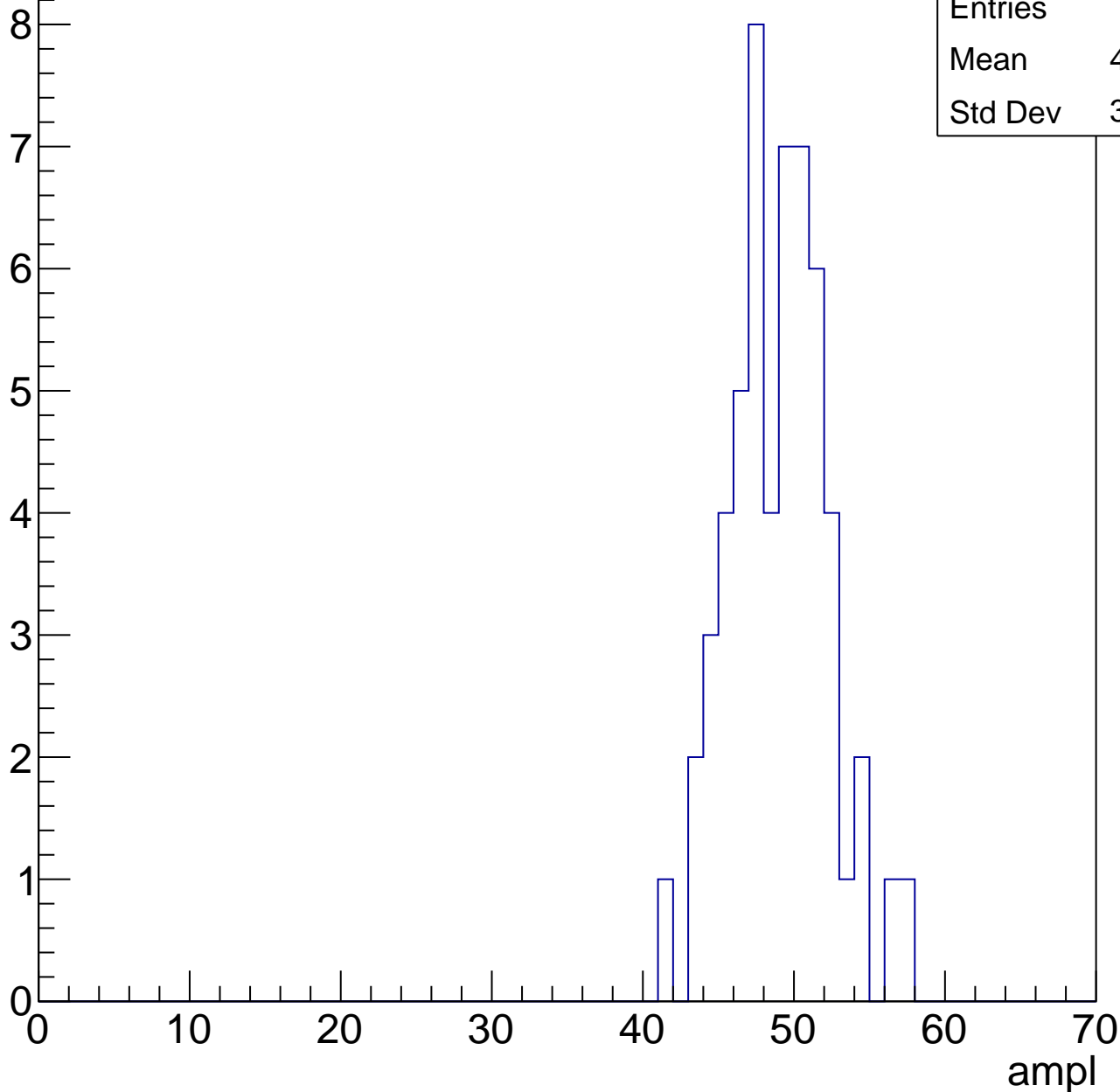


B0L002S, U18-ch48, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	48.54
Std Dev	3.246



B0L002S, U18-ch48, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	78
Mean	55.56
Std Dev	3.536

Entry

10

8

6

4

2

0

0

10

20

30

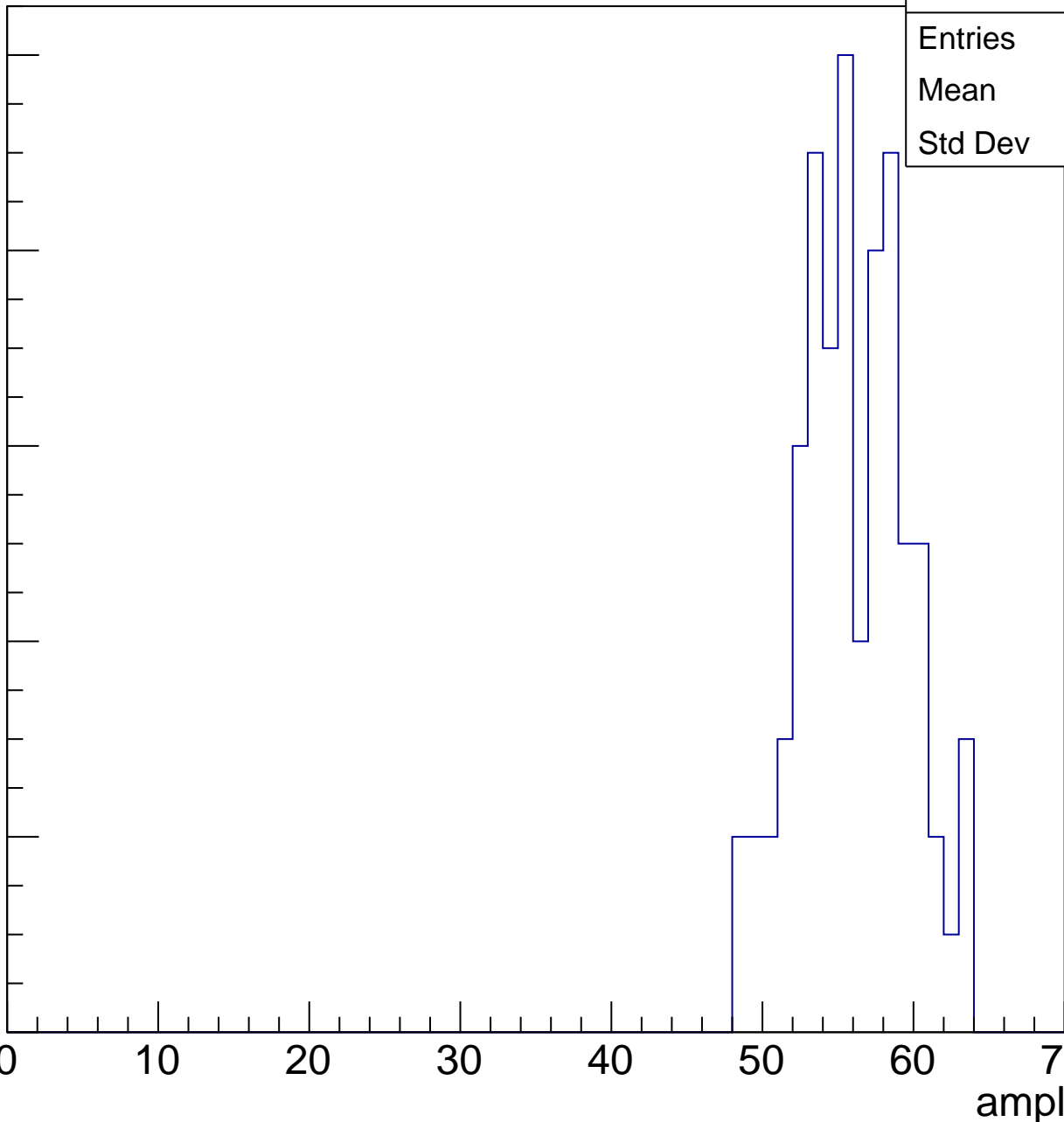
40

50

60

ampl

70

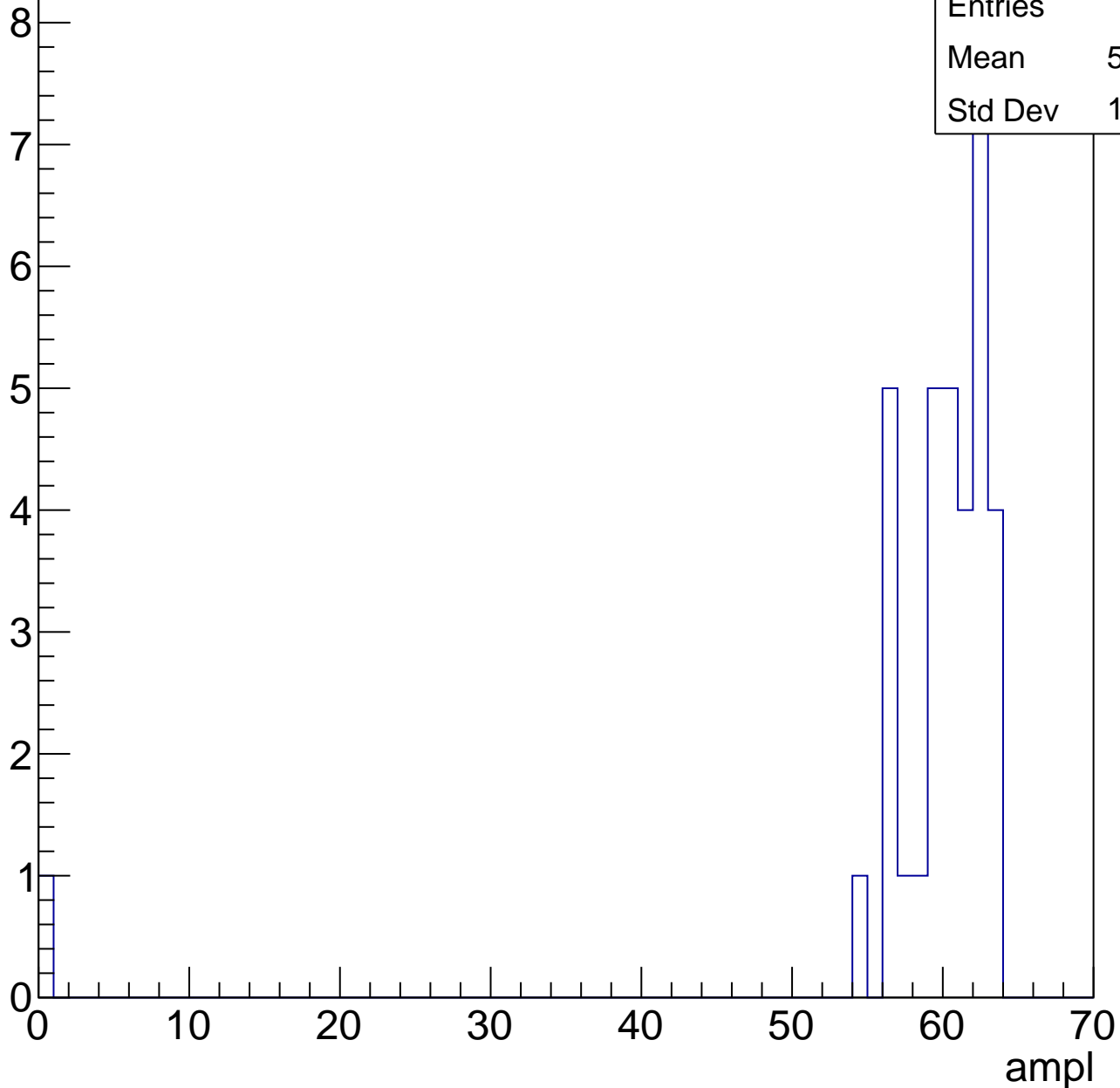


B0L002S, U18-ch48, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

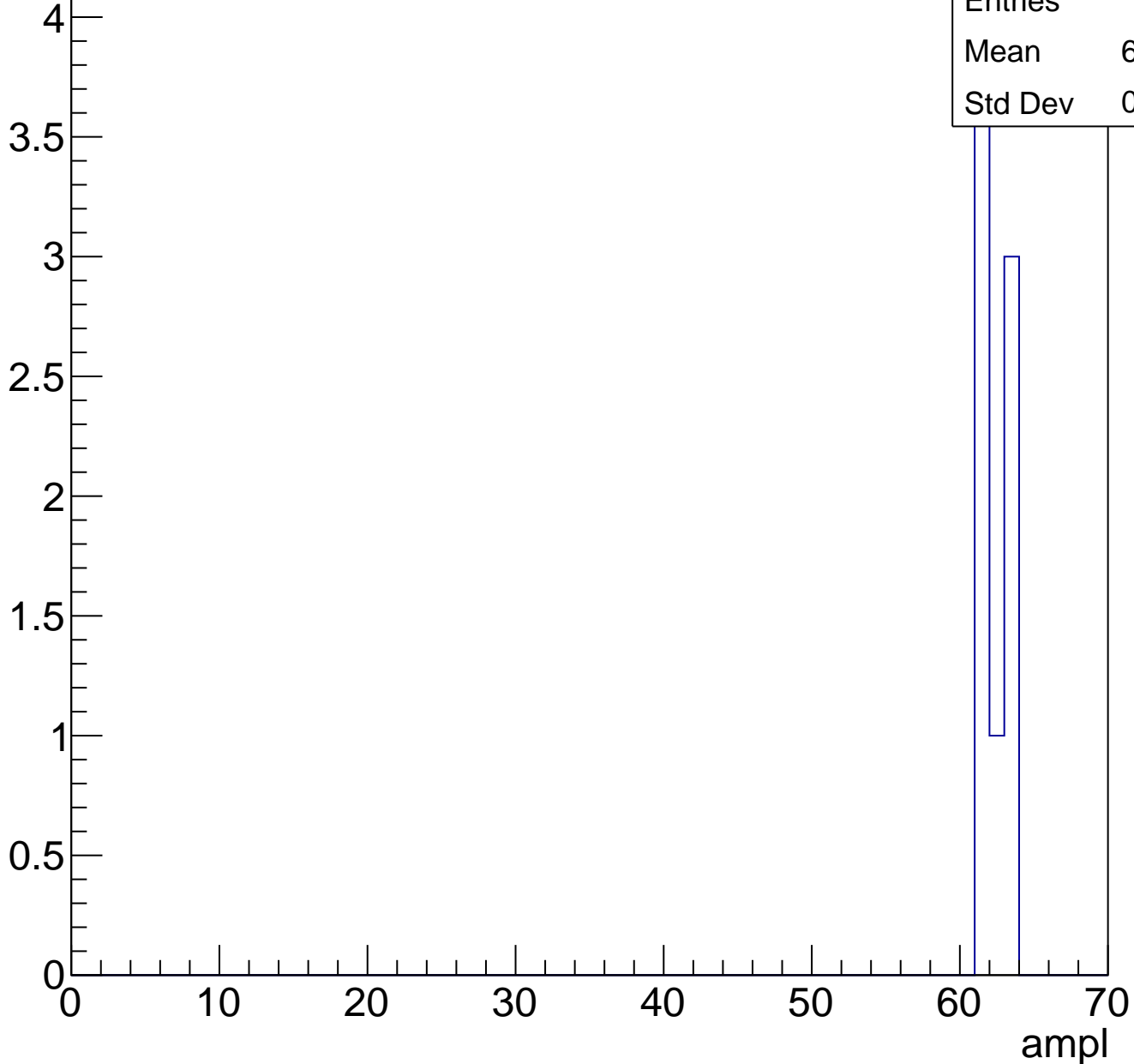
Entries	35
Mean	58.17
Std Dev	10.27



B0L002S, U18-ch48, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch48, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch49, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	30.05
Std Dev	5.005

Gaus mean : 31.2581

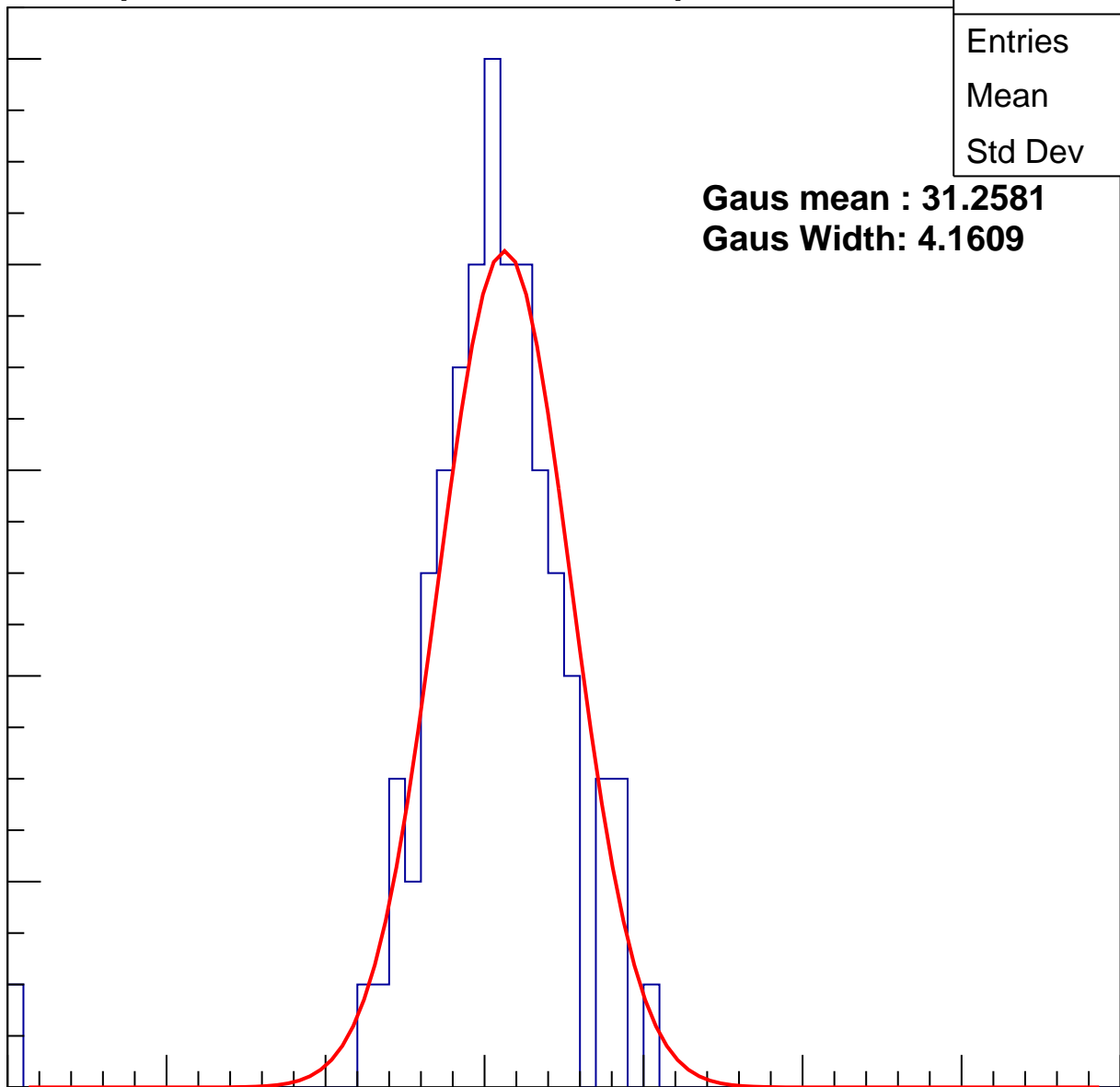
Gaus Width: 4.1609

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch49, adc1

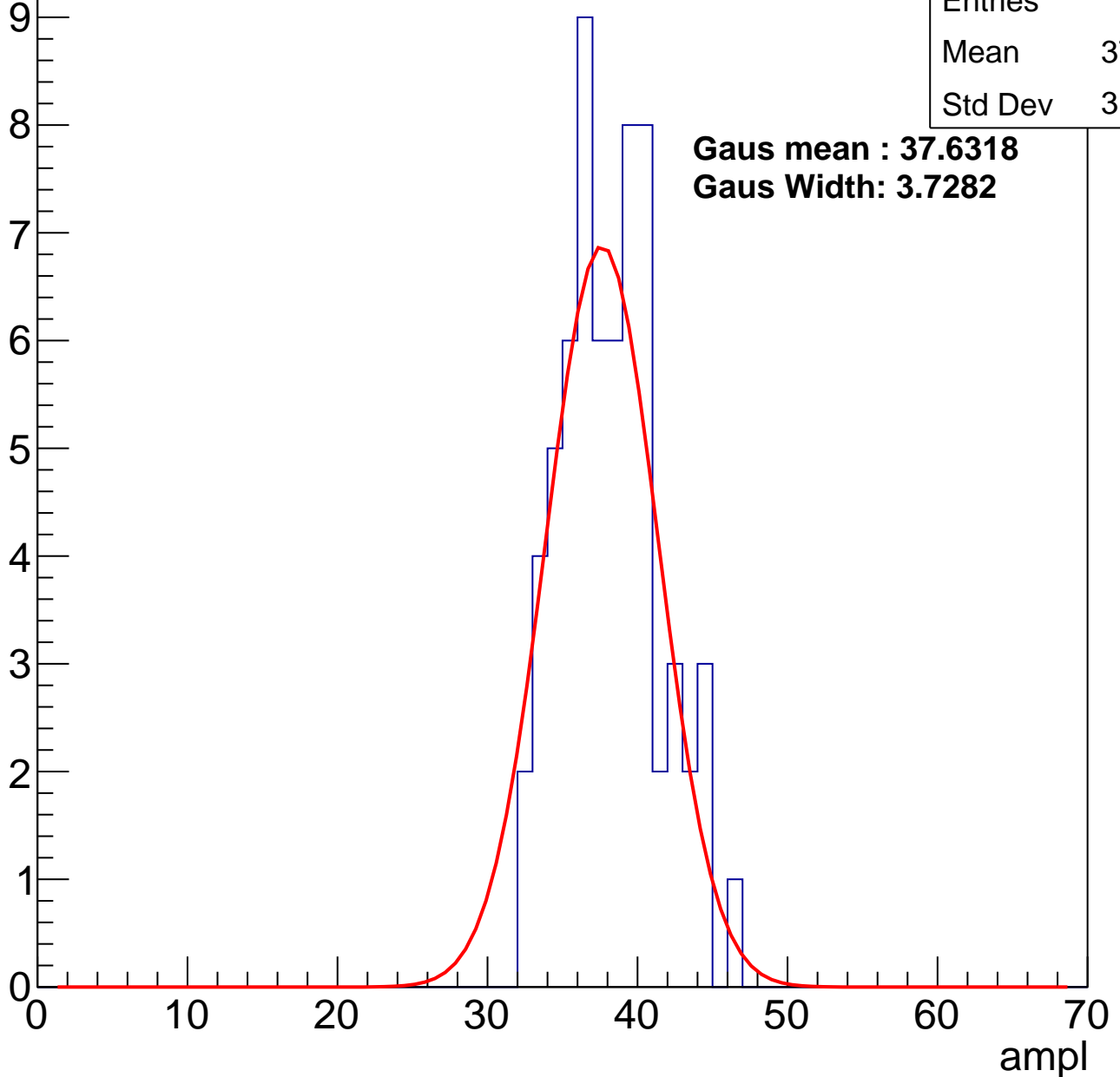
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	37.75
Std Dev	3.215

Gaus mean : 37.6318

Gaus Width: 3.7282



B0L002S, U18-ch49, adc2

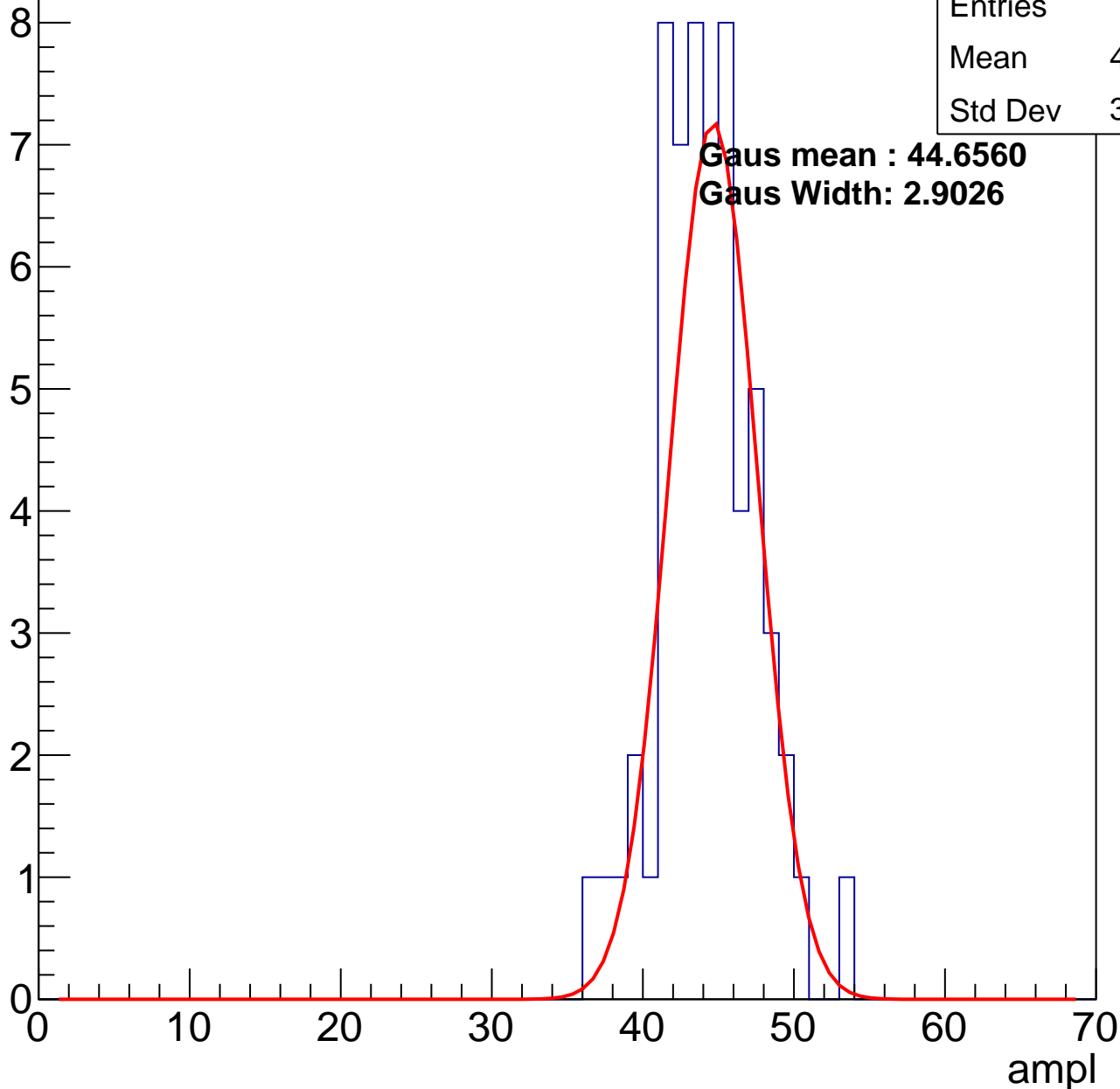
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	43.78
Std Dev	3.173

Gaus mean : 44.6560

Gaus Width: 2.9026

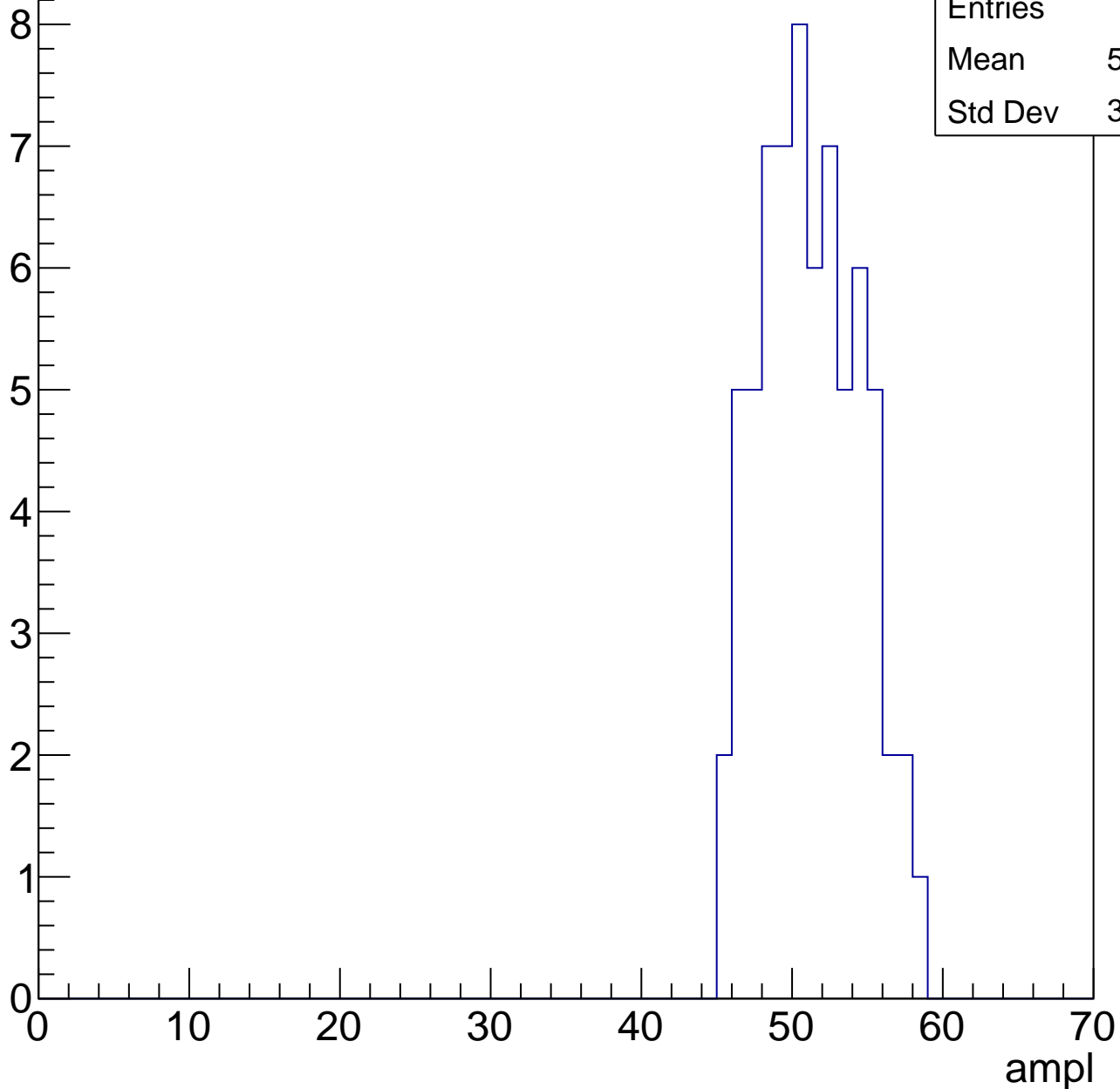


B0L002S, U18-ch49, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	50.76
Std Dev	3.218

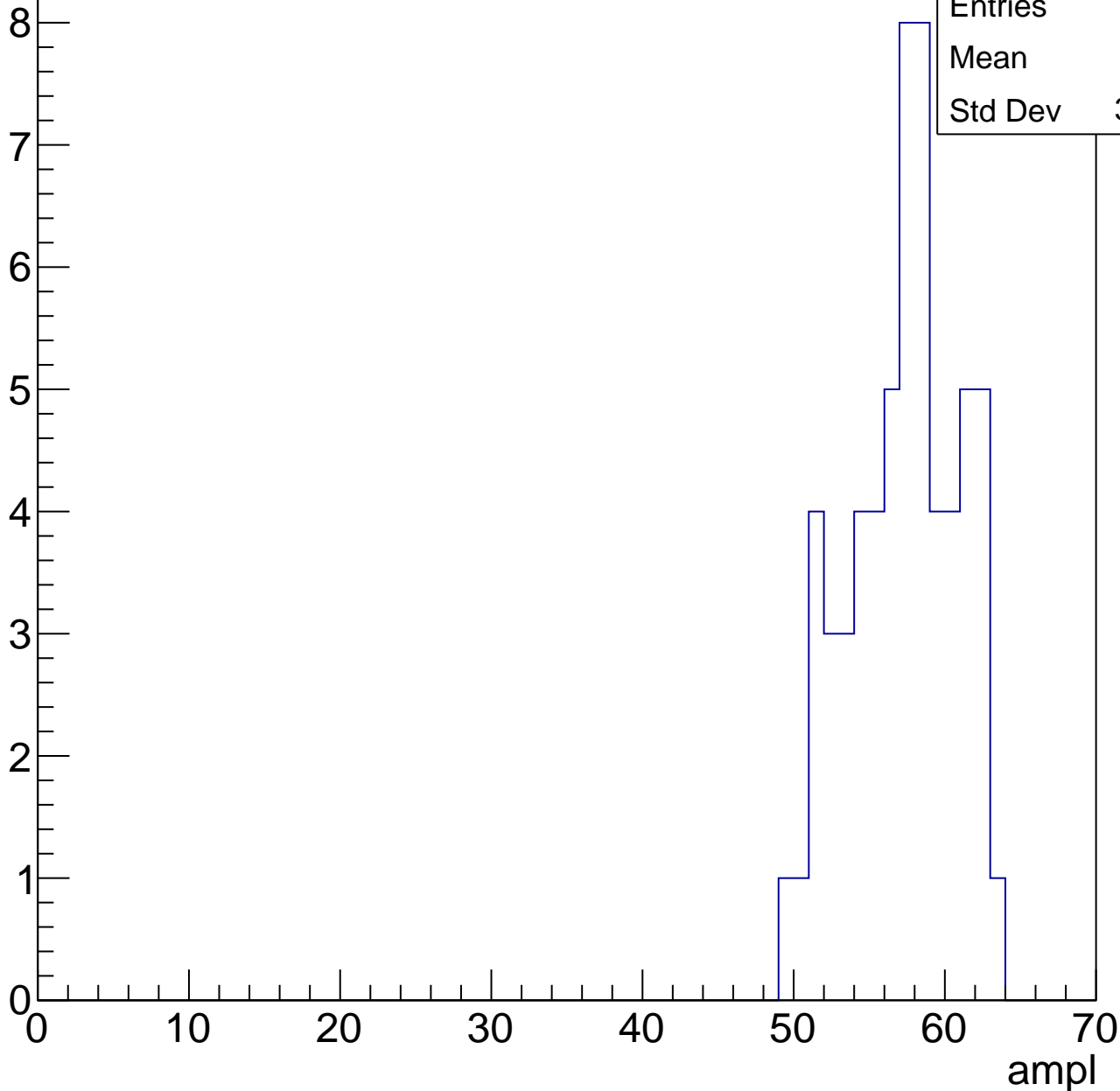


B0L002S, U18-ch49, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	56.8
Std Dev	3.501

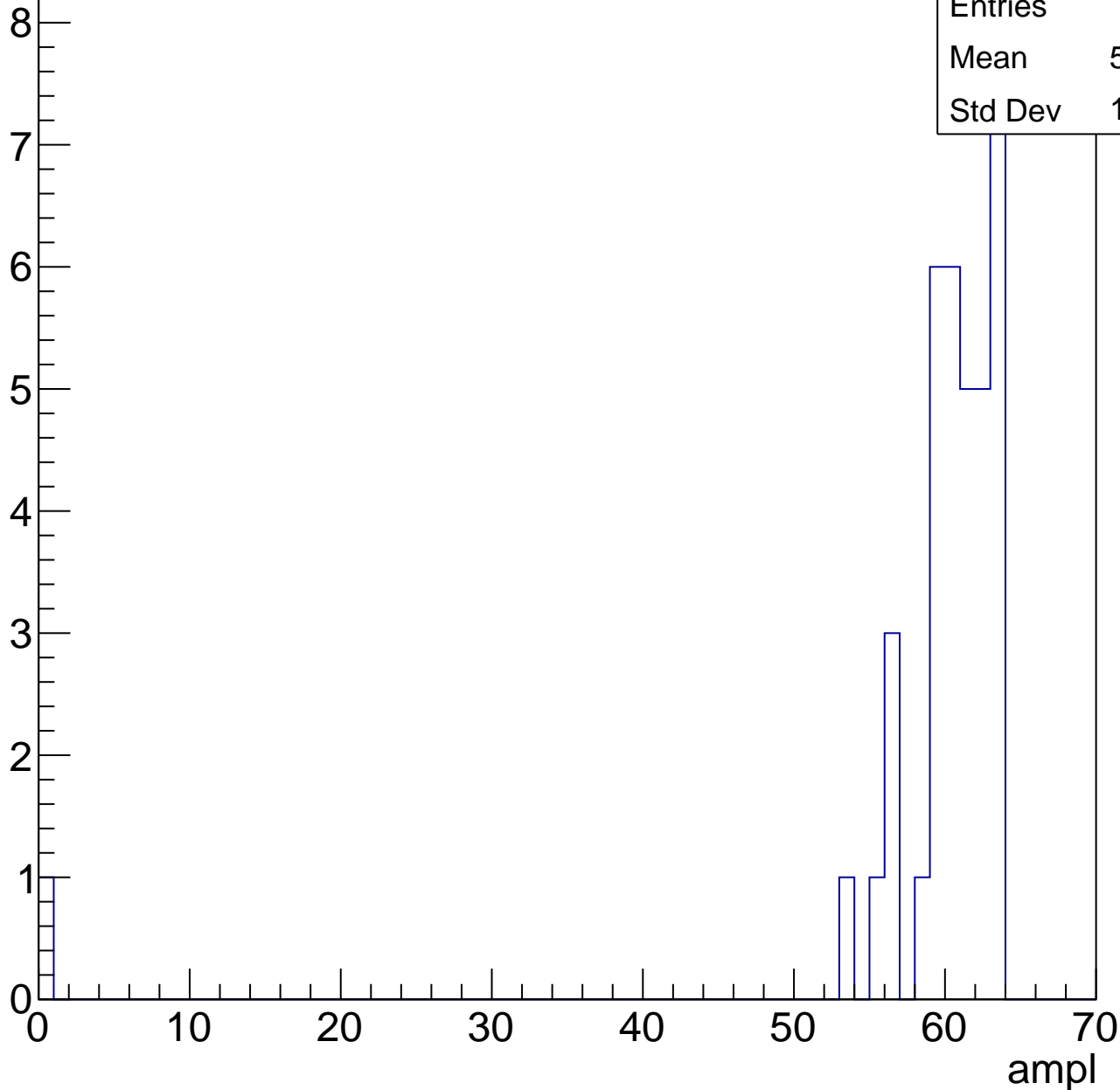


B0L002S, U18-ch49, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.57
Std Dev	10.07



B0L002S, U18-ch49, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch49, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch50, adc0

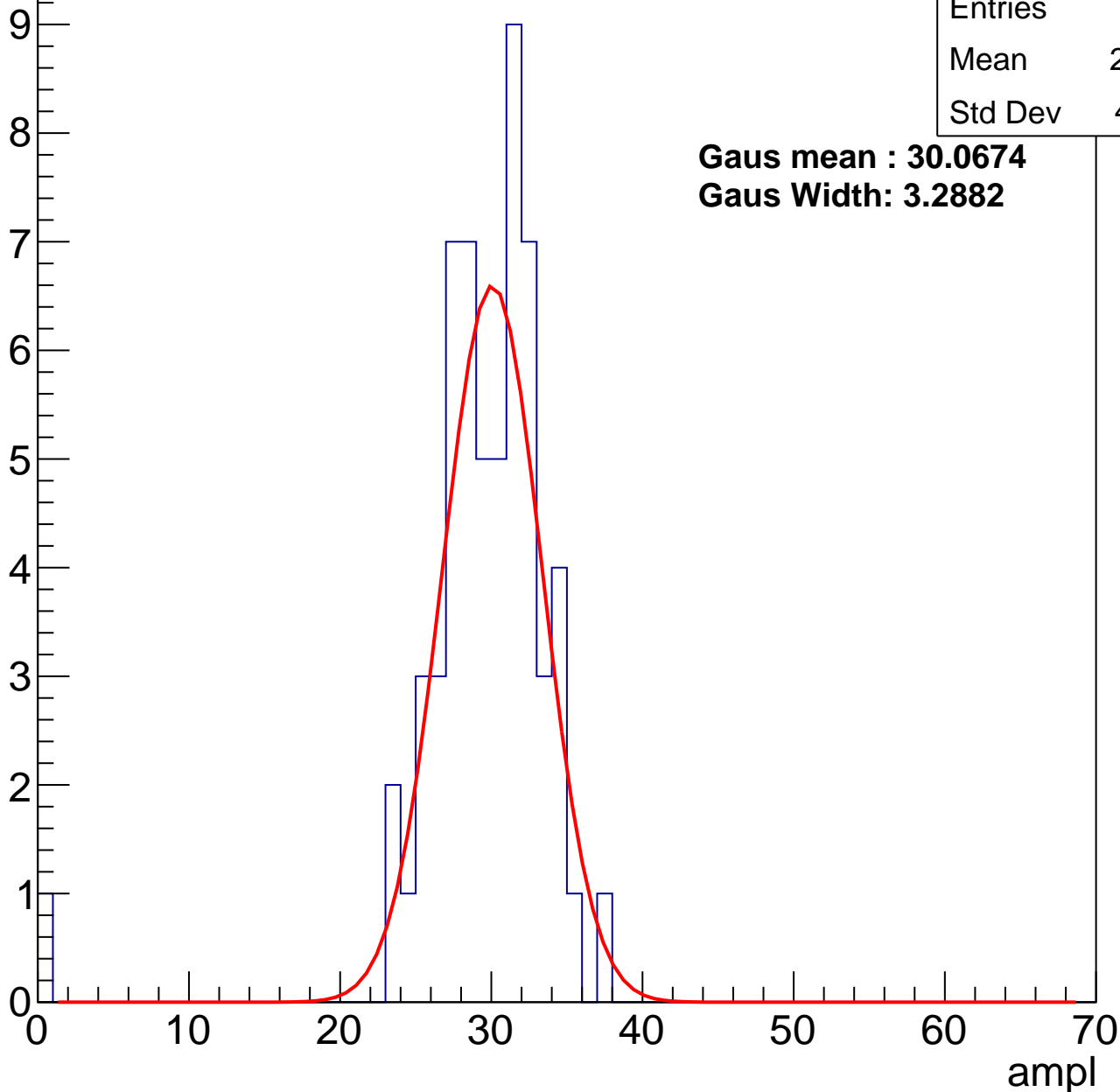
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.03
Std Dev	4.871

Gaus mean : 30.0674

Gaus Width: 3.2882



B0L002S, U18-ch50, adc1

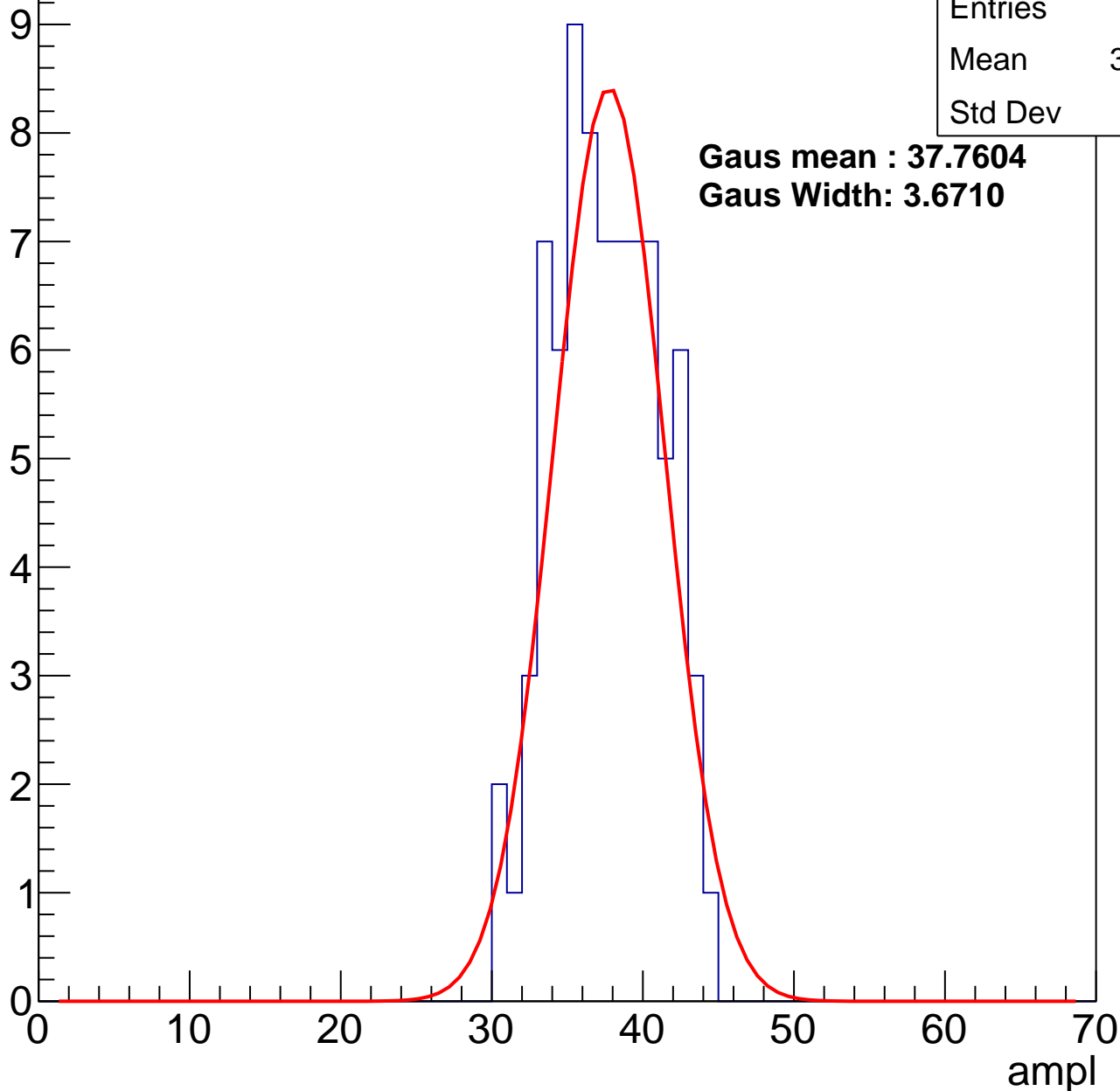
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.13
Std Dev	3.38

Gaus mean : 37.7604

Gaus Width: 3.6710



B0L002S, U18-ch50, adc2

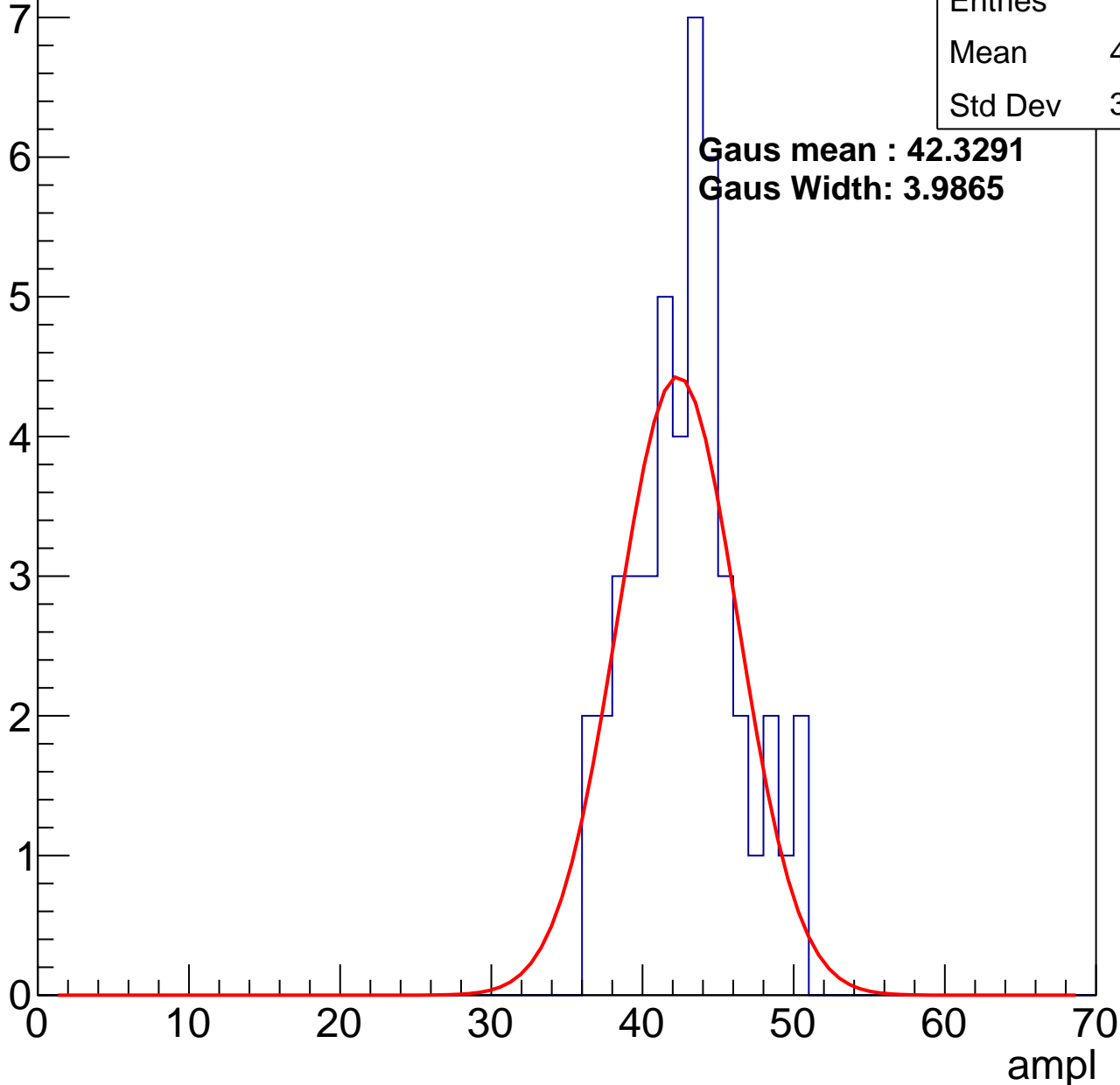
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	42.48
Std Dev	3.512

Gaus mean : 42.3291

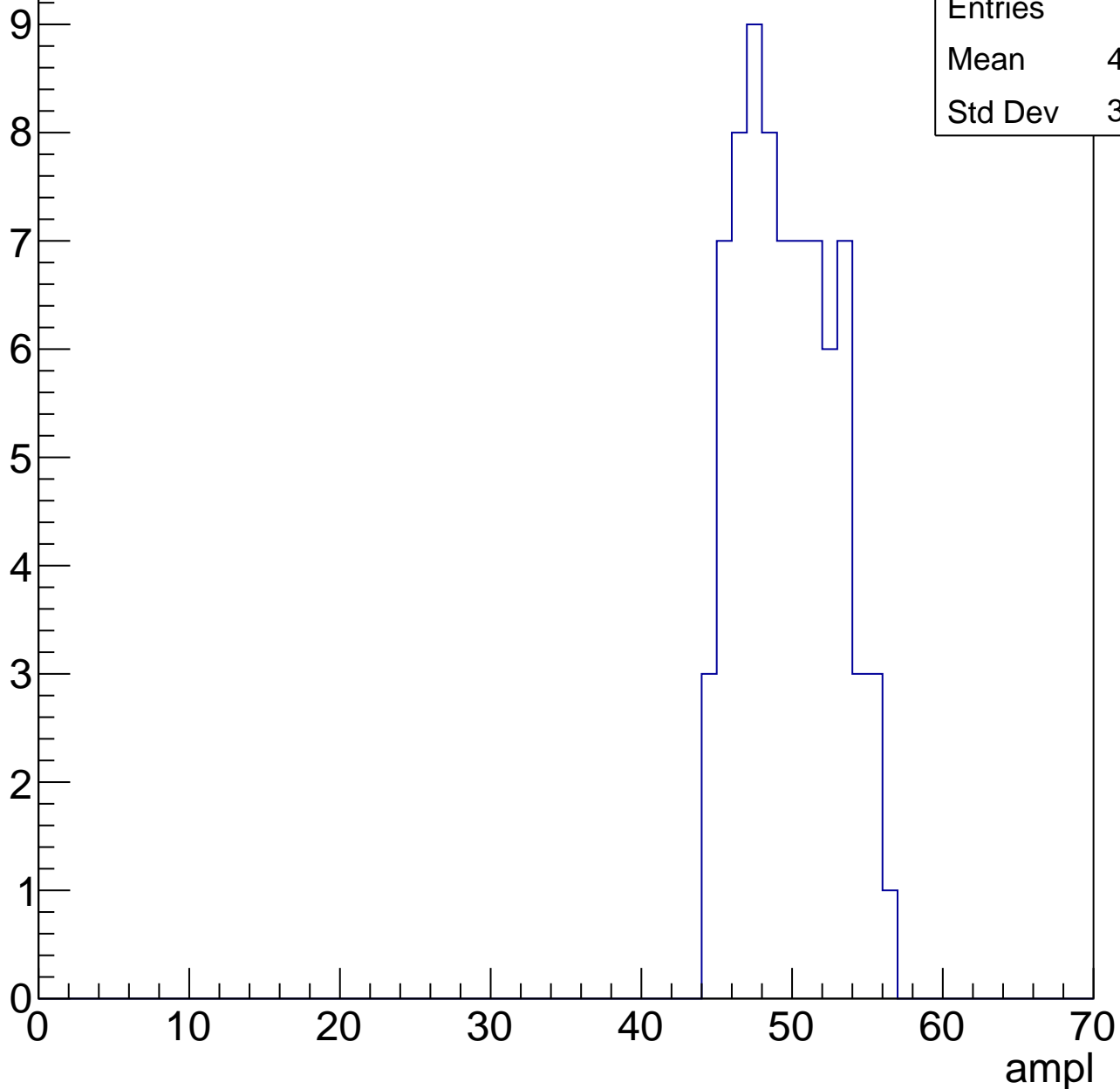
Gaus Width: 3.9865



B0L002S, U18-ch50, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

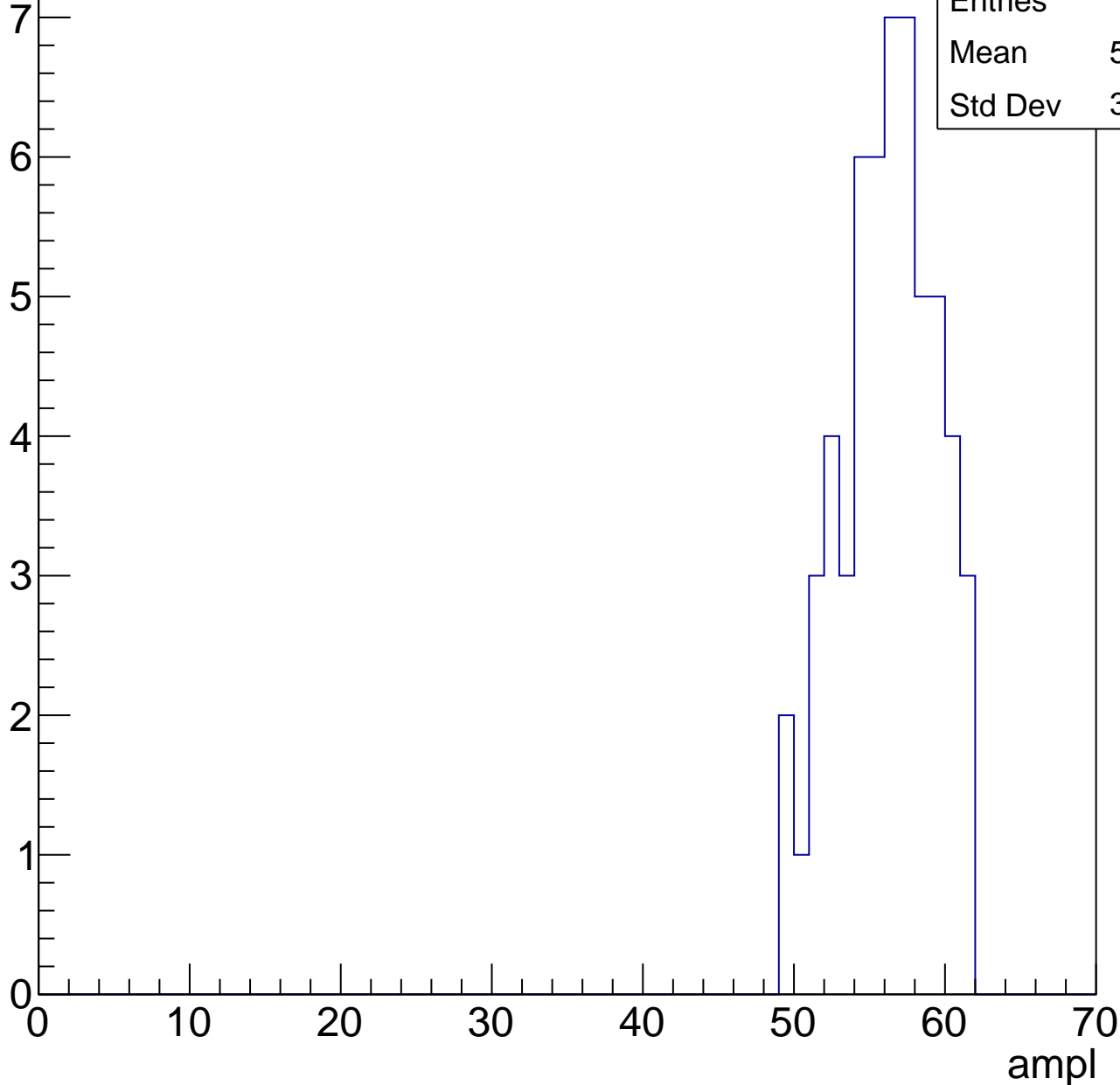


Entries	76
Mean	49.18
Std Dev	3.106

B0L002S, U18-ch50, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

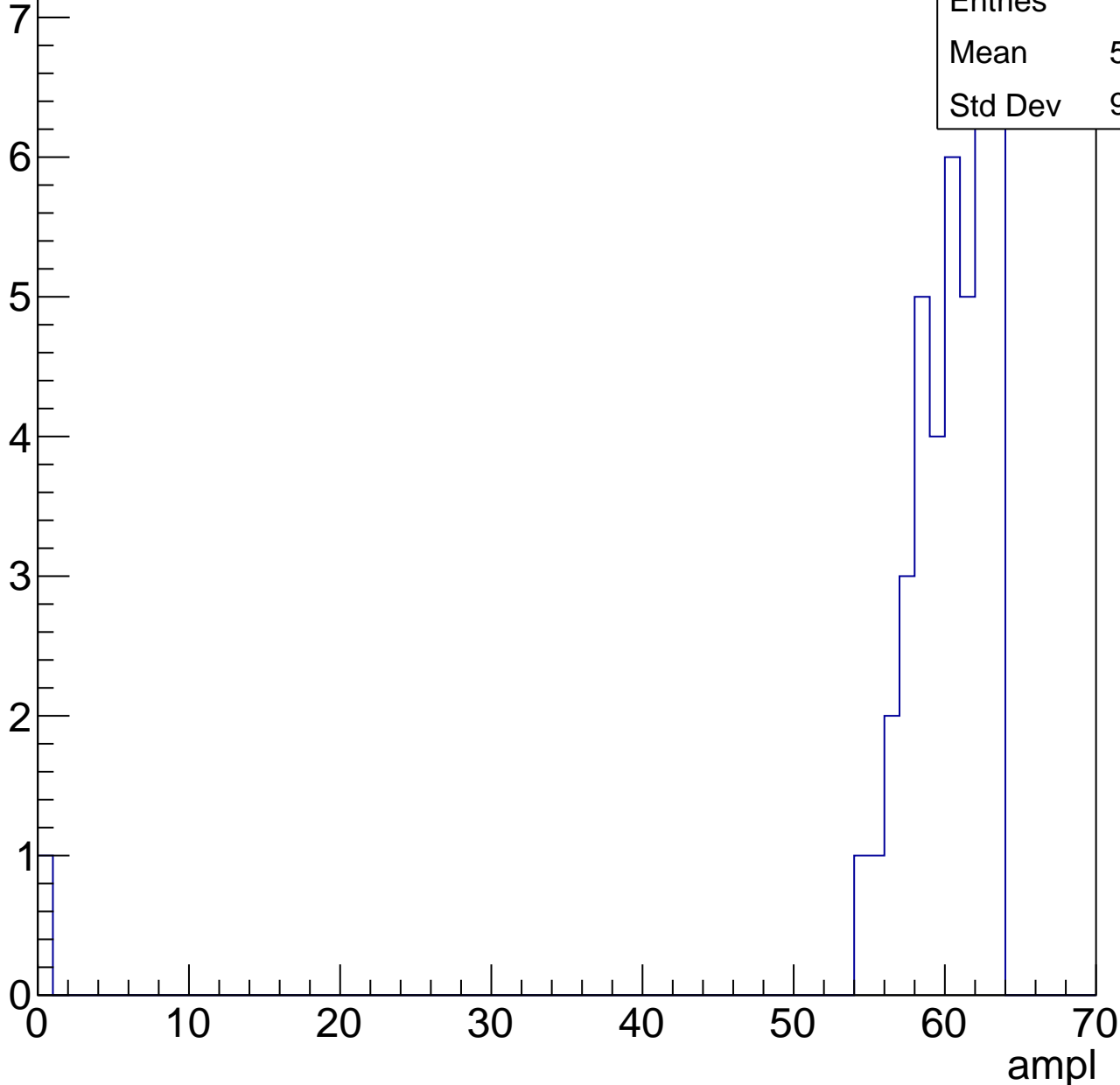


B0L002S, U18-ch50, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

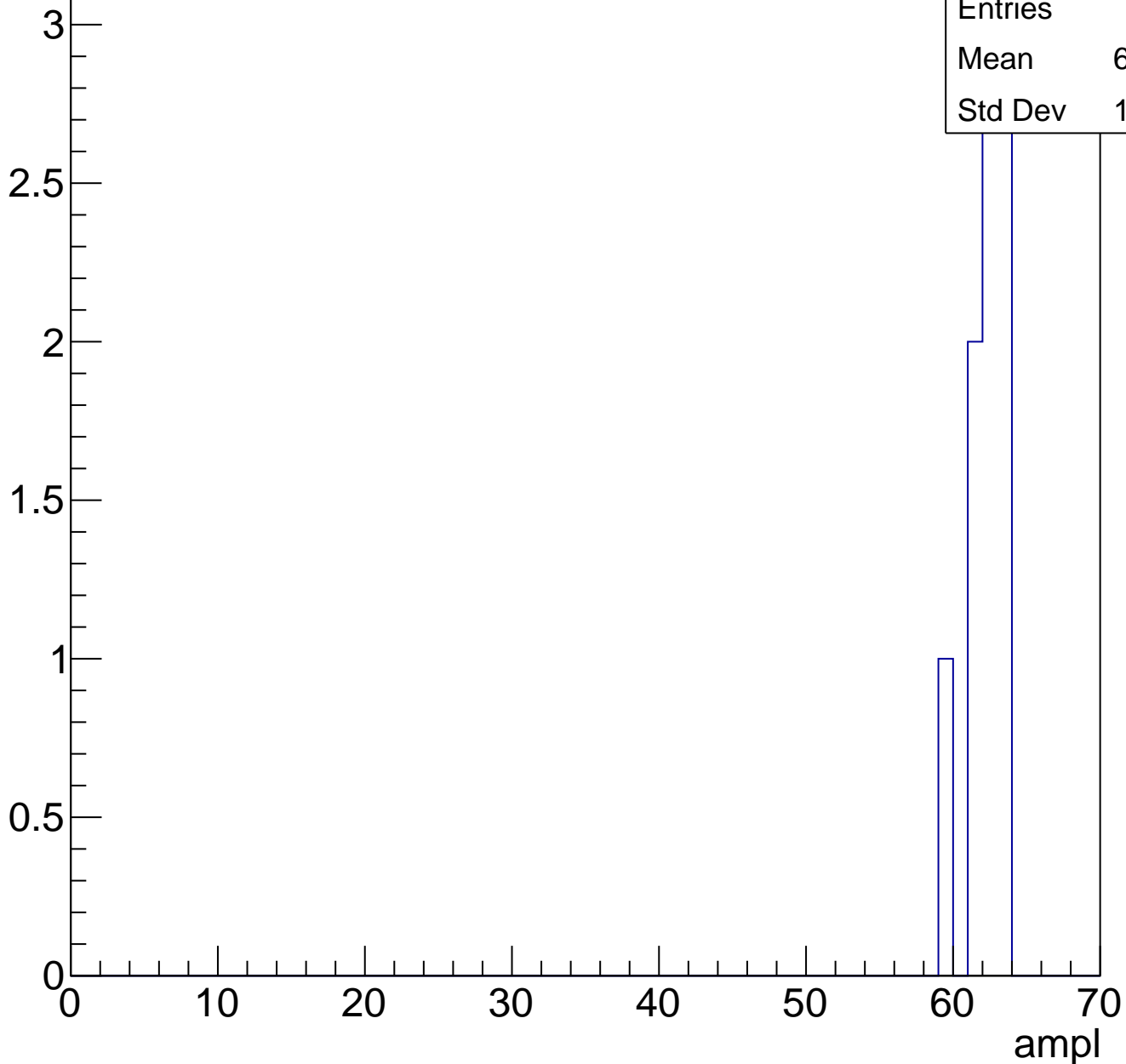
Entries	42
Mean	58.52
Std Dev	9.447



B0L002S, U18-ch50, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch50, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch51, adc0

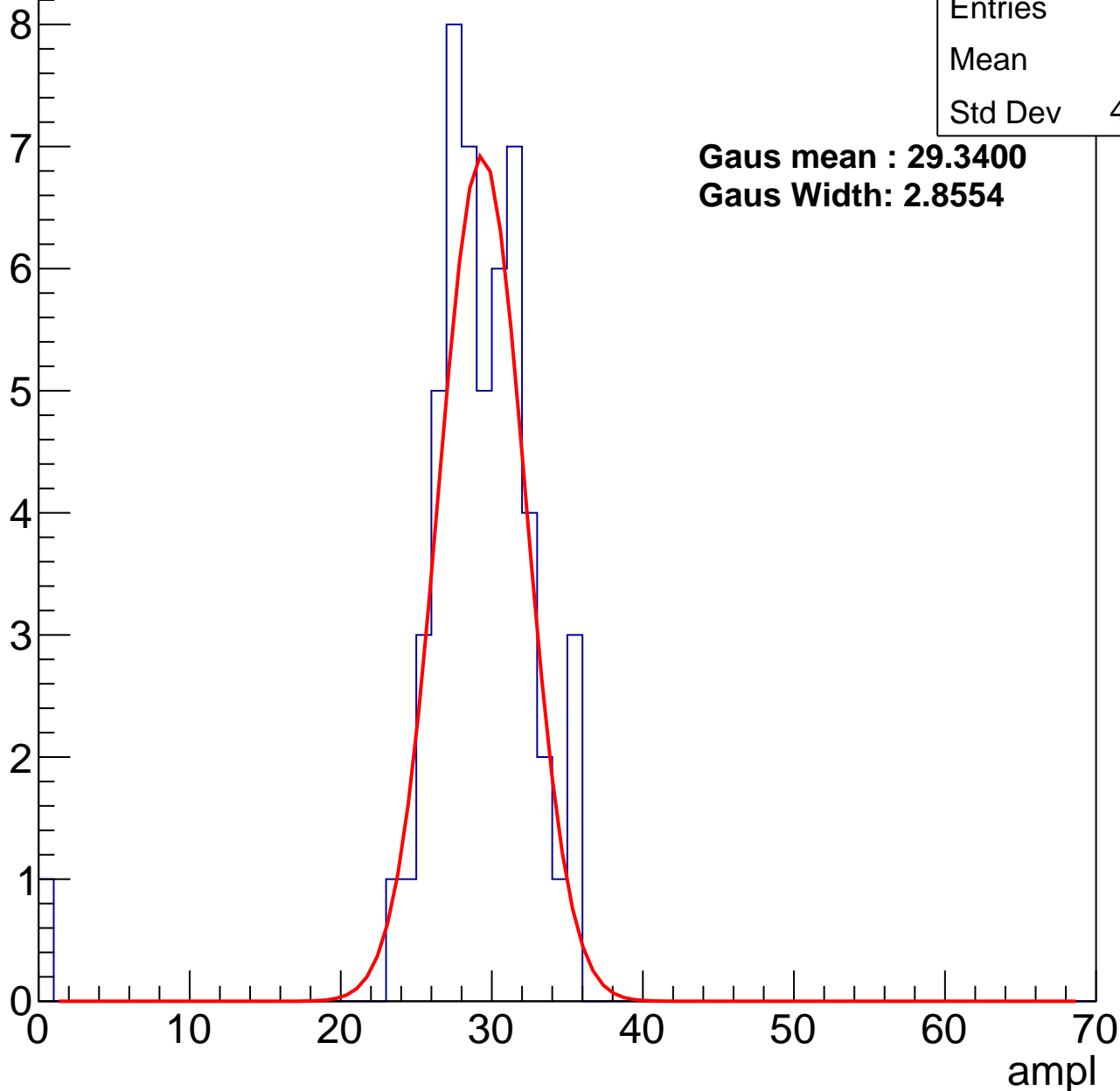
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	28.5
Std Dev	4.822

Gaus mean : 29.3400

Gaus Width: 2.8554



B0L002S, U18-ch51, adc1

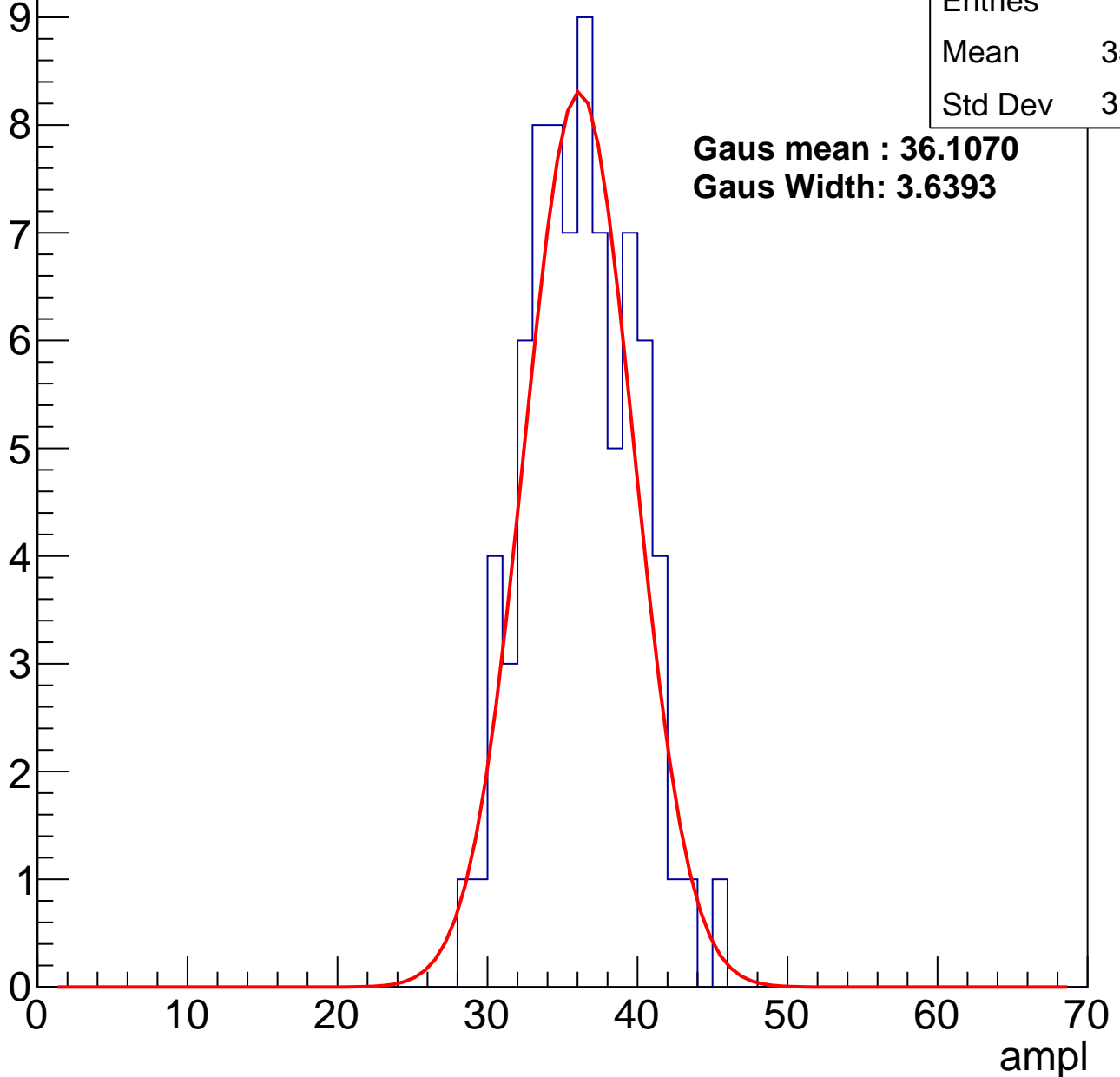
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	35.73
Std Dev	3.532

Gaus mean : 36.1070

Gaus Width: 3.6393



B0L002S, U18-ch51, adc2

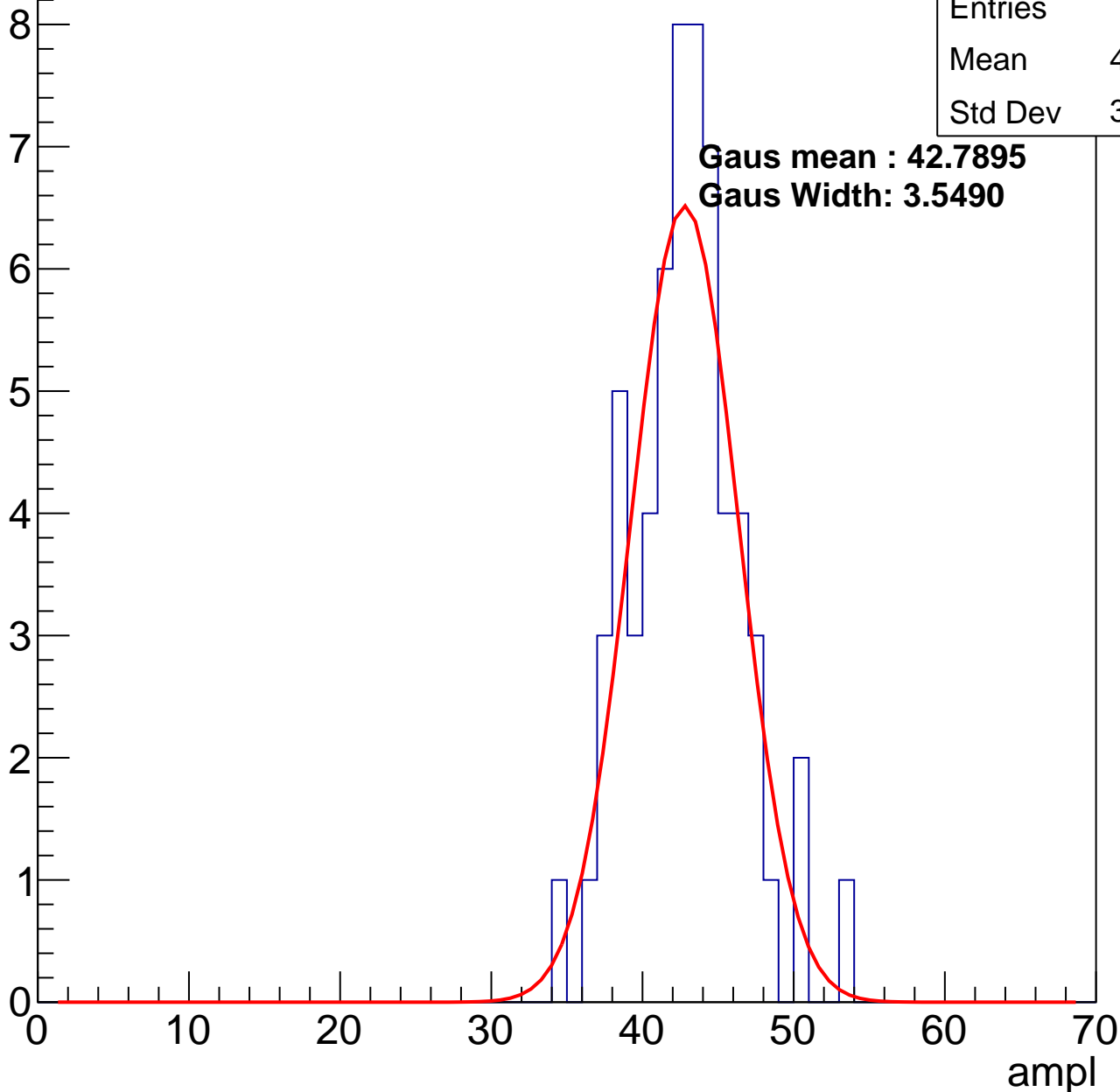
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.43
Std Dev	3.596

Gaus mean : 42.7895

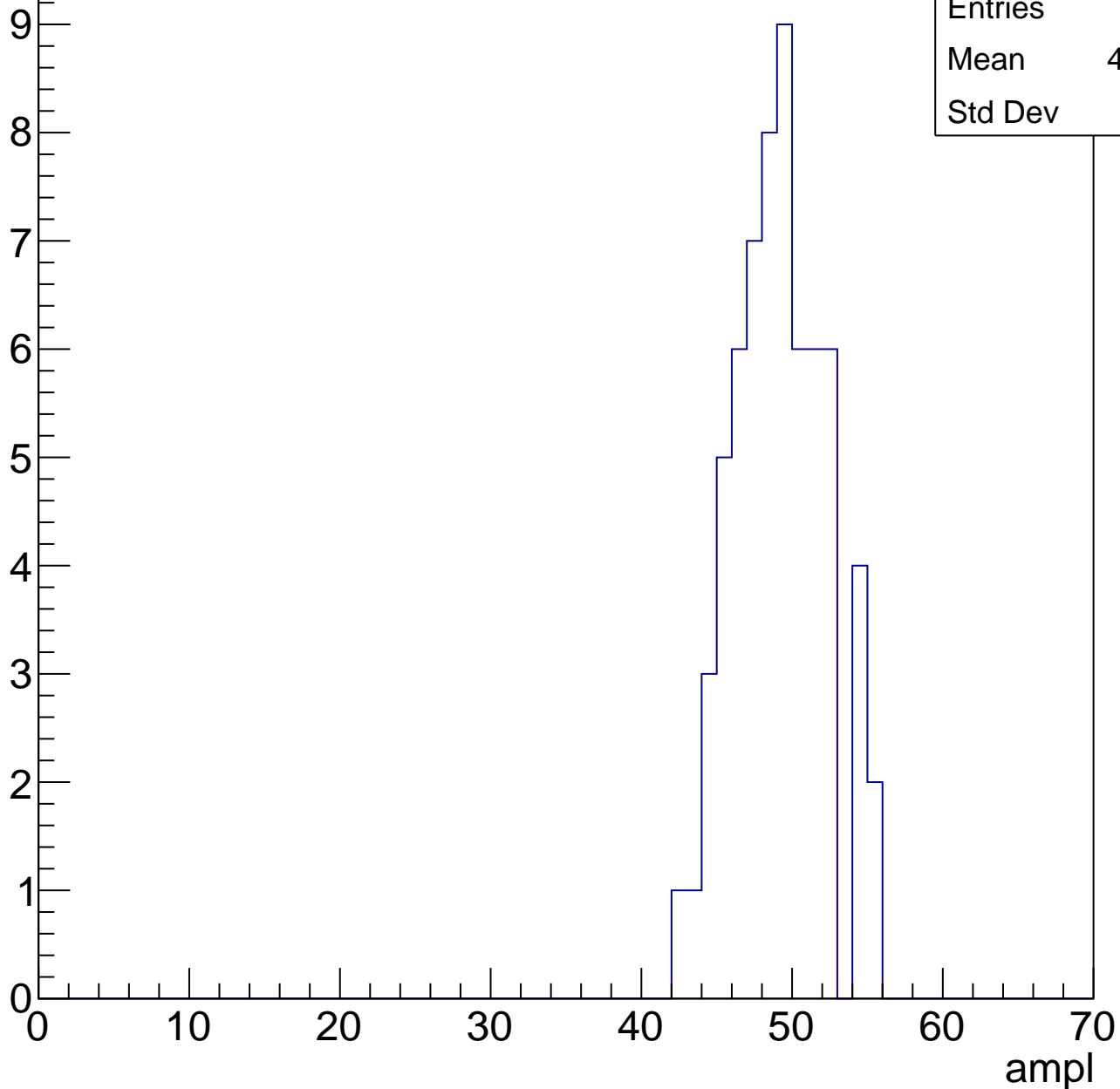
Gaus Width: 3.5490



B0L002S, U18-ch51, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

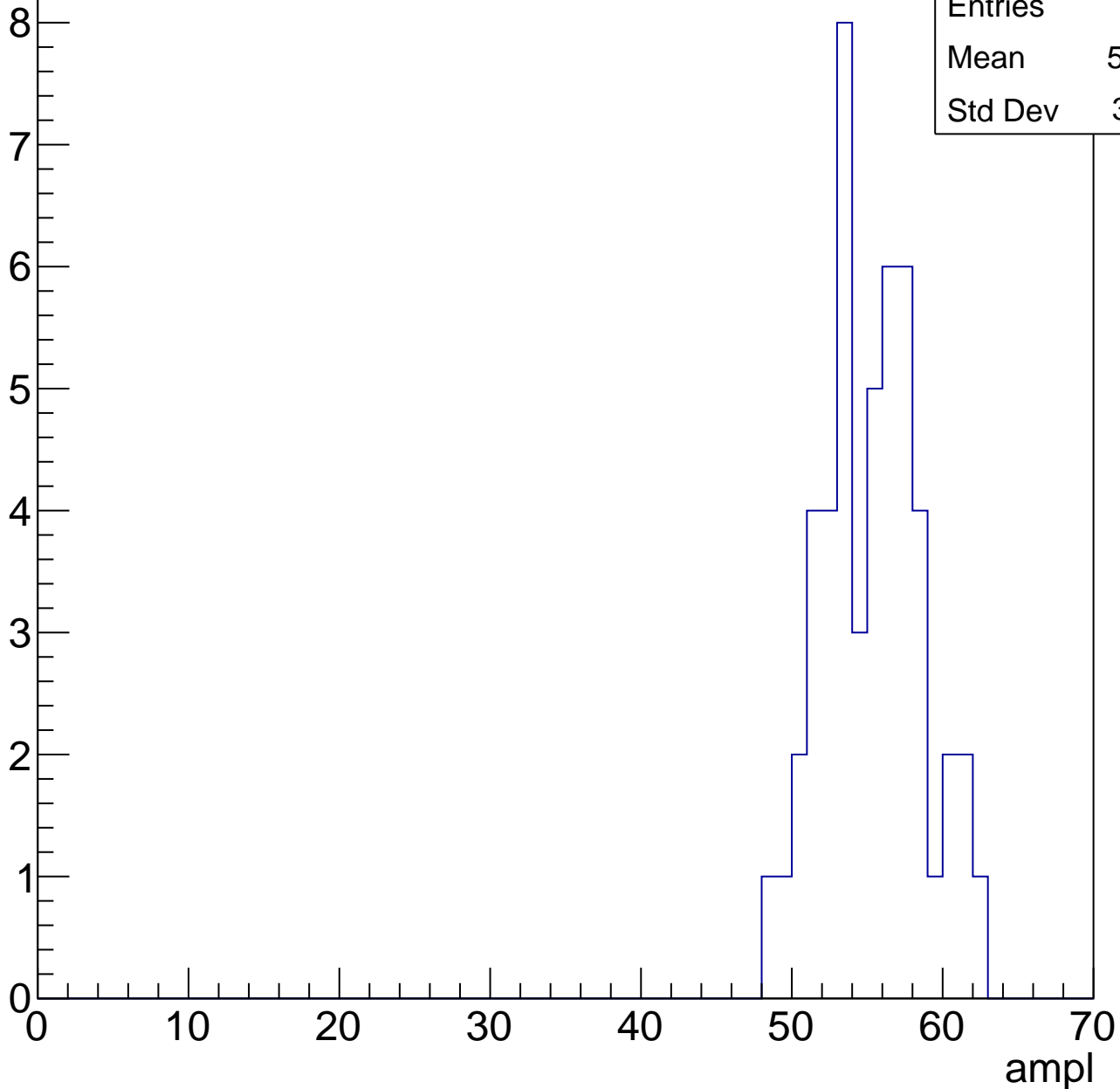


B0L002S, U18-ch51, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

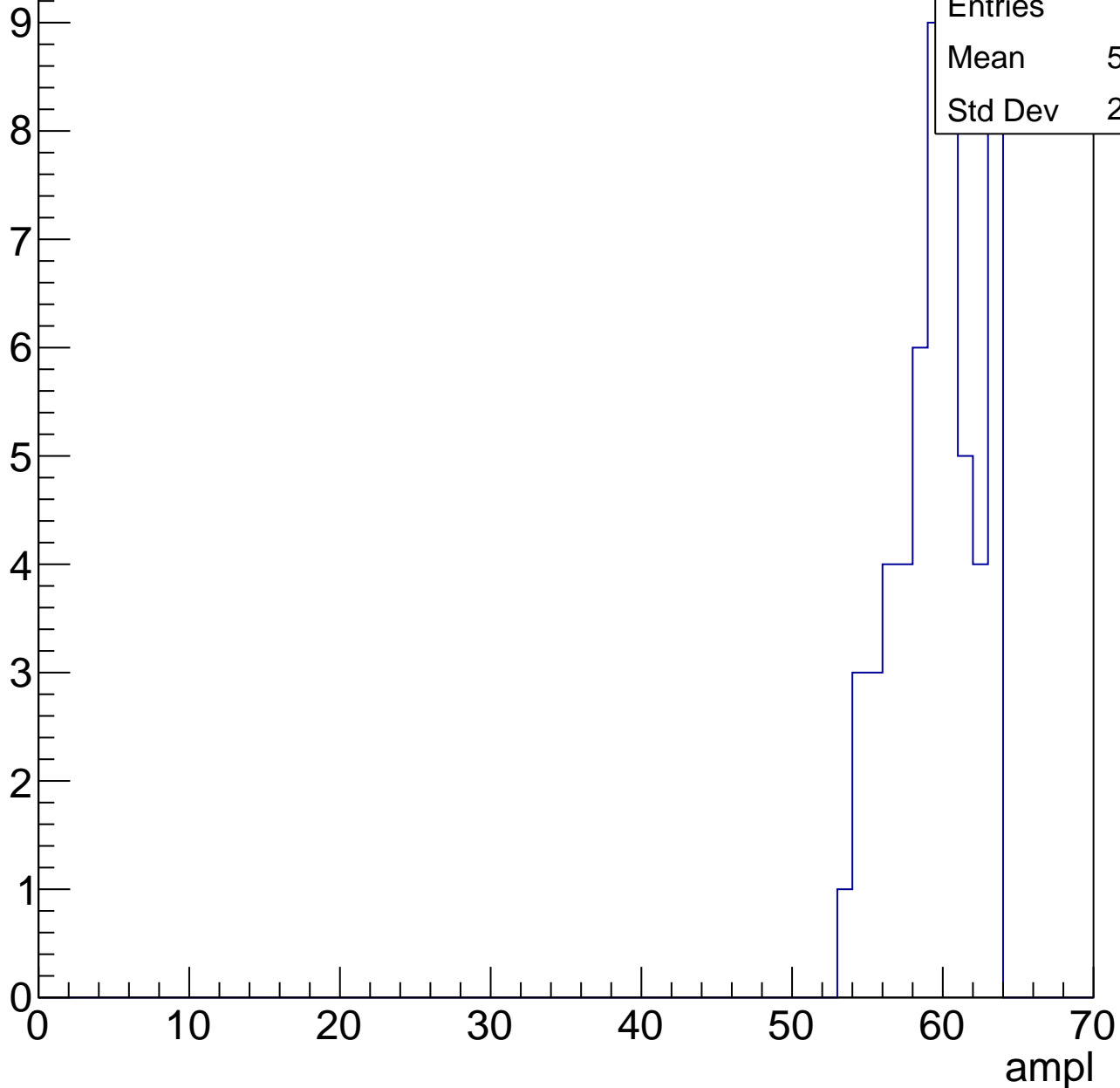
Entries	50
Mean	54.86
Std Dev	3.231



B0L002S, U18-ch51, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



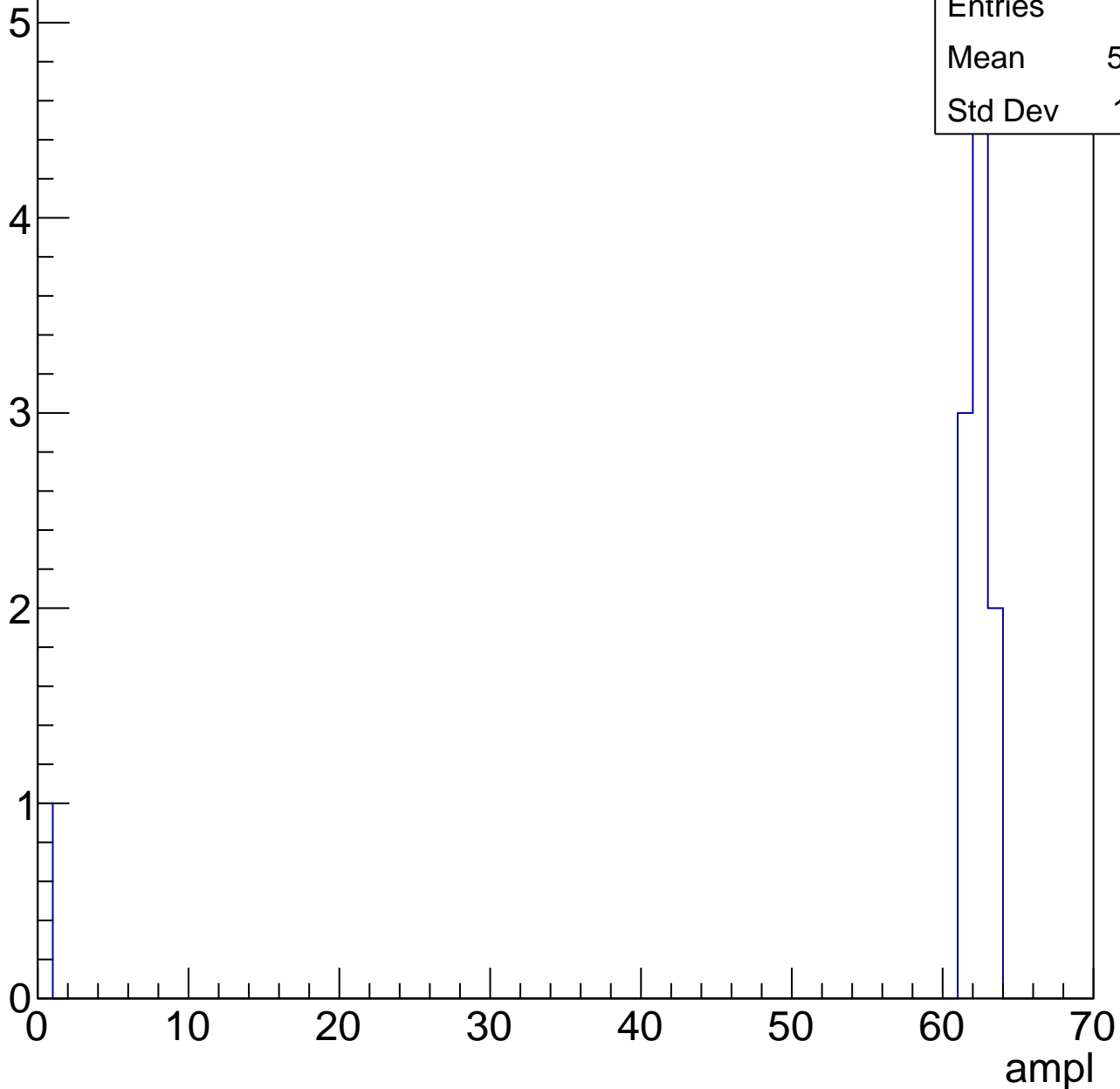
Entries	55
Mean	59.05
Std Dev	2.726

B0L002S, U18-ch51, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	11
Mean	56.27
Std Dev	17.81



B0L002S, U18-ch51, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11
Std Dev	11

B0L002S, U18-ch52, adc0

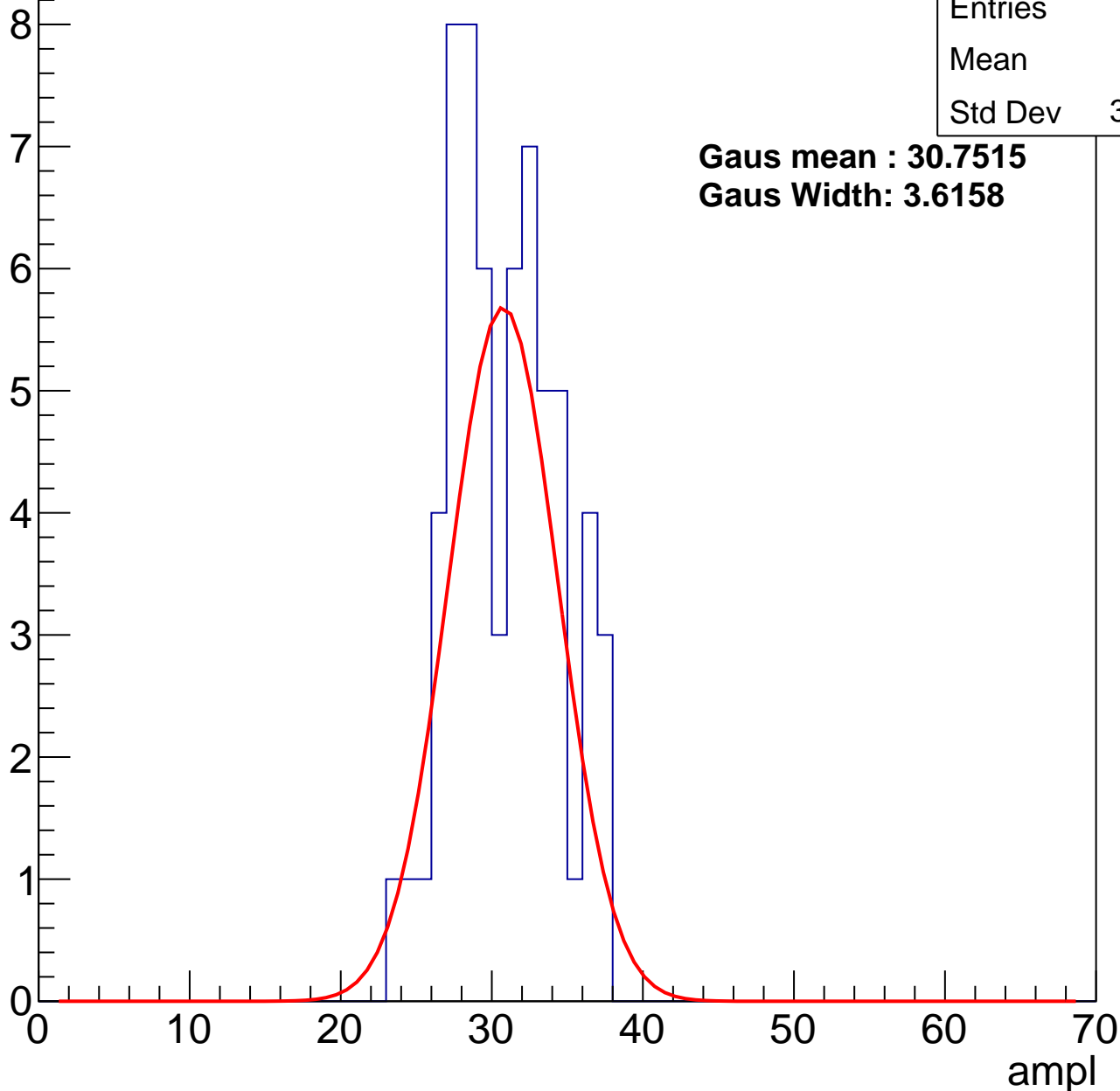
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	30.4
Std Dev	3.448

Gaus mean : 30.7515

Gaus Width: 3.6158



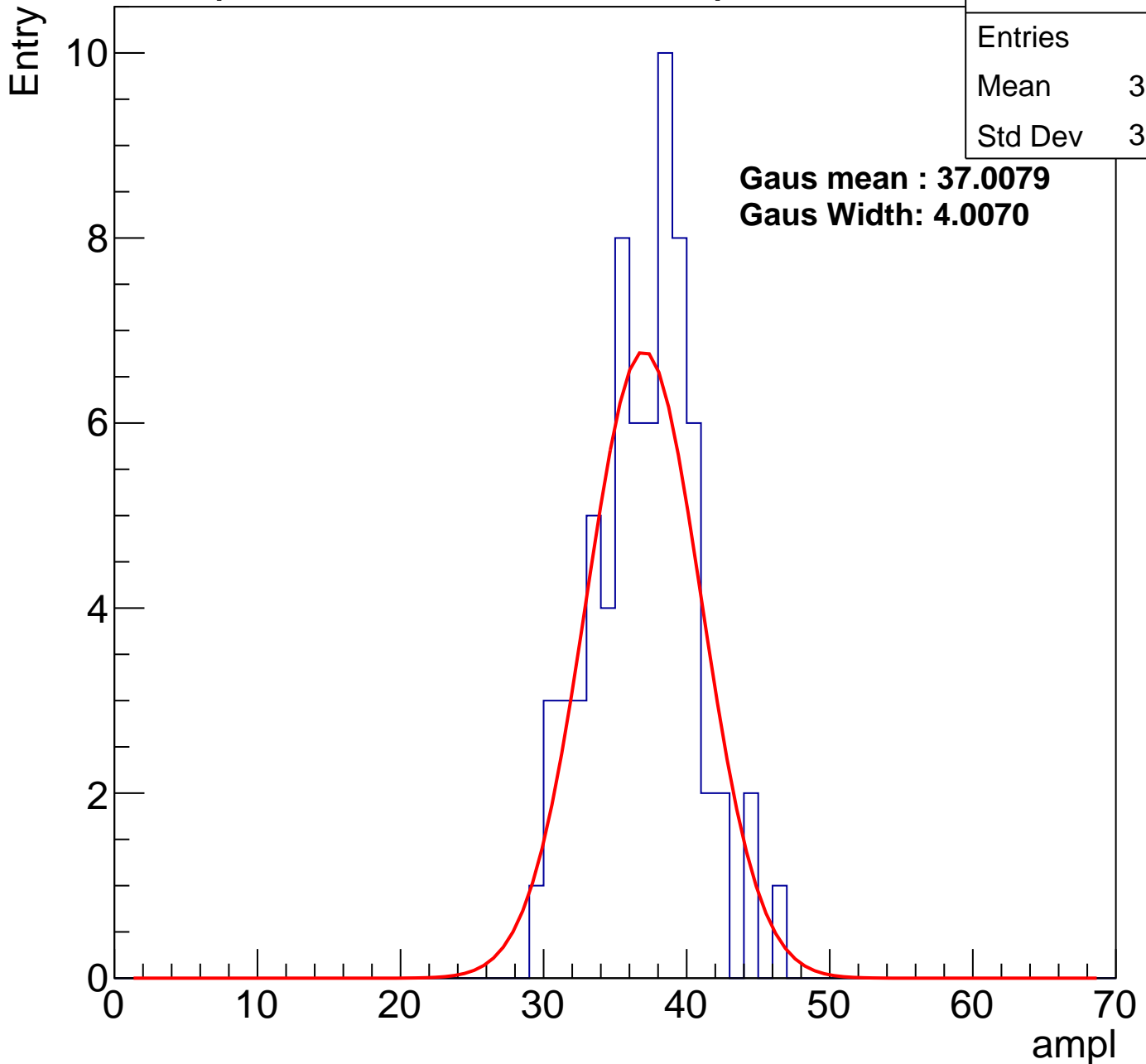
B0L002S, U18-ch52, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	36.56
Std Dev	3.568

Gaus mean : 37.0079

Gaus Width: 4.0070



B0L002S, U18-ch52, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	43.73
Std Dev	3.38

Gaus mean : 44.2697

Gaus Width: 3.5891

Entry

10

8

6

4

2

0

0

10

20

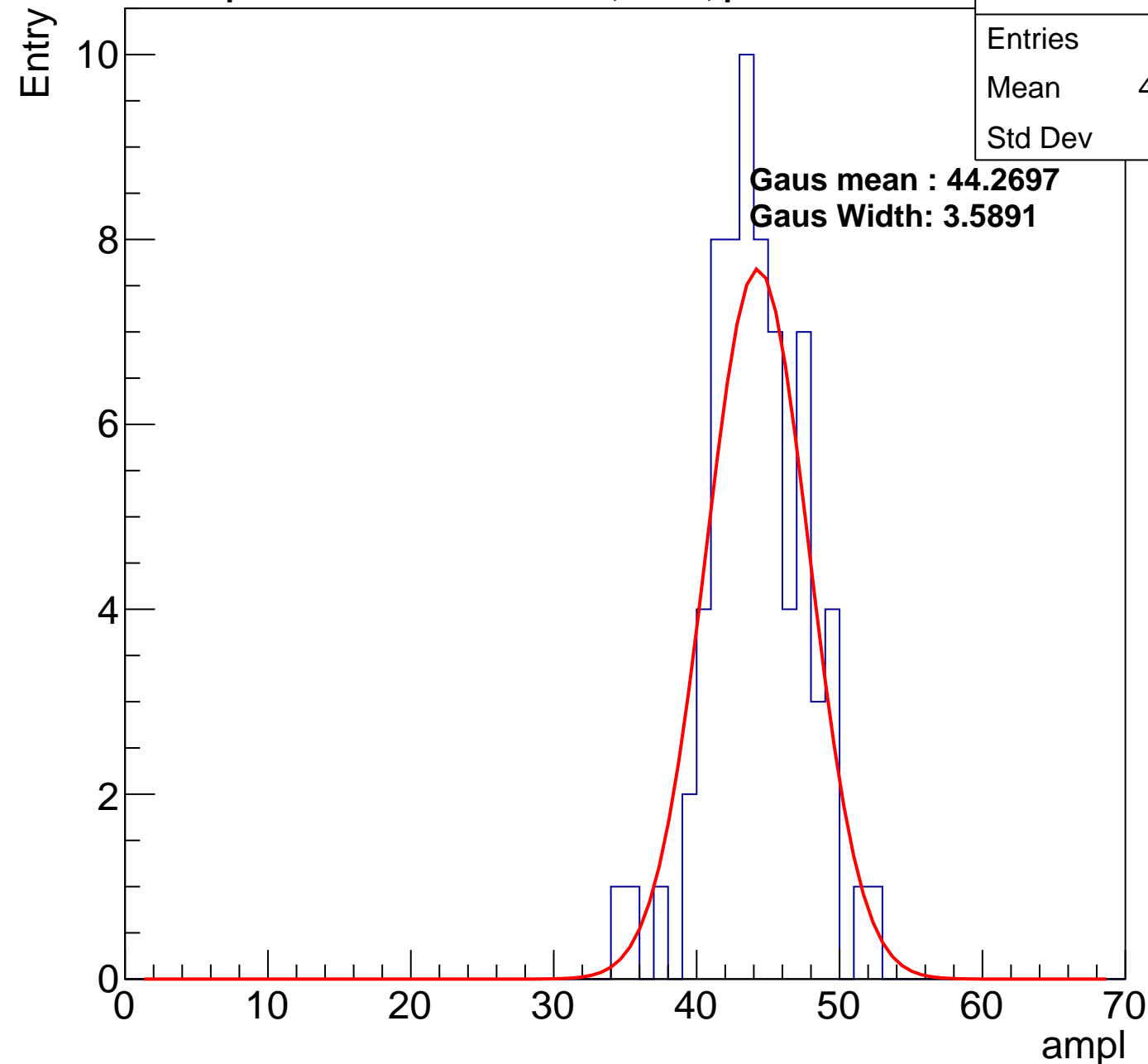
30

40

50

60

ampl

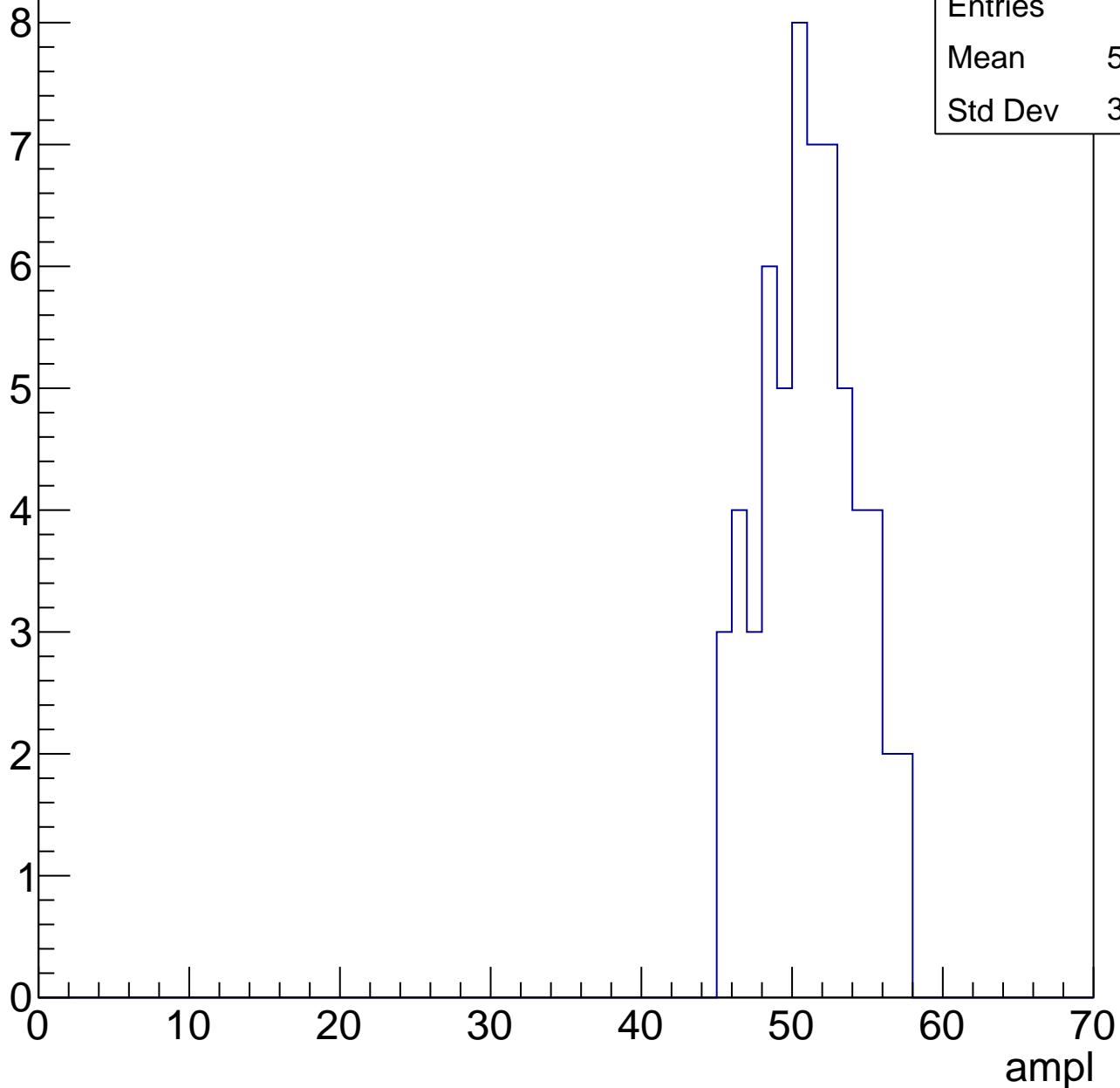


B0L002S, U18-ch52, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	50.68
Std Dev	3.112

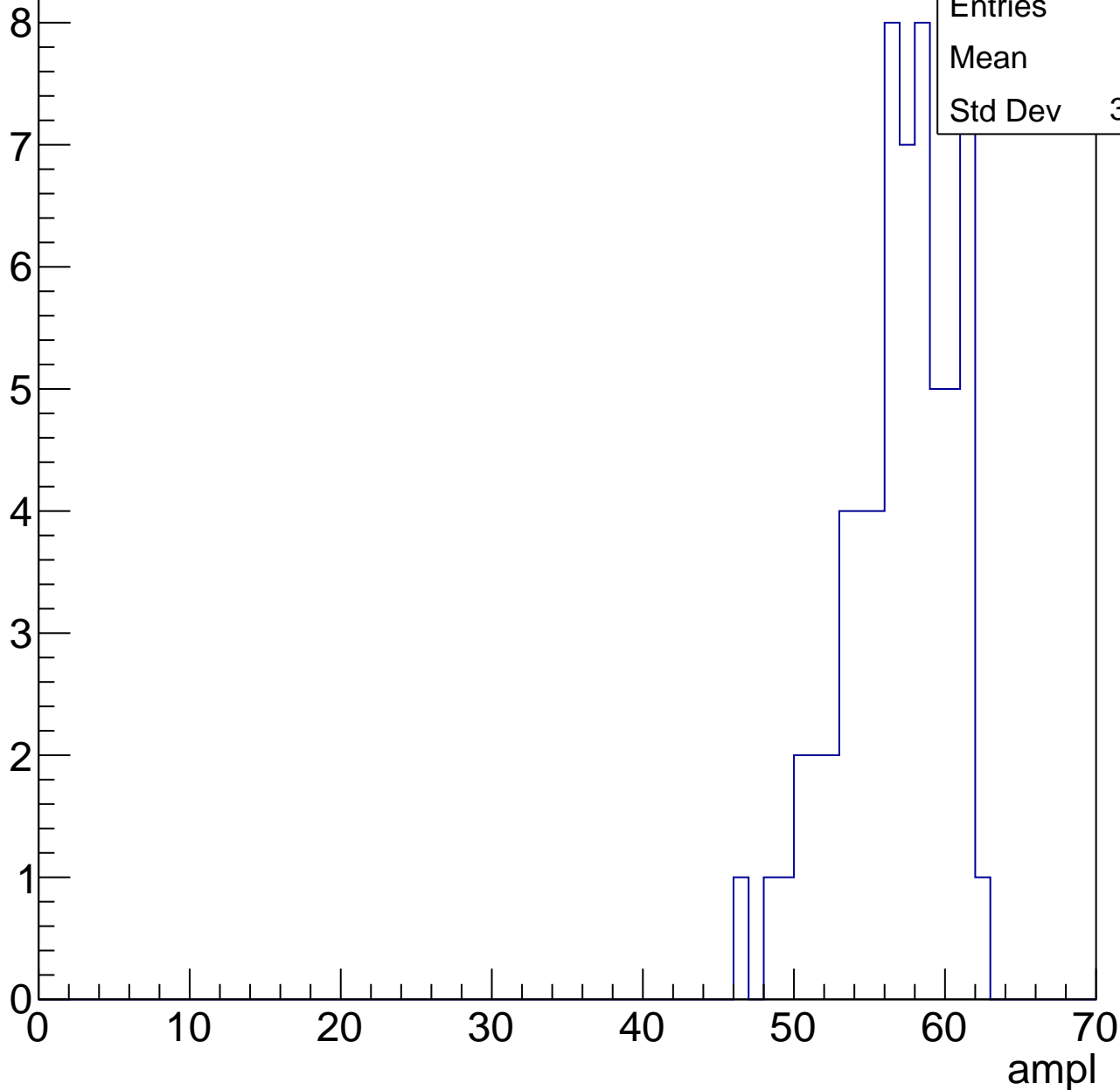


B0L002S, U18-ch52, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.4
Std Dev	3.592

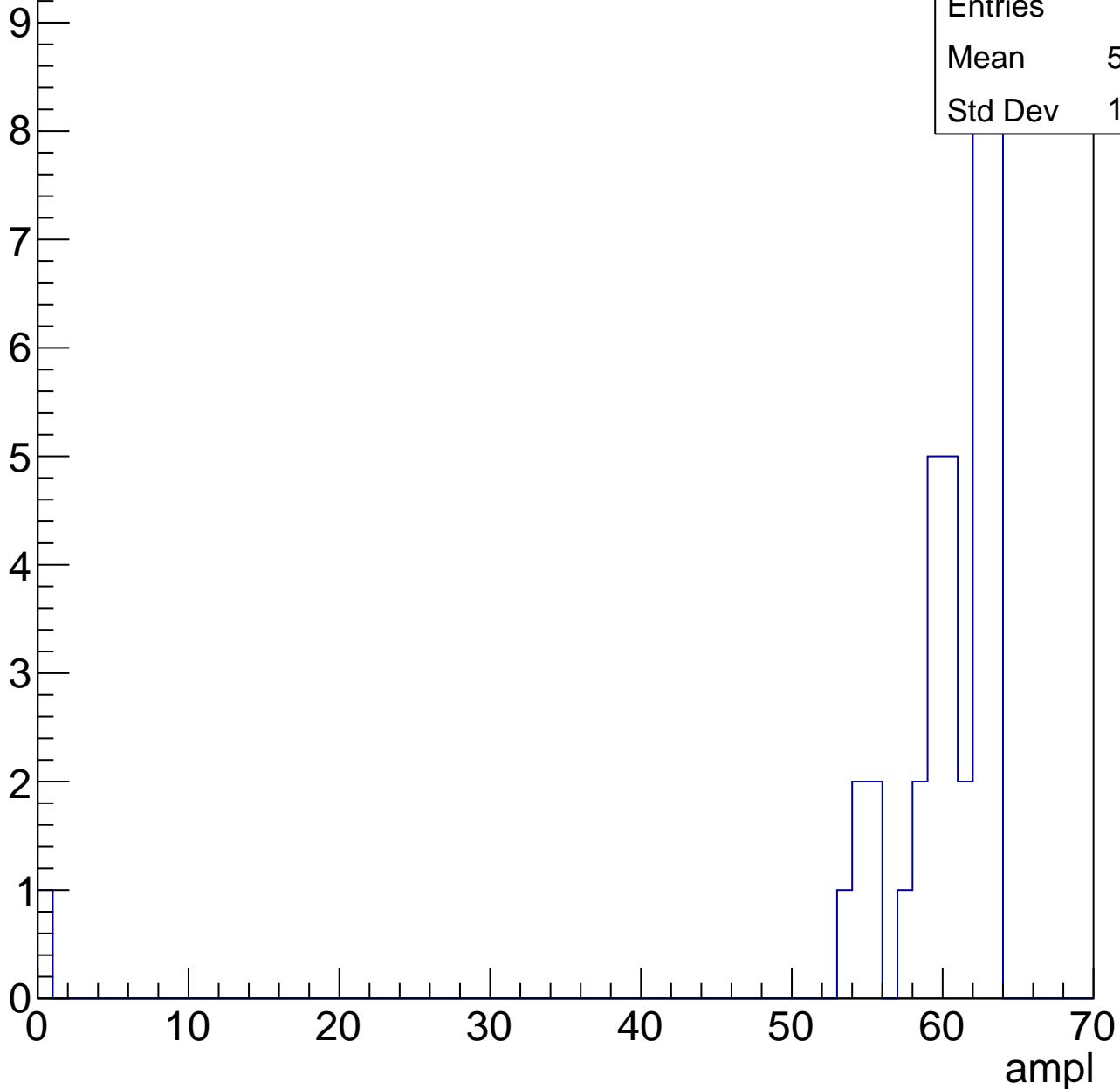


B0L002S, U18-ch52, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.53
Std Dev	10.03



B0L002S, U18-ch52, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch52, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch53, adc0

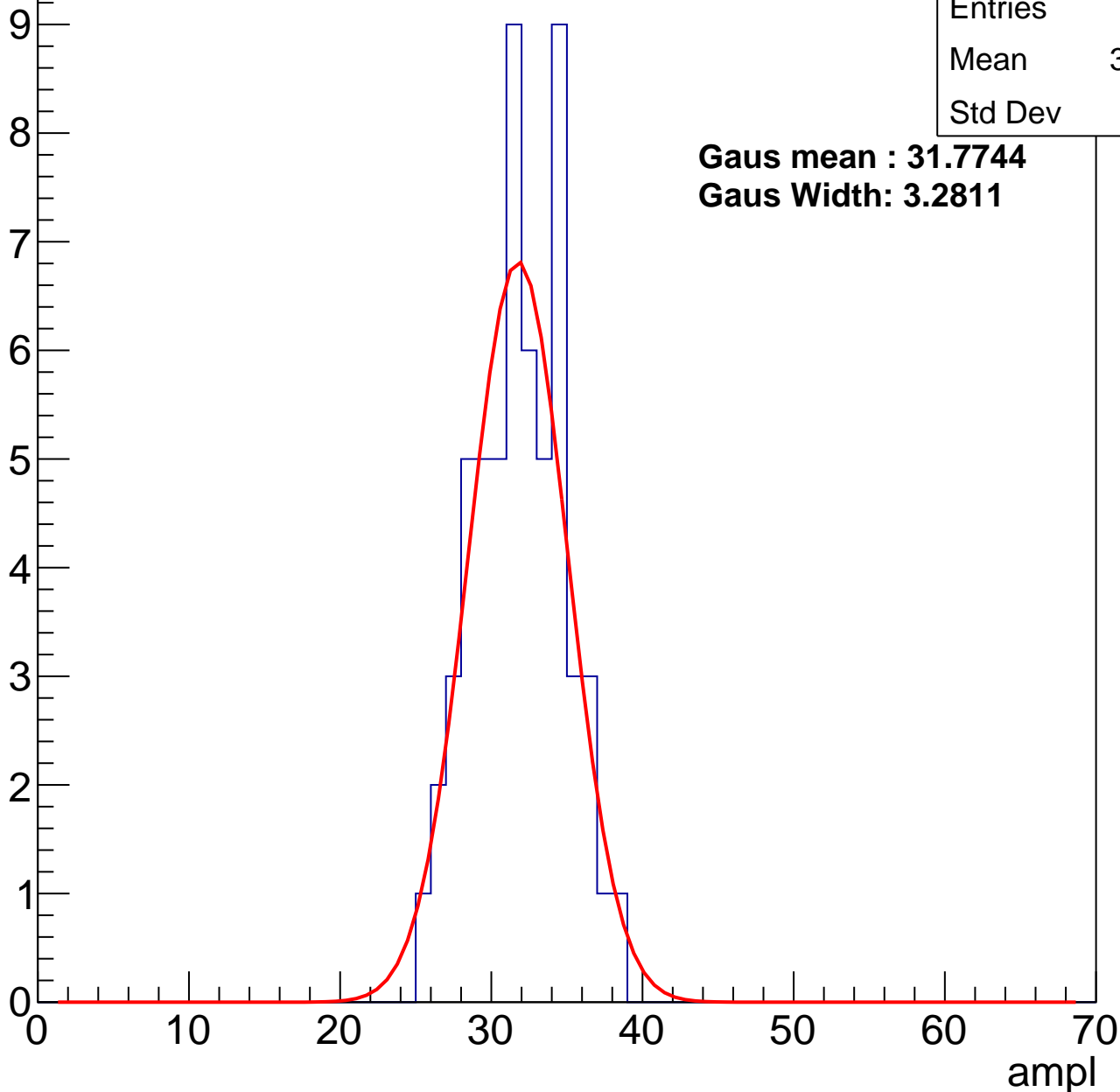
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	31.43
Std Dev	2.96

Gaus mean : 31.7744

Gaus Width: 3.2811



B0L002S, U18-ch53, adc1

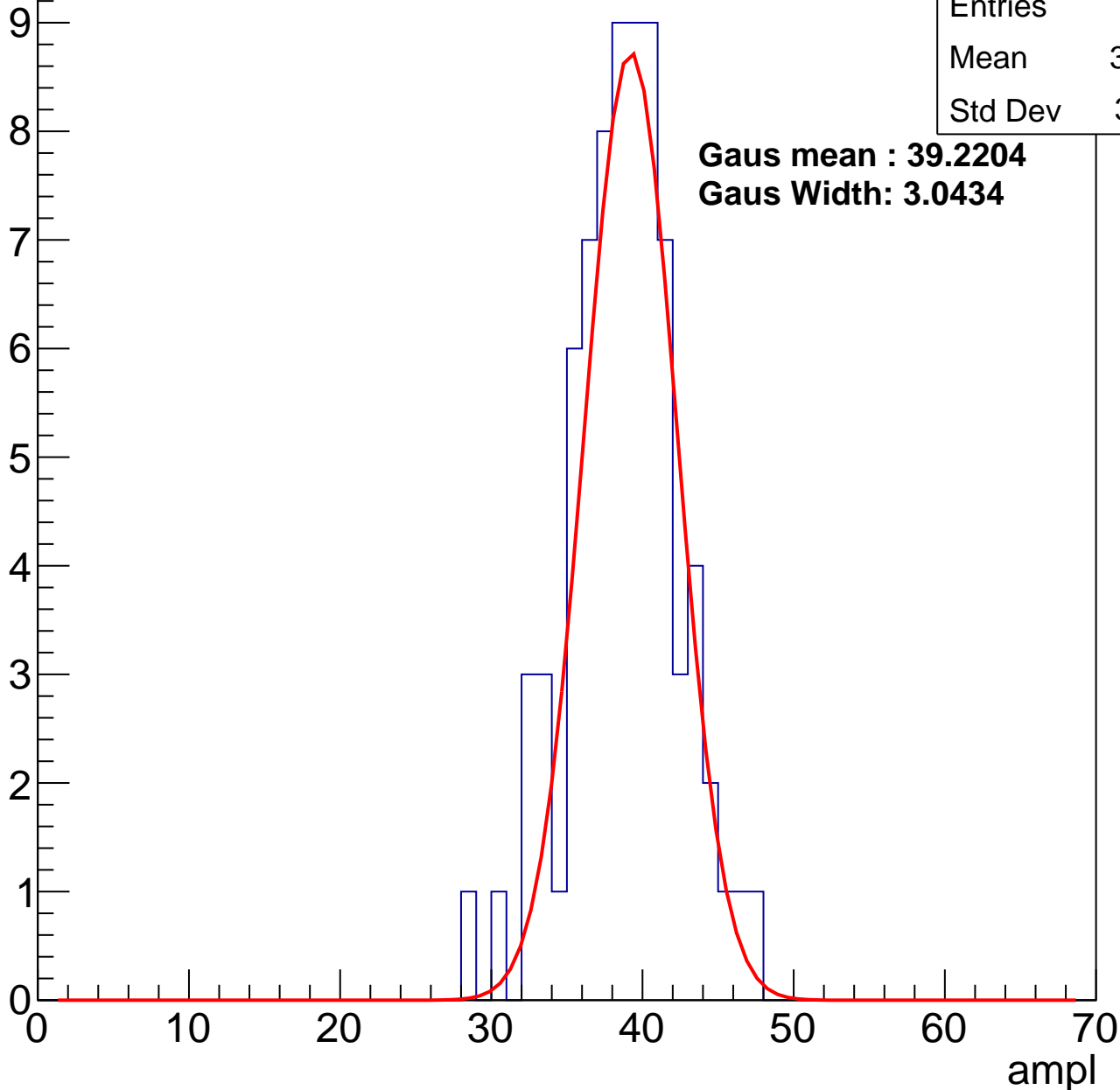
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	76
Mean	38.28
Std Dev	3.571

Gaus mean : 39.2204

Gaus Width: 3.0434



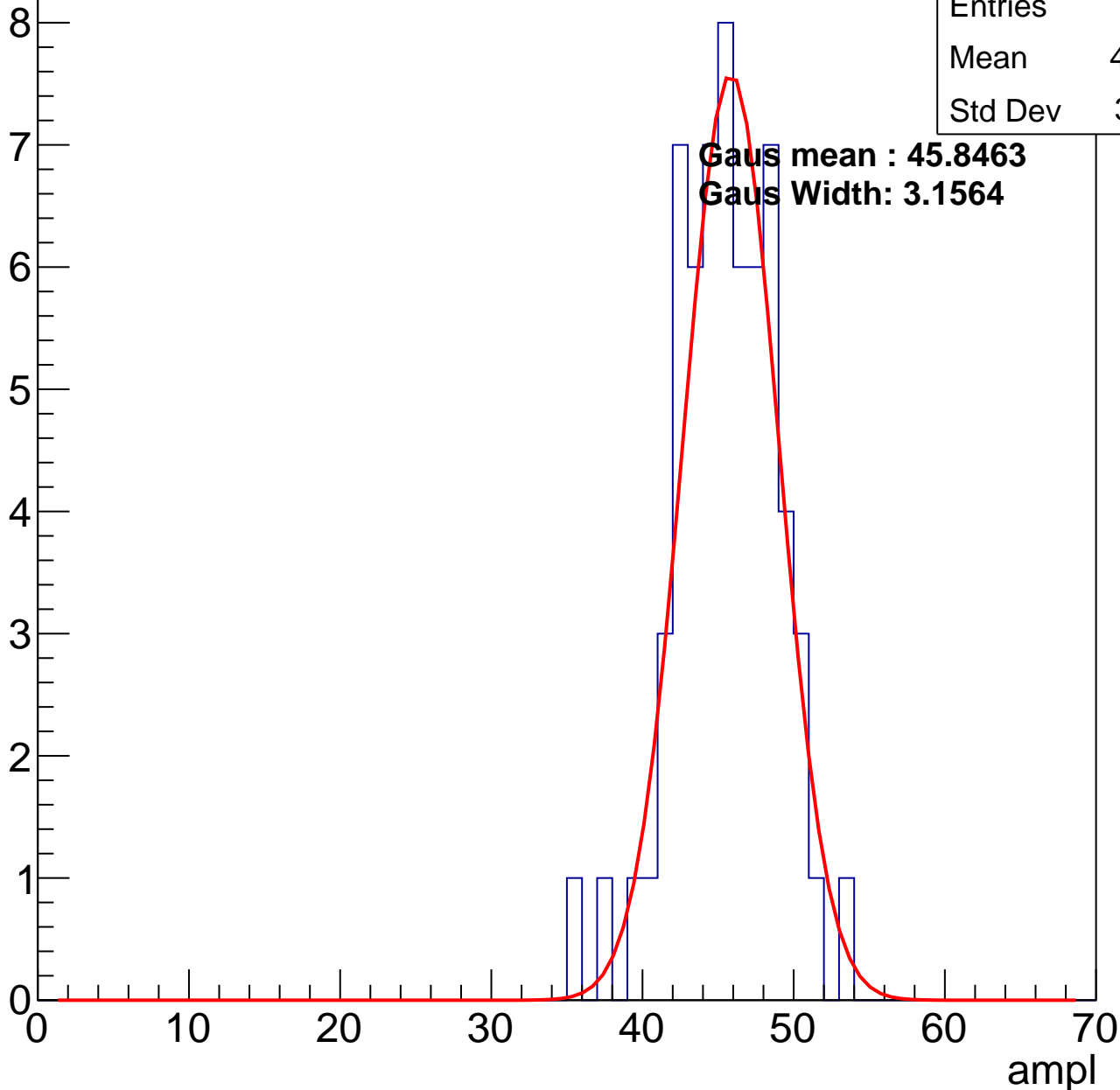
B0L002S, U18-ch53, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	45.05
Std Dev	3.331

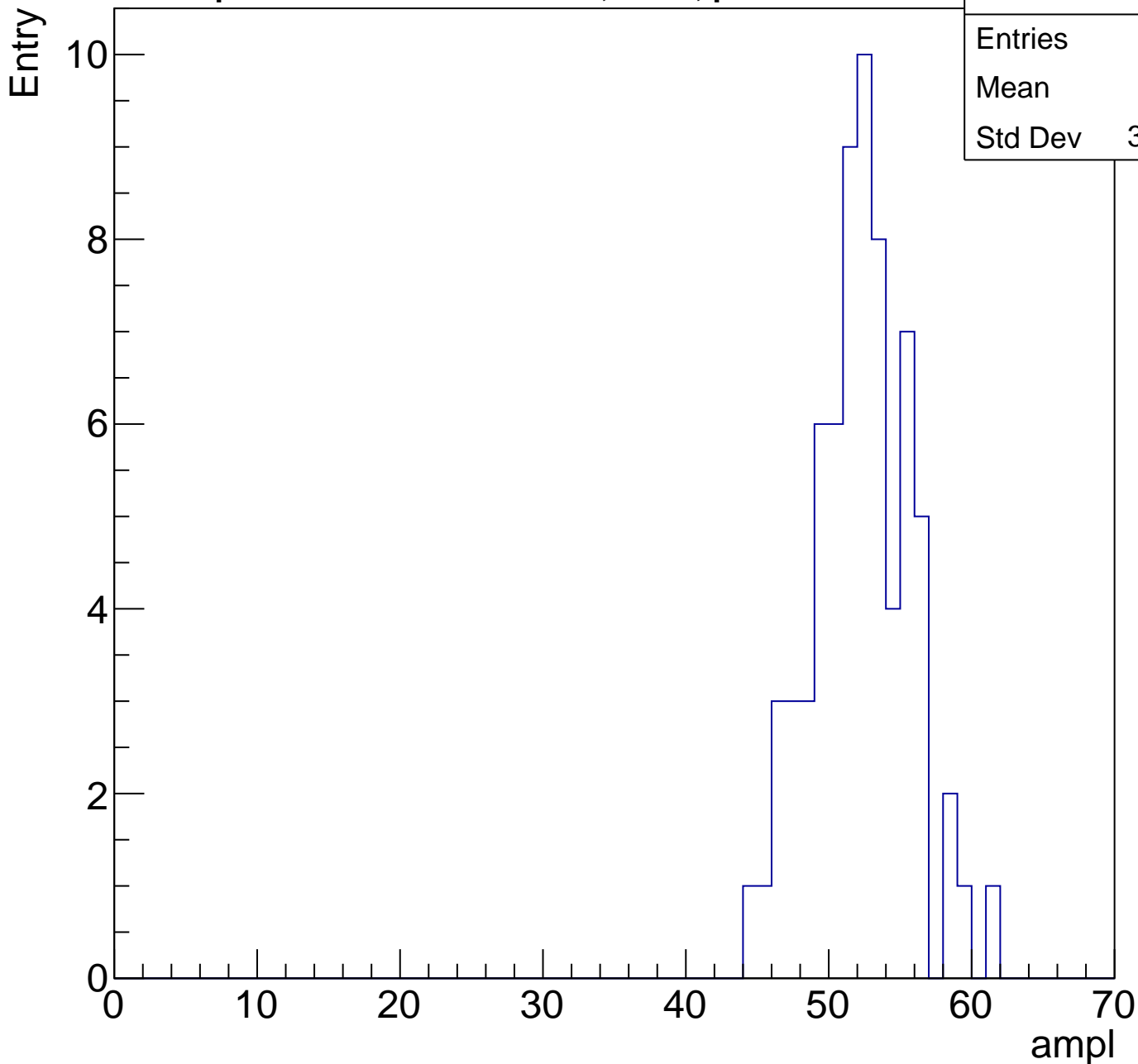
Gaus mean : 45.8463
Gaus Width: 3.1564



B0L002S, U18-ch53, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	70
Mean	51.8
Std Dev	3.375

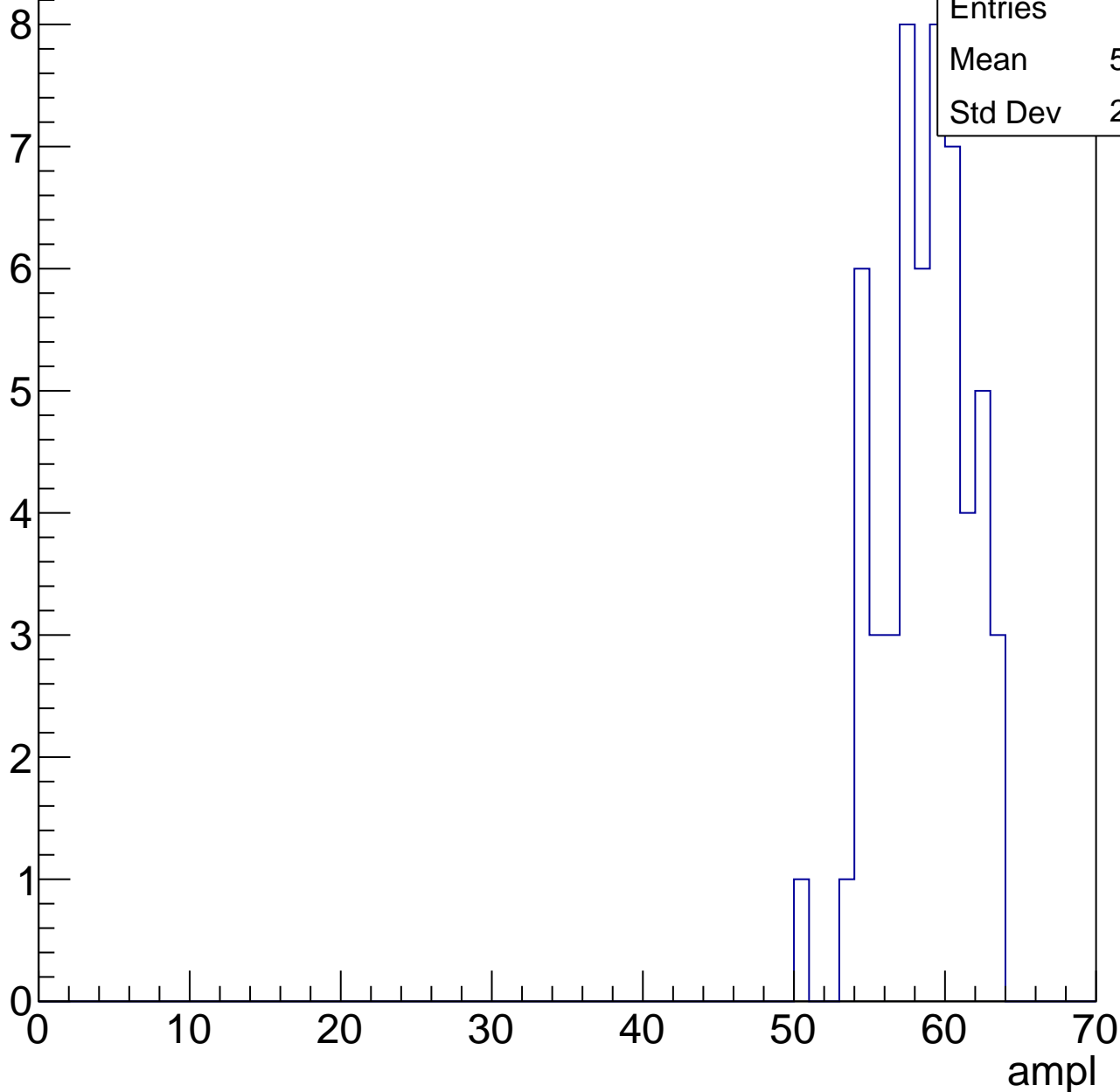


B0L002S, U18-ch53, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	58.16
Std Dev	2.884

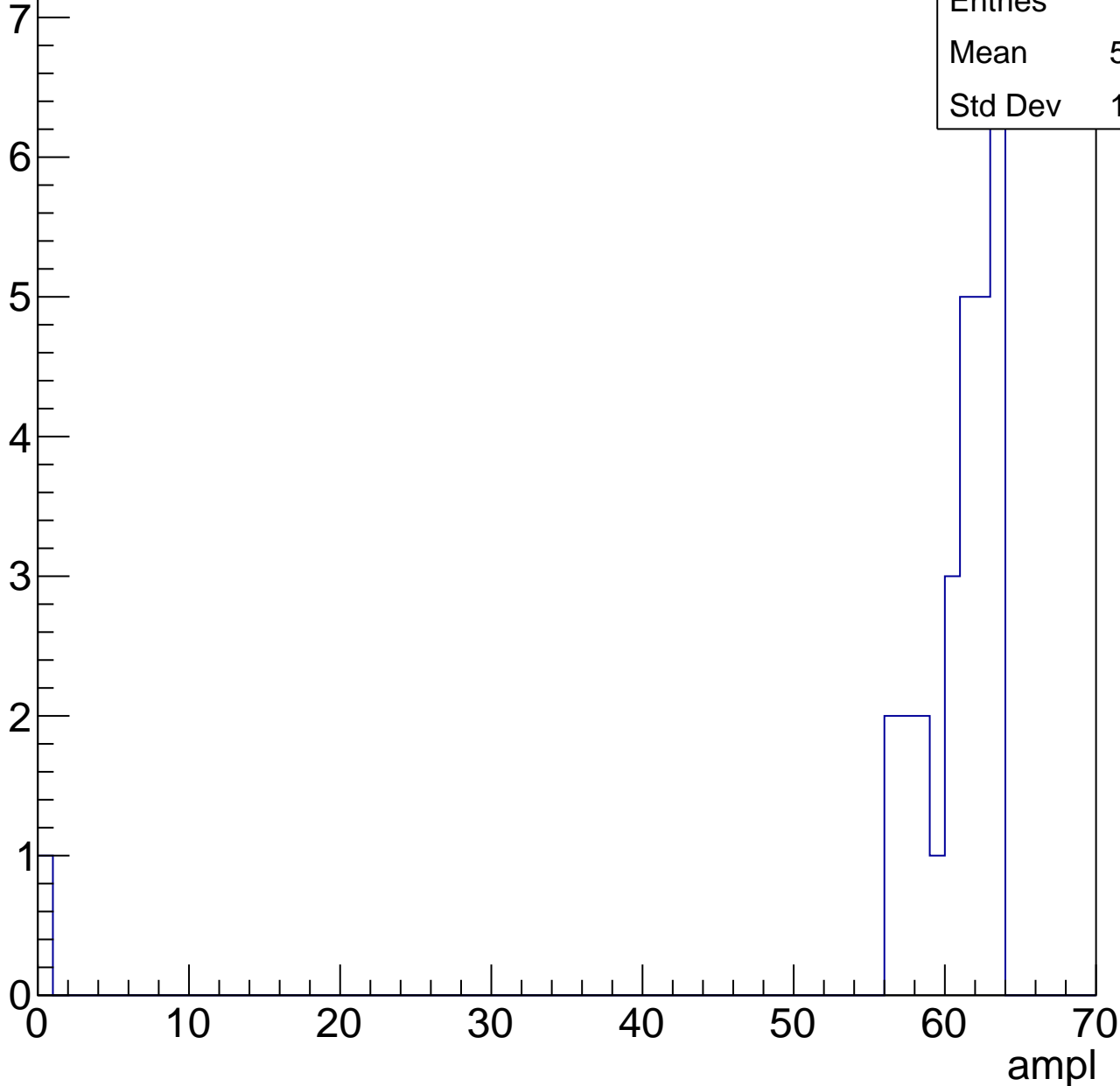


B0L002S, U18-ch53, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	28
Mean	58.46
Std Dev	11.47



B0L002S, U18-ch53, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch53, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch54, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	60
Mean	30.35
Std Dev	4.878

Gaus mean : 31.2573

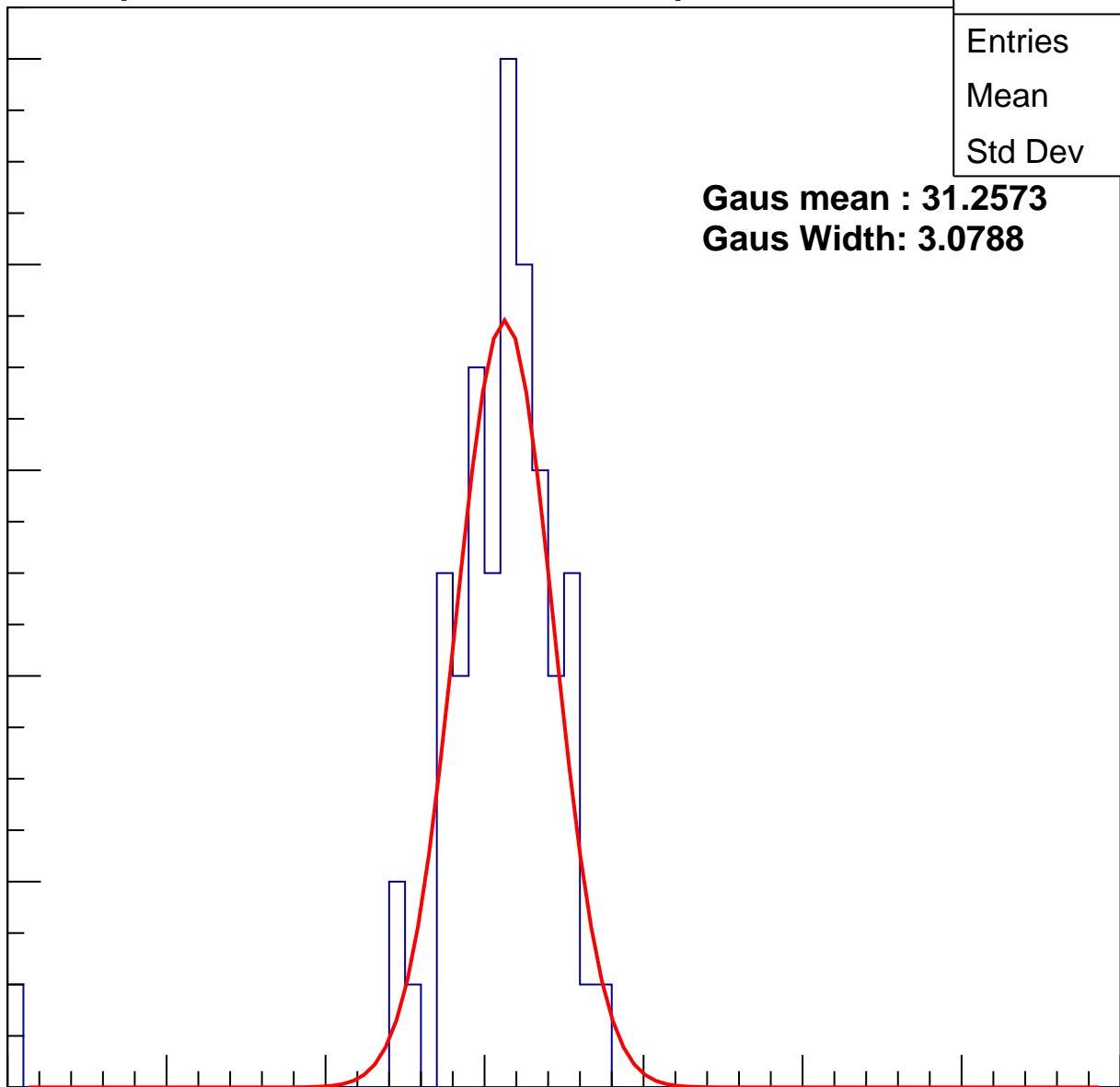
Gaus Width: 3.0788

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch54, adc1

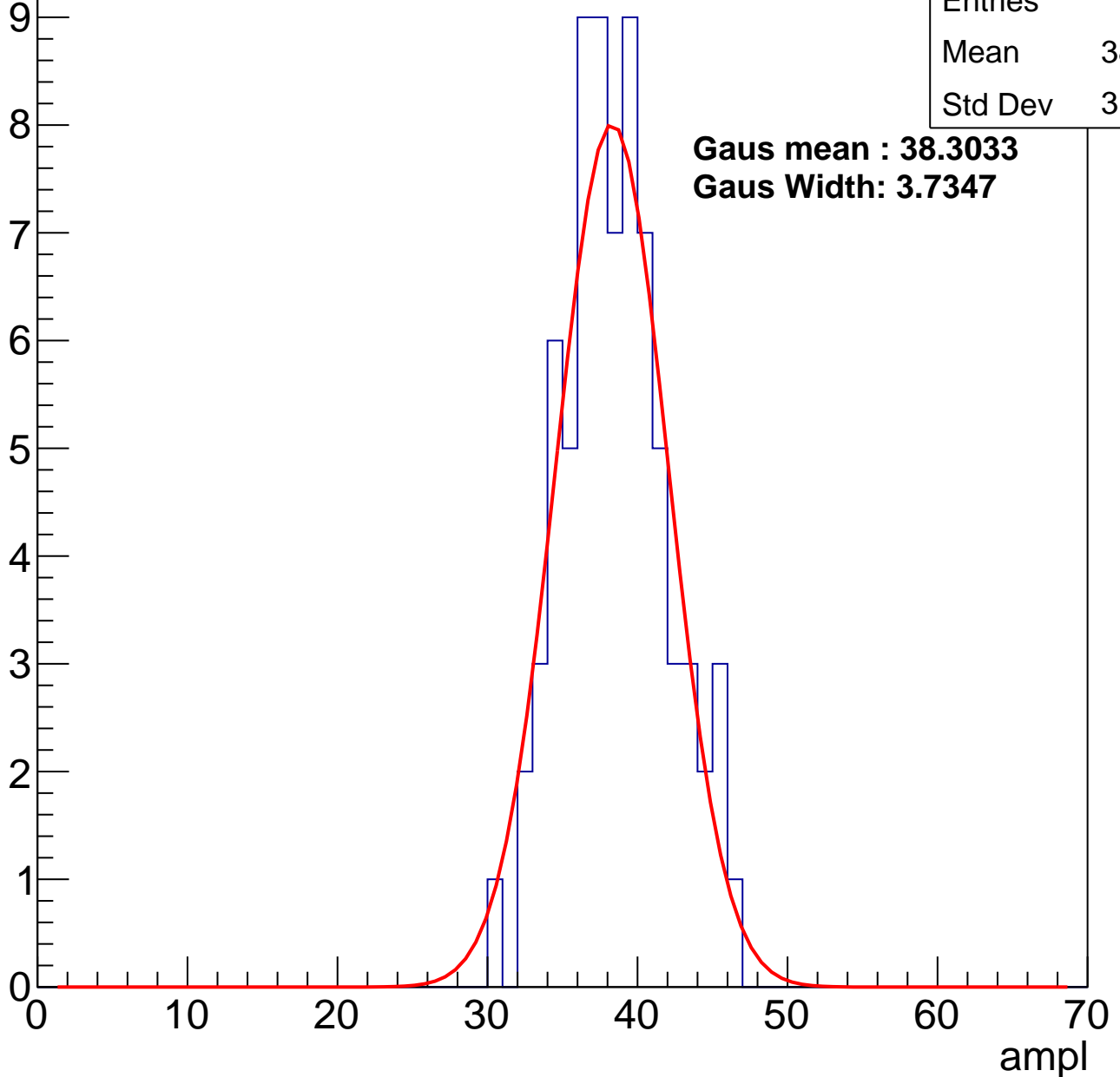
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	38.07
Std Dev	3.434

Gaus mean : 38.3033

Gaus Width: 3.7347



B0L002S, U18-ch54, adc2

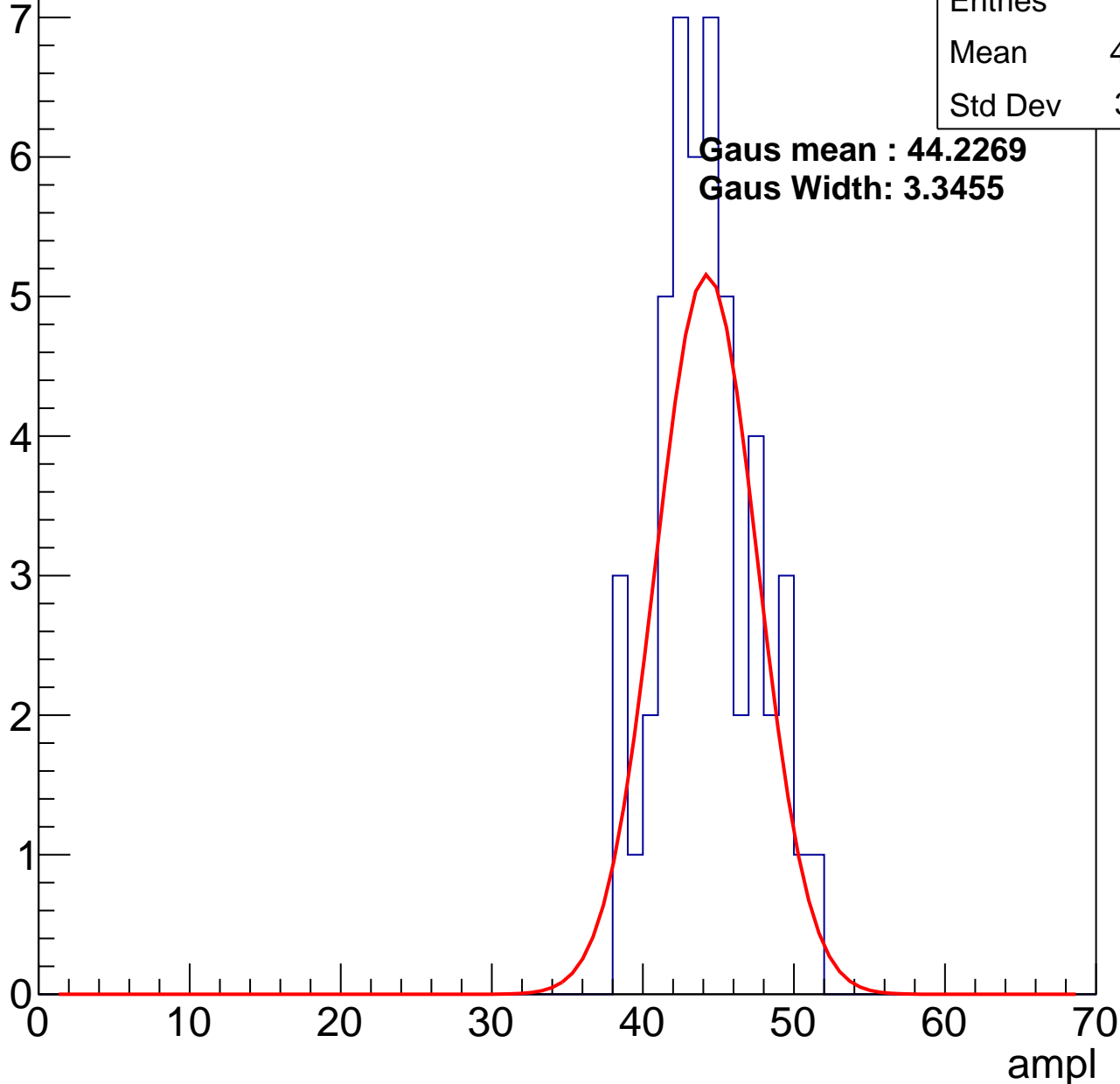
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	43.82
Std Dev	3.141

Gaus mean : 44.2269

Gaus Width: 3.3455

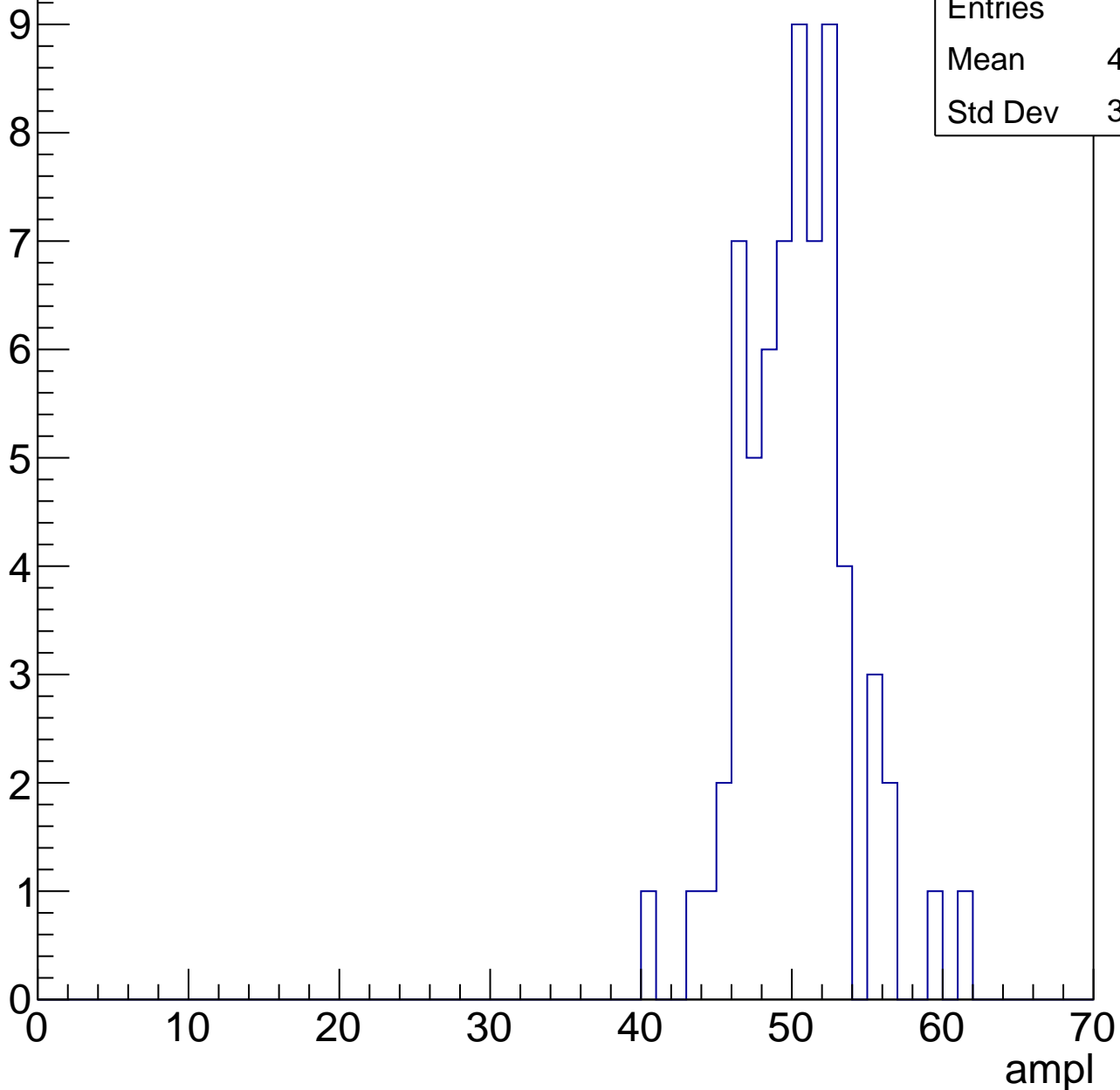


B0L002S, U18-ch54, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	49.83
Std Dev	3.587

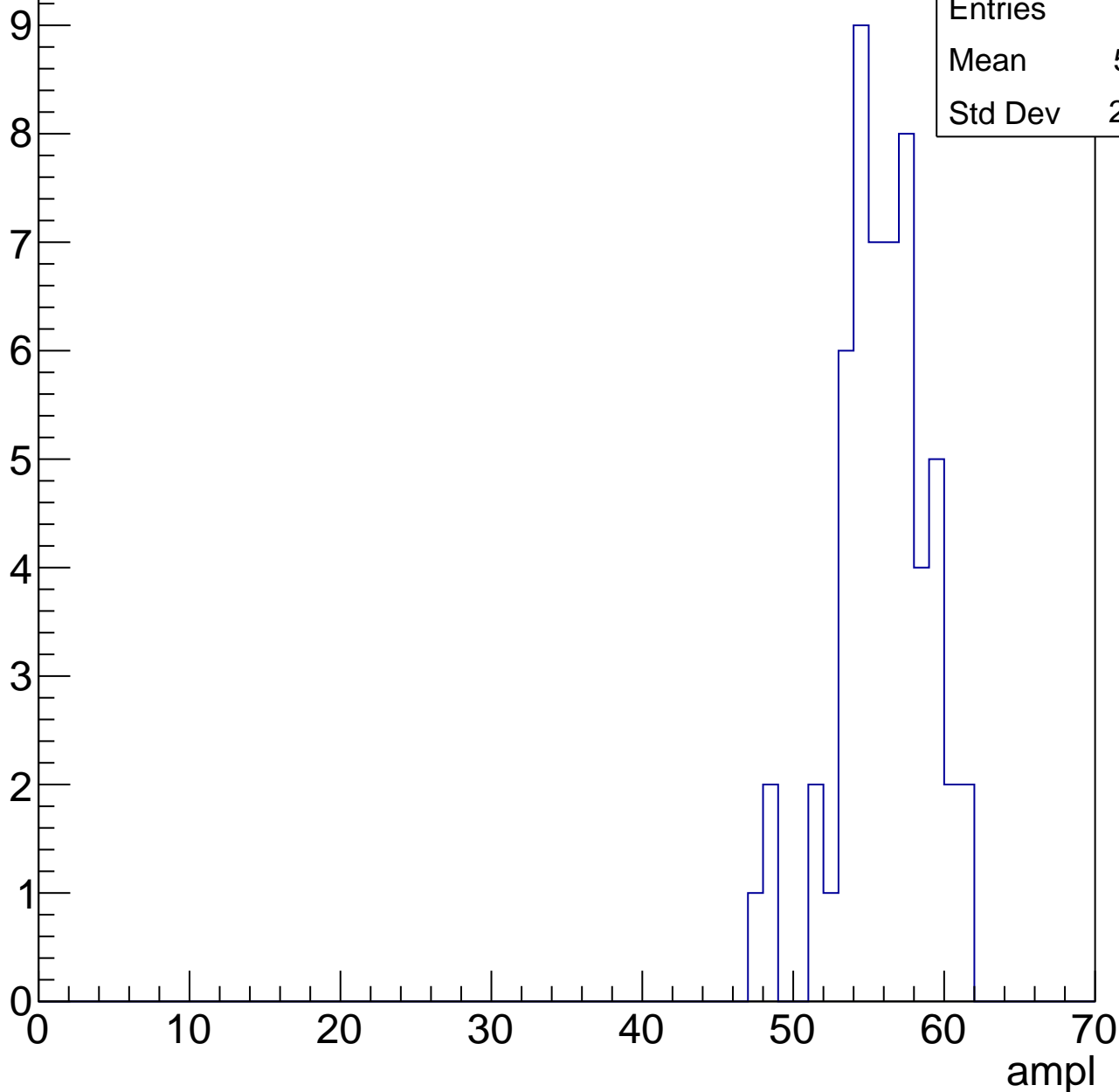


B0L002S, U18-ch54, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.41
Std Dev	2.999



B0L002S, U18-ch54, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	49
Mean	59.24
Std Dev	8.81

Entry

10

8

6

4

2

0

0

10

20

30

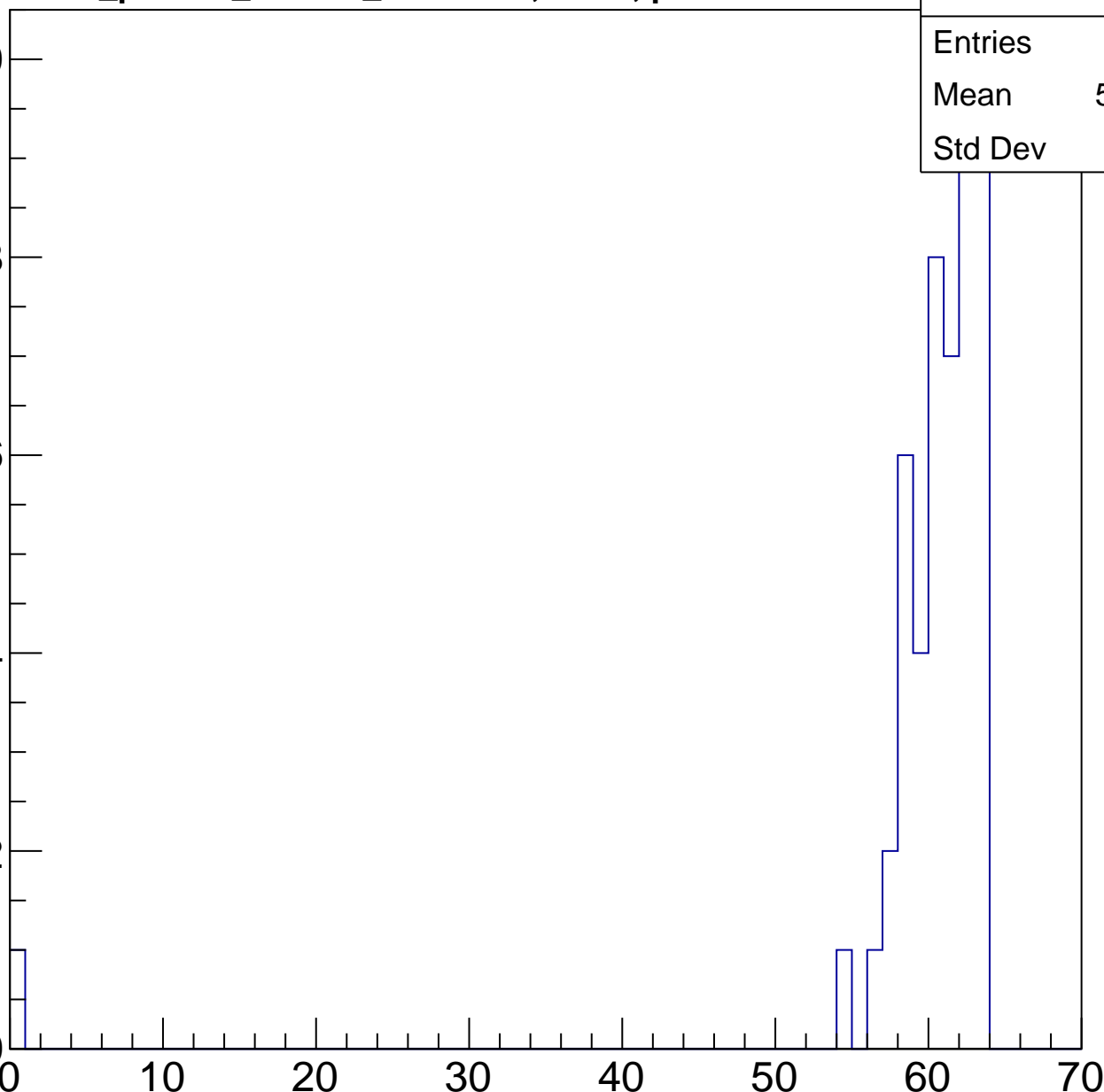
40

50

ampl

60

70



B0L002S, U18-ch54, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	62
Std Dev	0

B0L002S, U18-ch54, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	0
Std Dev	0

B0L002S, U18-ch55, adc0

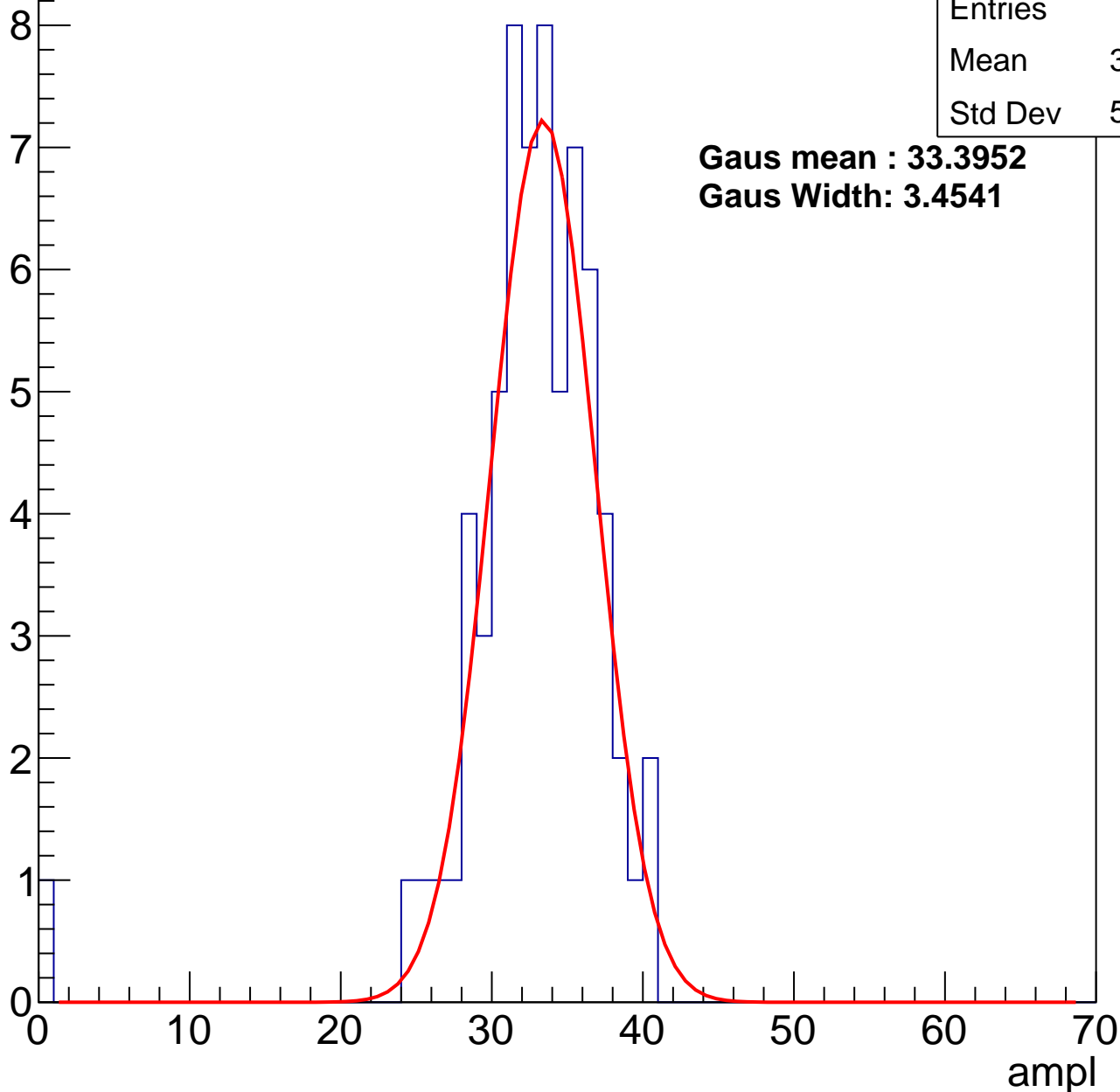
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	32.25
Std Dev	5.253

Gaus mean : 33.3952

Gaus Width: 3.4541



B0L002S, U18-ch55, adc1

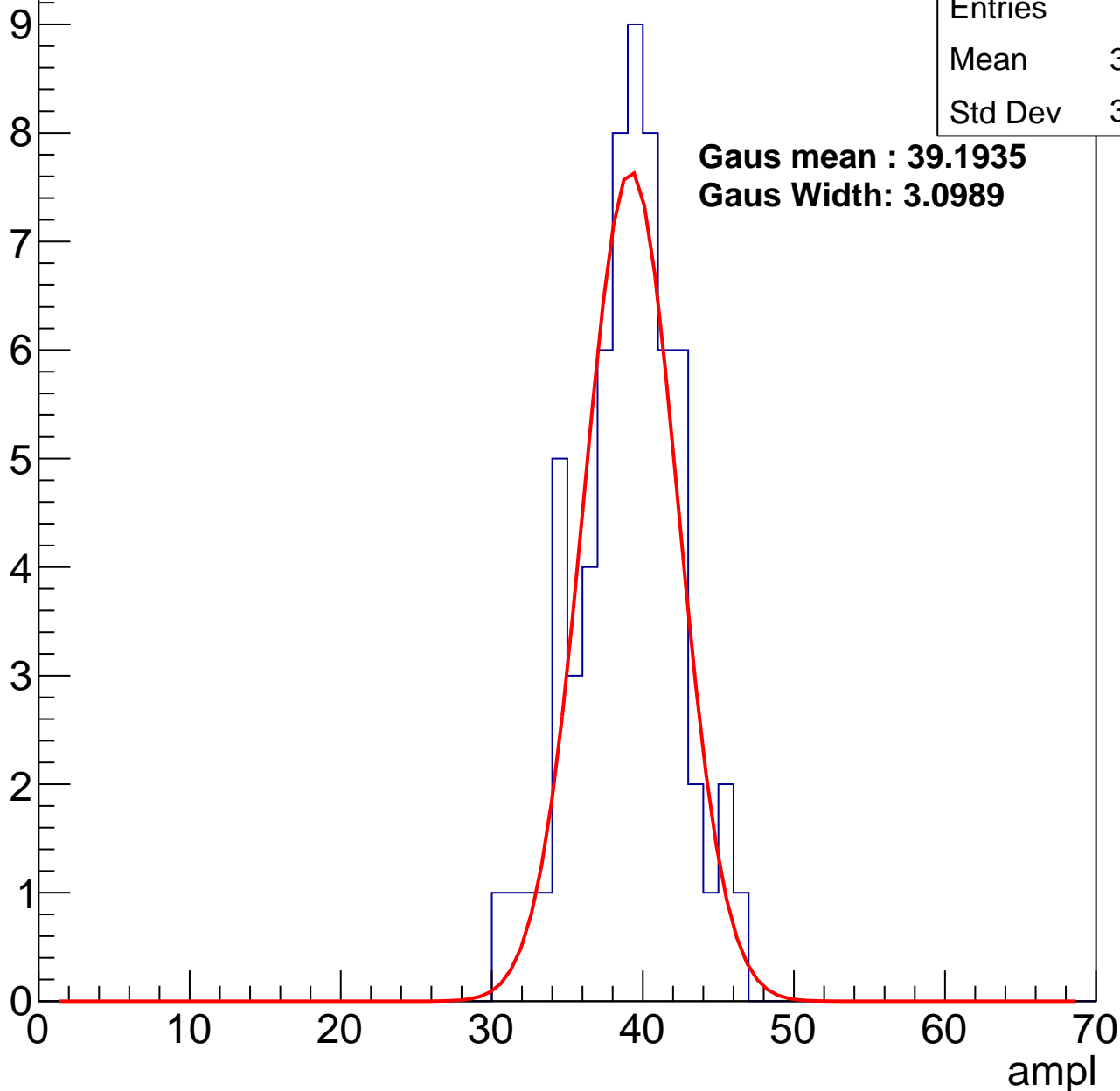
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	38.55
Std Dev	3.333

Gaus mean : 39.1935

Gaus Width: 3.0989



B0L002S, U18-ch55, adc2

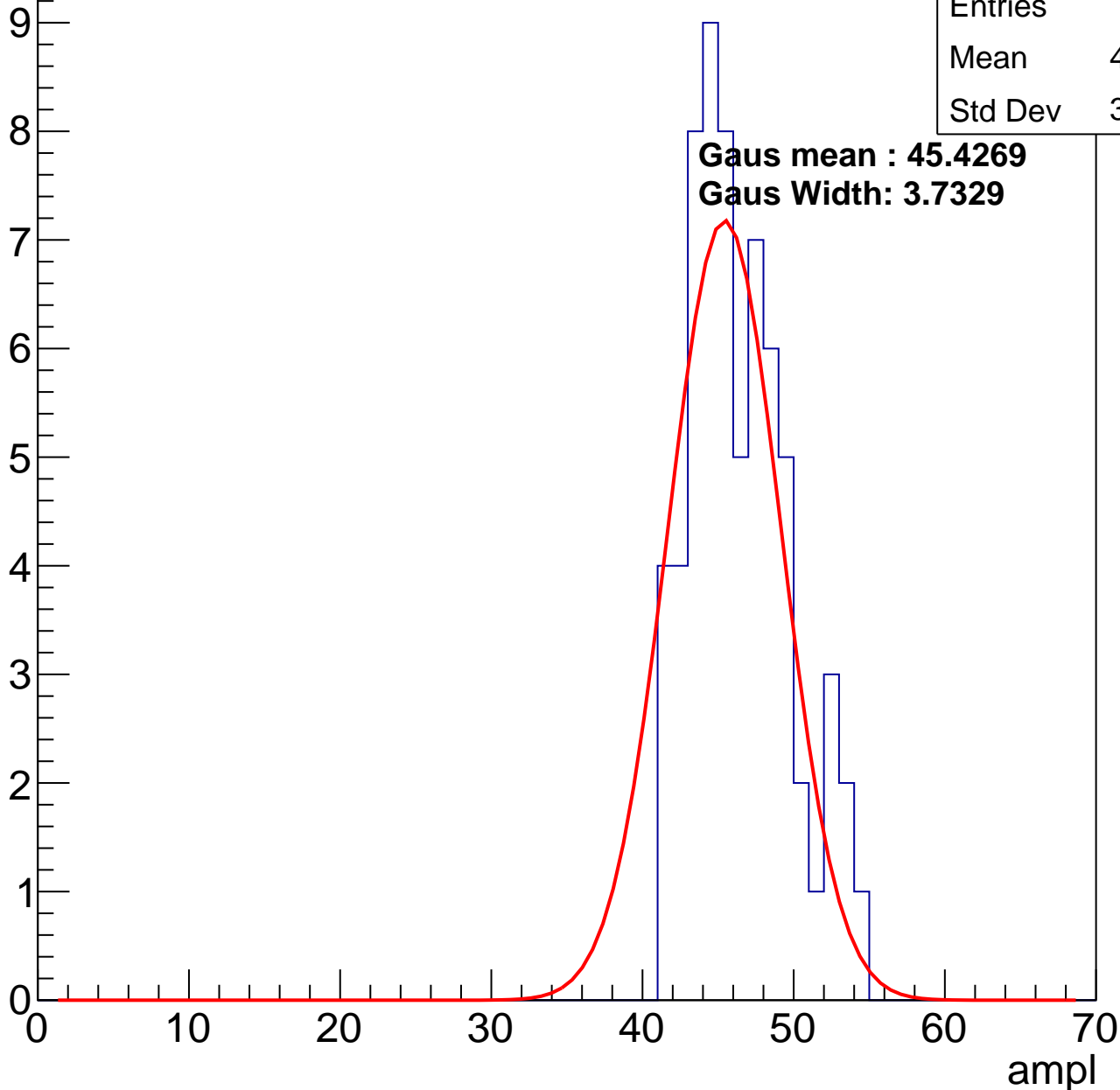
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	46.02
Std Dev	3.242

Gaus mean : 45.4269

Gaus Width: 3.7329

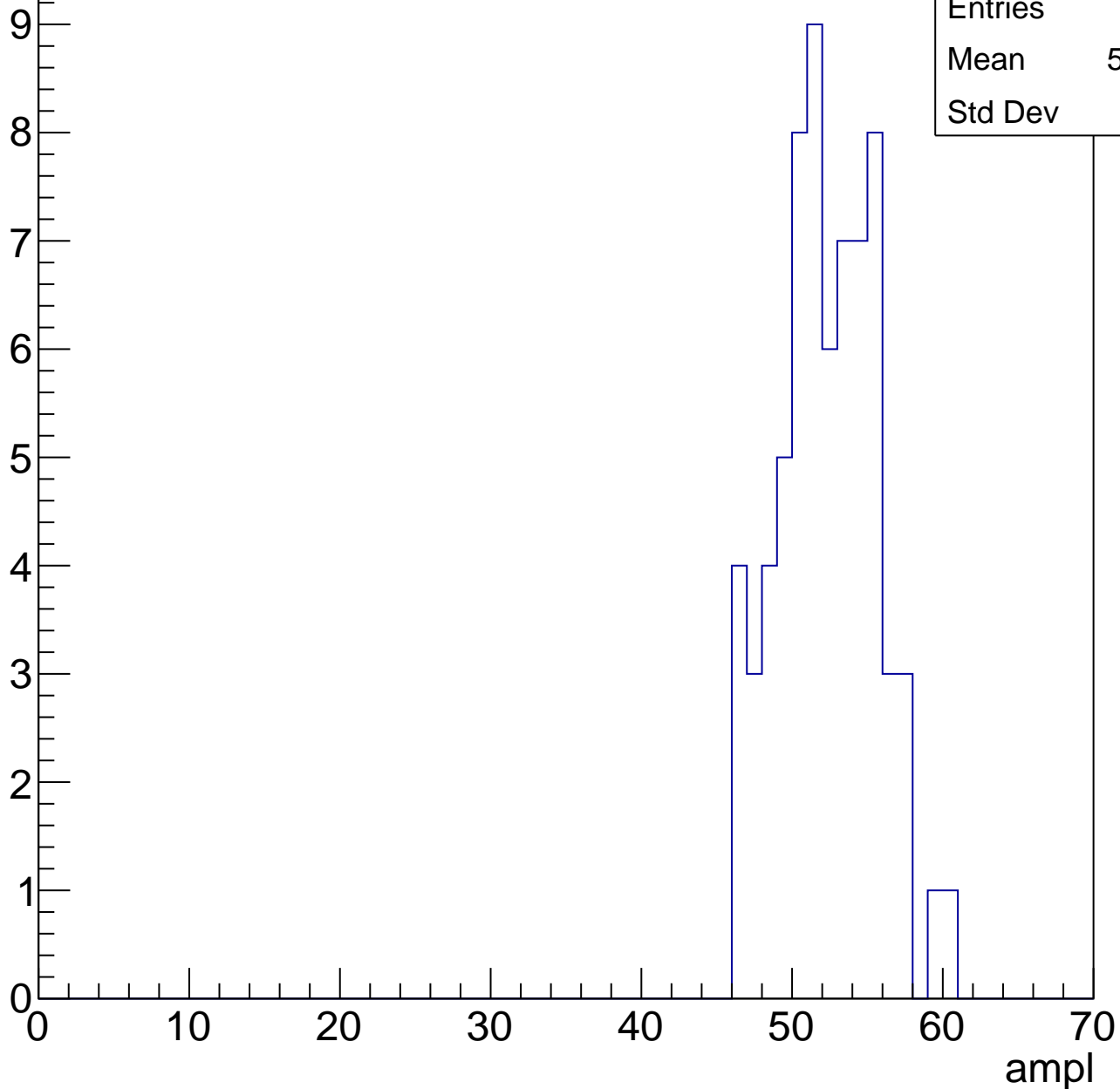


B0L002S, U18-ch55, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	51.88
Std Dev	3.21

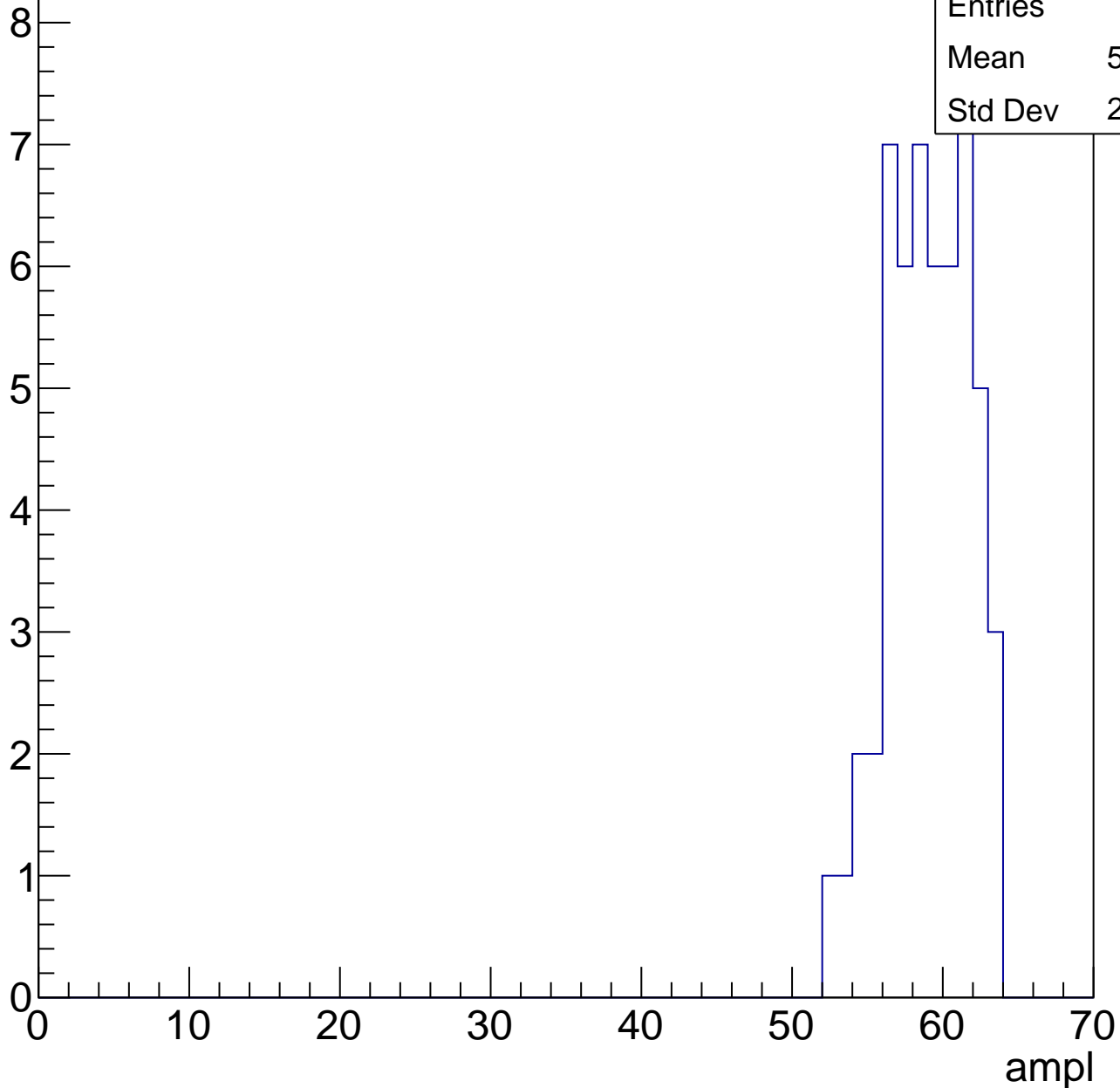


B0L002S, U18-ch55, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	58.59
Std Dev	2.663

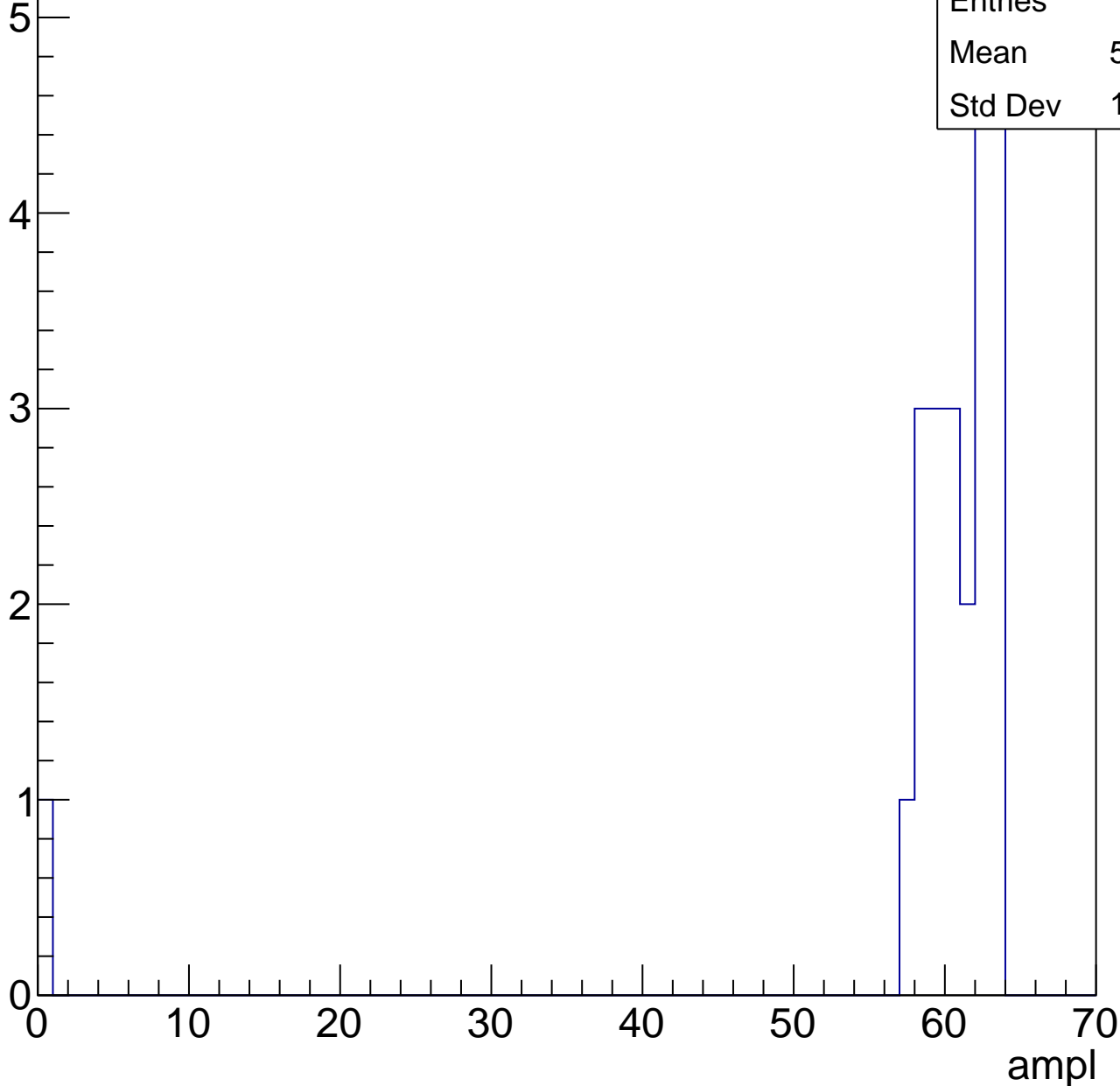


B0L002S, U18-ch55, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	23
Mean	58.04
Std Dev	12.52



B0L002S, U18-ch55, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch55, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch56, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	77
Mean	29.3
Std Dev	5.071

Gaus mean : 30.0745

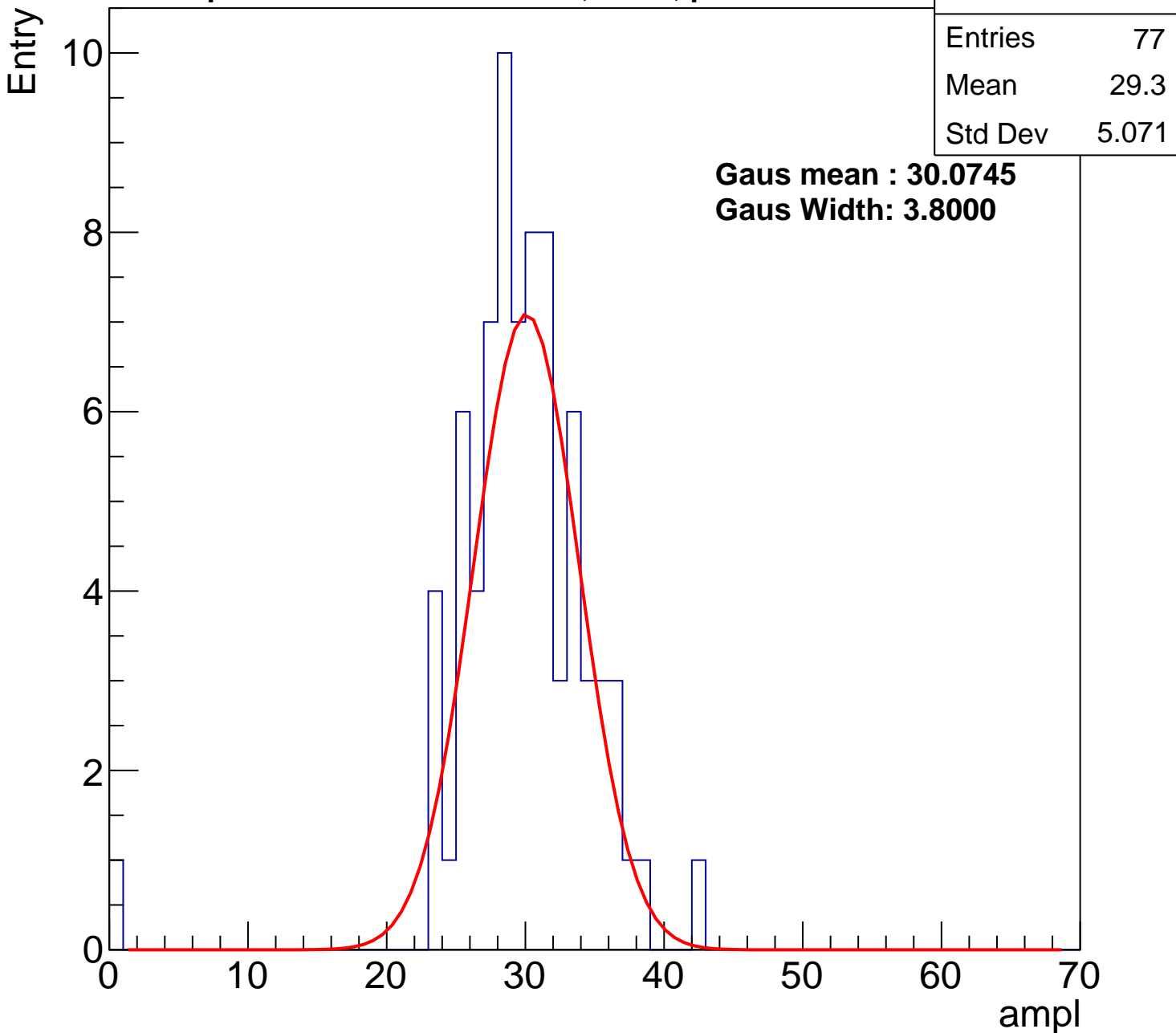
Gaus Width: 3.8000

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch56, adc1

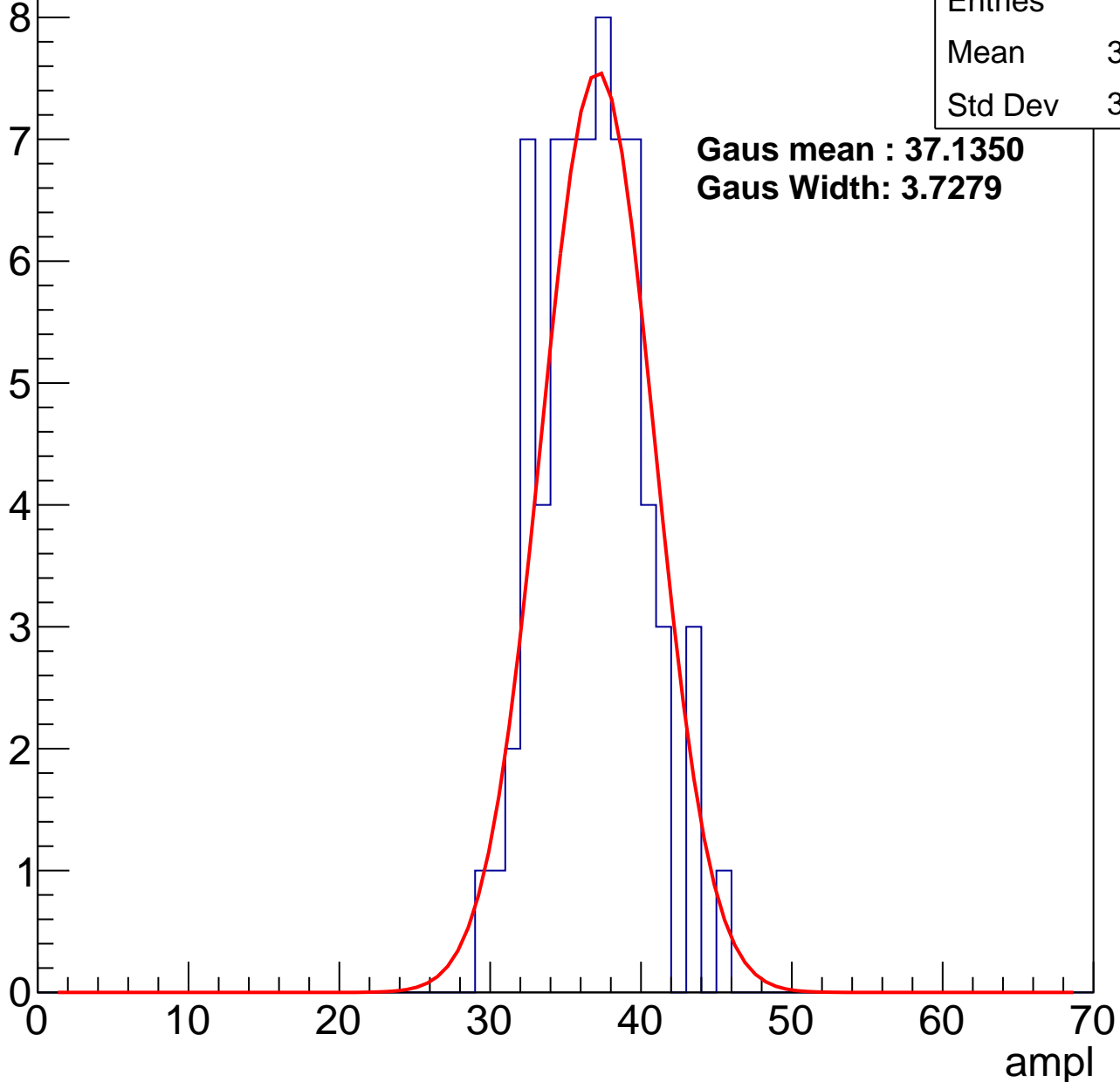
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	36.29
Std Dev	3.358

Gaus mean : 37.1350

Gaus Width: 3.7279



B0L002S, U18-ch56, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	43.78
Std Dev	3.094

Gaus mean : 44.2328

Gaus Width: 3.4781

Entry

10

8

6

4

2

0

0

10

20

30

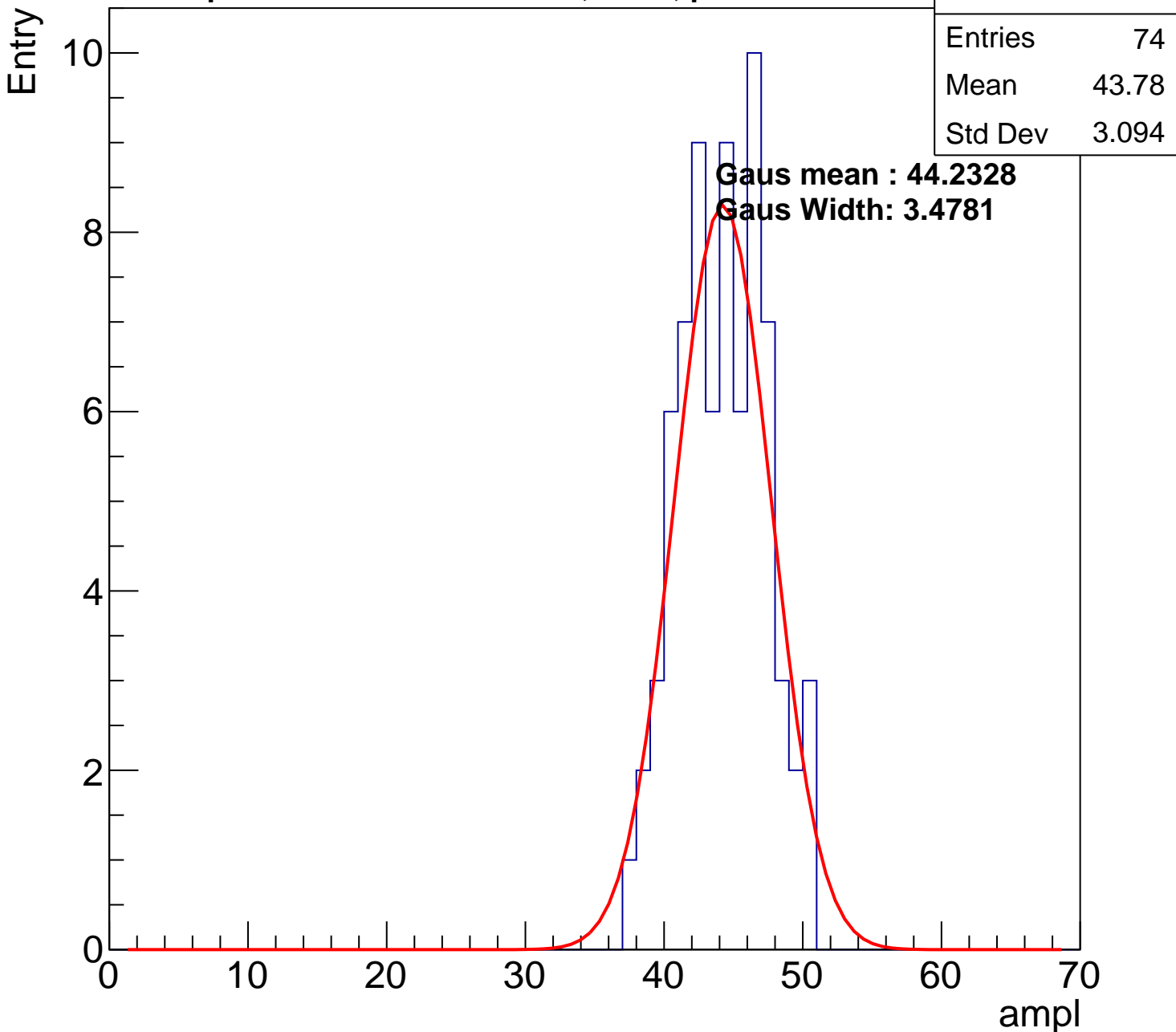
40

50

60

70

ampl

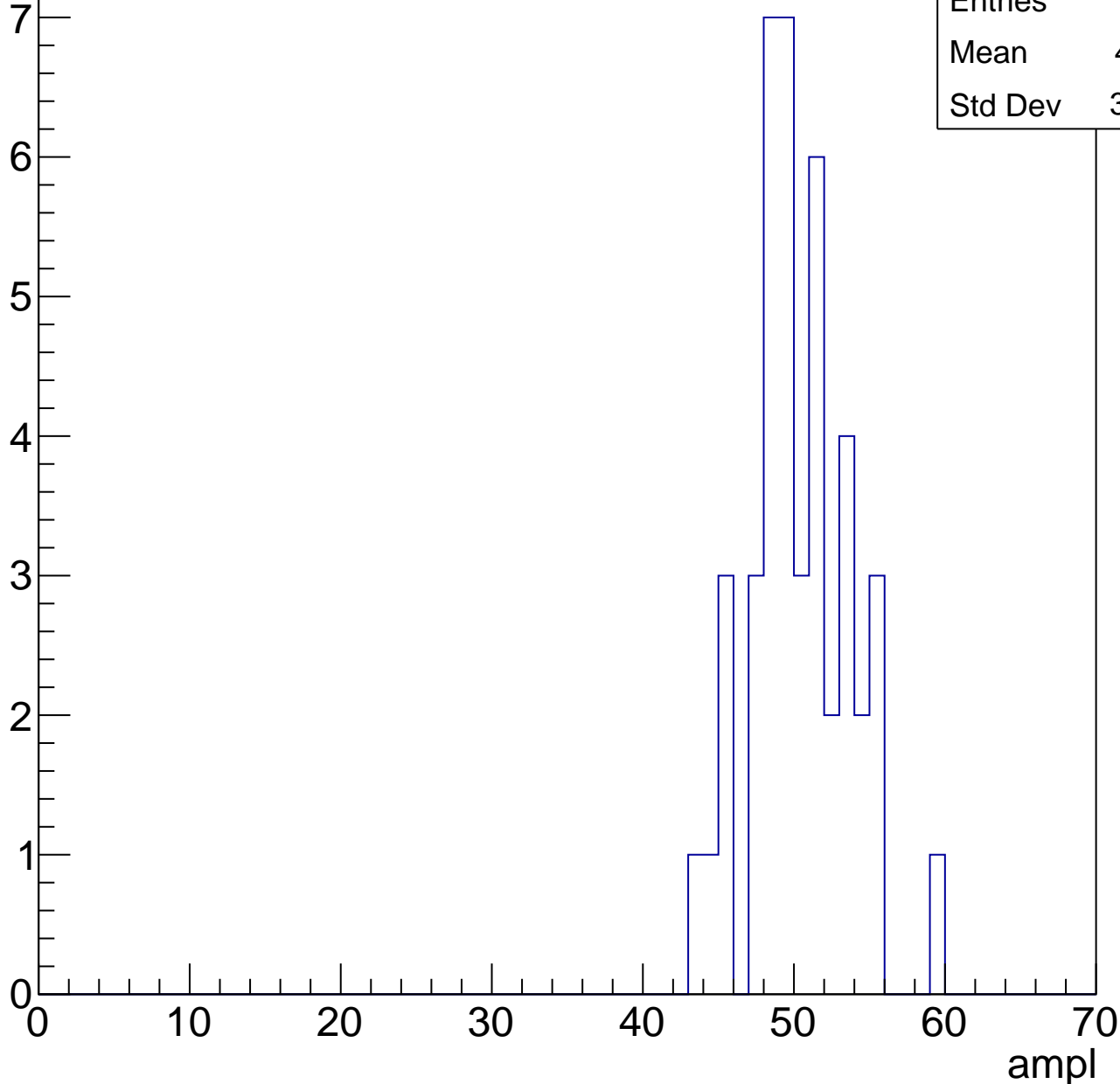


B0L002S, U18-ch56, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	49.91
Std Dev	3.269

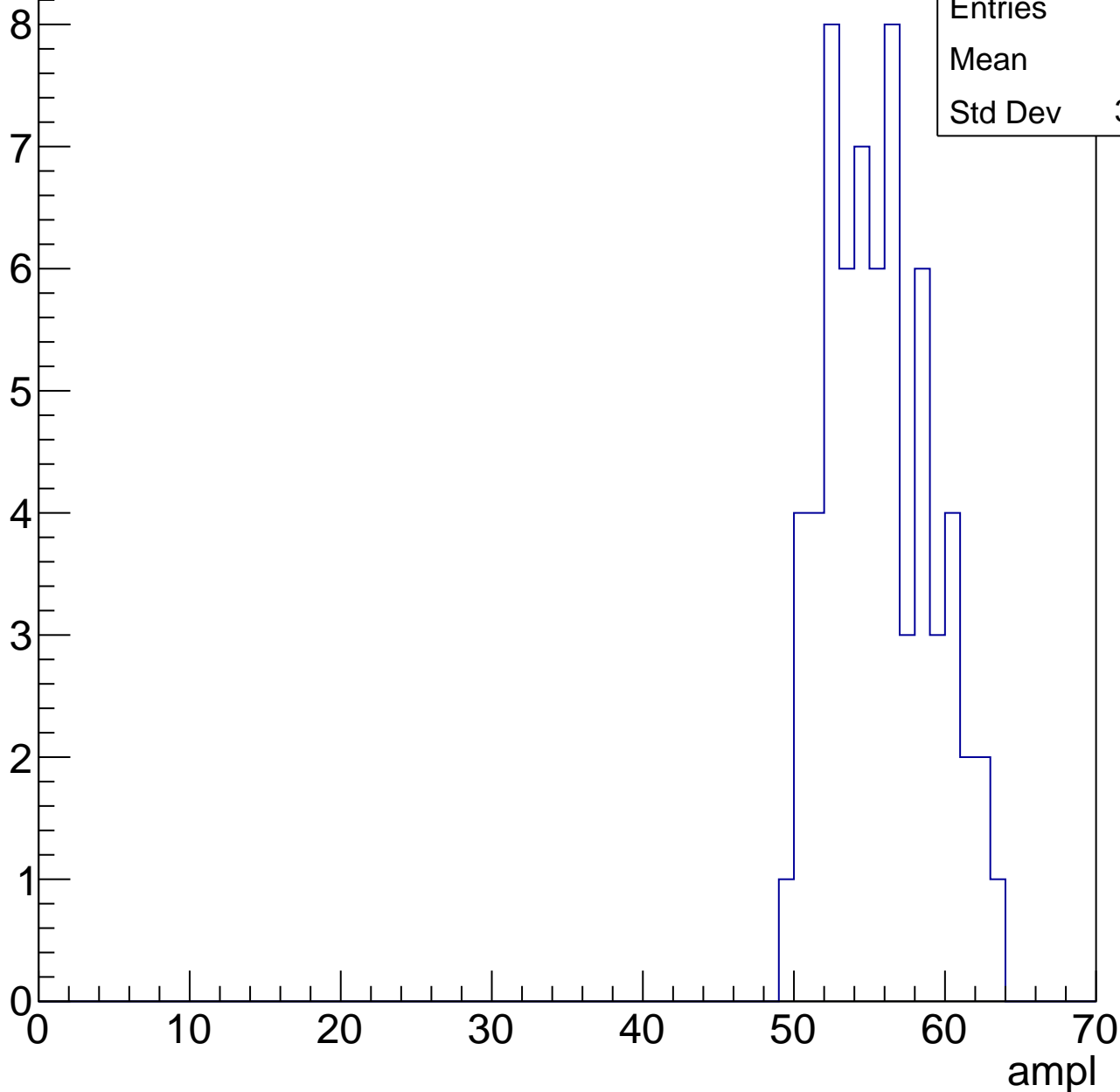


B0L002S, U18-ch56, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	55.2
Std Dev	3.411

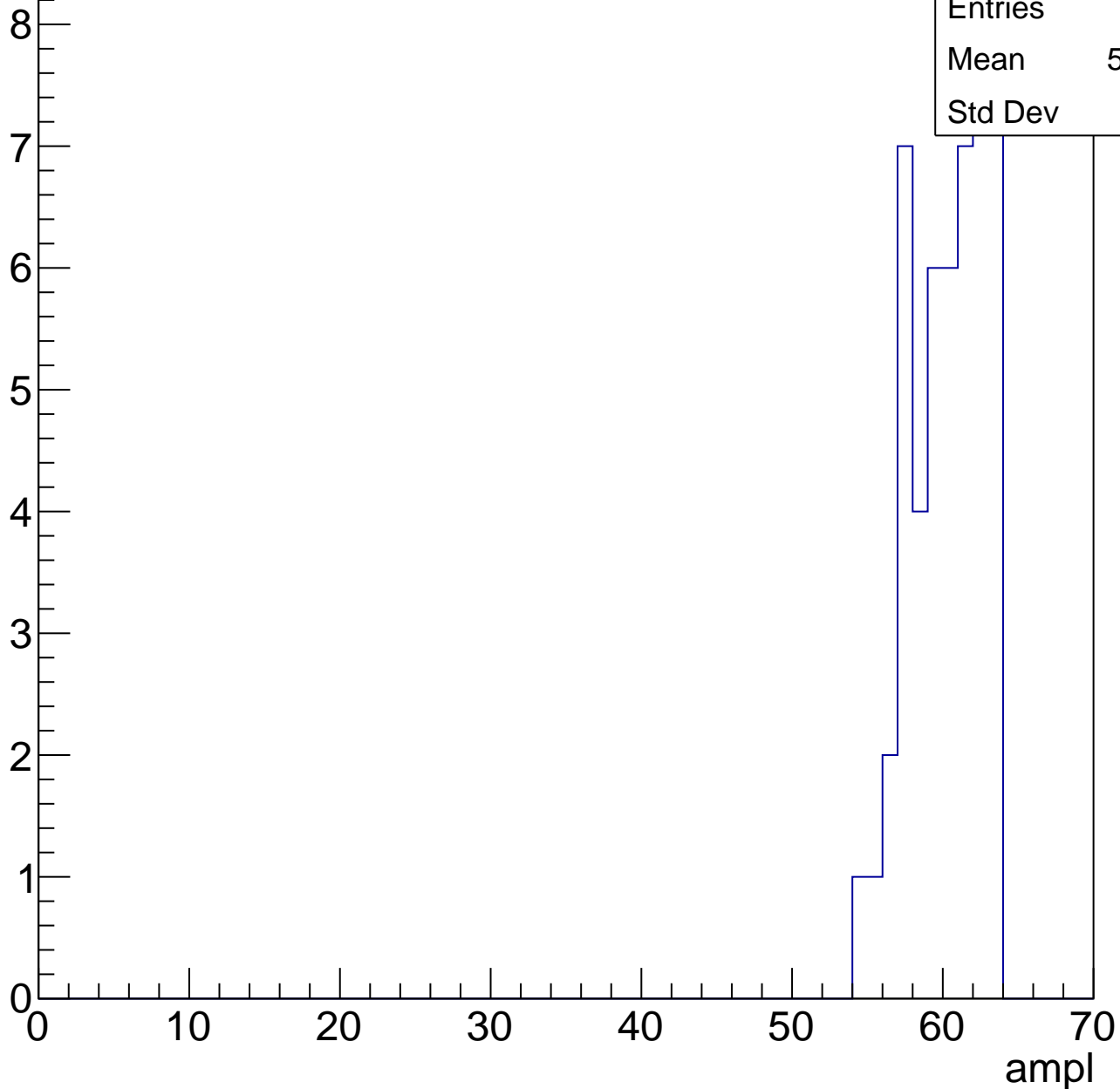


B0L002S, U18-ch56, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	59.86
Std Dev	2.4



B0L002S, U18-ch56, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch56, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	10.5
Std Dev	10.5

B0L002S, U18-ch57, adc0

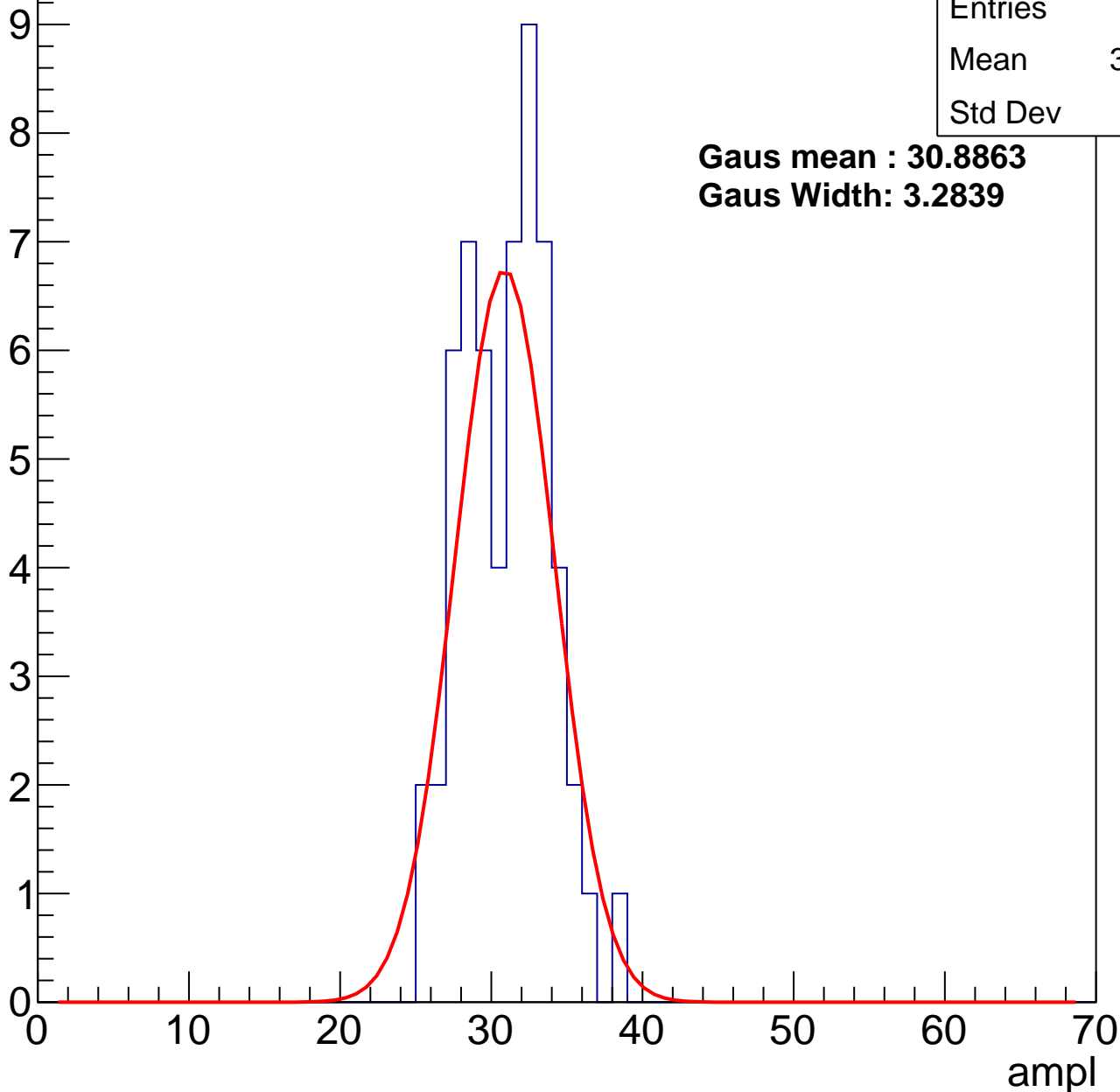
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	30.52
Std Dev	2.86

Gaus mean : 30.8863

Gaus Width: 3.2839



B0L002S, U18-ch57, adc1

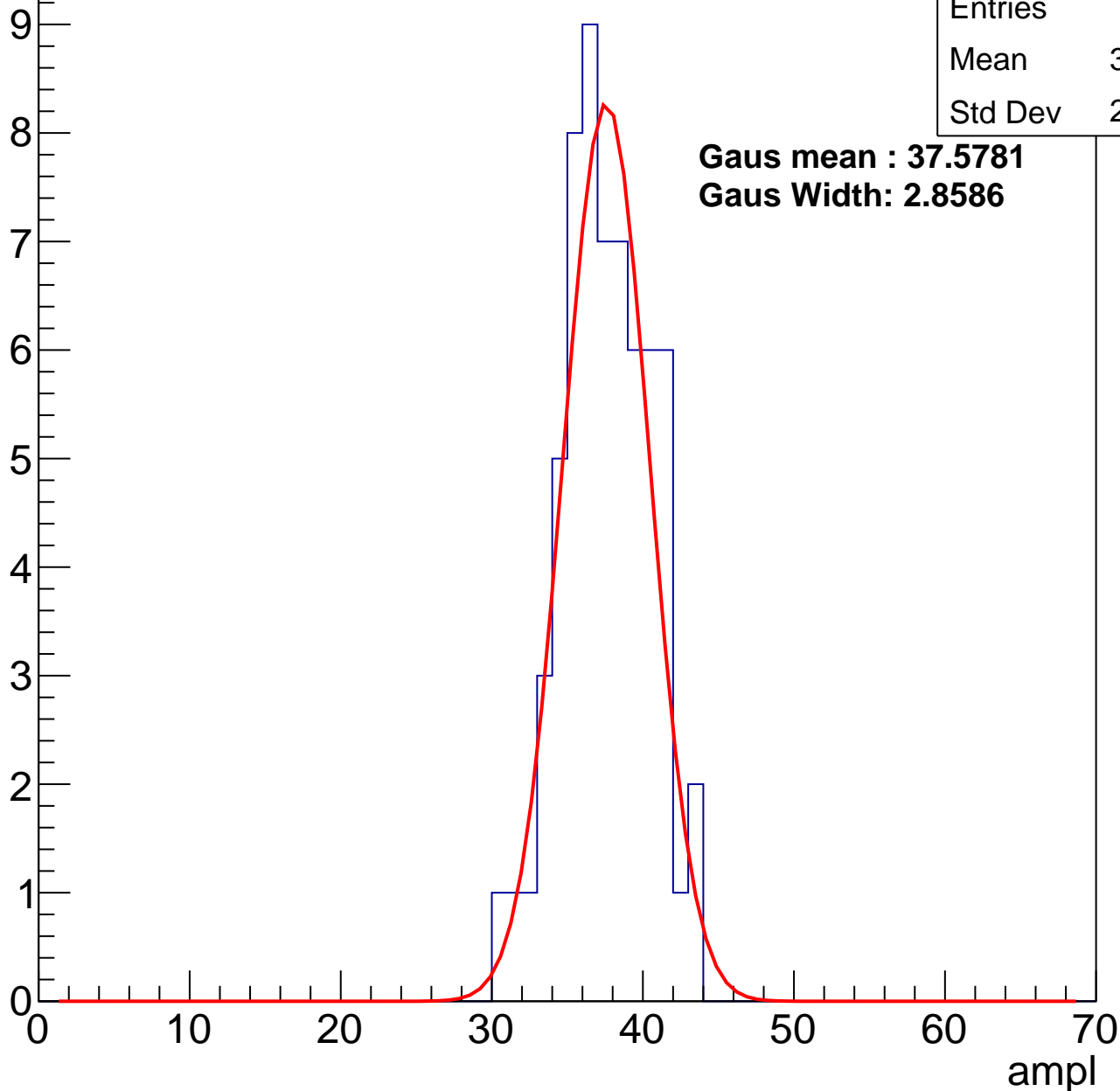
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.13
Std Dev	2.876

Gaus mean : 37.5781

Gaus Width: 2.8586



B0L002S, U18-ch57, adc2

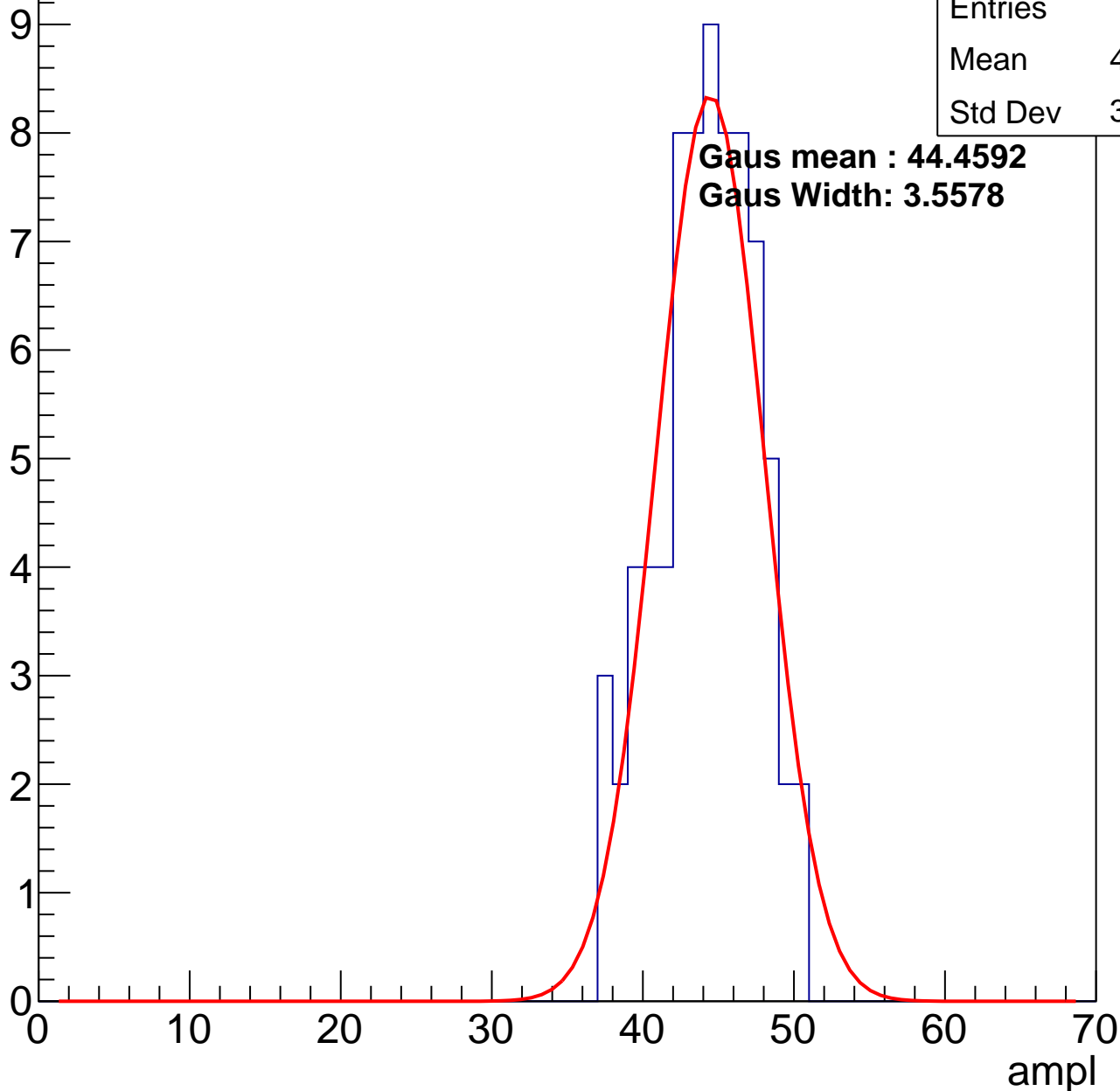
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	43.76
Std Dev	3.204

Gaus mean : 44.4592

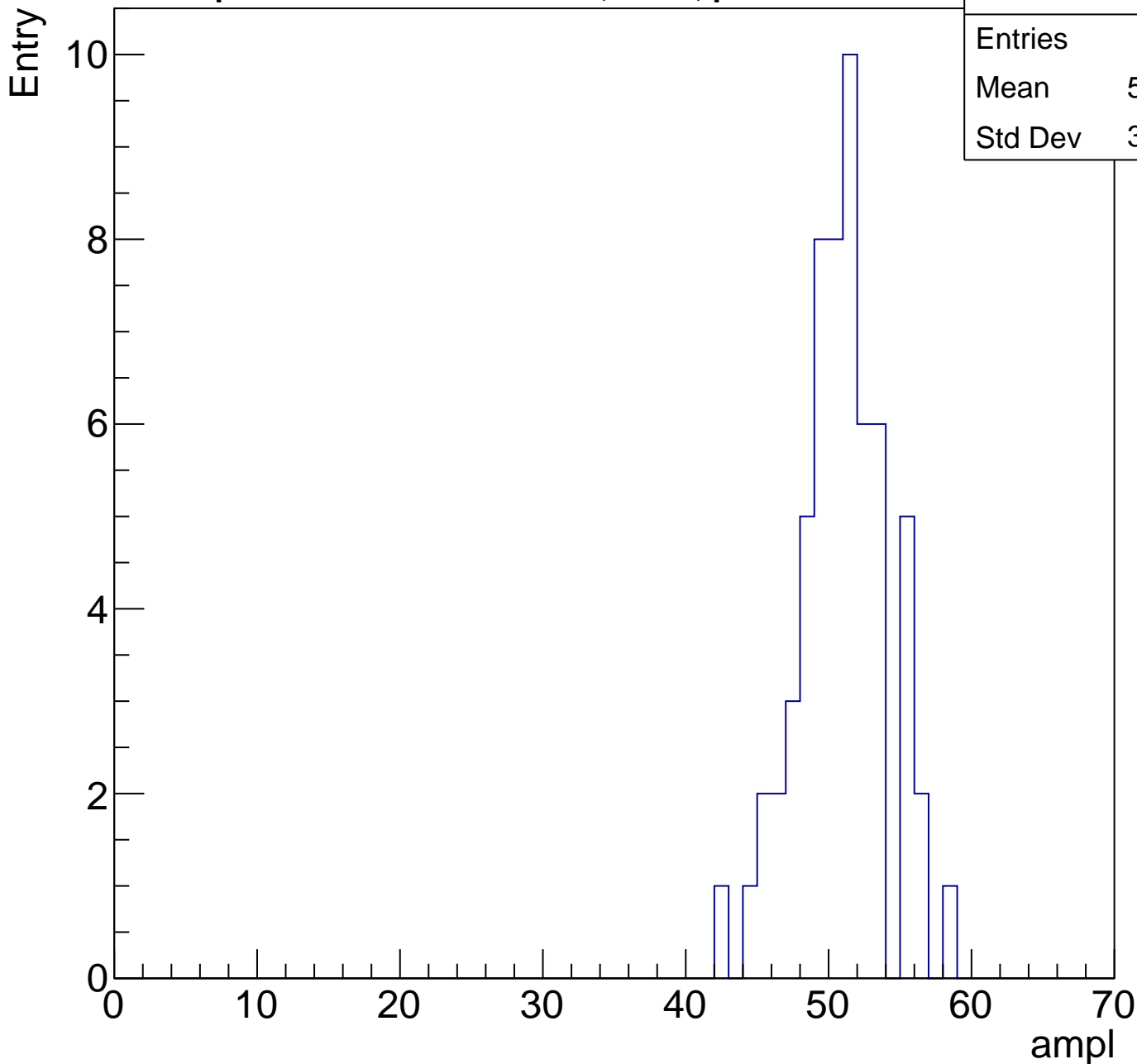
Gaus Width: 3.5578



B0L002S, U18-ch57, adc3

calib_packv5_042523_0143.root, FC#8, port C1

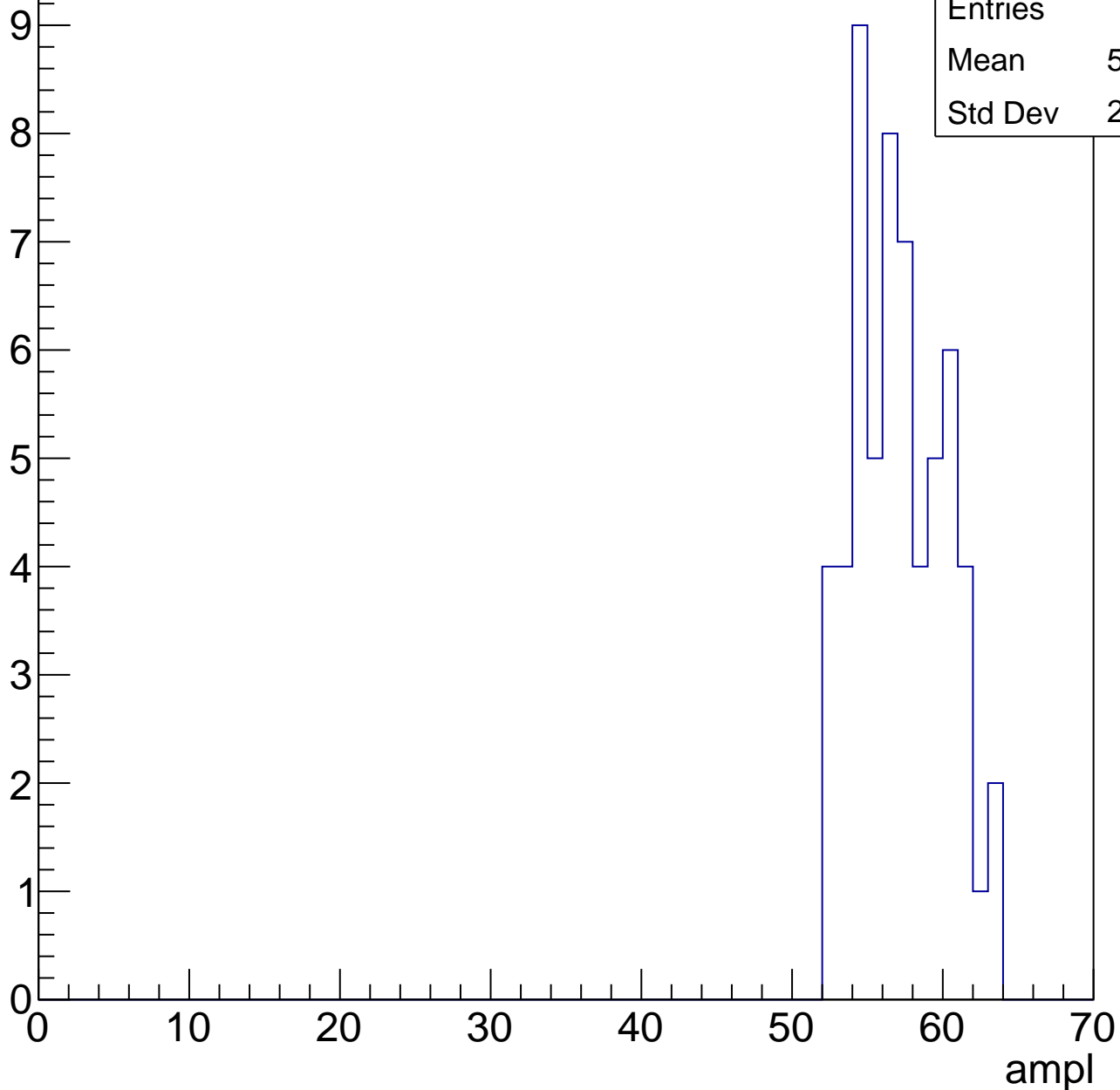
Entries	60
Mean	50.43
Std Dev	3.095



B0L002S, U18-ch57, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

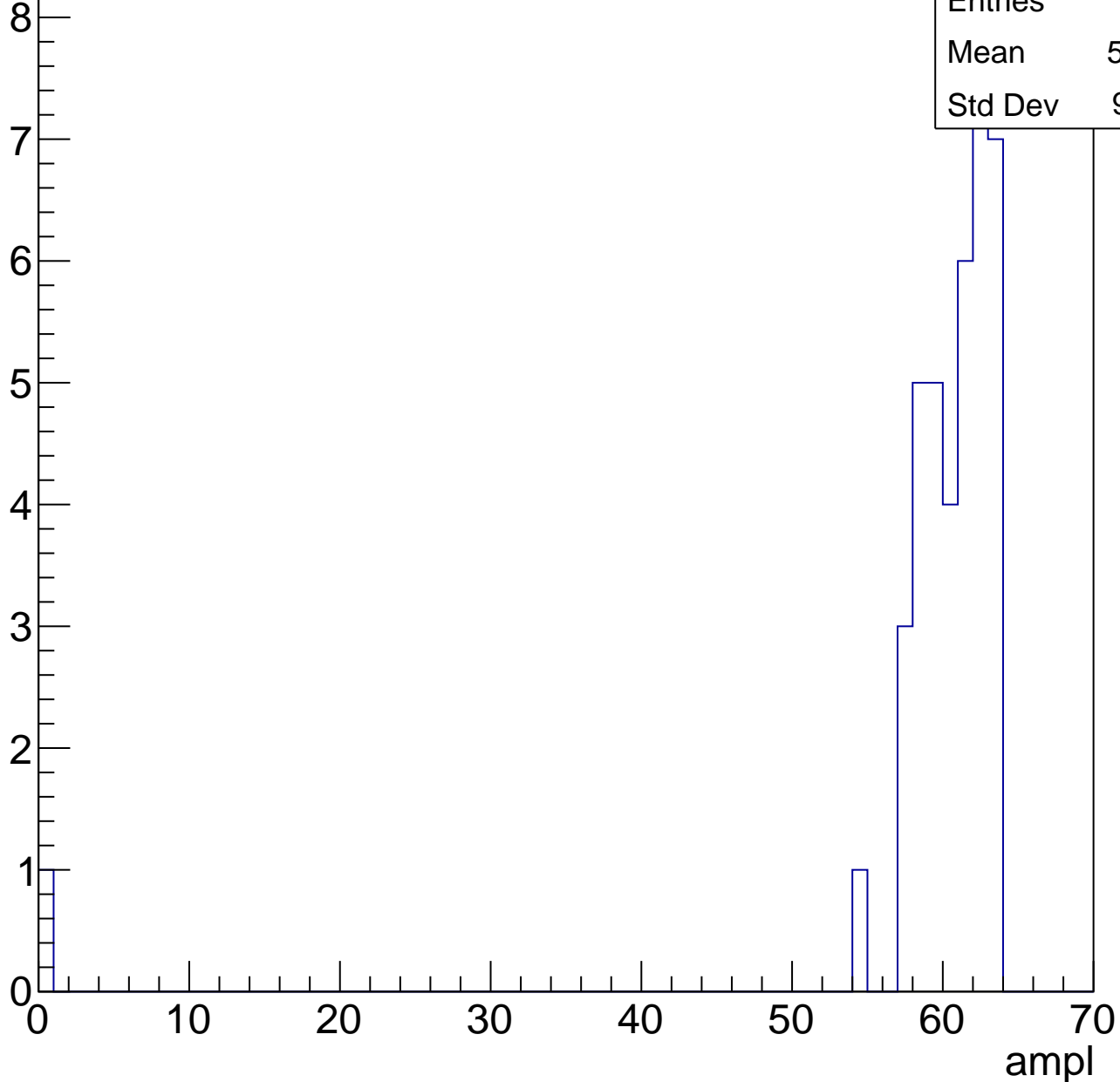


B0L002S, U18-ch57, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	40
Mean	58.83
Std Dev	9.661



B0L002S, U18-ch57, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch57, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch58, adc0

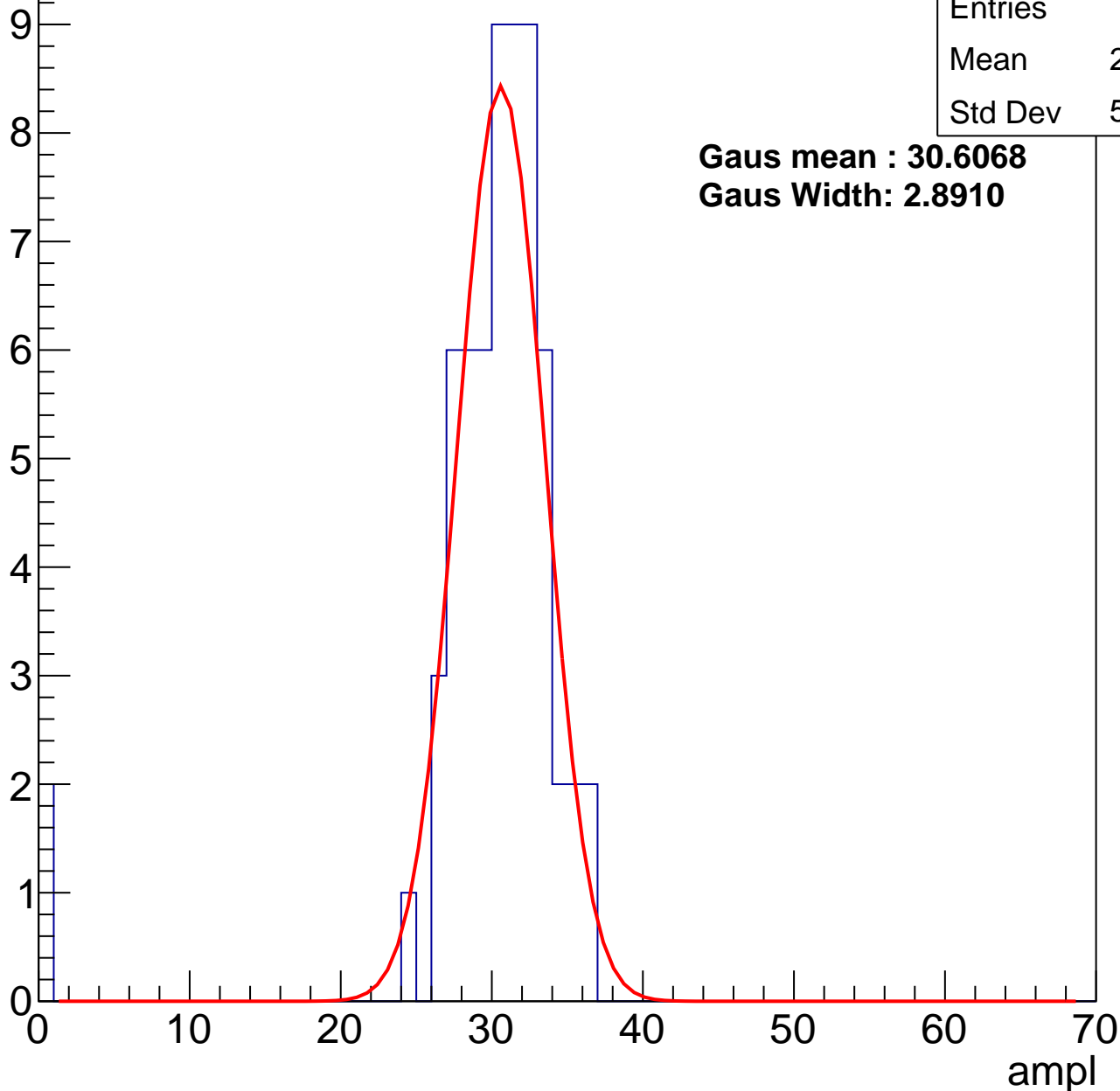
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.38
Std Dev	5.905

Gaus mean : 30.6068

Gaus Width: 2.8910



B0L002S, U18-ch58, adc1

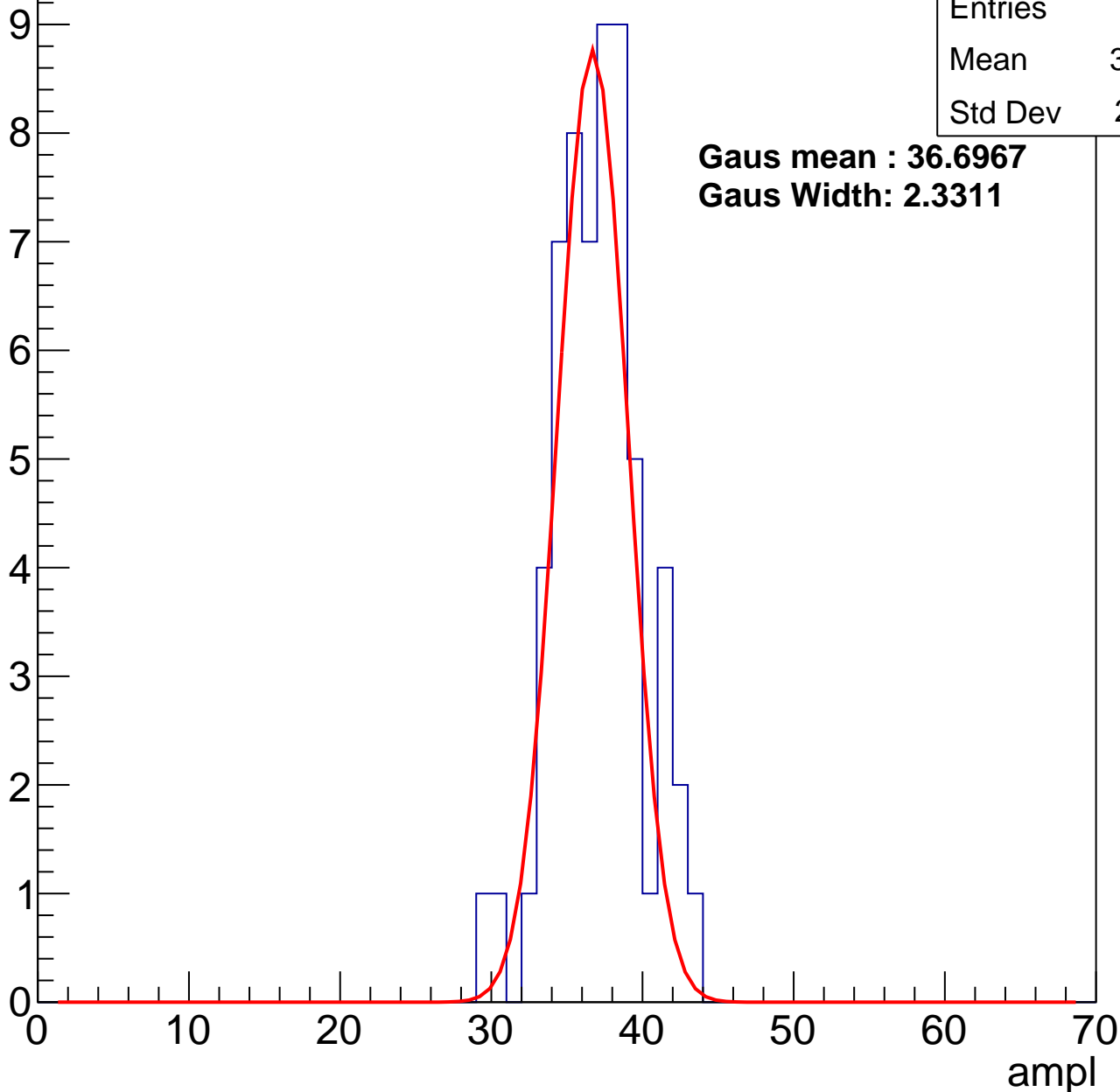
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	36.57
Std Dev	2.831

Gaus mean : 36.6967

Gaus Width: 2.3311



B0L002S, U18-ch58, adc2

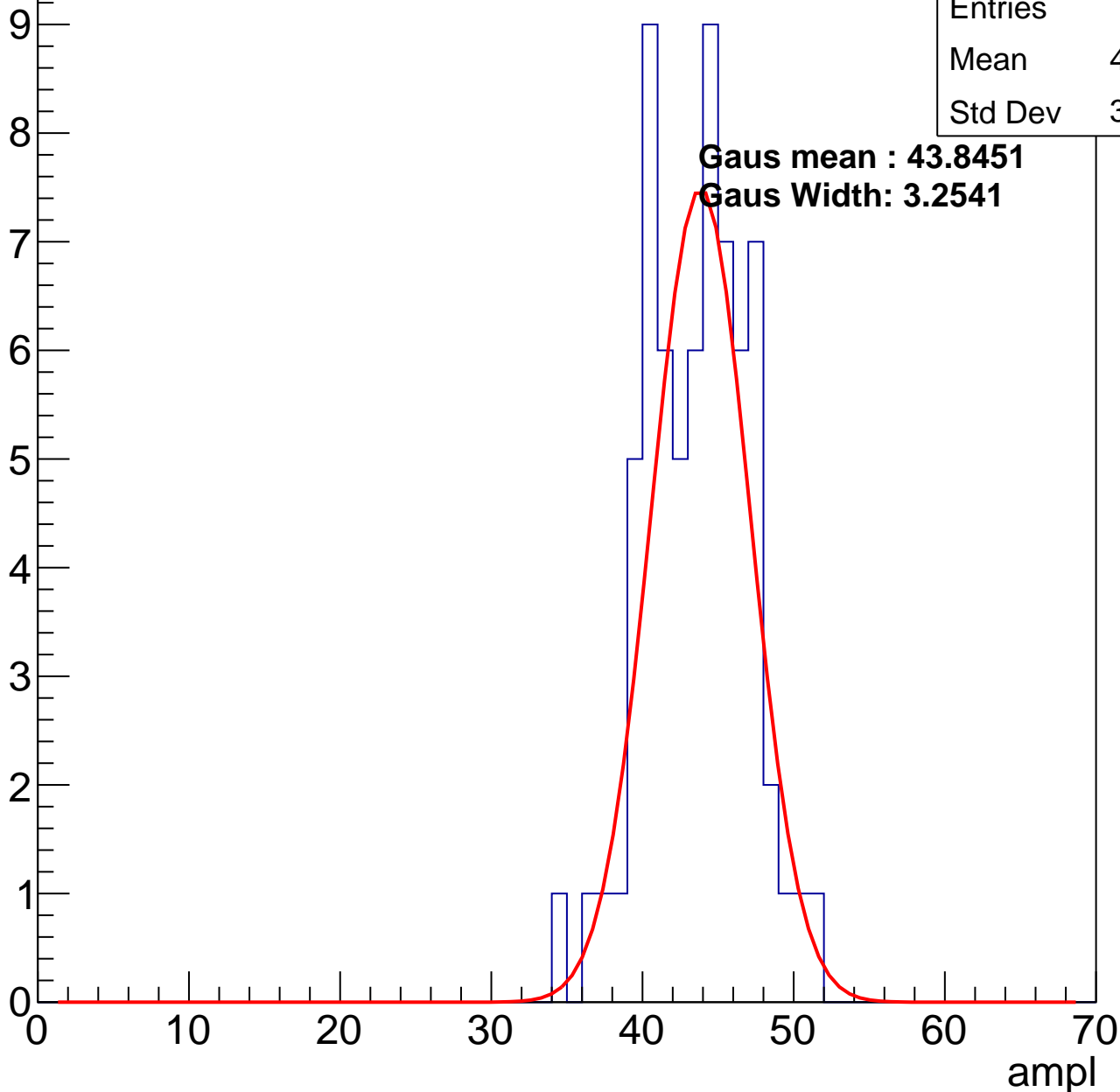
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.13
Std Dev	3.366

Gaus mean : 43.8451

Gaus Width: 3.2541

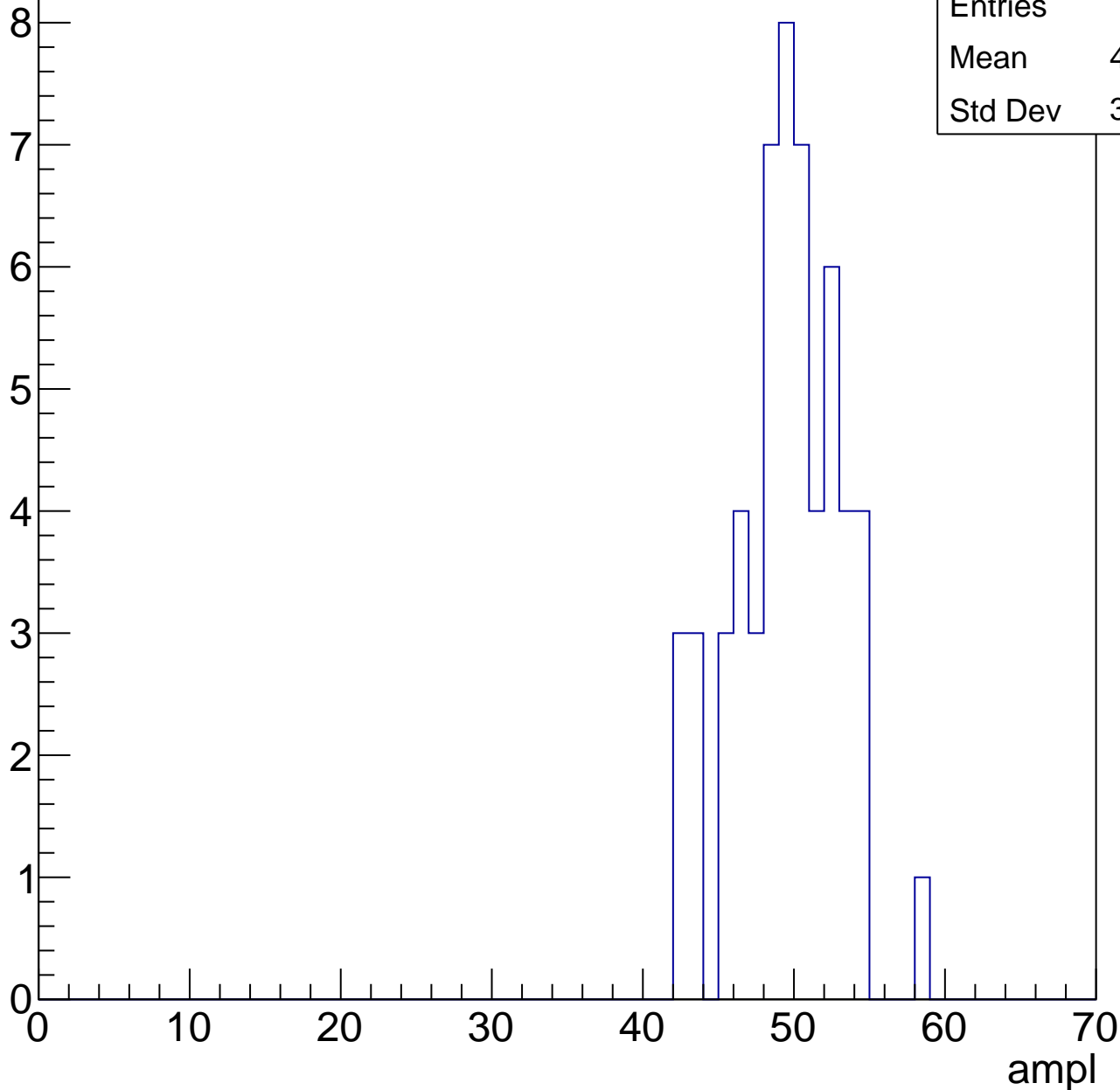


B0L002S, U18-ch58, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49.04
Std Dev	3.454



B0L002S, U18-ch58, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	79
Mean	55.78
Std Dev	3.581

Entry

10

8

6

4

2

0

0

10

20

30

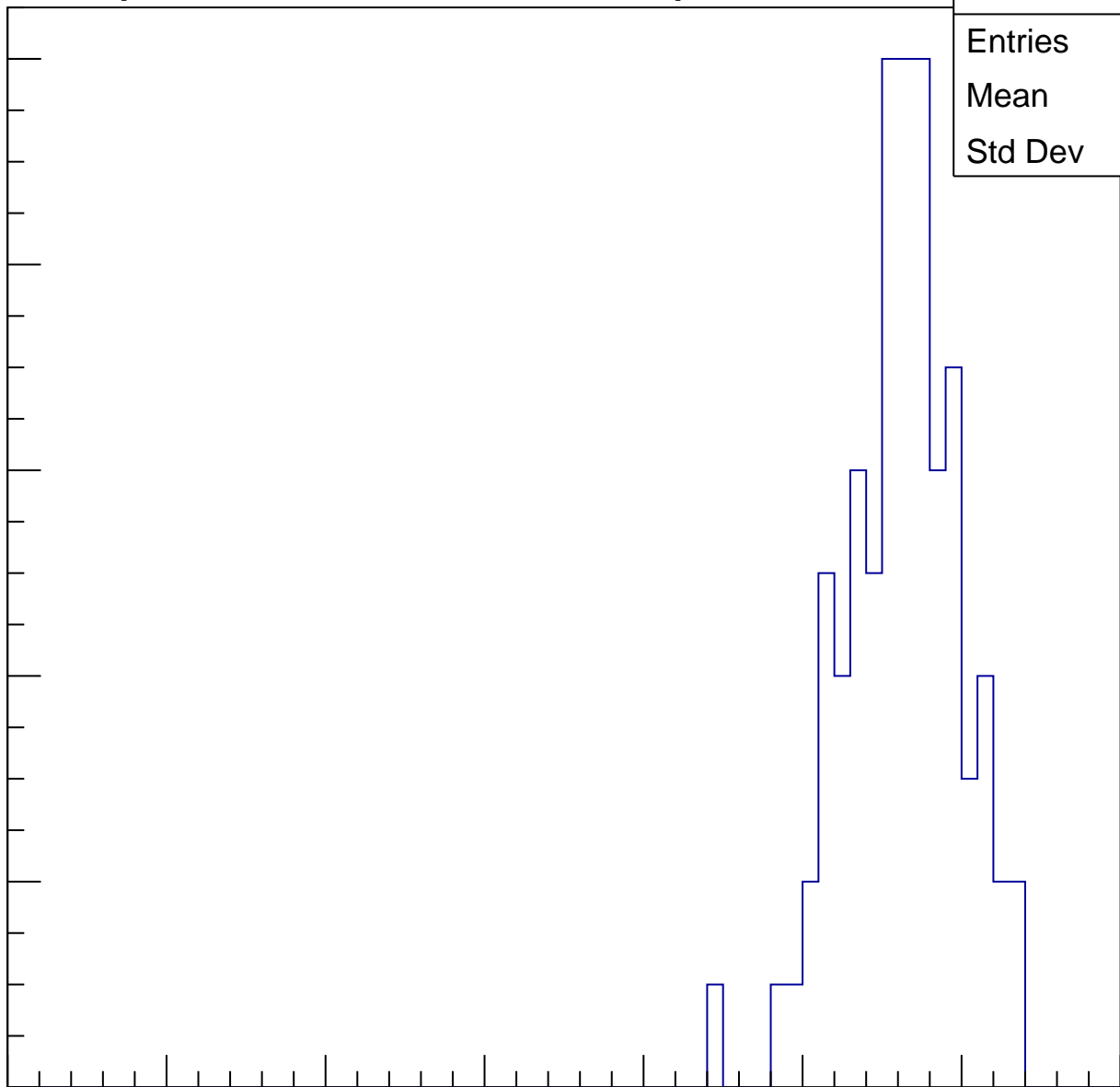
40

50

60

70

ampl

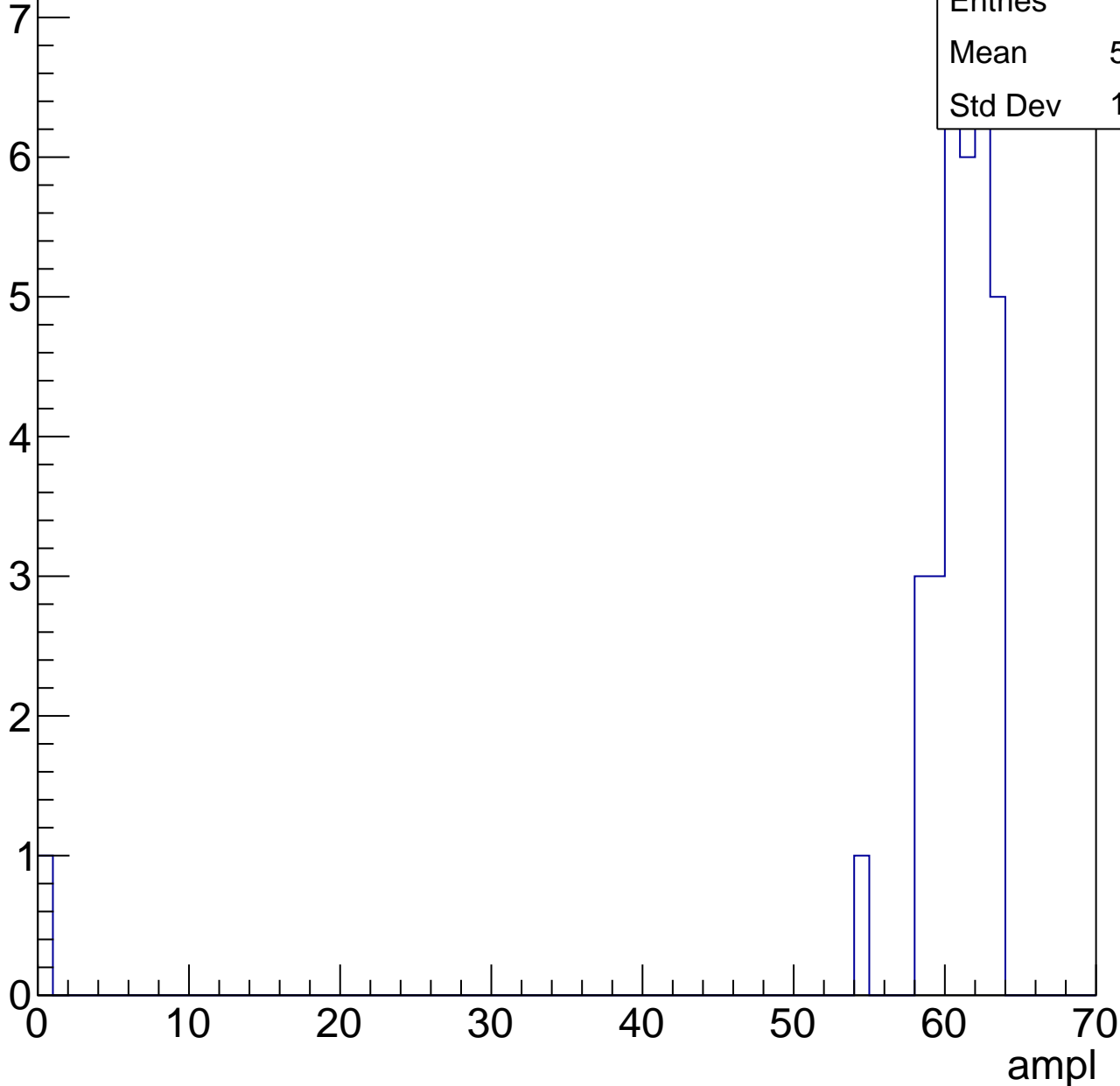


B0L002S, U18-ch58, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	33
Mean	58.79
Std Dev	10.56



B0L002S, U18-ch58, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch58, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch59, adc0

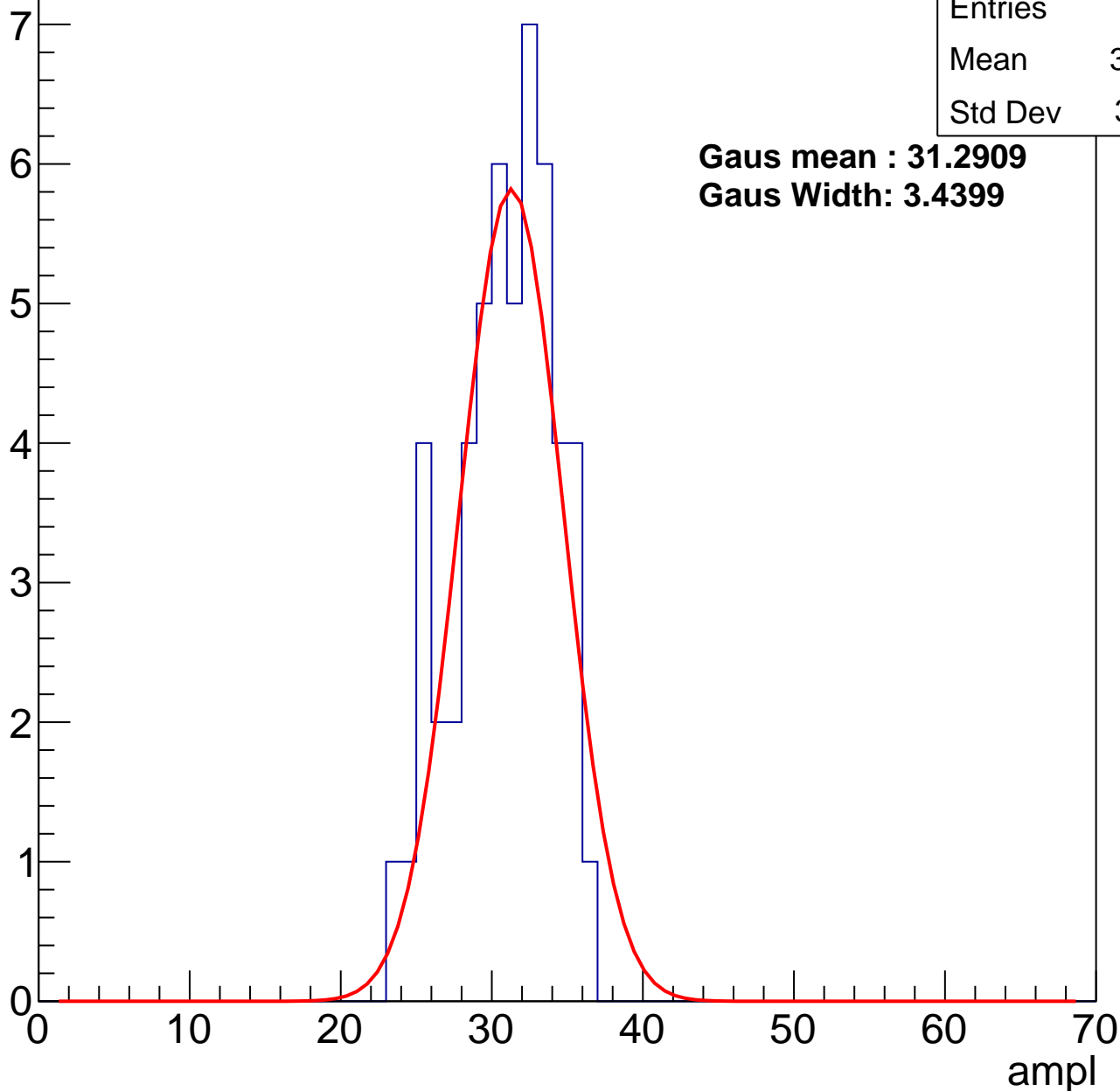
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	30.37
Std Dev	3.211

Gaus mean : 31.2909

Gaus Width: 3.4399



B0L002S, U18-ch59, adc1

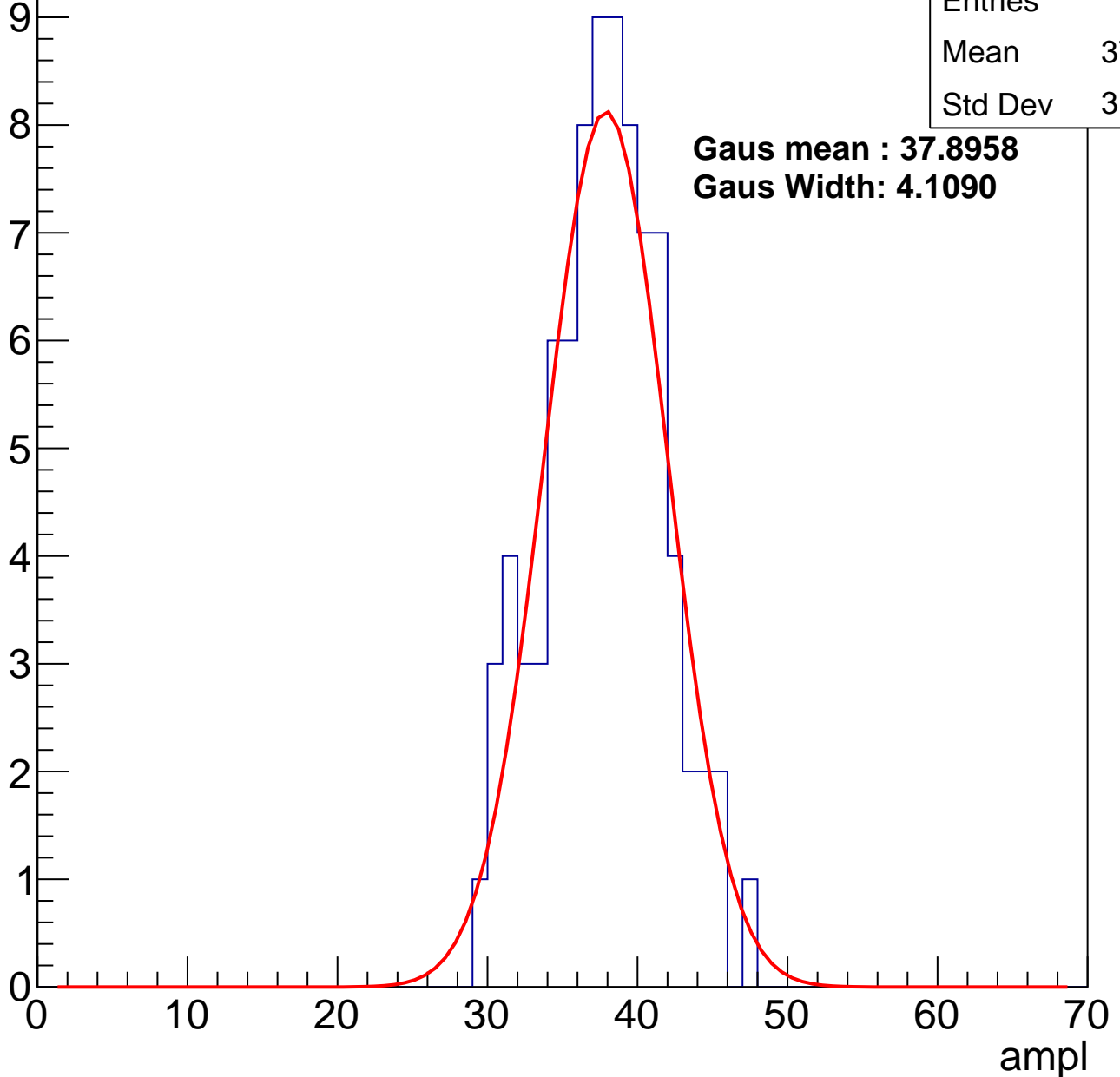
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	37.33
Std Dev	3.864

Gaus mean : 37.8958

Gaus Width: 4.1090

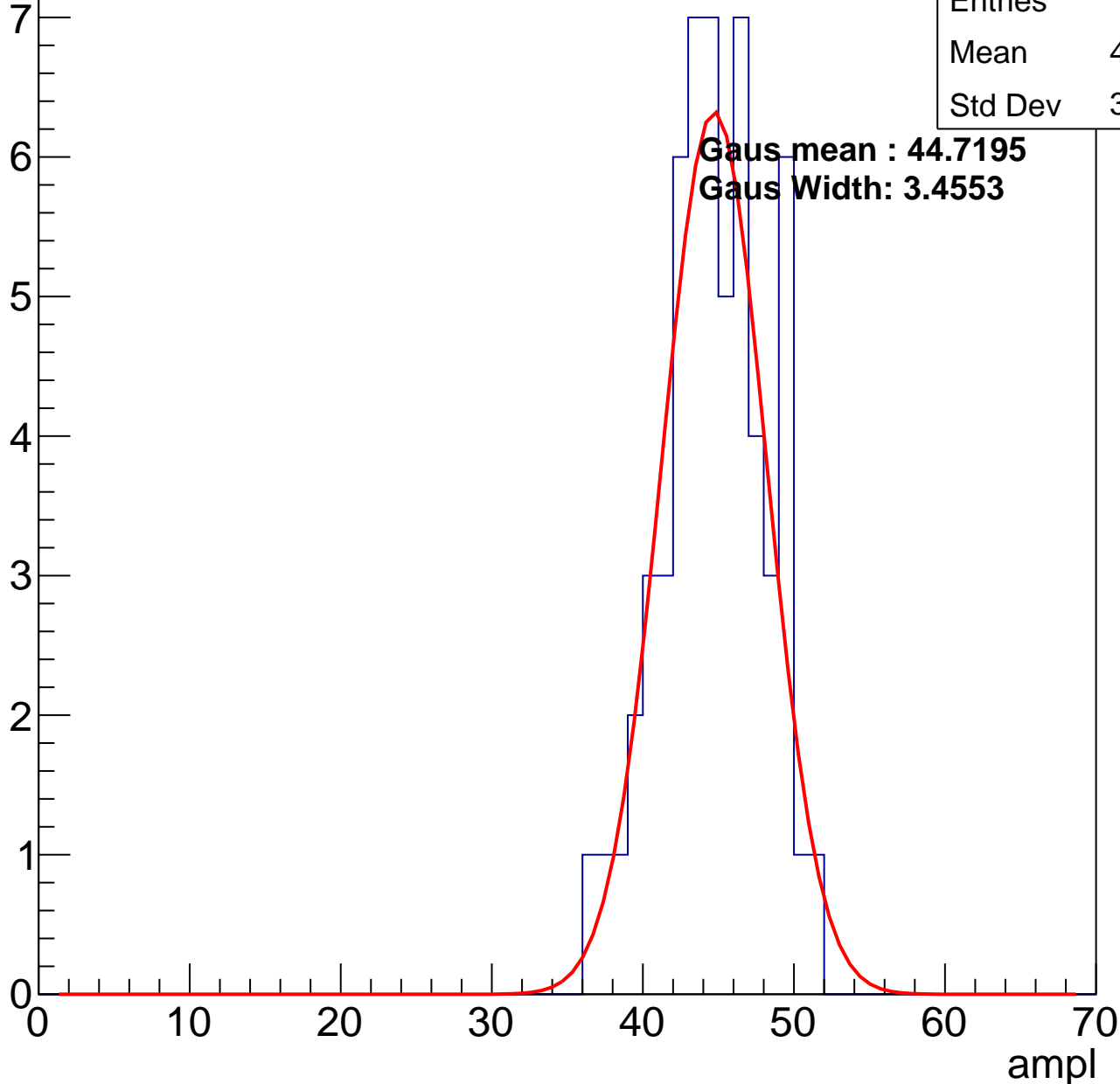


B0L002S, U18-ch59, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	44.26
Std Dev	3.356

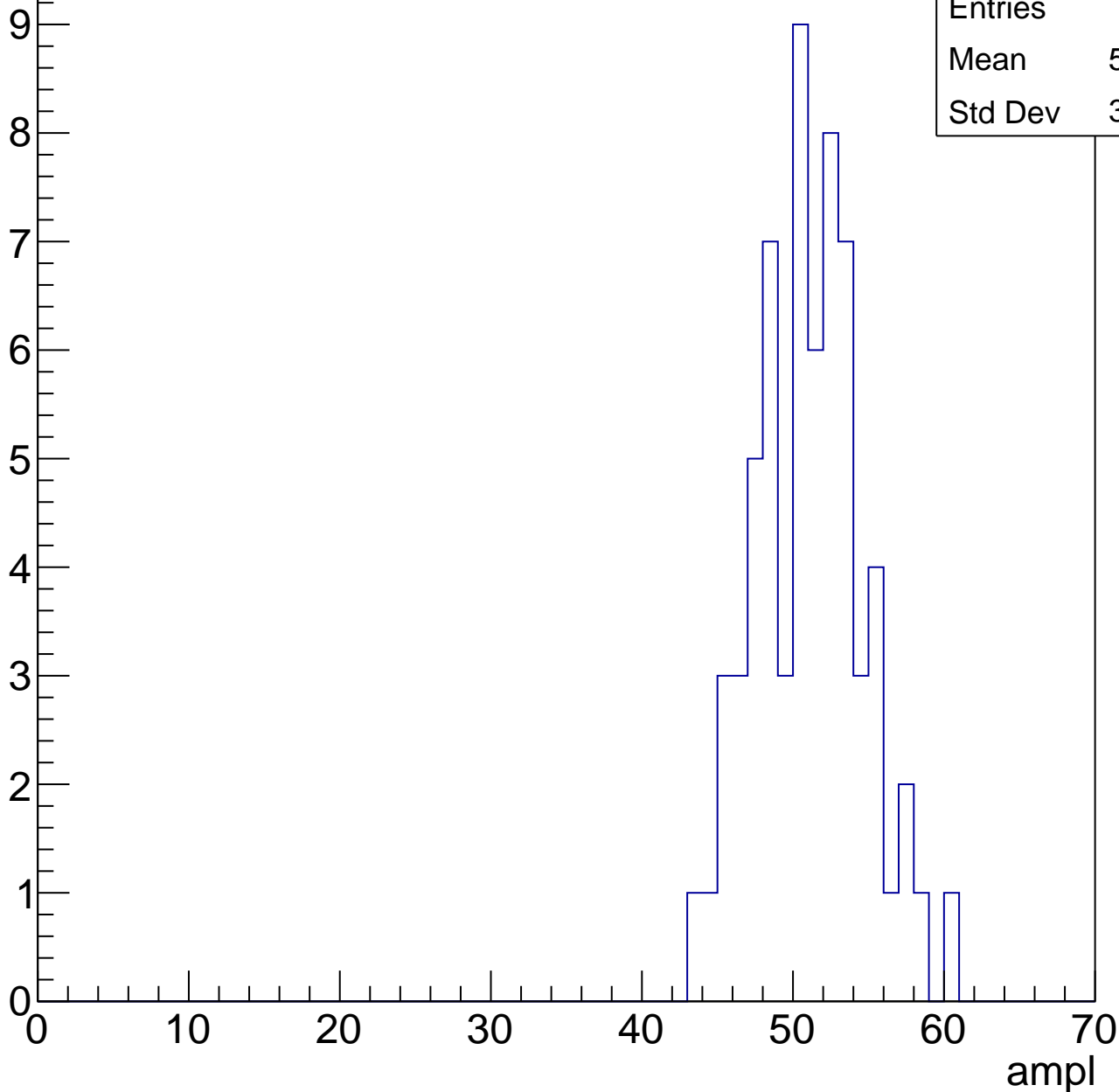


B0L002S, U18-ch59, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	50.63
Std Dev	3.519

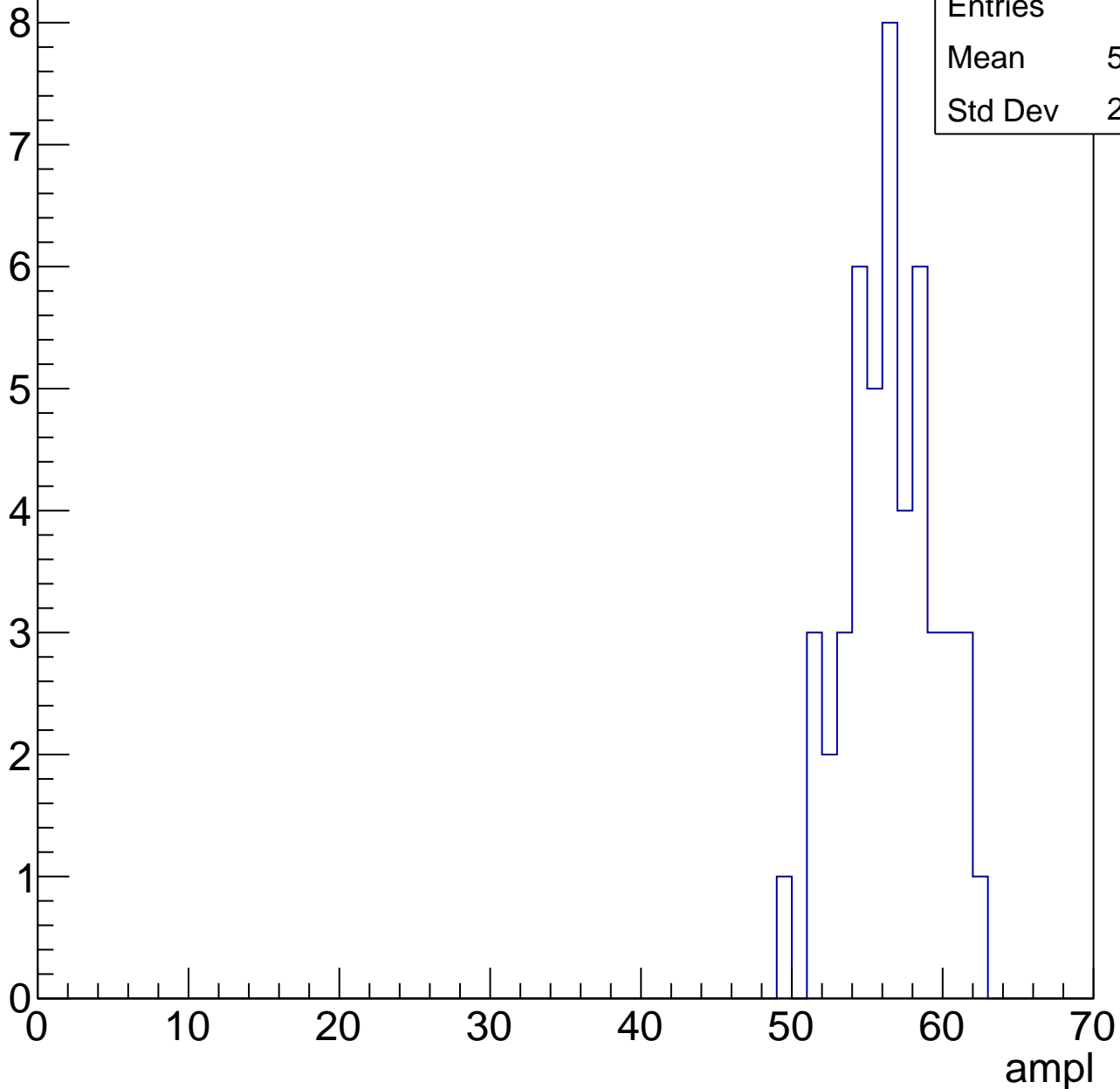


B0L002S, U18-ch59, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

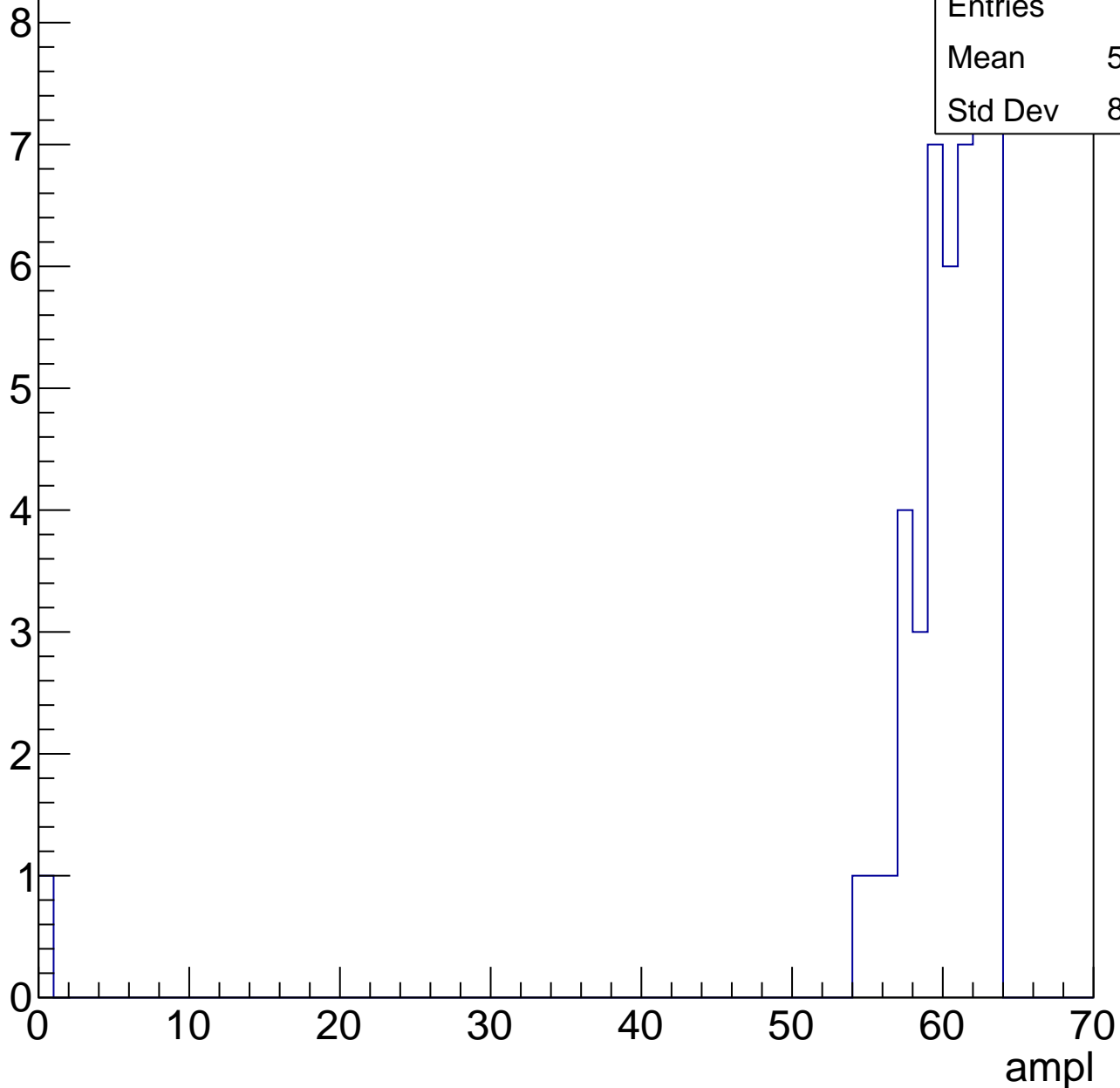
Entries	48
Mean	56.04
Std Dev	2.979



B0L002S, U18-ch59, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch59, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U18-ch59, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch60, adc0

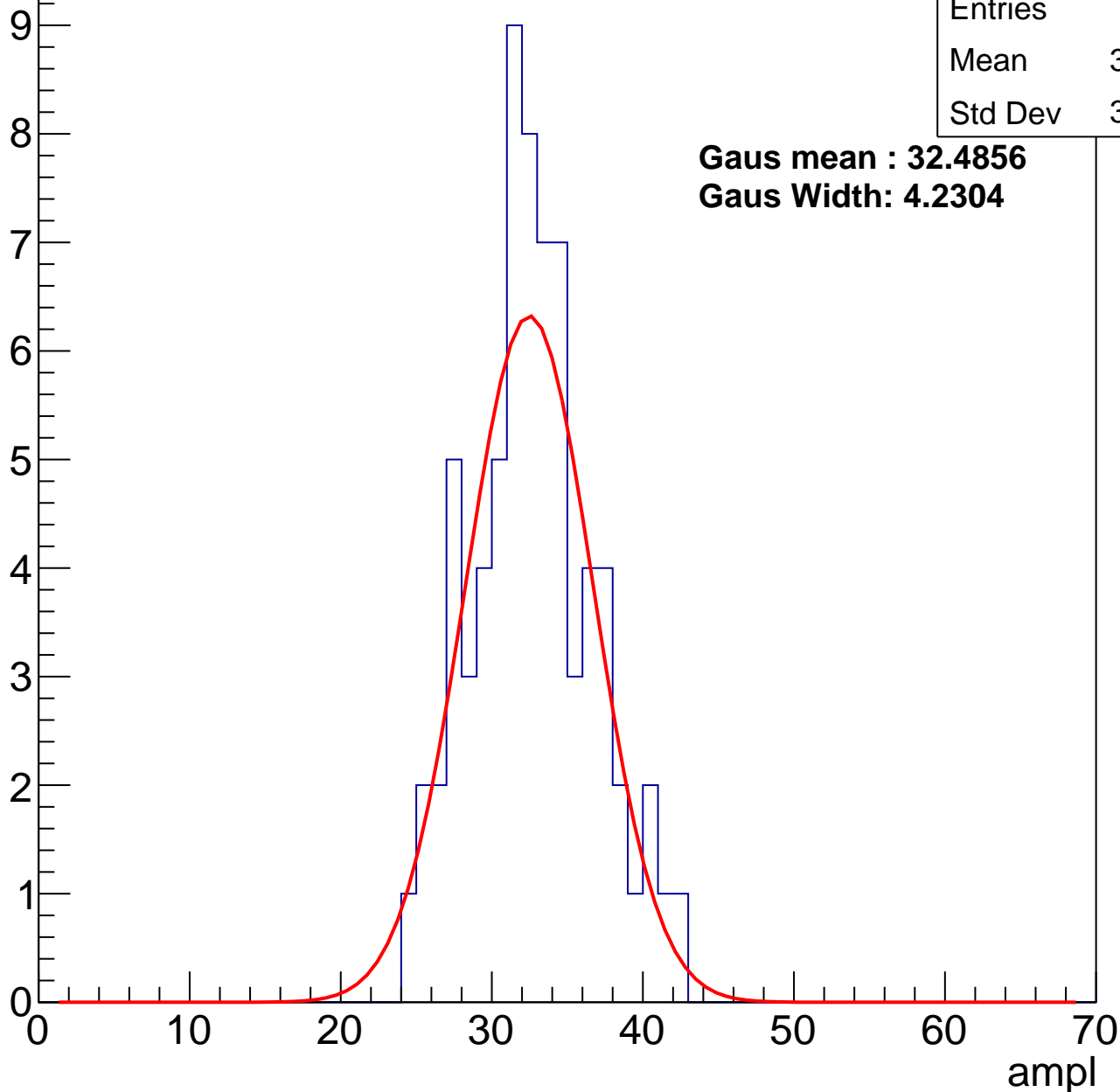
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	32.25
Std Dev	3.978

Gaus mean : 32.4856

Gaus Width: 4.2304



B0L002S, U18-ch60, adc1

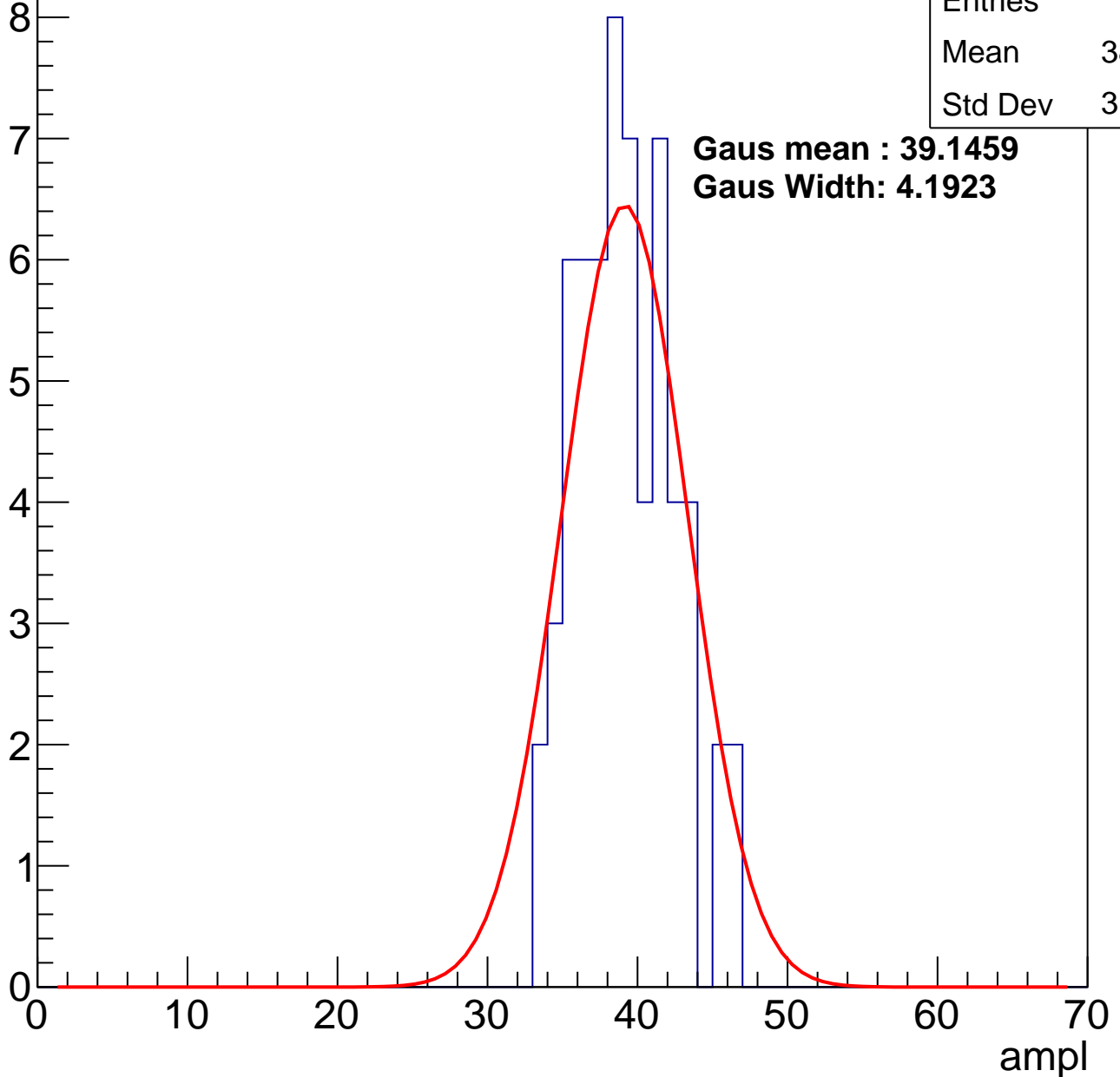
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.72
Std Dev	3.204

Gaus mean : 39.1459

Gaus Width: 4.1923



B0L002S, U18-ch60, adc2

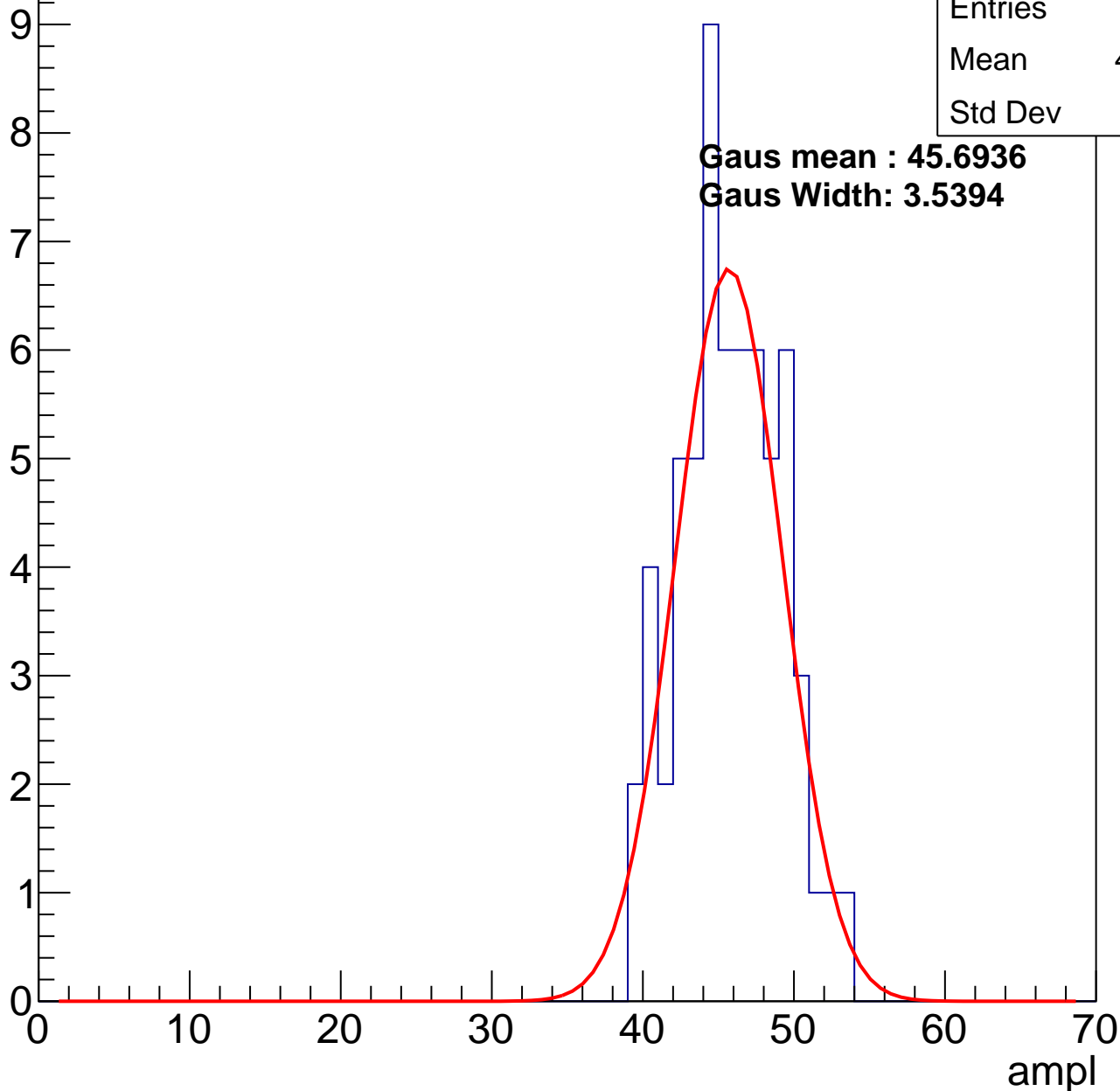
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	45.31
Std Dev	3.28

Gaus mean : 45.6936

Gaus Width: 3.5394

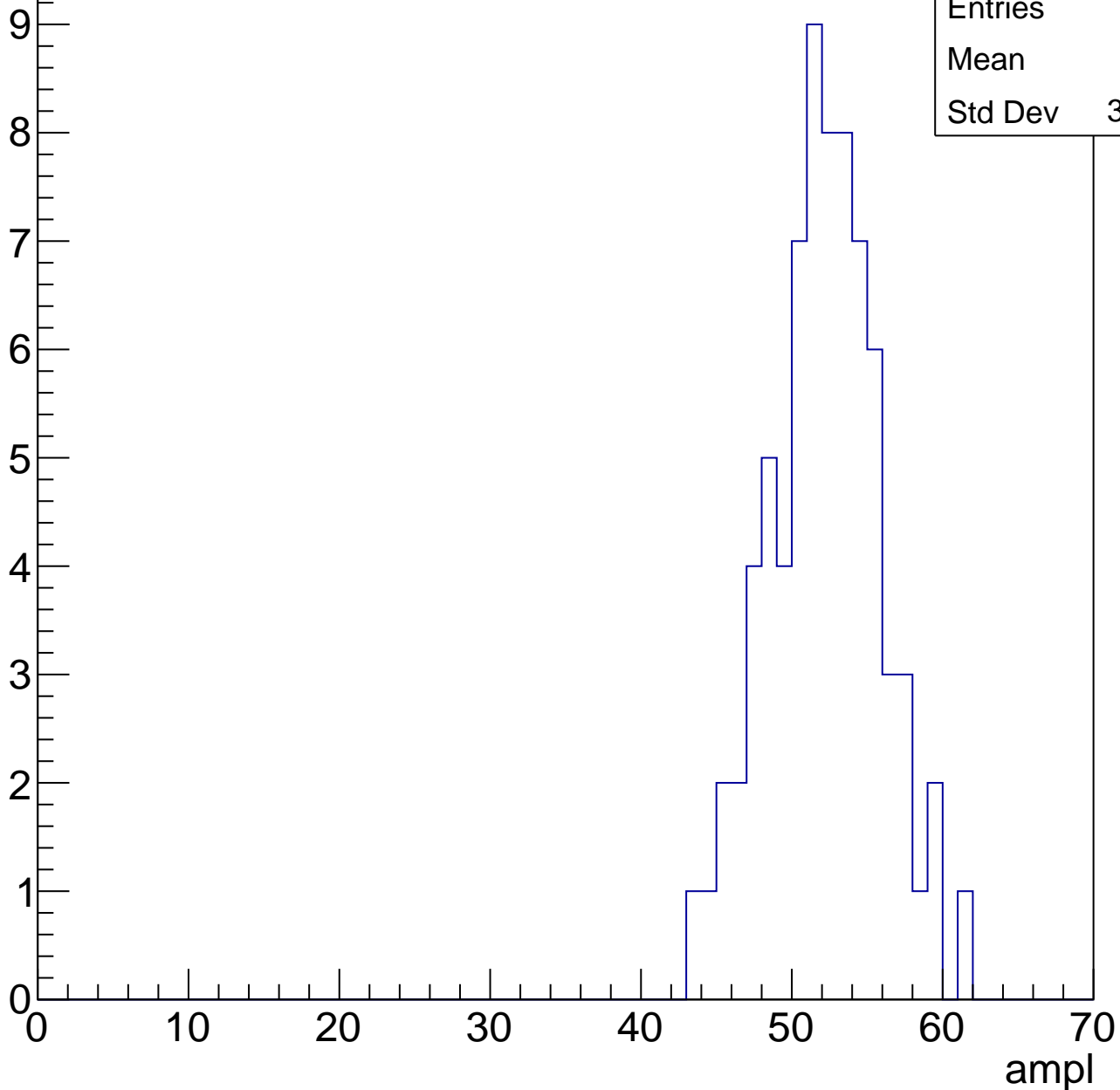


B0L002S, U18-ch60, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	51.7
Std Dev	3.657

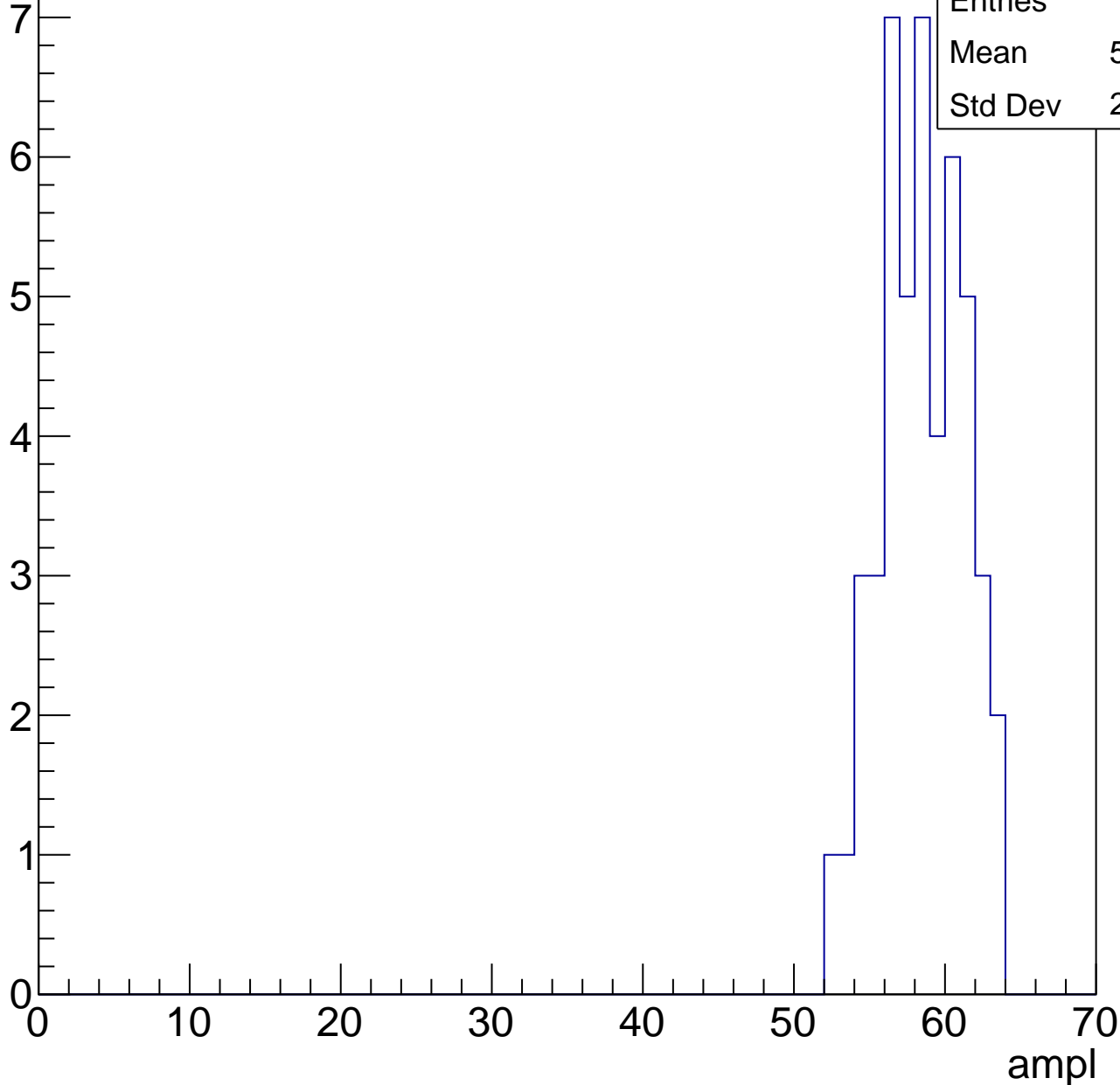


B0L002S, U18-ch60, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	58.04
Std Dev	2.689

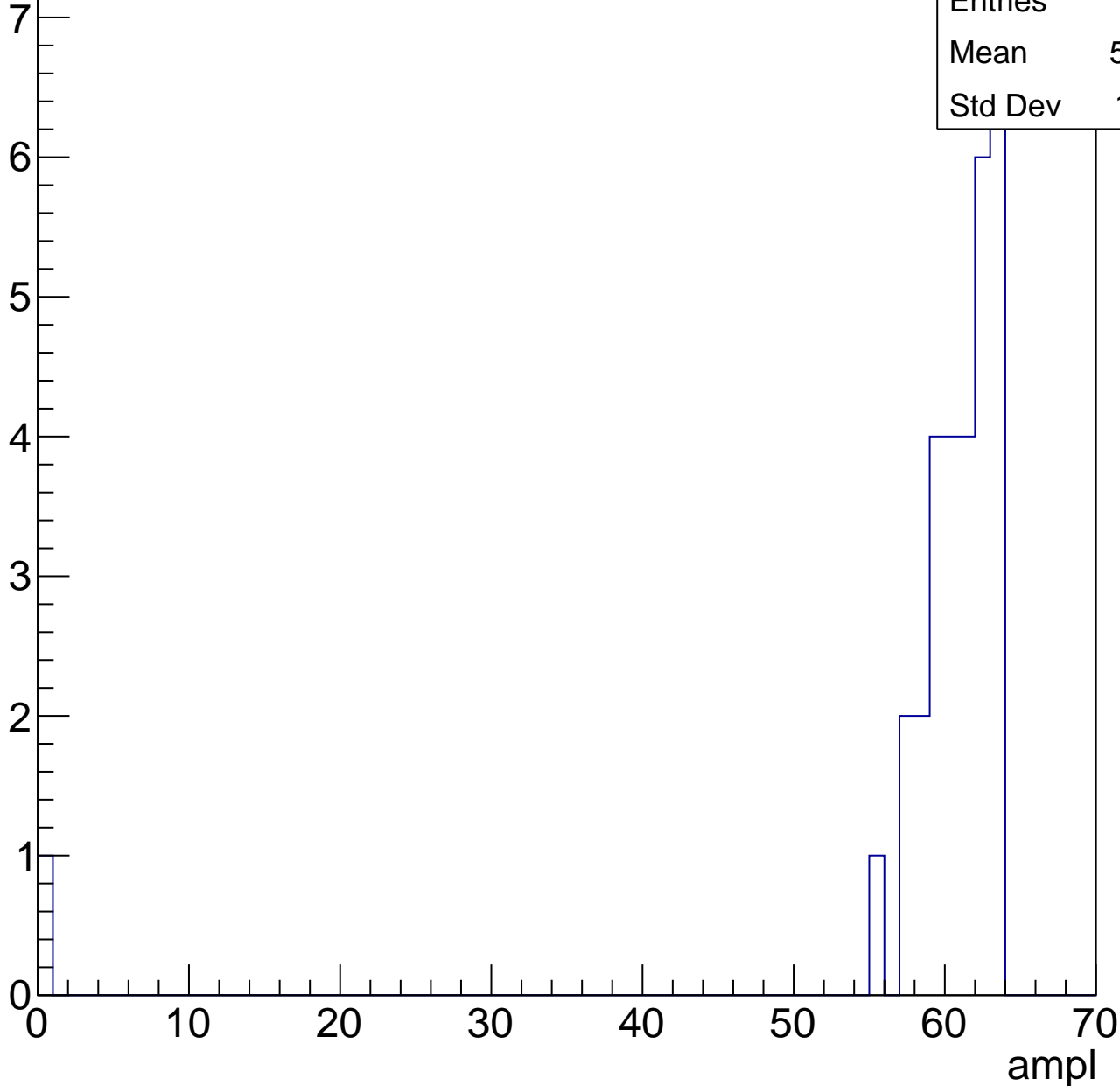


B0L002S, U18-ch60, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	58.65
Std Dev	10.91



B0L002S, U18-ch60, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch60, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch61, adc0

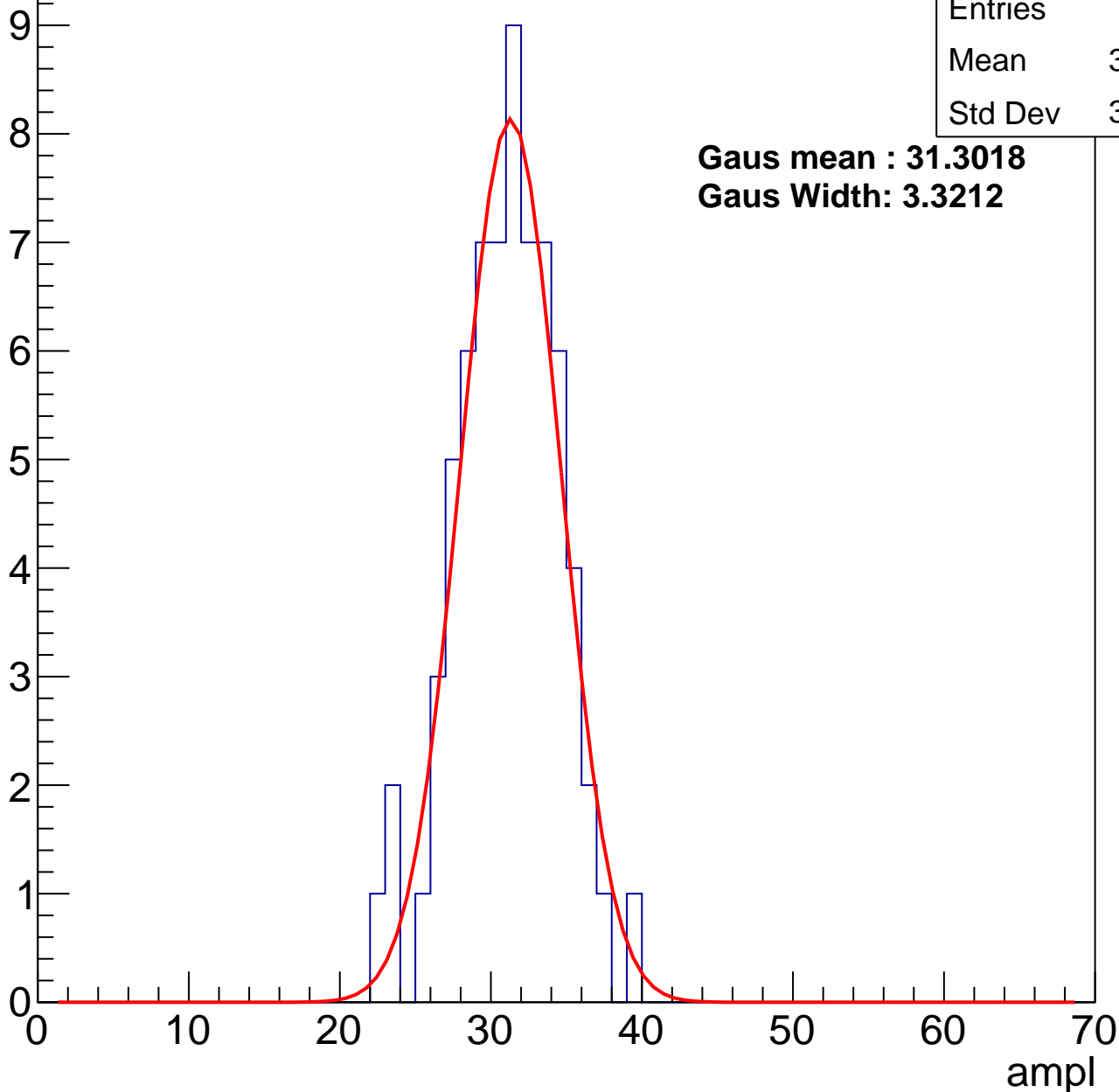
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.62
Std Dev	3.367

Gaus mean : 31.3018

Gaus Width: 3.3212



B0L002S, U18-ch61, adc1

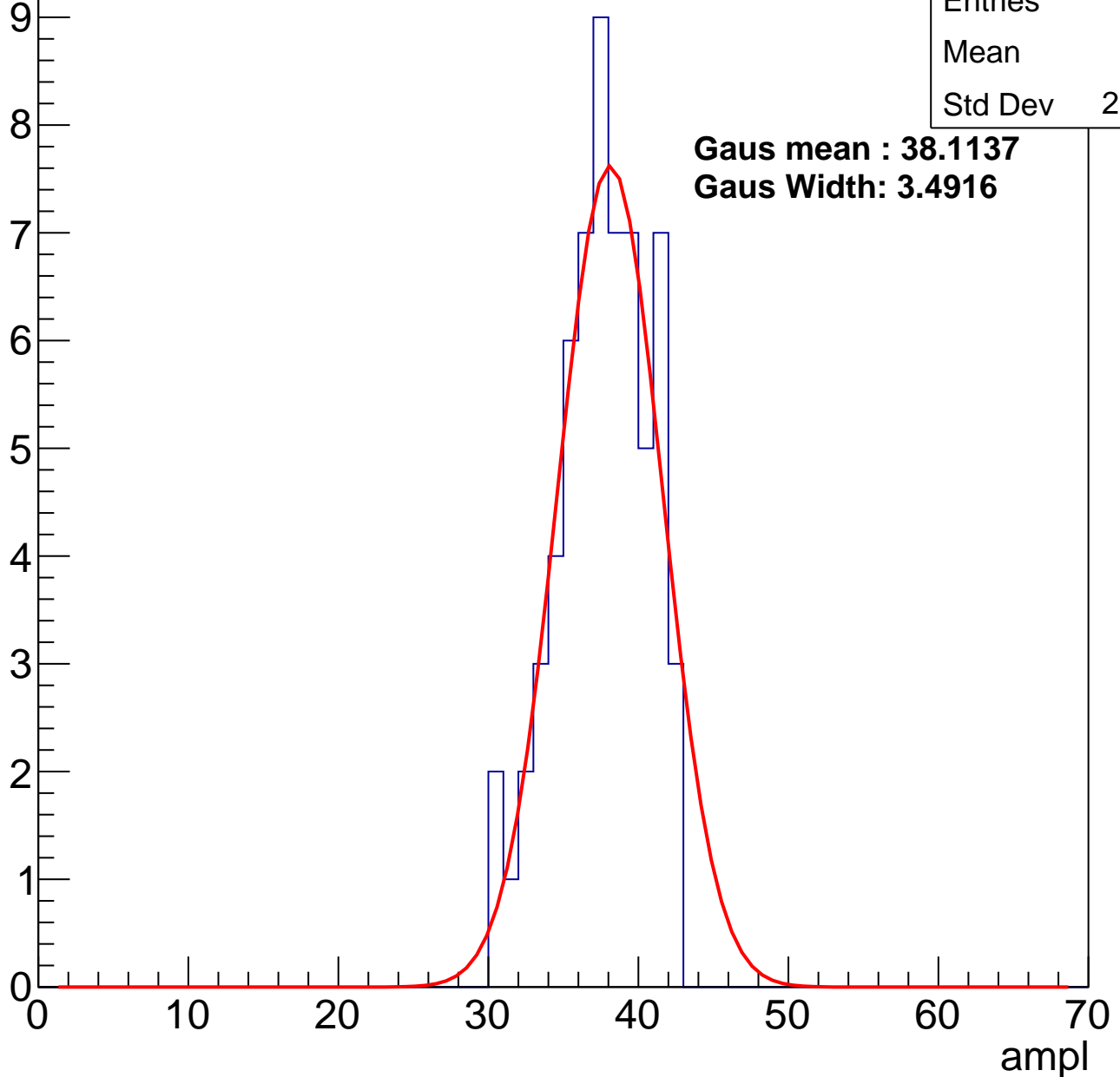
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.1
Std Dev	2.996

Gaus mean : 38.1137

Gaus Width: 3.4916



B0L002S, U18-ch61, adc2

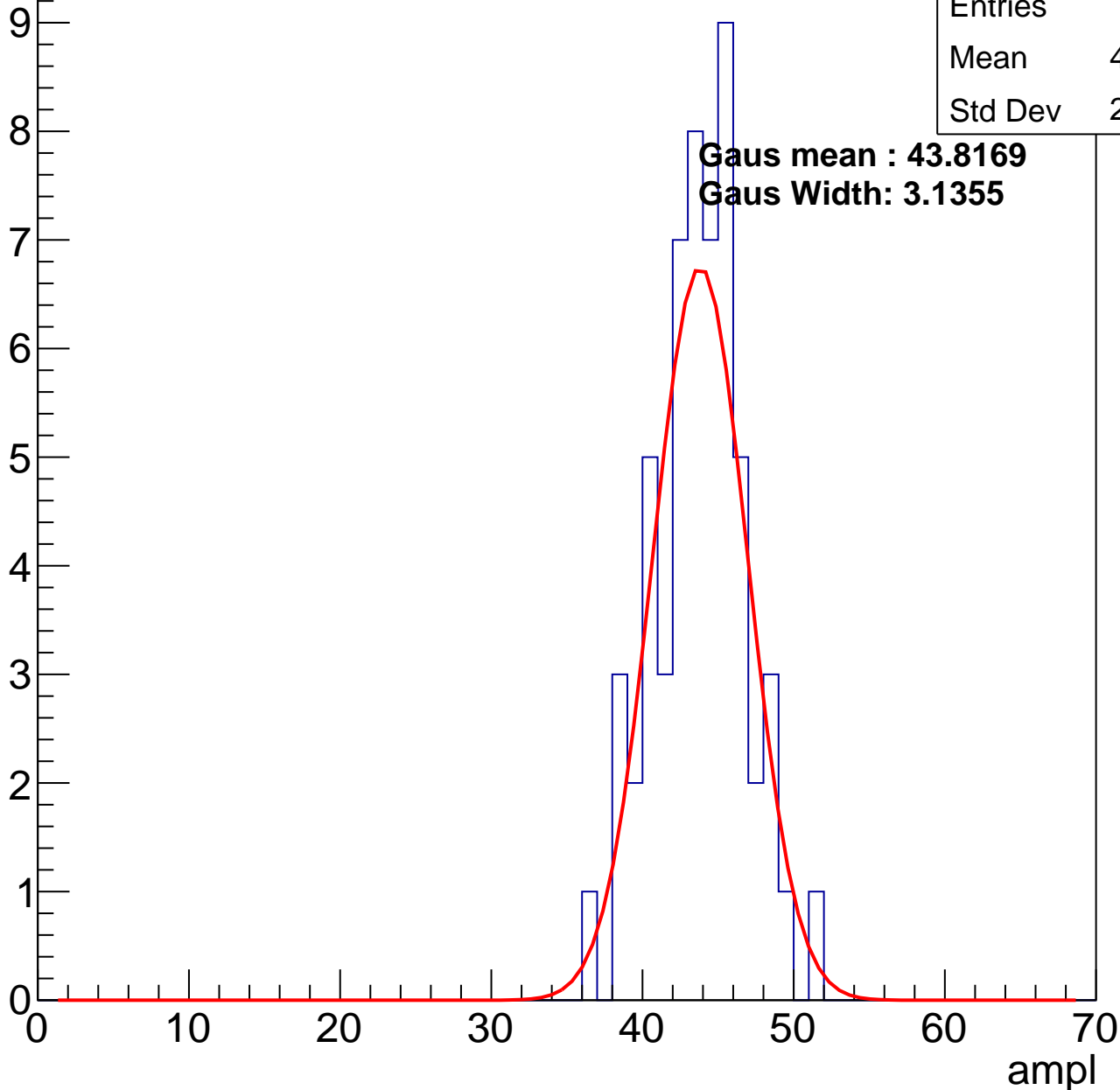
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	43.33
Std Dev	2.987

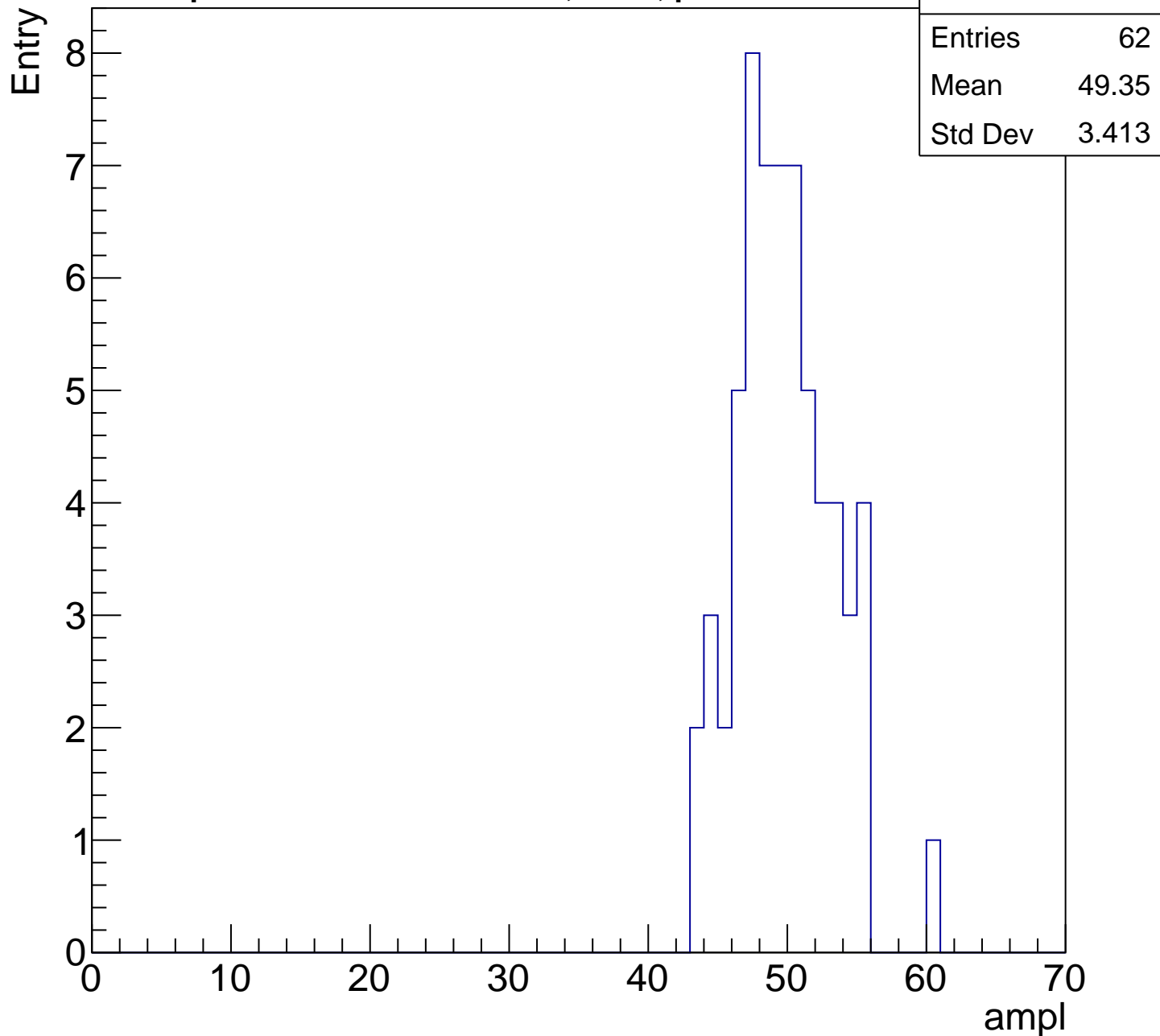
Gaus mean : 43.8169

Gaus Width: 3.1355



B0L002S, U18-ch61, adc3

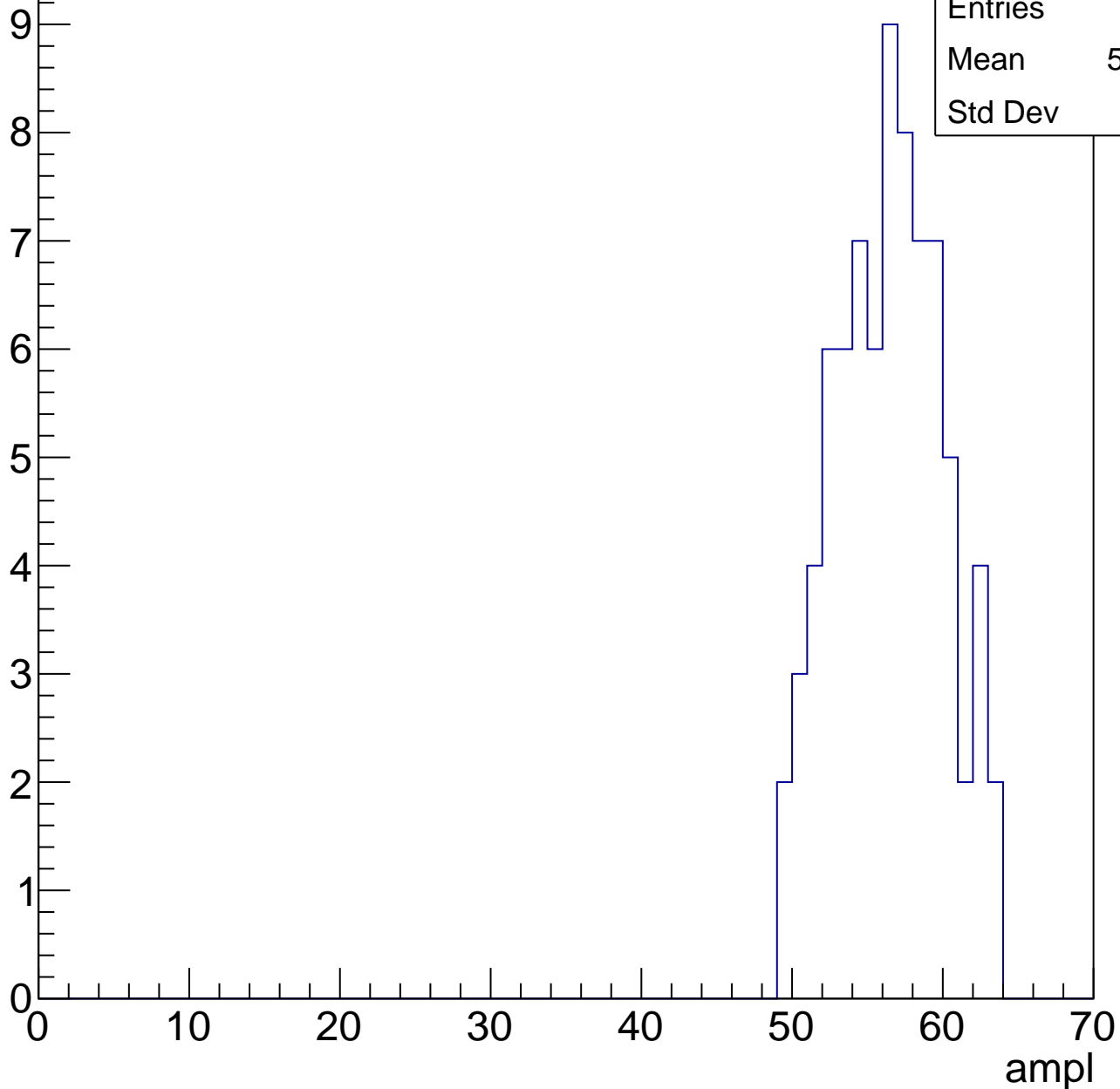
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U18-ch61, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

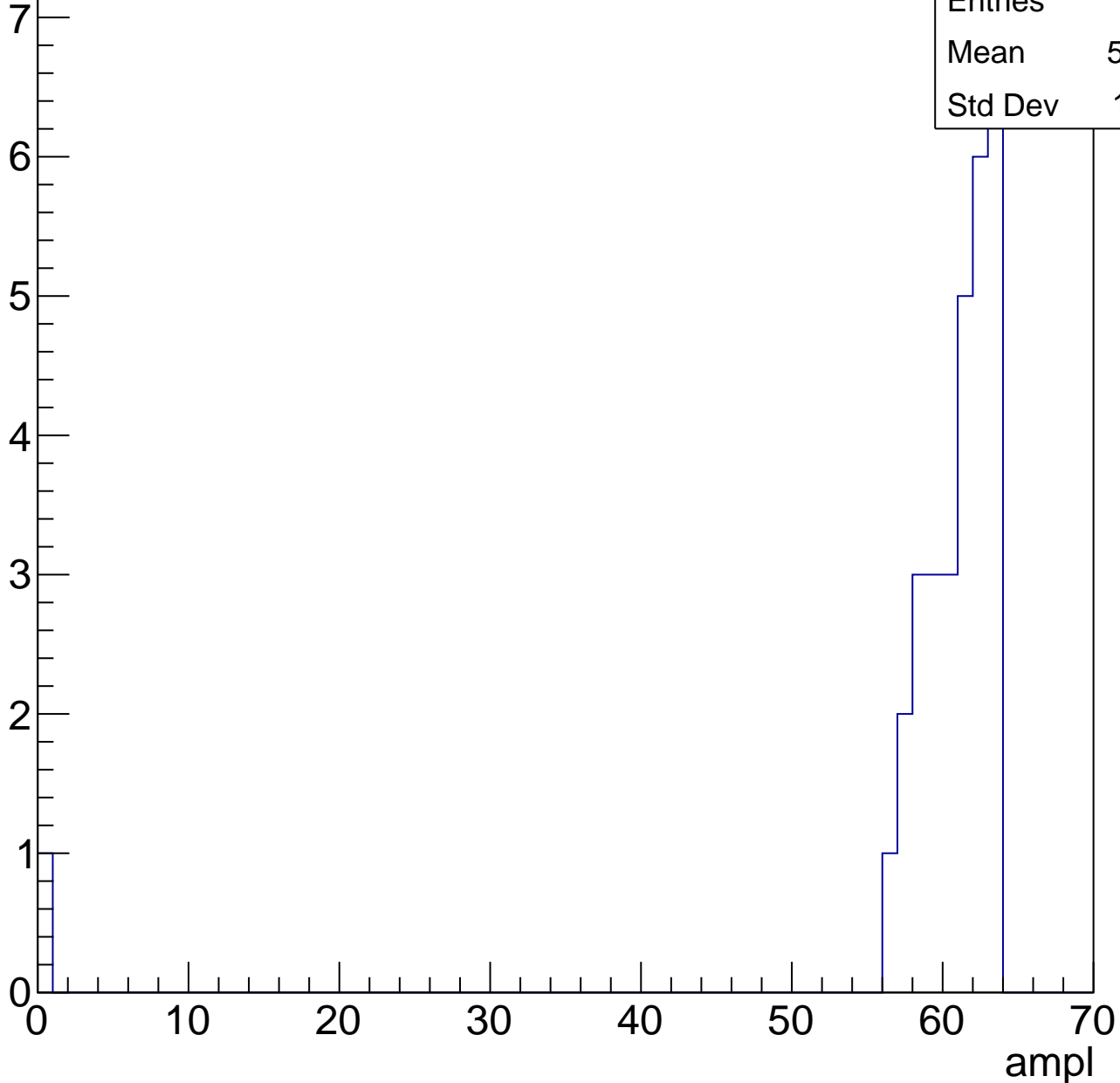


B0L002S, U18-ch61, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

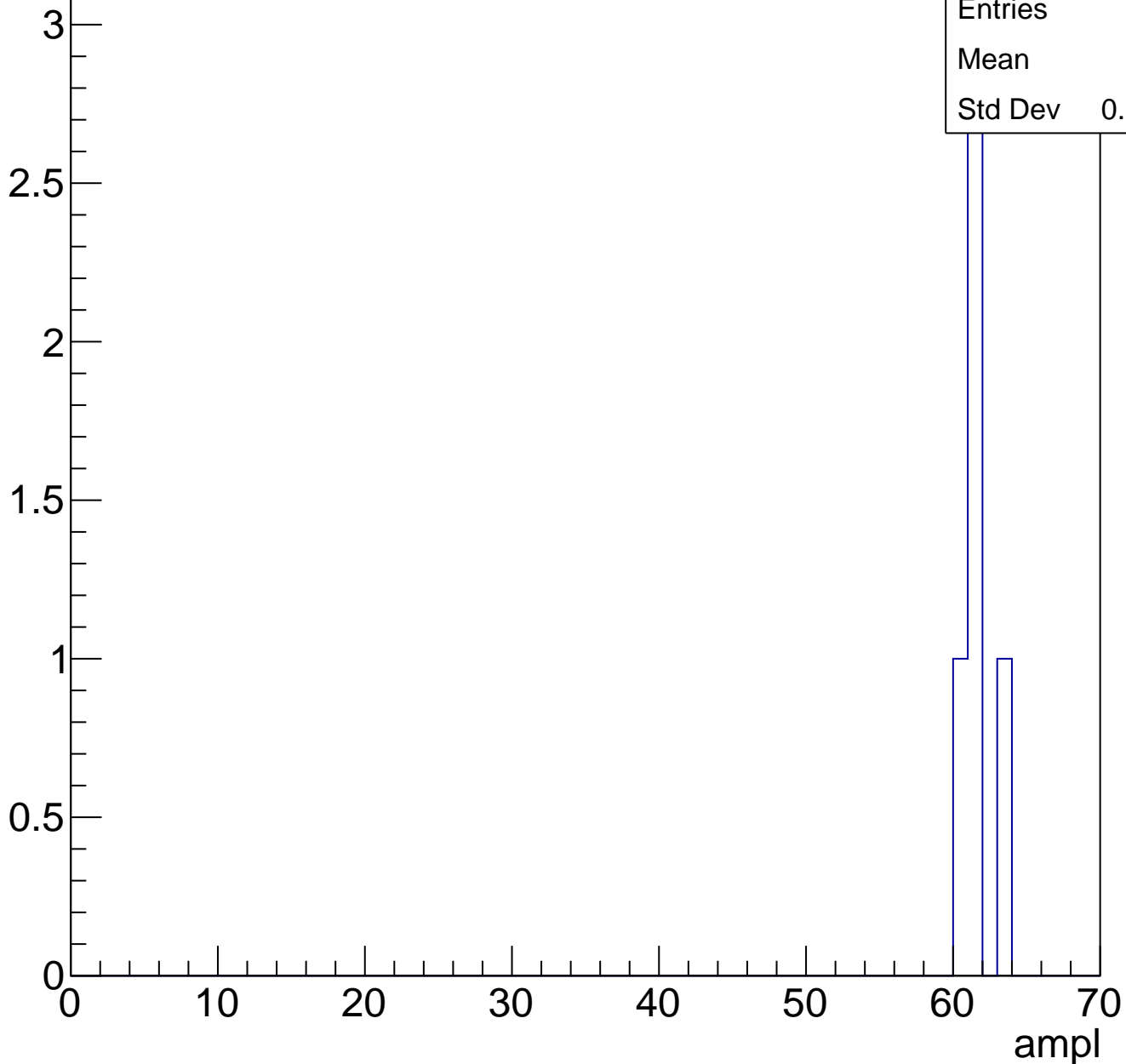
Entries	31
Mean	58.68
Std Dev	10.91



B0L002S, U18-ch61, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch61, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch62, adc0

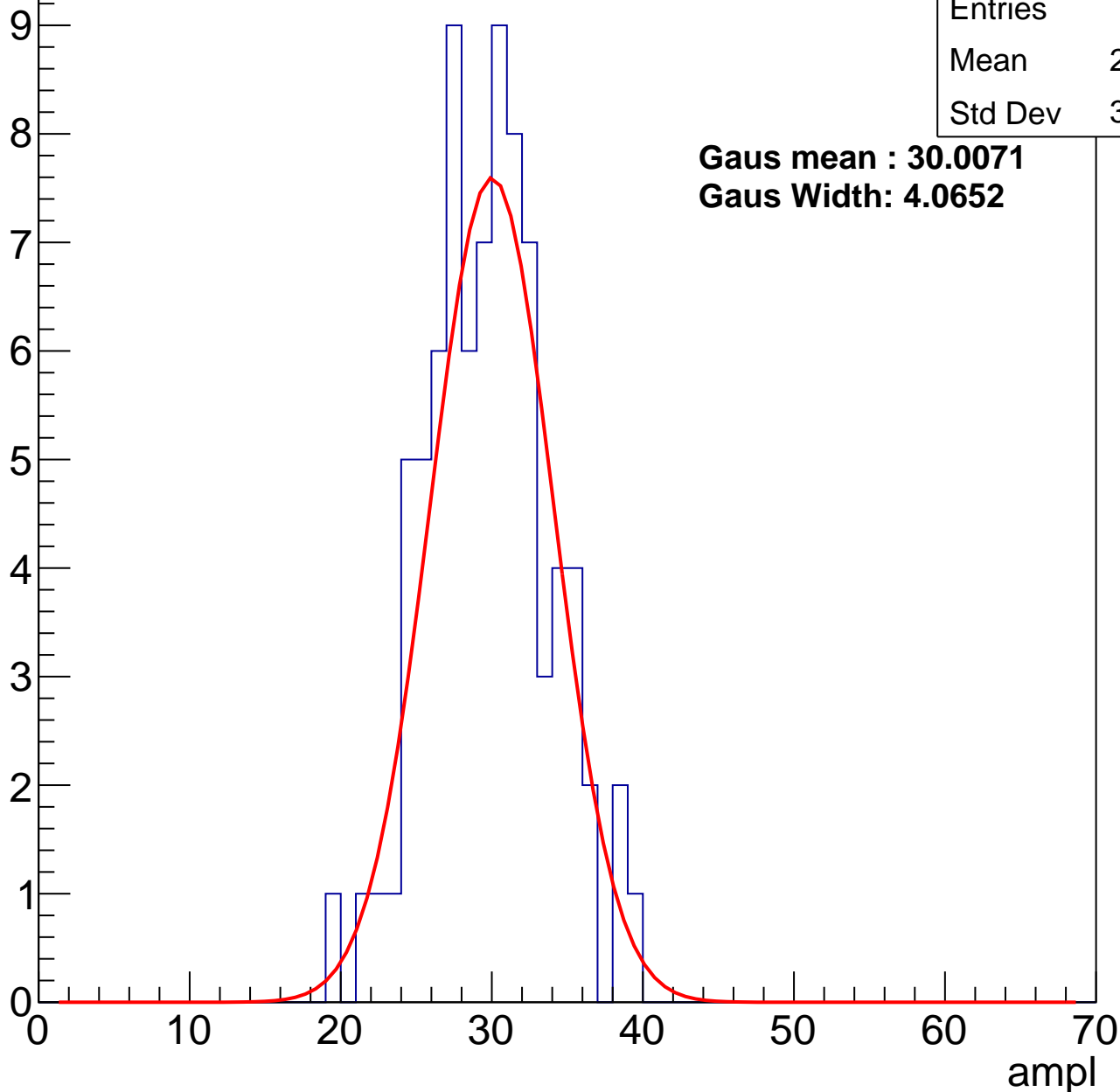
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	29.32
Std Dev	3.978

Gaus mean : 30.0071

Gaus Width: 4.0652



B0L002S, U18-ch62, adc1

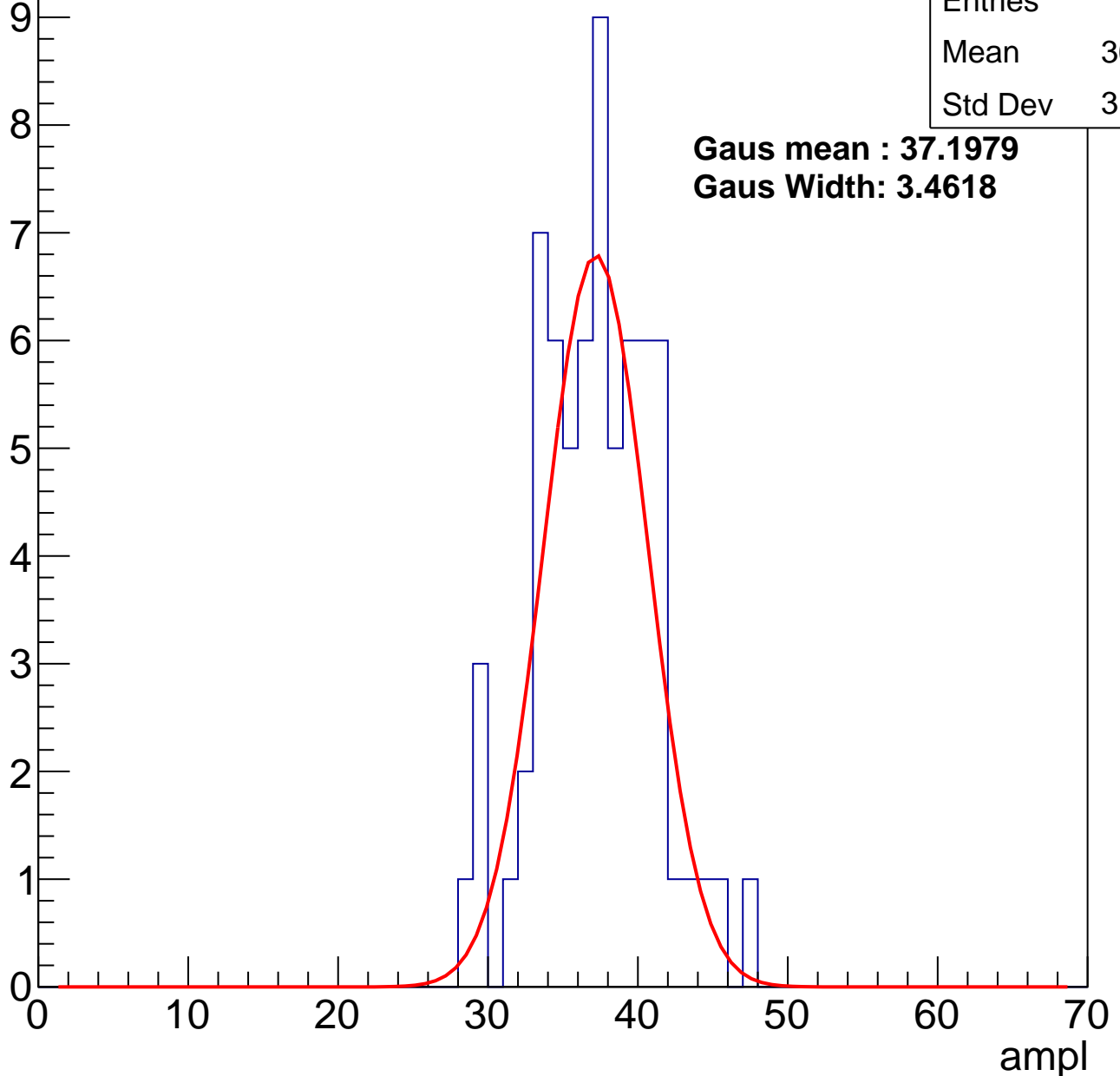
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	36.76
Std Dev	3.835

Gaus mean : 37.1979

Gaus Width: 3.4618



B0L002S, U18-ch62, adc2

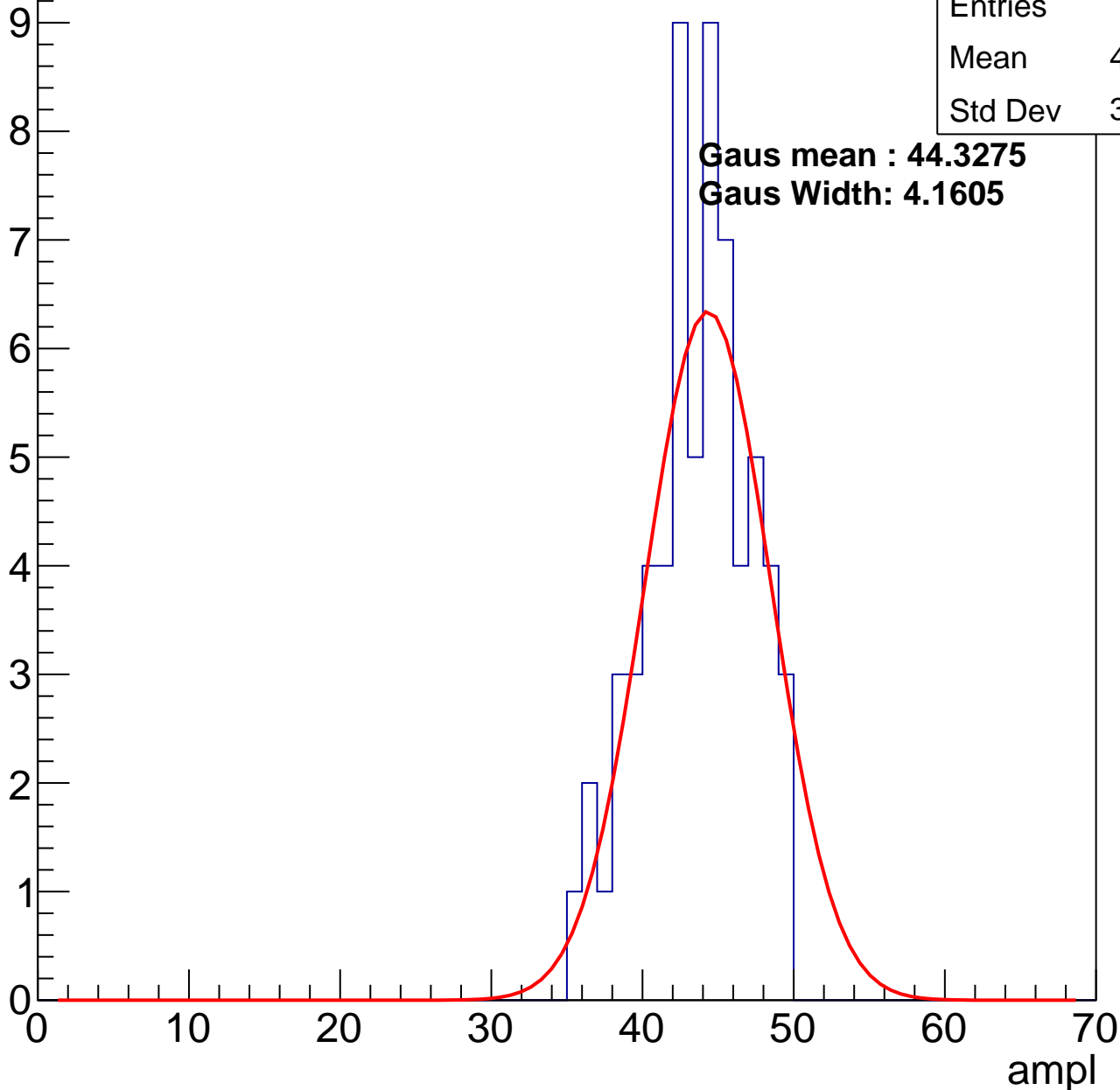
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	43.14
Std Dev	3.404

Gaus mean : 44.3275

Gaus Width: 4.1605

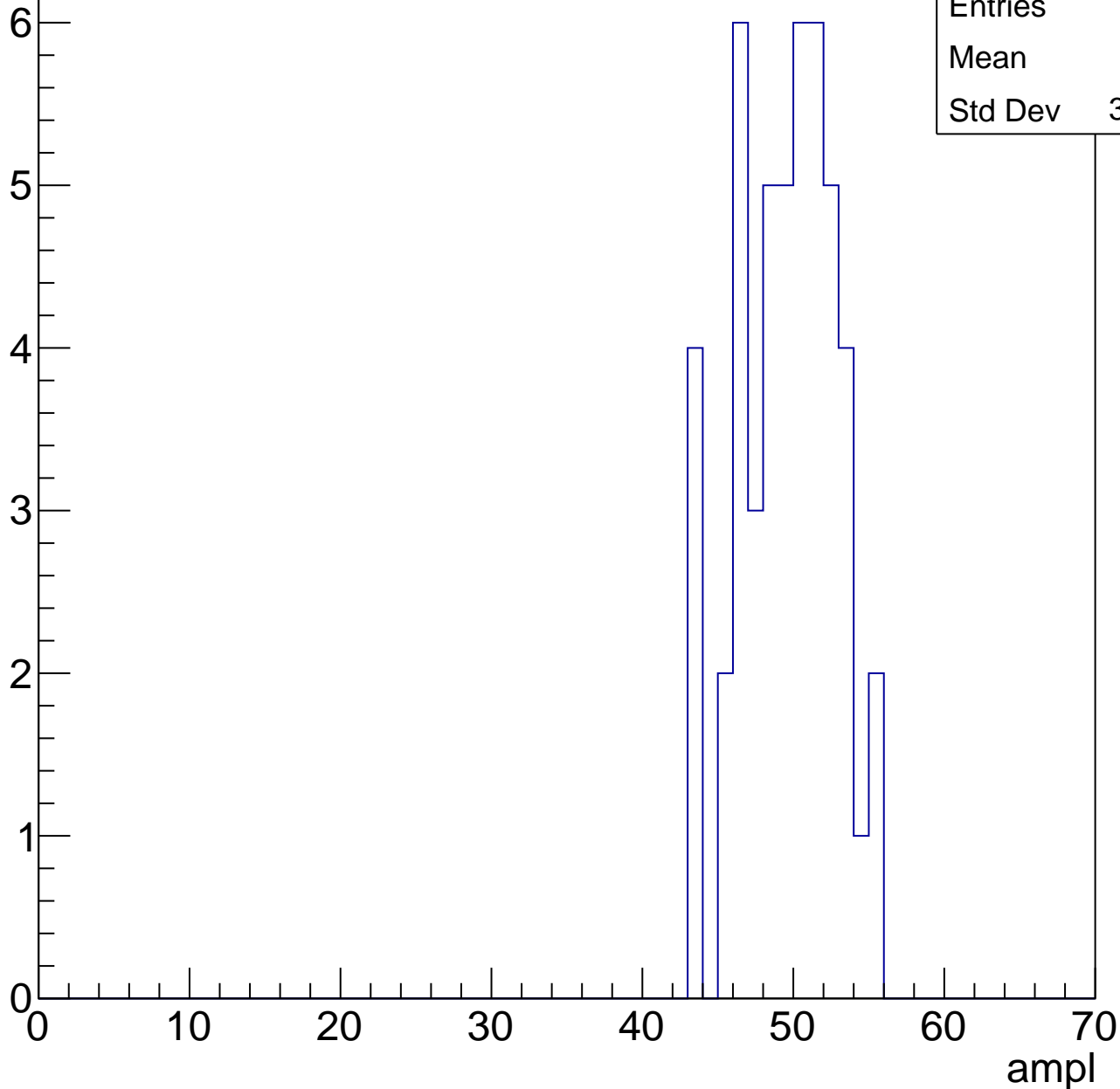


B0L002S, U18-ch62, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

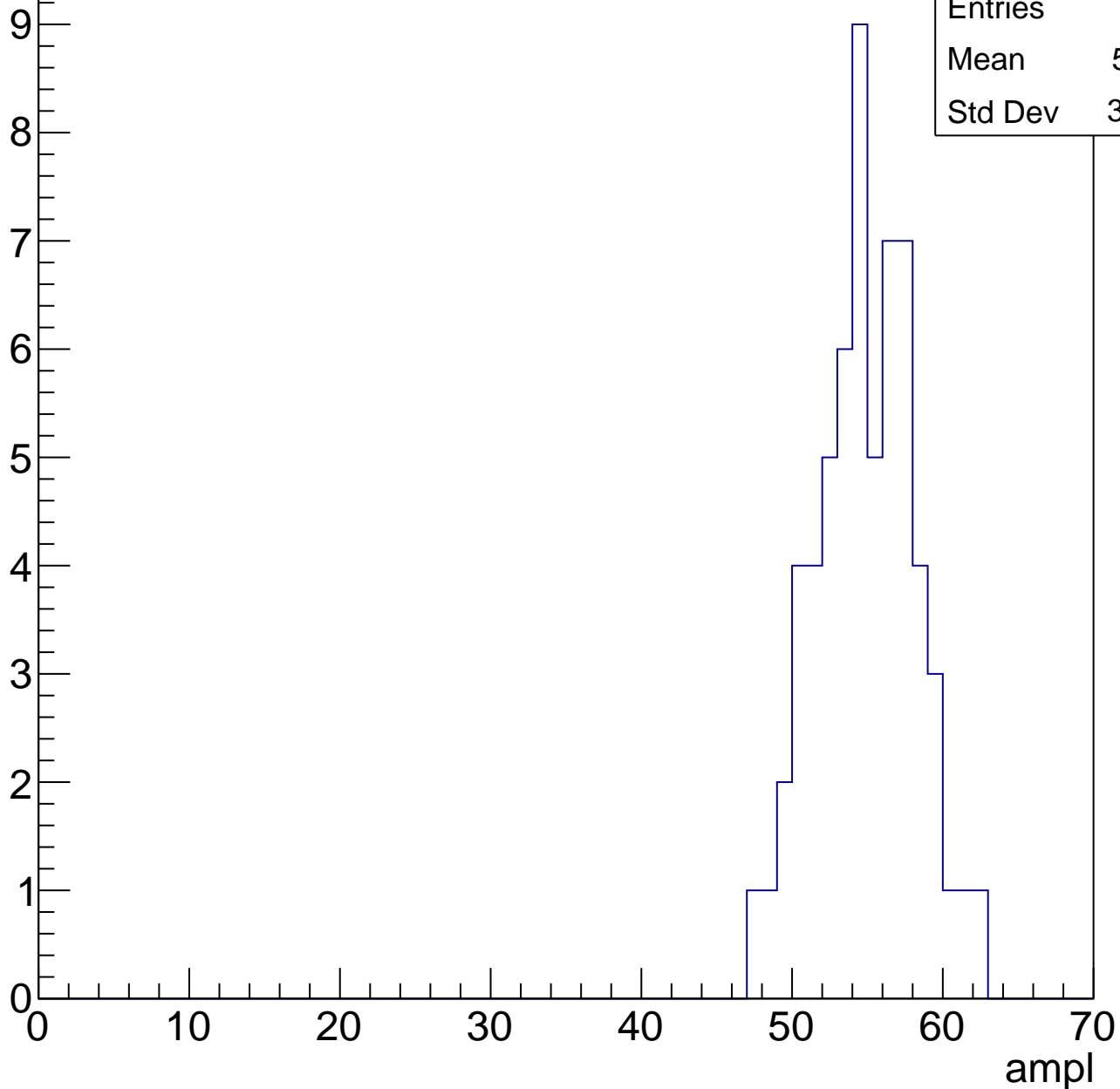
Entries	49
Mean	49.1
Std Dev	3.138



B0L002S, U18-ch62, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch62, adc5

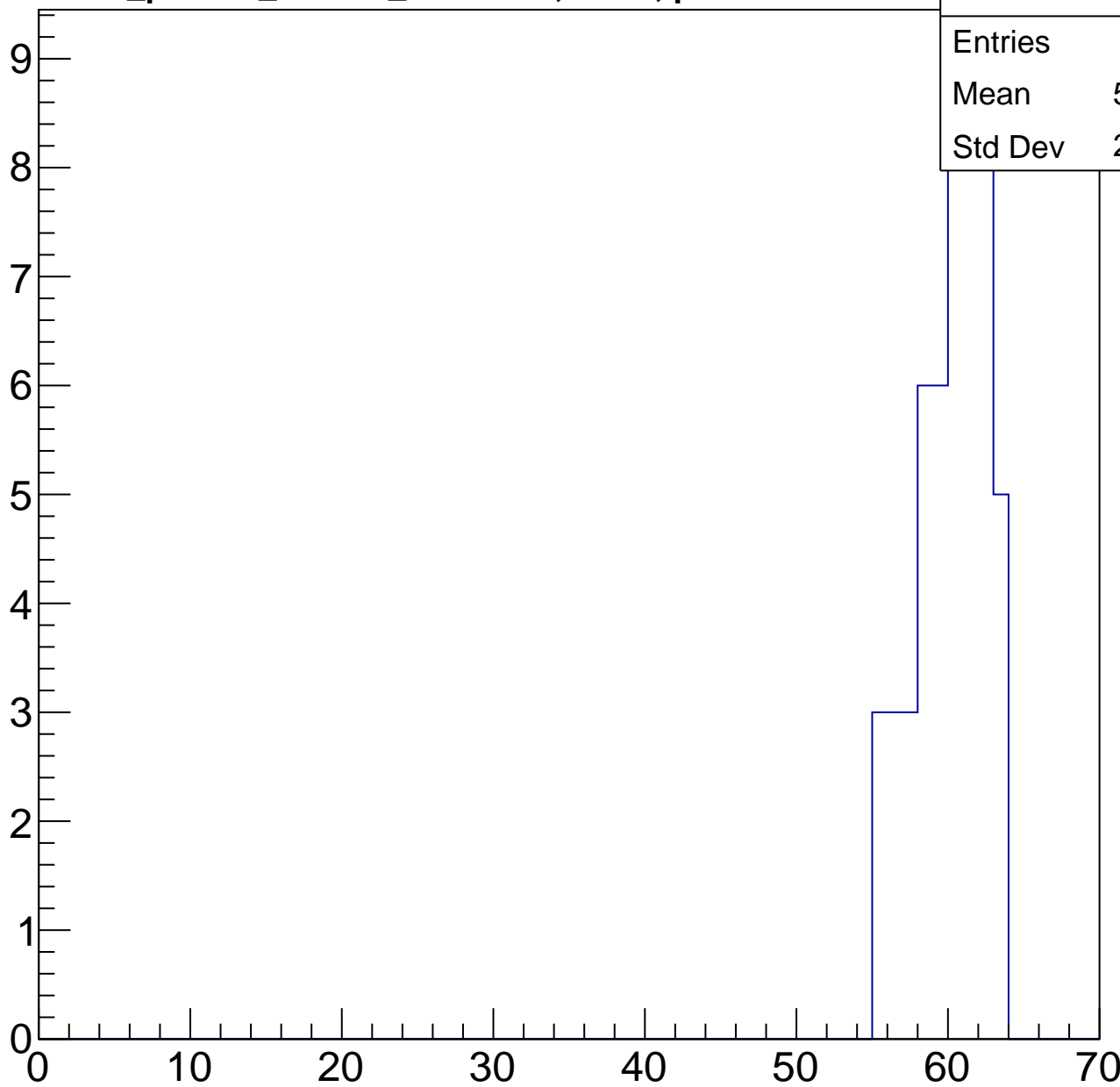
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.73
Std Dev	2.267

ampl

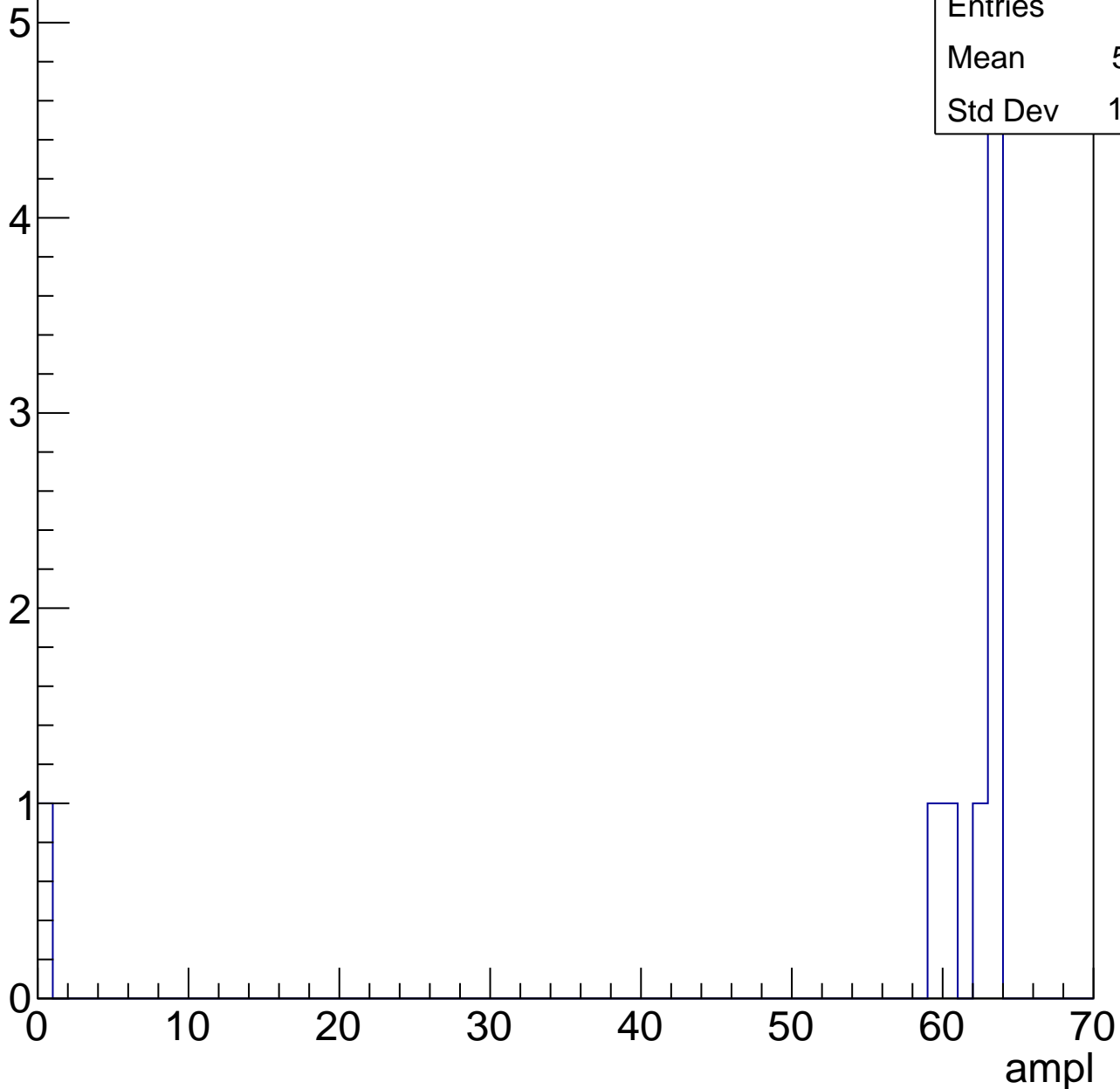


B0L002S, U18-ch62, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	9
Mean	55.11
Std Dev	19.54



B0L002S, U18-ch62, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch63, adc0

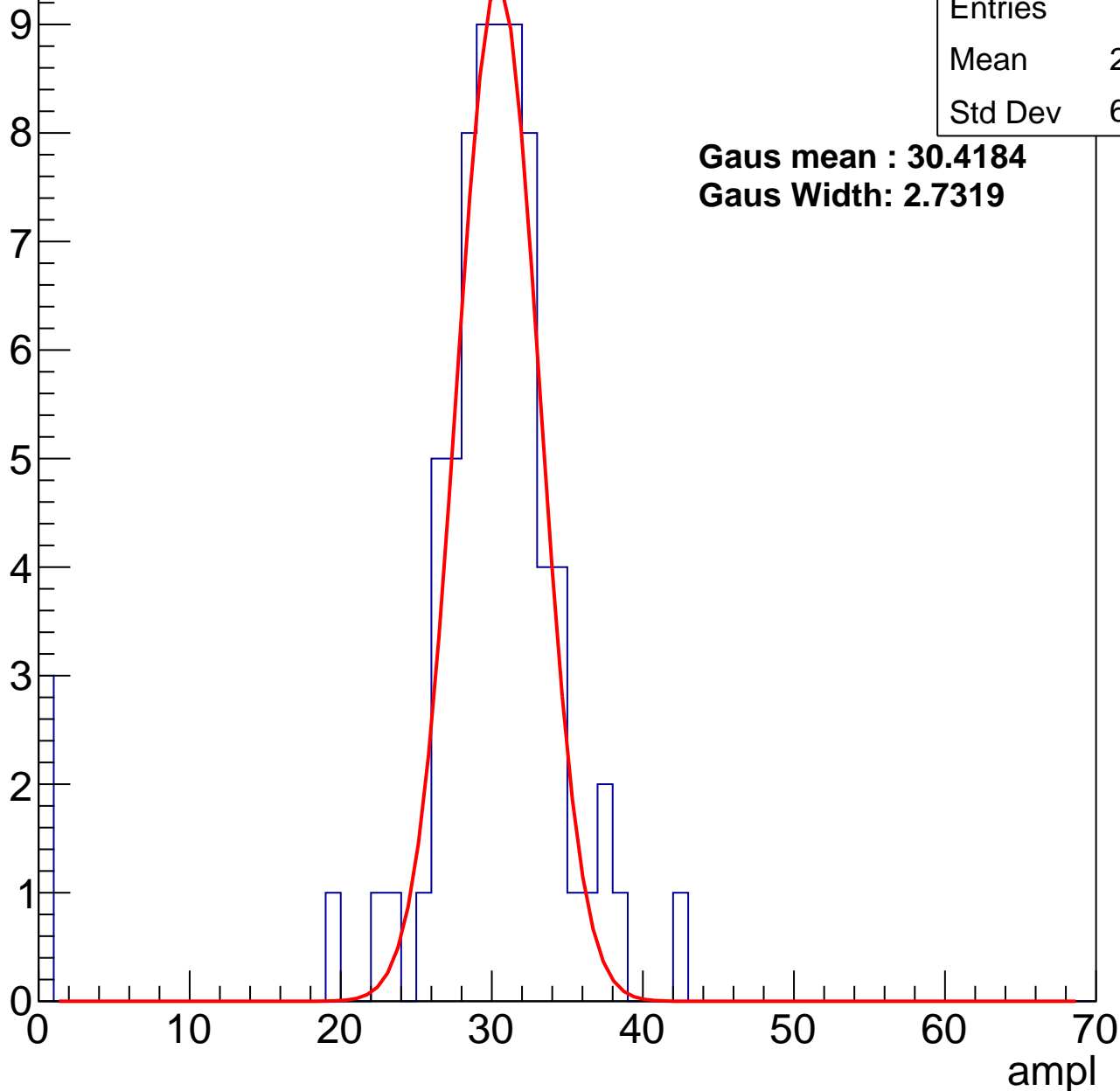
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	28.88
Std Dev	6.916

Gaus mean : 30.4184

Gaus Width: 2.7319



B0L002S, U18-ch63, adc1

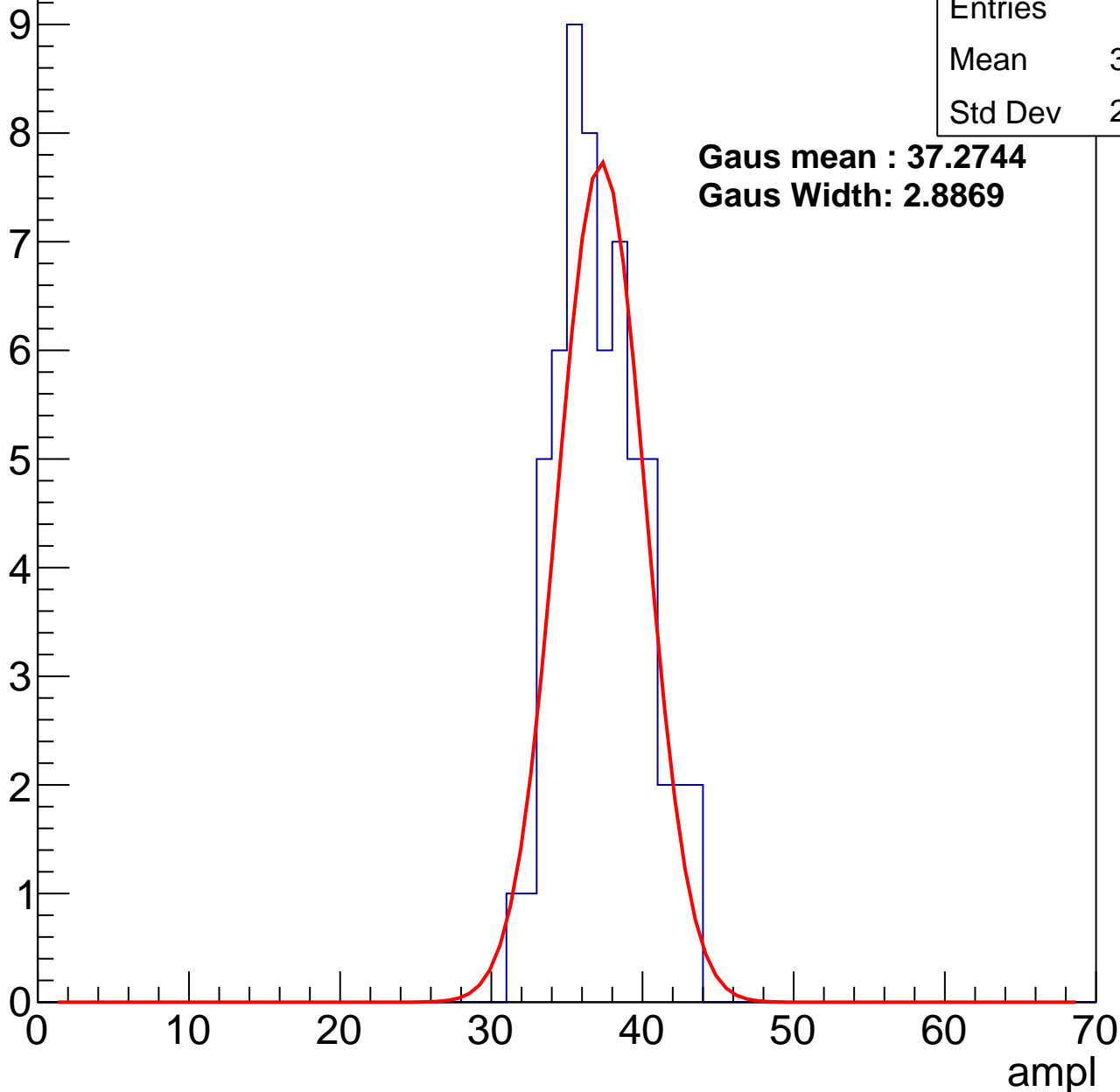
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.78
Std Dev	2.799

Gaus mean : 37.2744

Gaus Width: 2.8869



B0L002S, U18-ch63, adc2

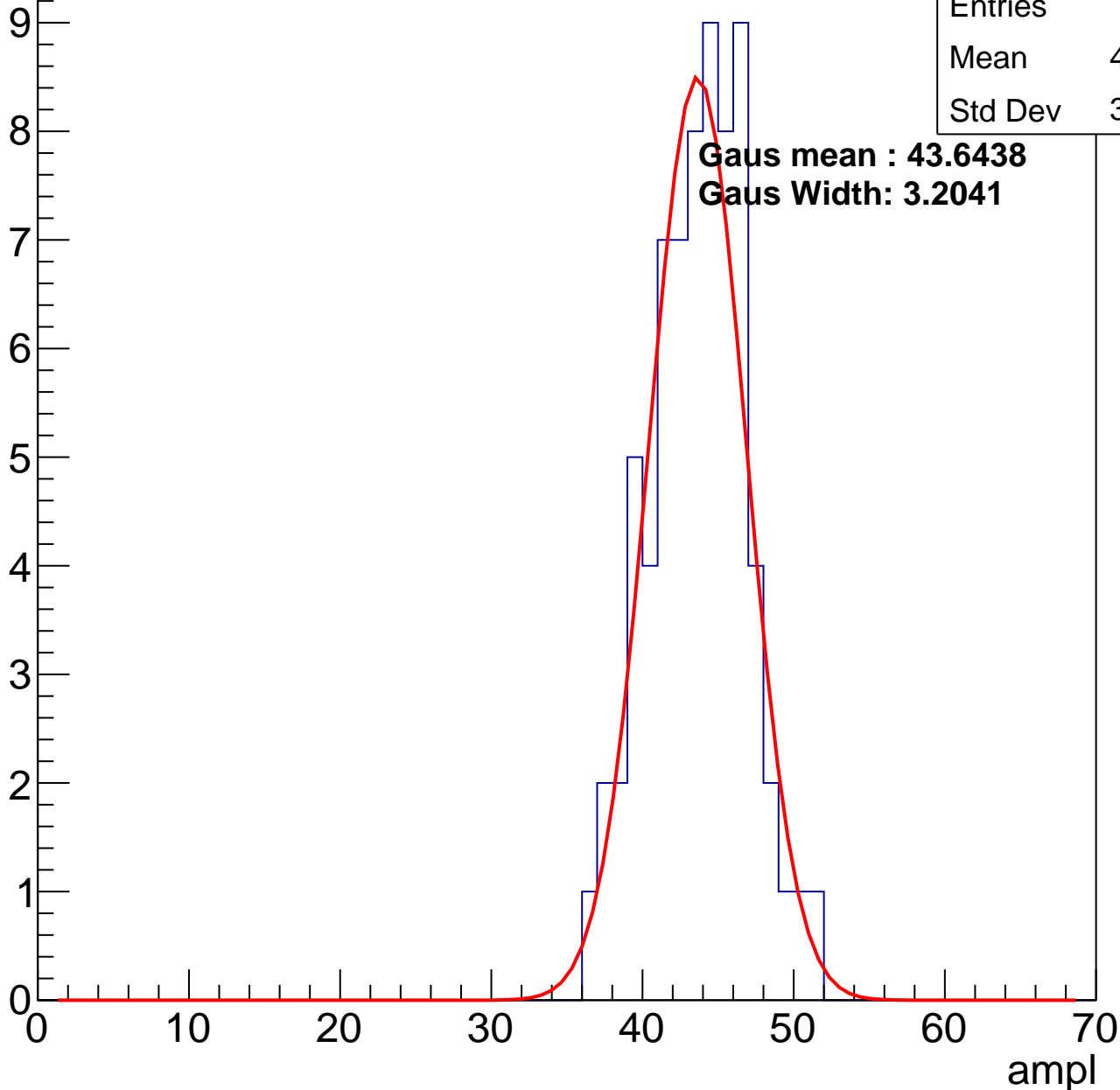
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.24
Std Dev	3.146

Gaus mean : 43.6438

Gaus Width: 3.2041

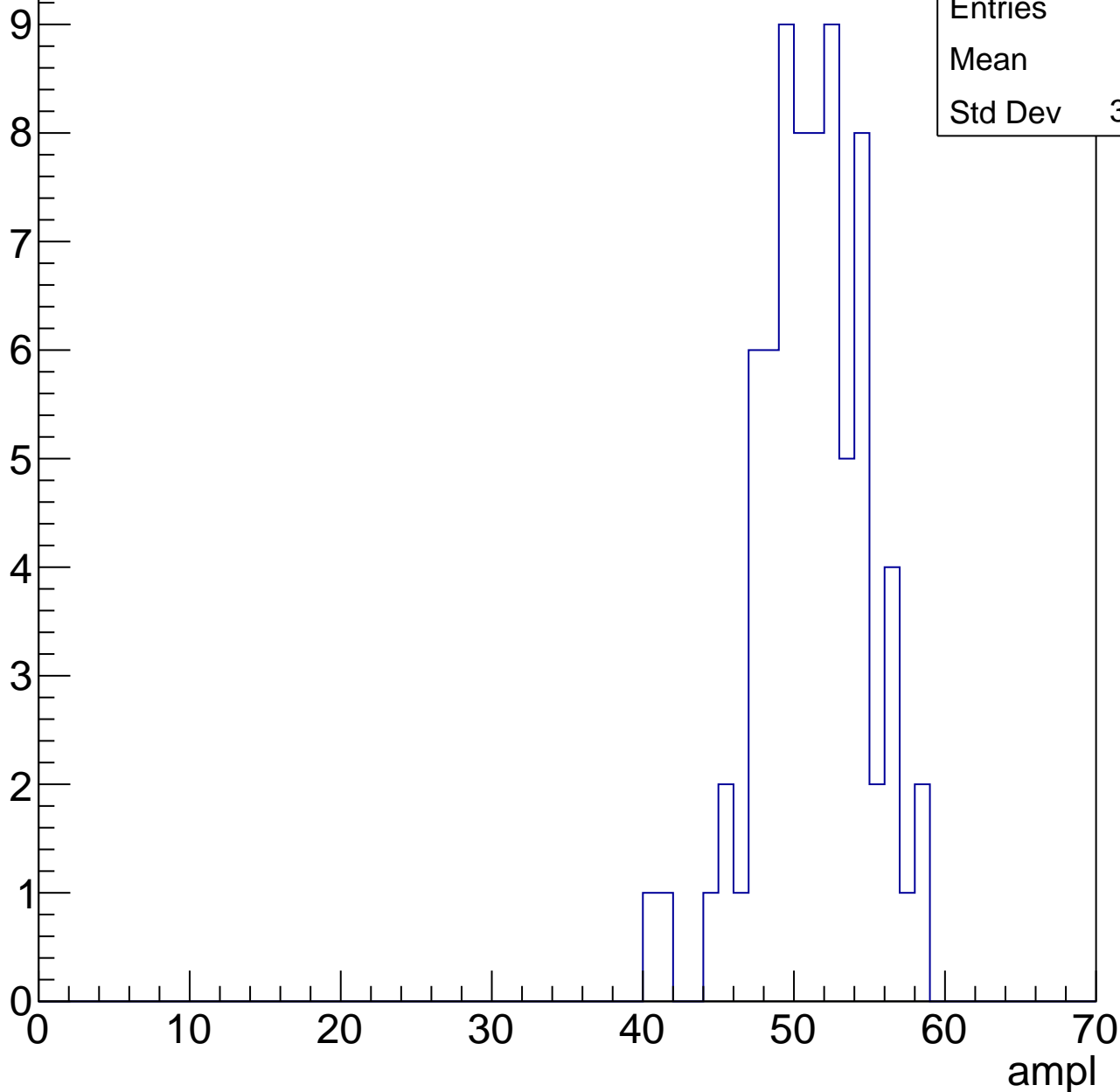


B0L002S, U18-ch63, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	50.7
Std Dev	3.529

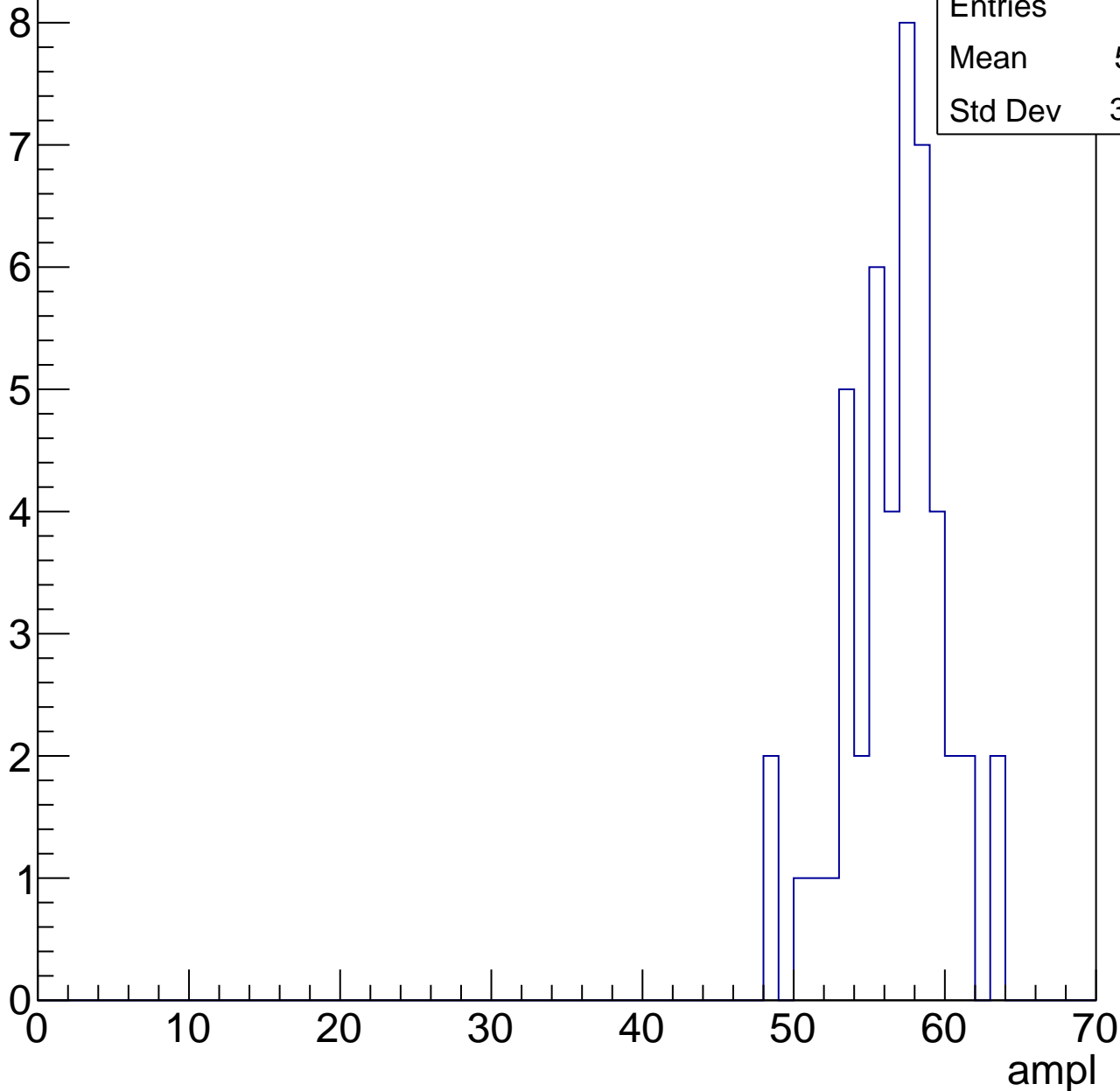


B0L002S, U18-ch63, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	56.21
Std Dev	3.307

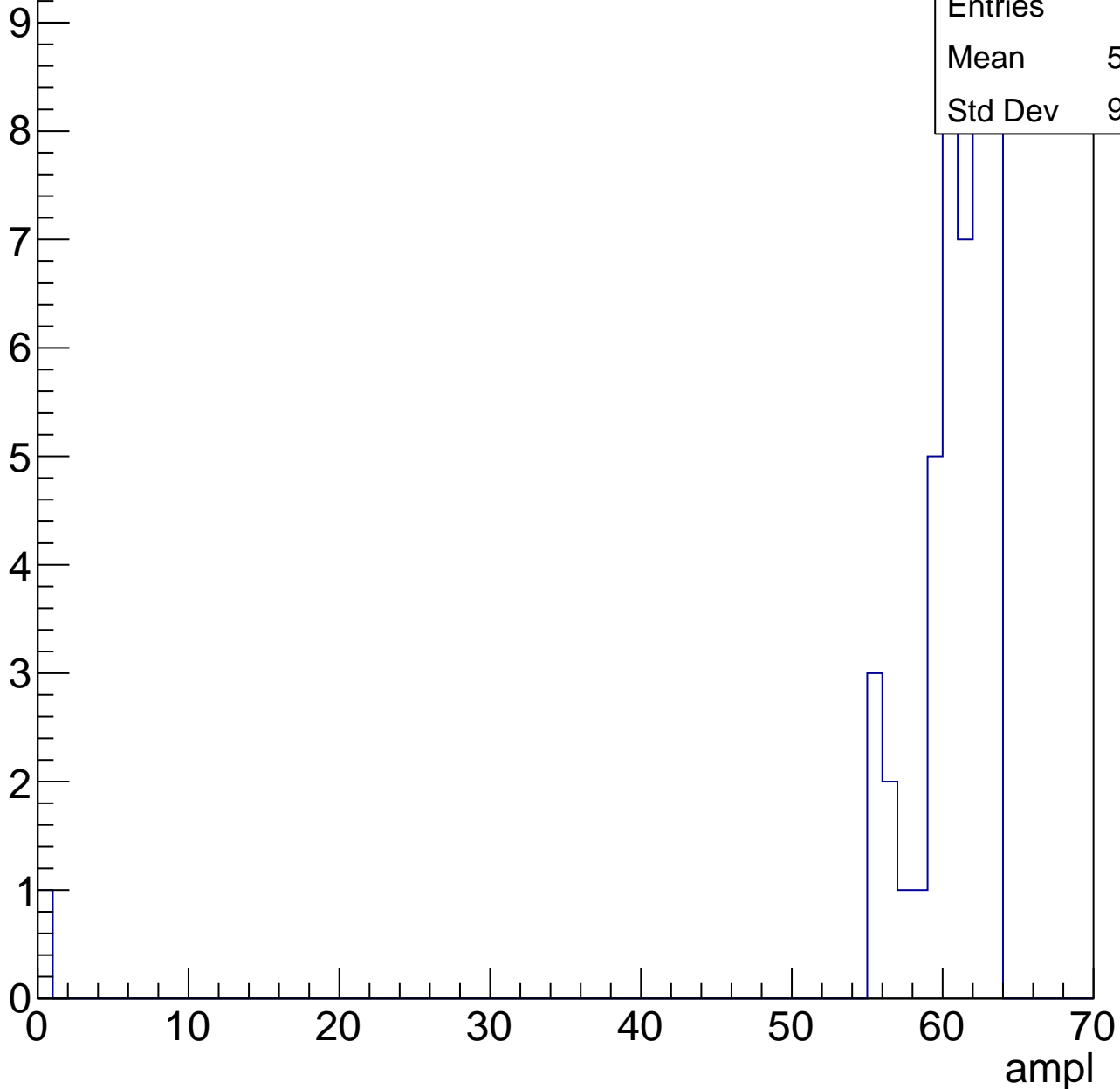


B0L002S, U18-ch63, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

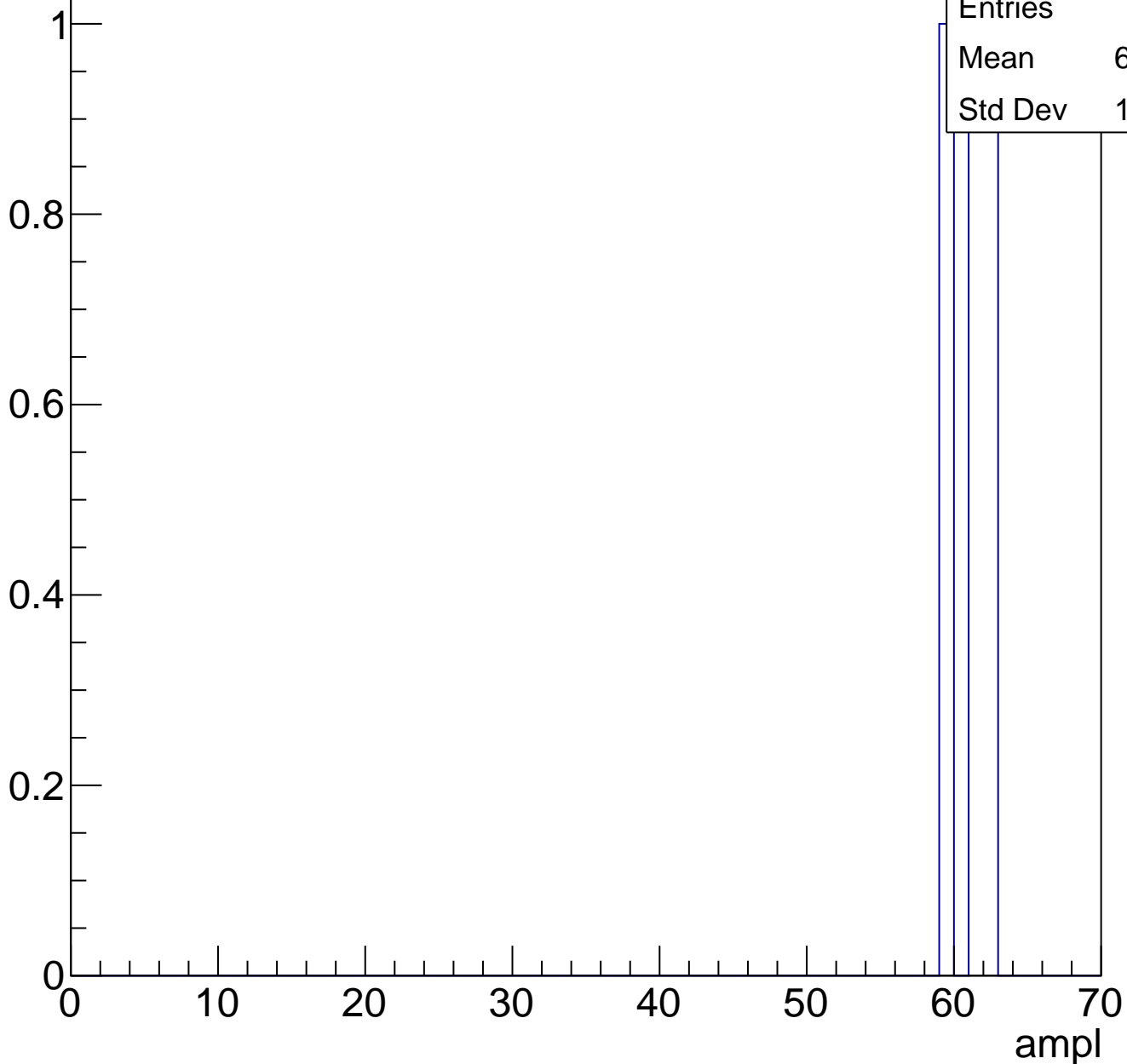
Entries	45
Mean	59.02
Std Dev	9.186



B0L002S, U18-ch63, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch63, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch64, adc0

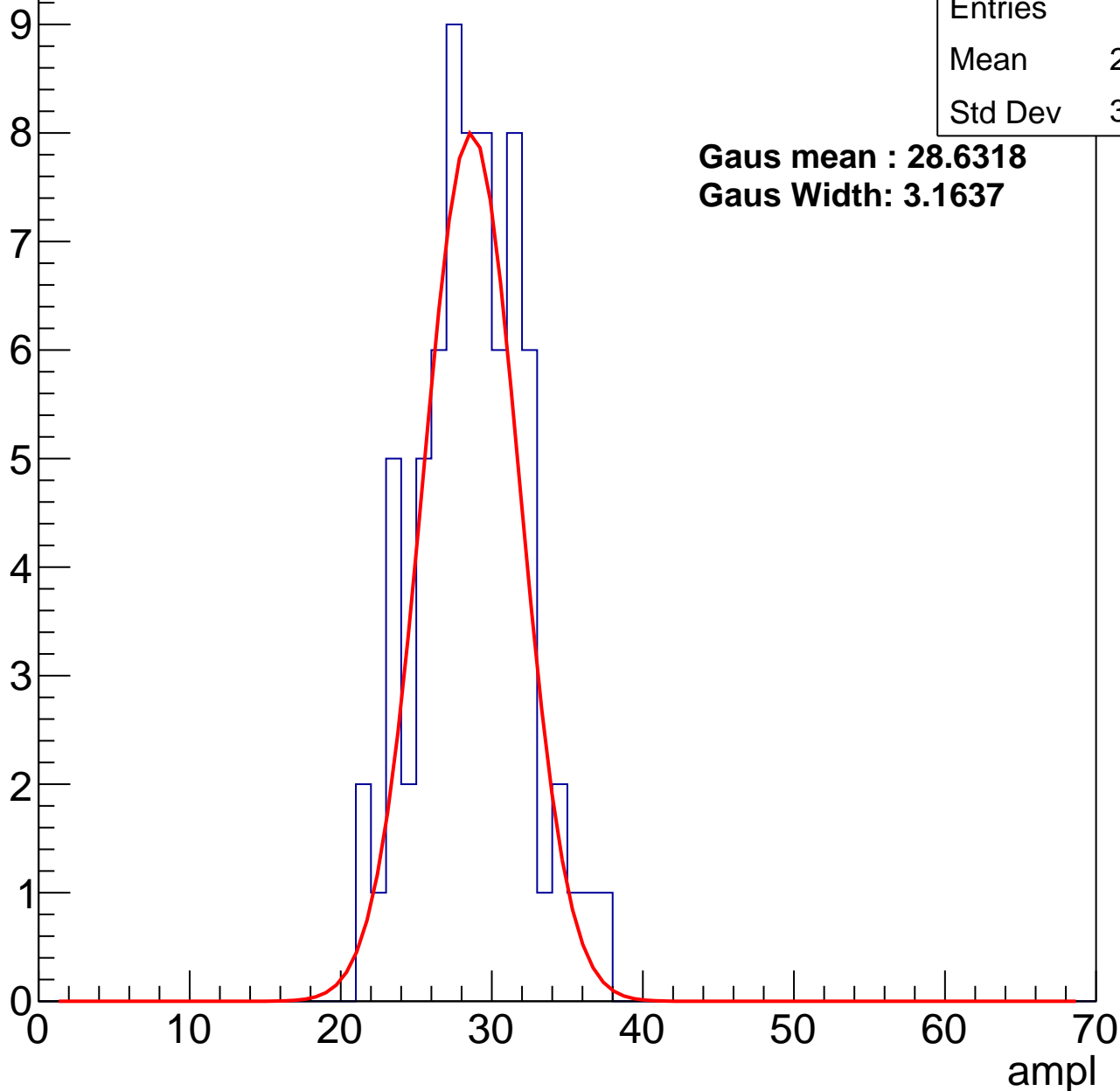
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	28.28
Std Dev	3.445

Gaus mean : 28.6318

Gaus Width: 3.1637



B0L002S, U18-ch64, adc1

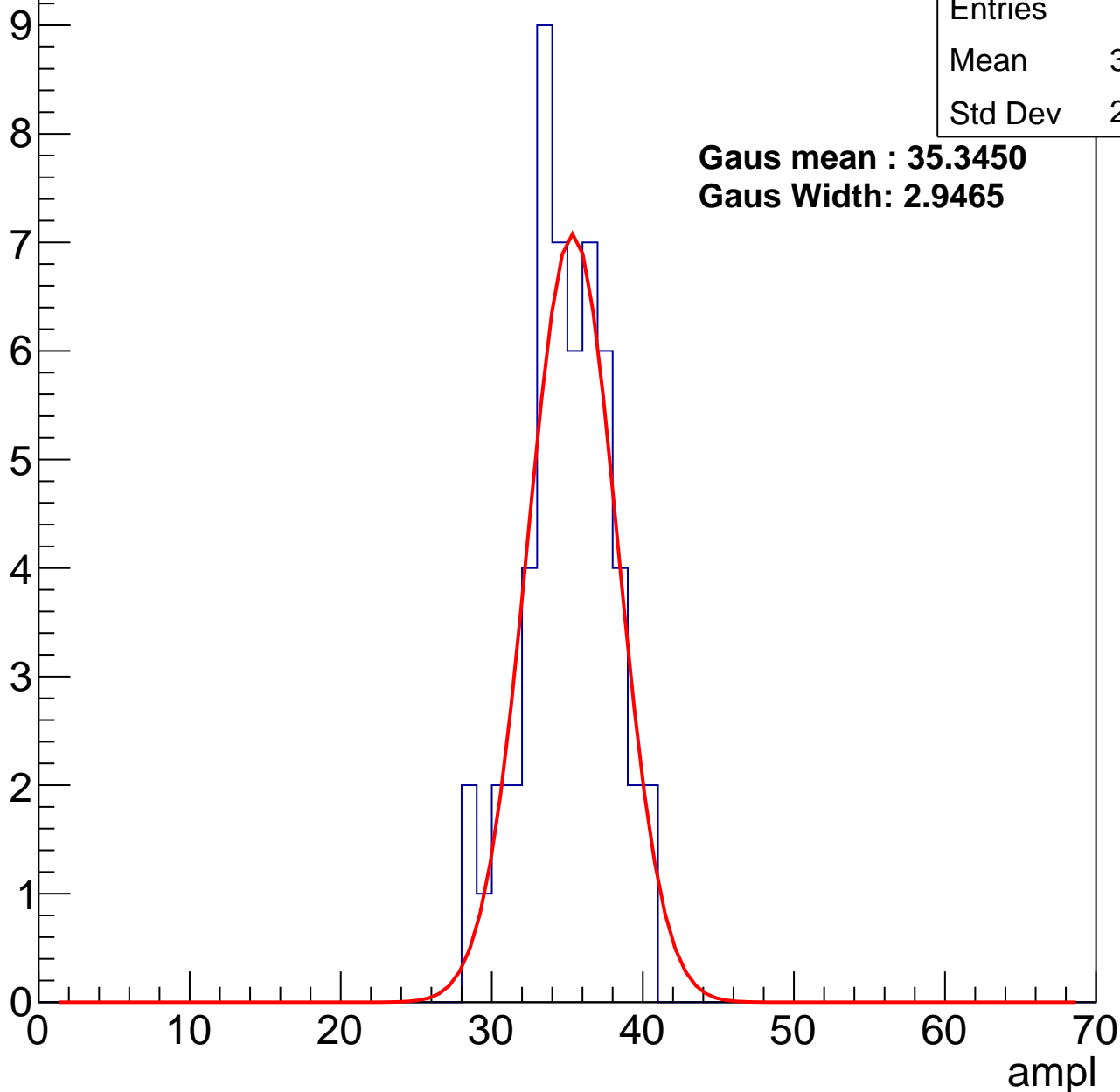
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	34.52
Std Dev	2.827

Gaus mean : 35.3450

Gaus Width: 2.9465



B0L002S, U18-ch64, adc2

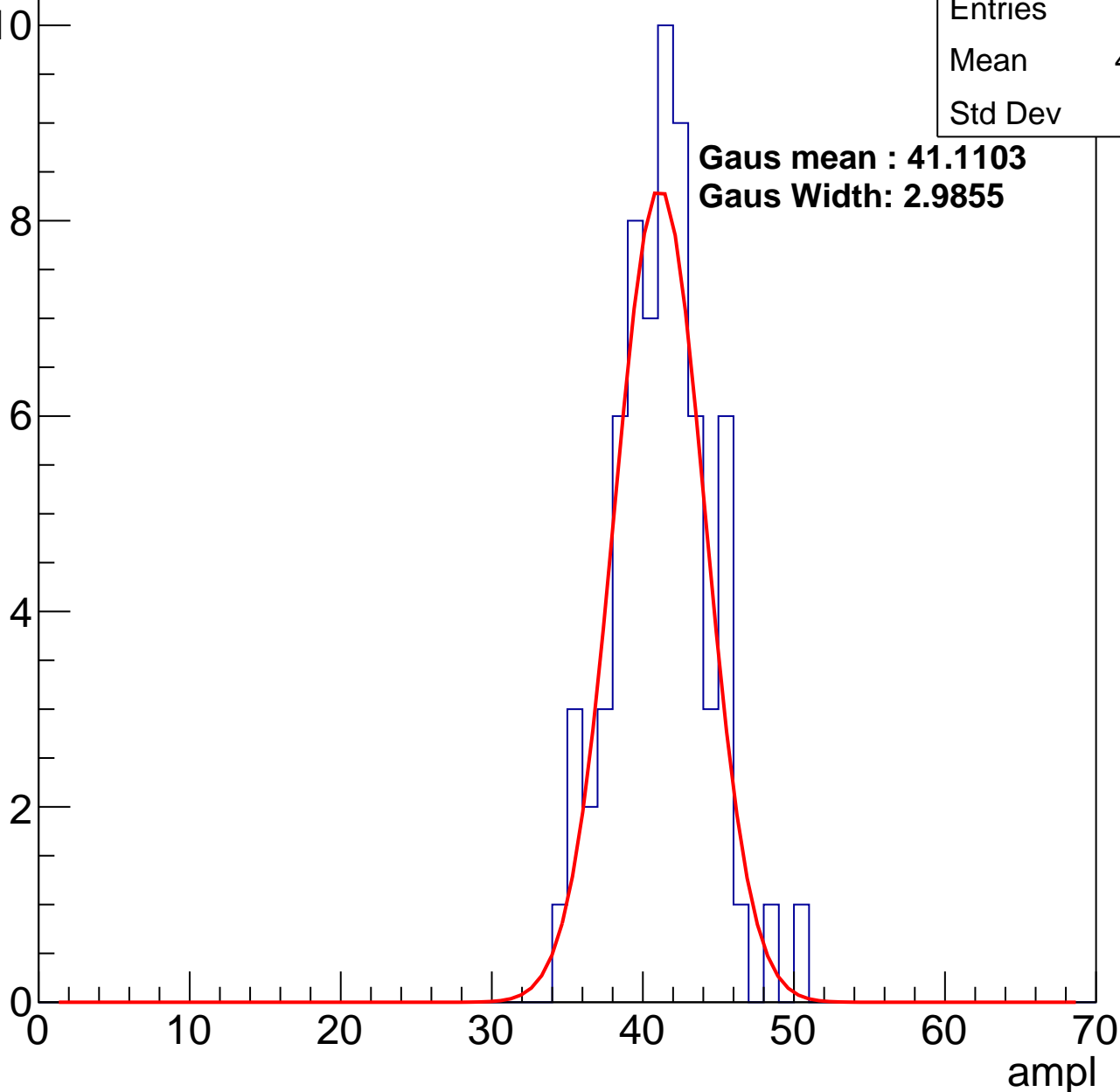
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	40.81
Std Dev	3.13

Gaus mean : 41.1103

Gaus Width: 2.9855

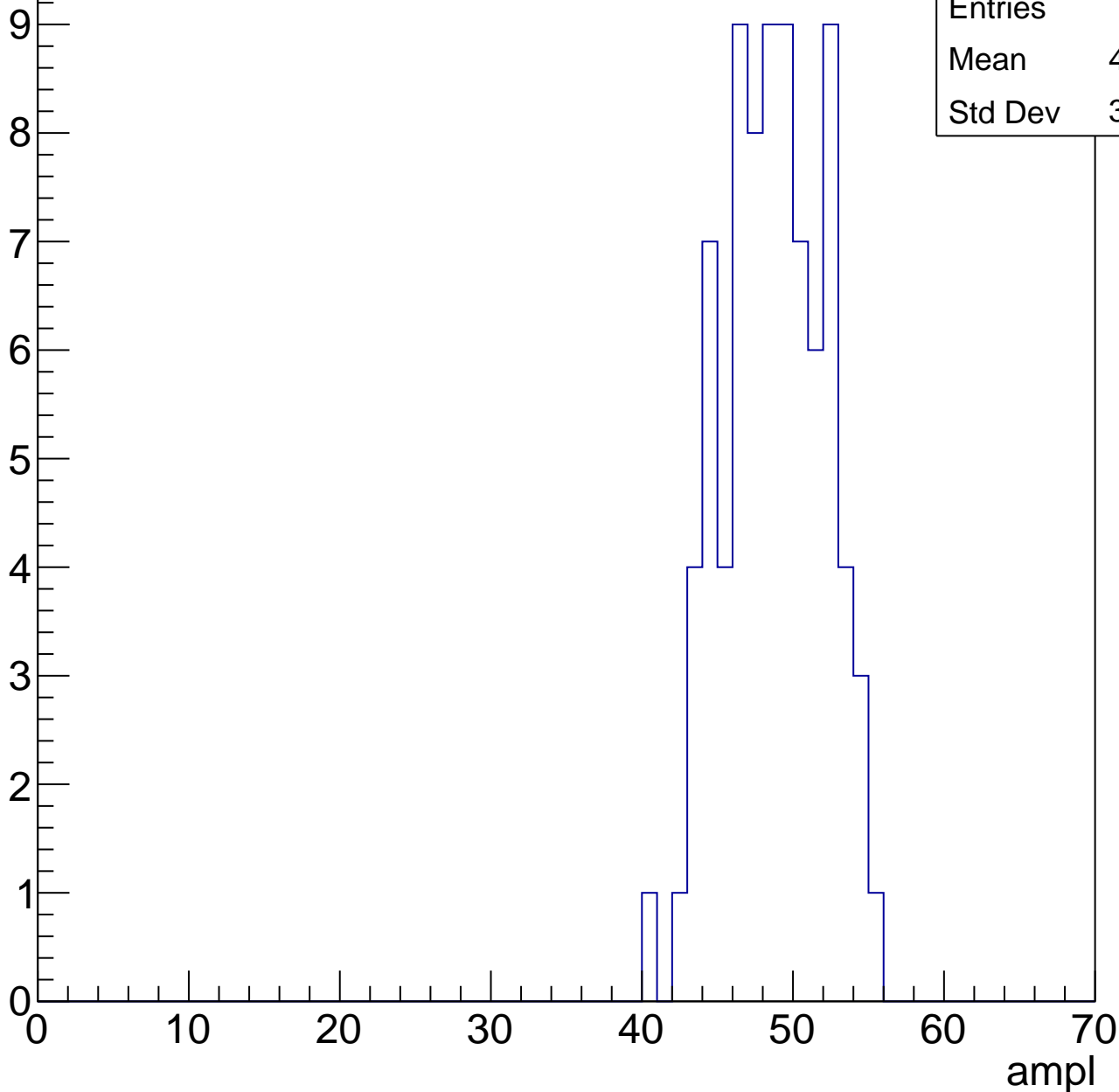


B0L002S, U18-ch64, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	82
Mean	48.27
Std Dev	3.272

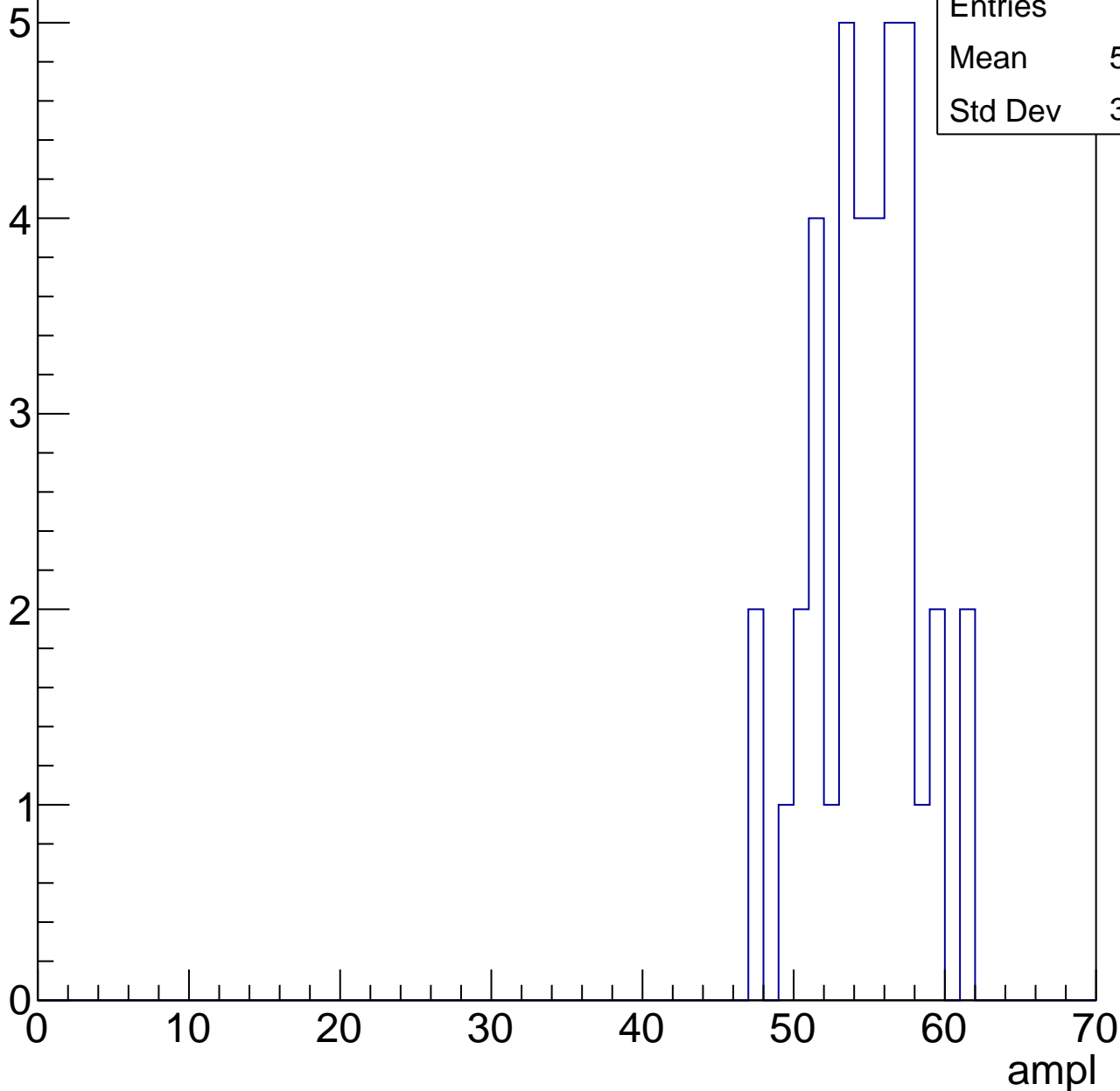


B0L002S, U18-ch64, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

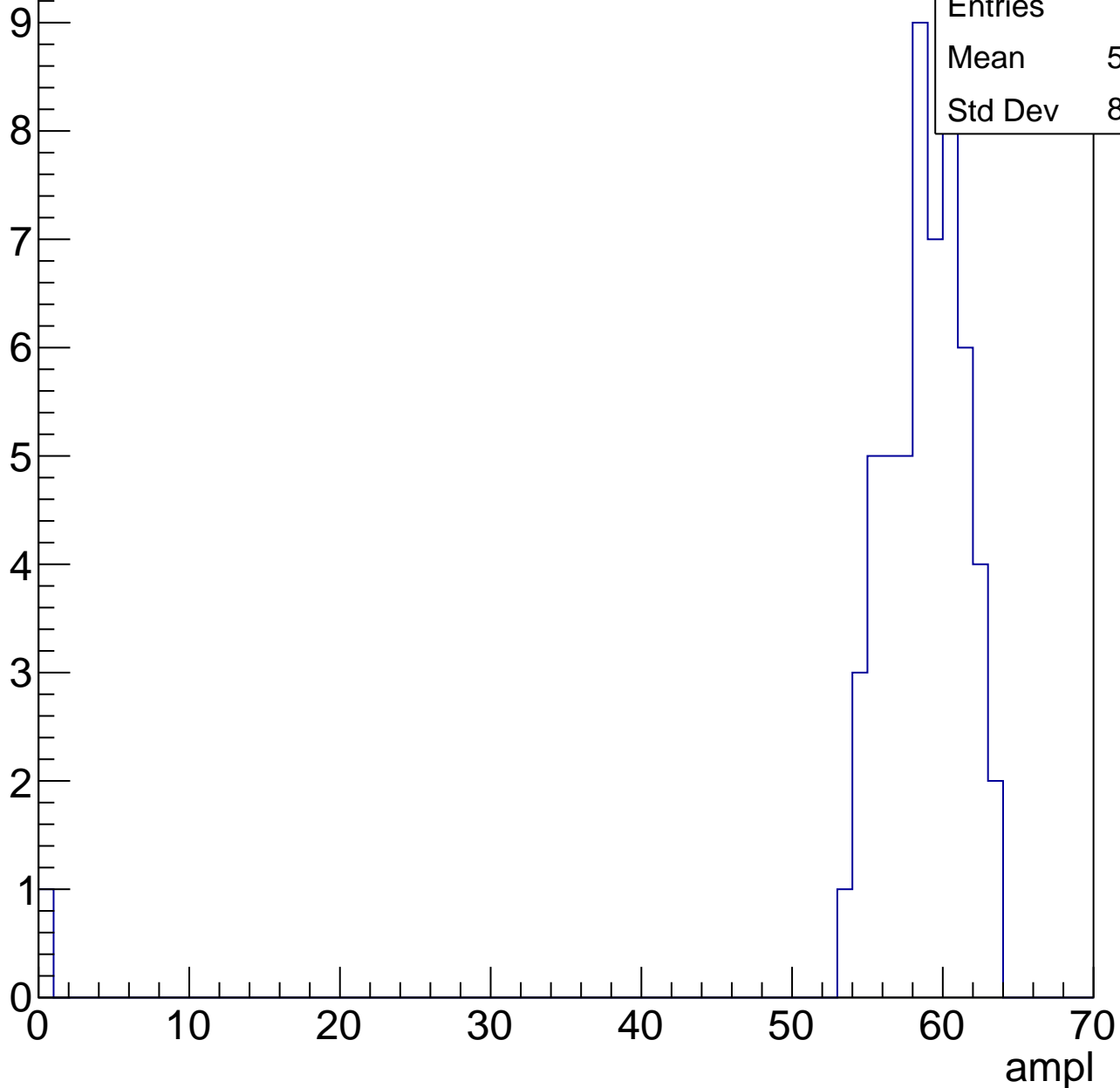
Entries	38
Mean	54.29
Std Dev	3.363



B0L002S, U18-ch64, adc5

calib_packv5_042523_0143.root, FC#8, port C1

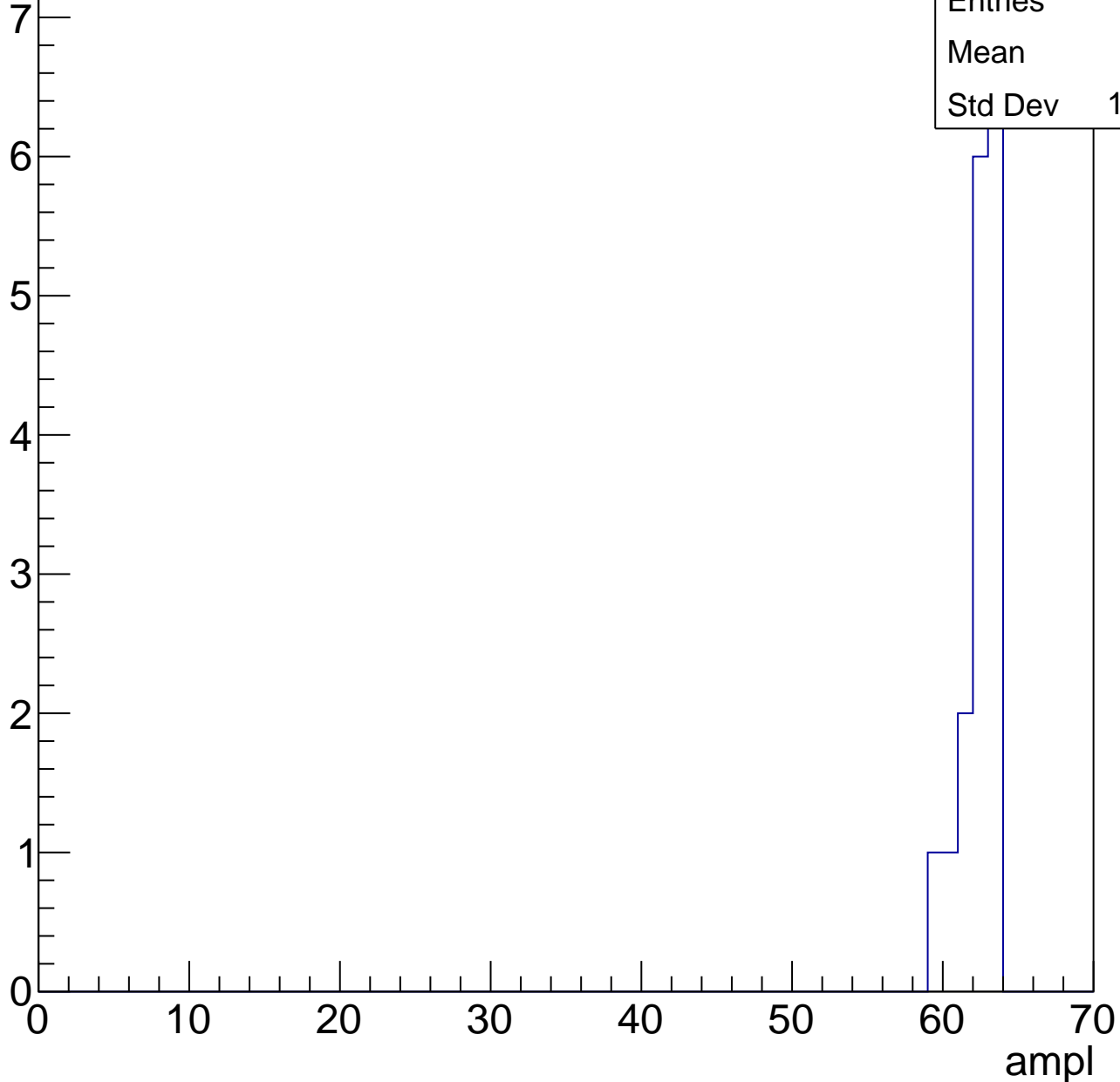
Entry



B0L002S, U18-ch64, adc6

calib_packv5_042523_0143.root, FC#8, port C1

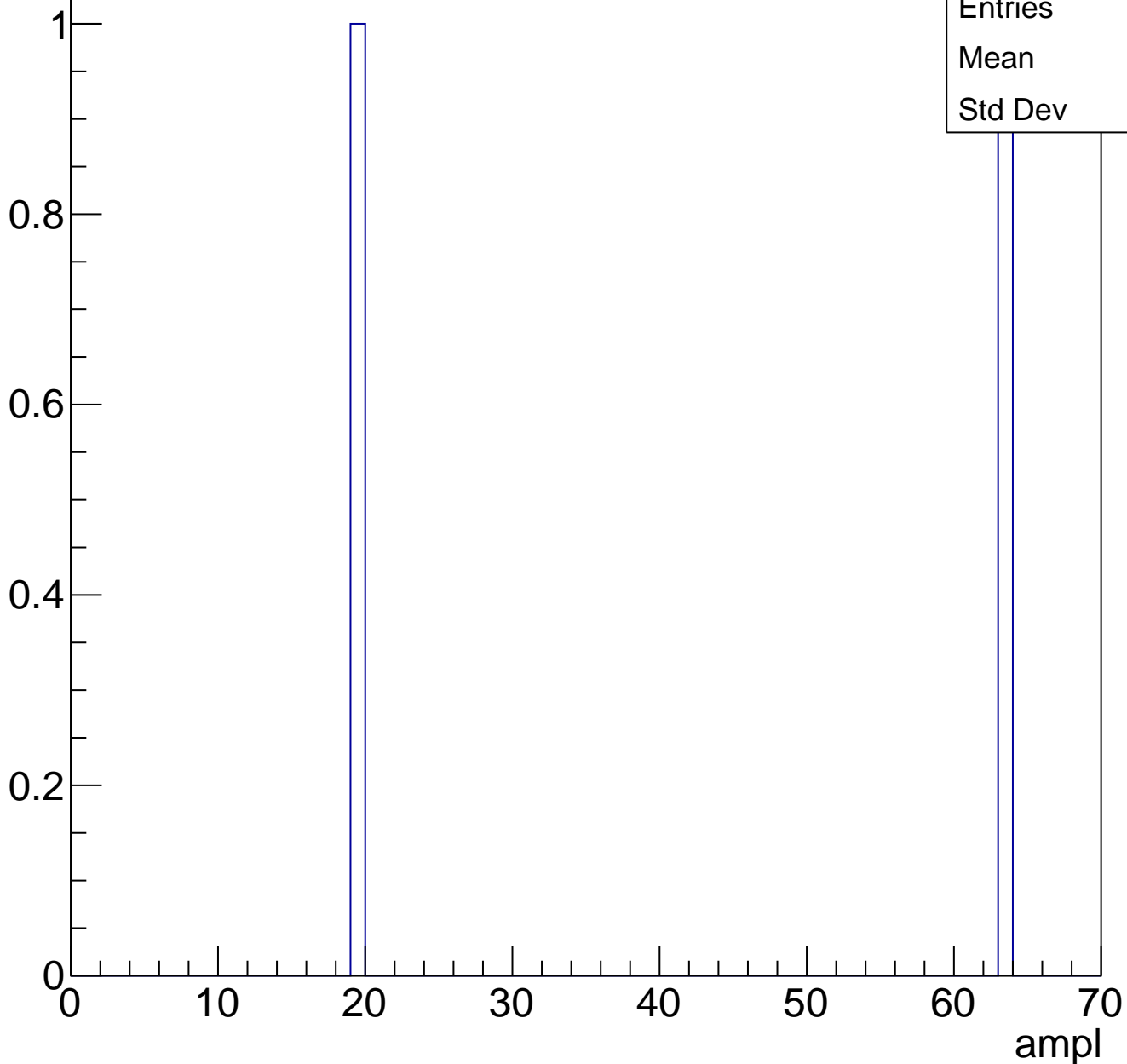
Entry



B0L002S, U18-ch64, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch65, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	31.15
Std Dev	3.497

Gaus mean : 31.5519

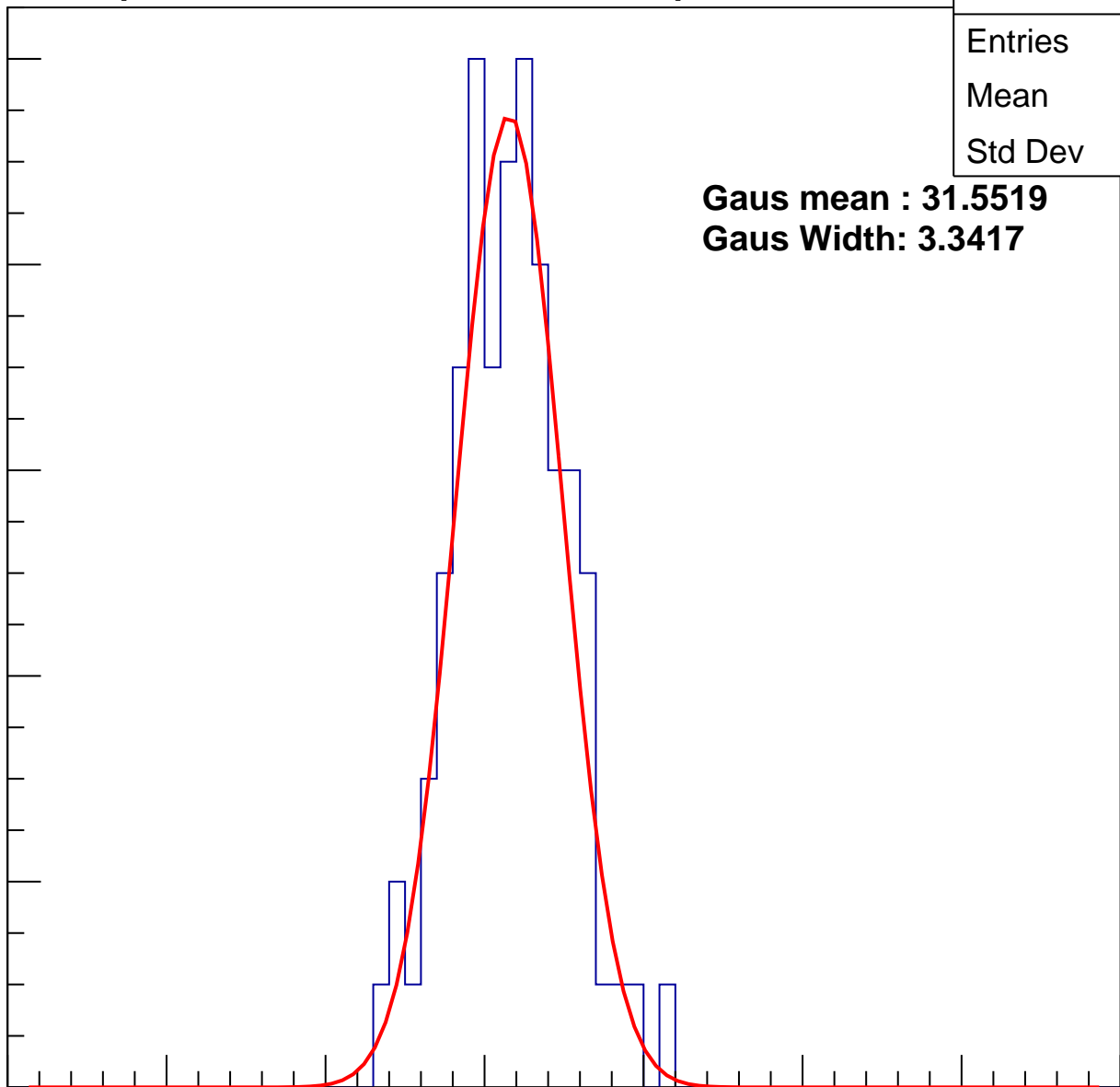
Gaus Width: 3.3417

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch65, adc1

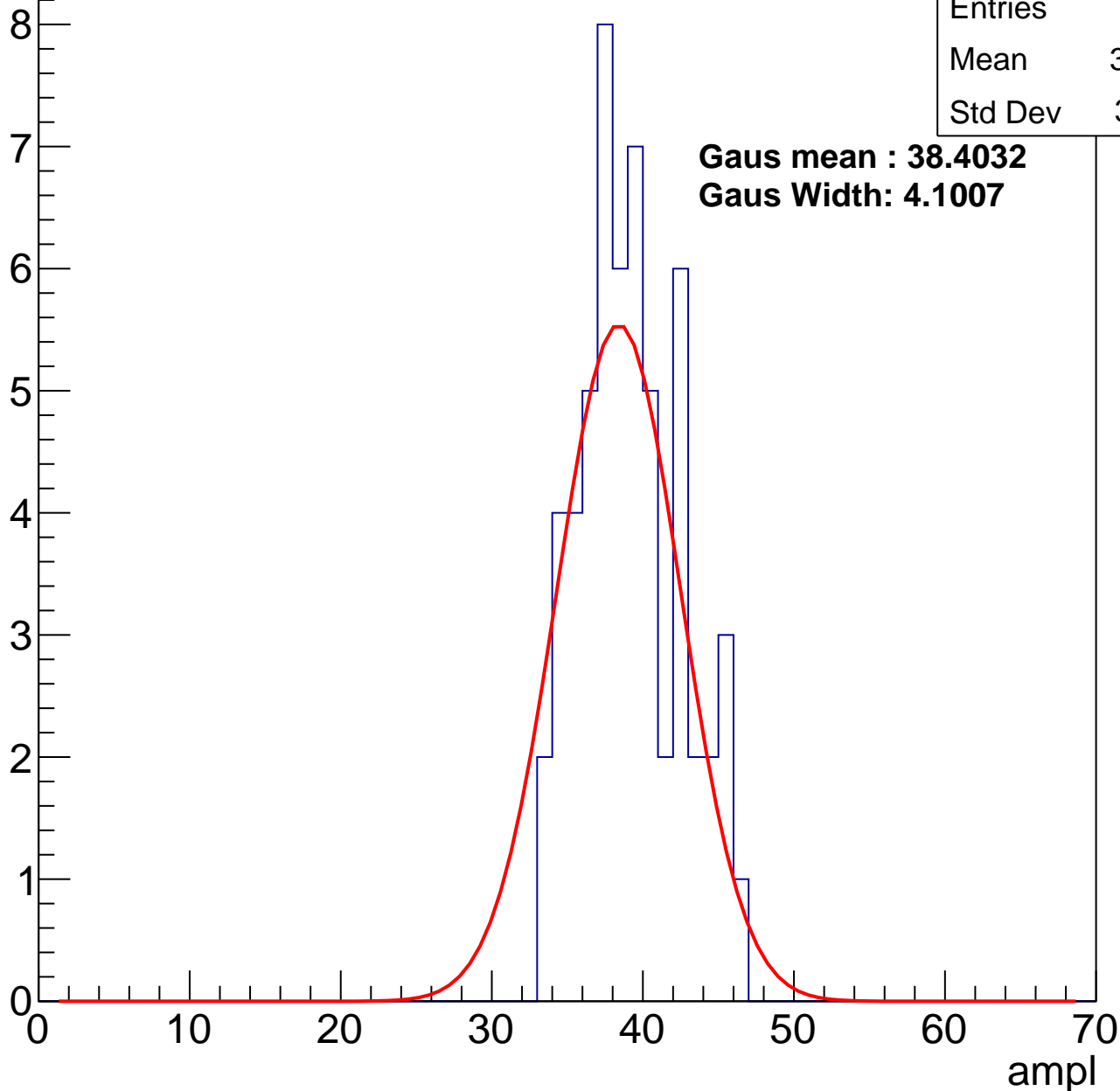
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	38.74
Std Dev	3.301

Gaus mean : 38.4032

Gaus Width: 4.1007



B0L002S, U18-ch65, adc2

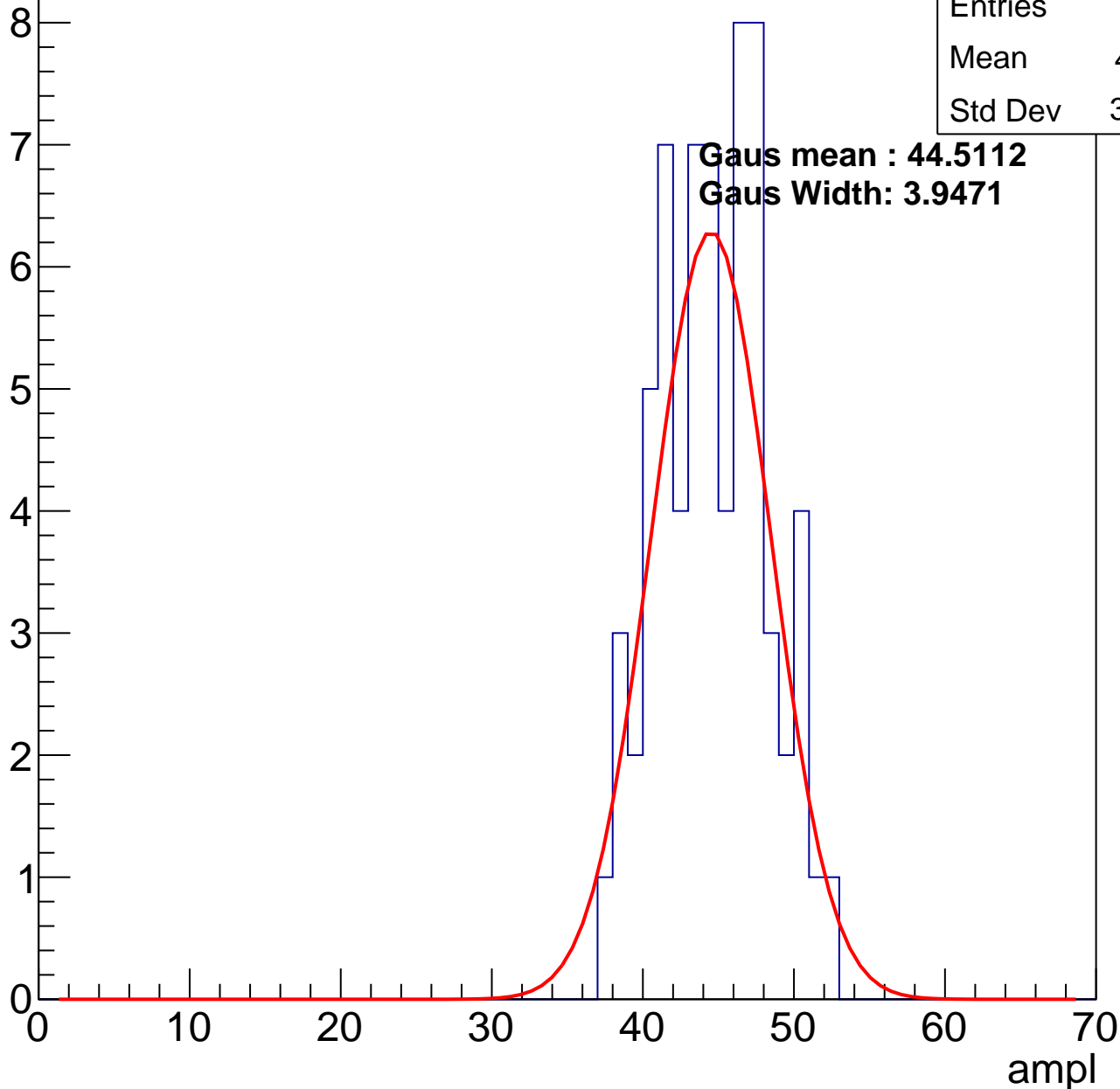
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	44.21
Std Dev	3.526

Gaus mean : 44.5112

Gaus Width: 3.9471

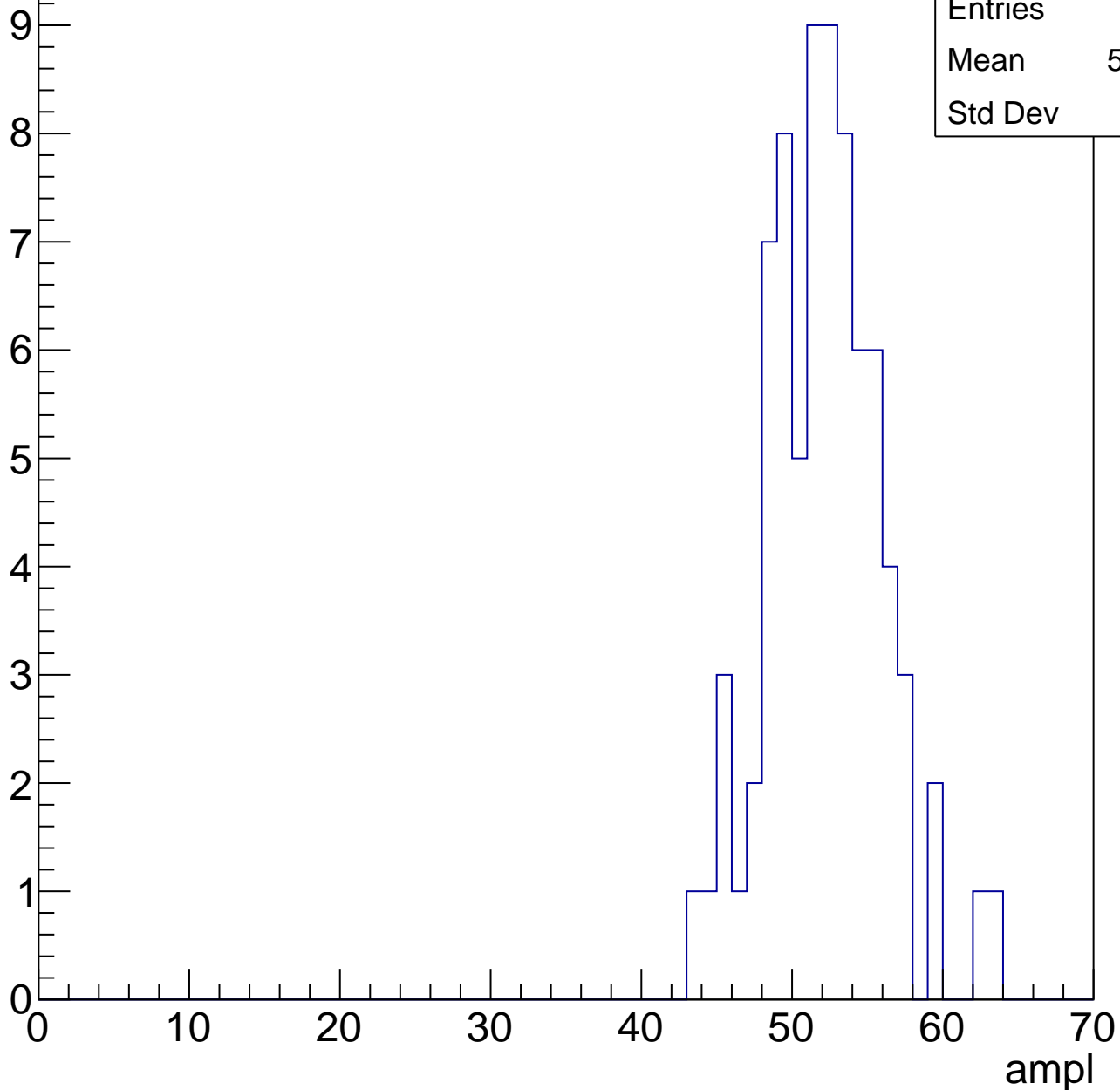


B0L002S, U18-ch65, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	51.73
Std Dev	3.83

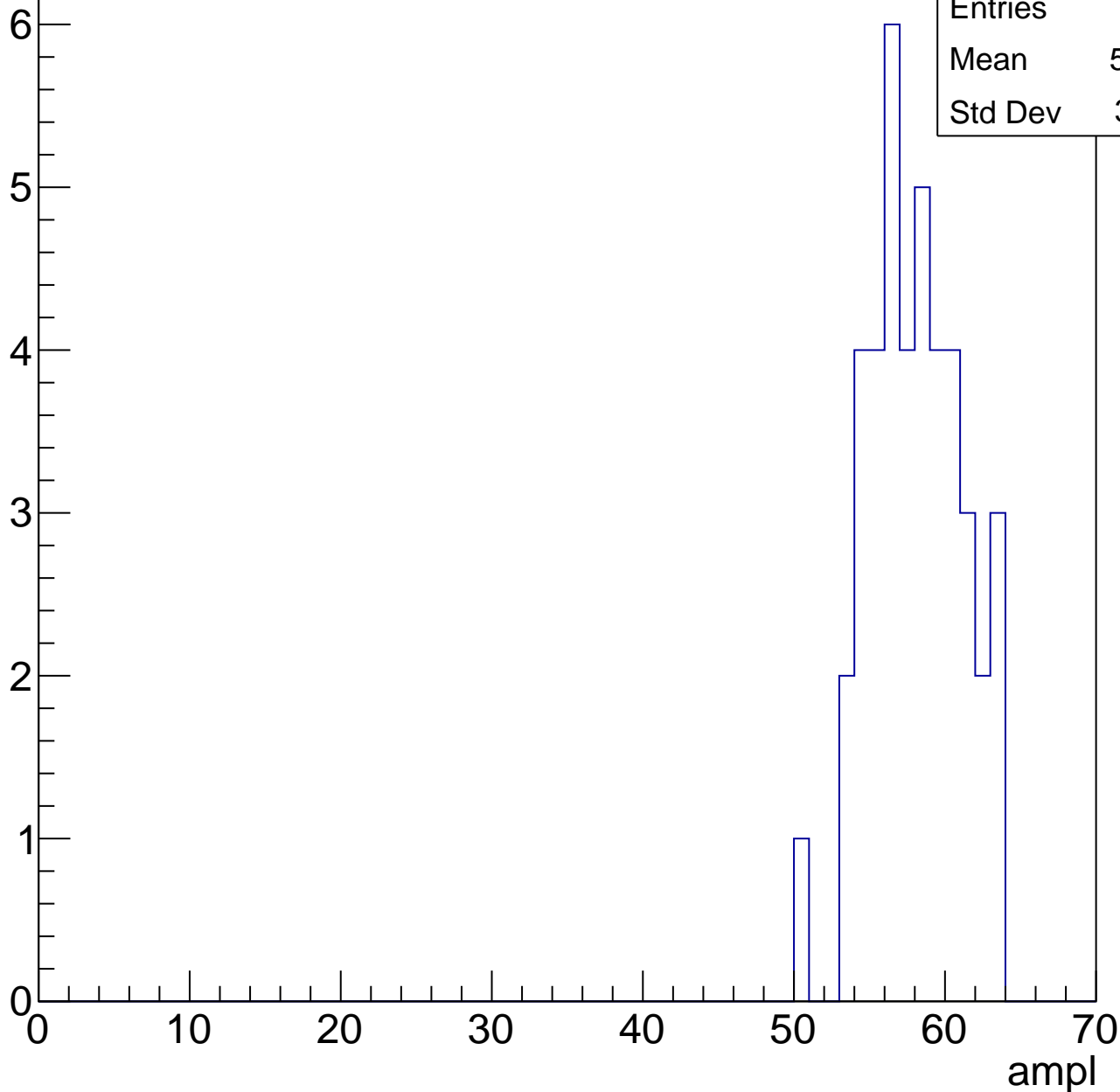


B0L002S, U18-ch65, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	42
Mean	57.57
Std Dev	3.041

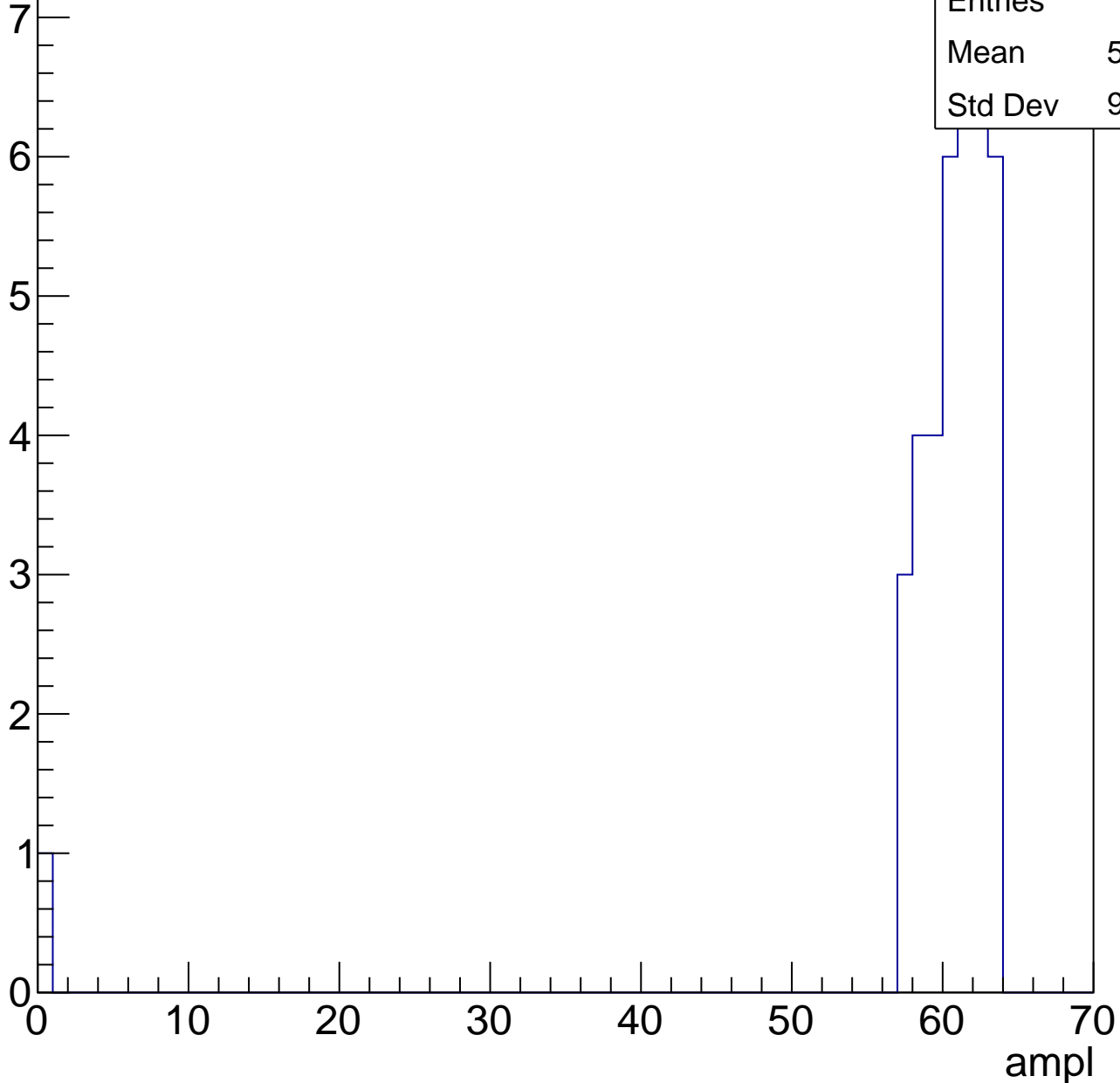


B0L002S, U18-ch65, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

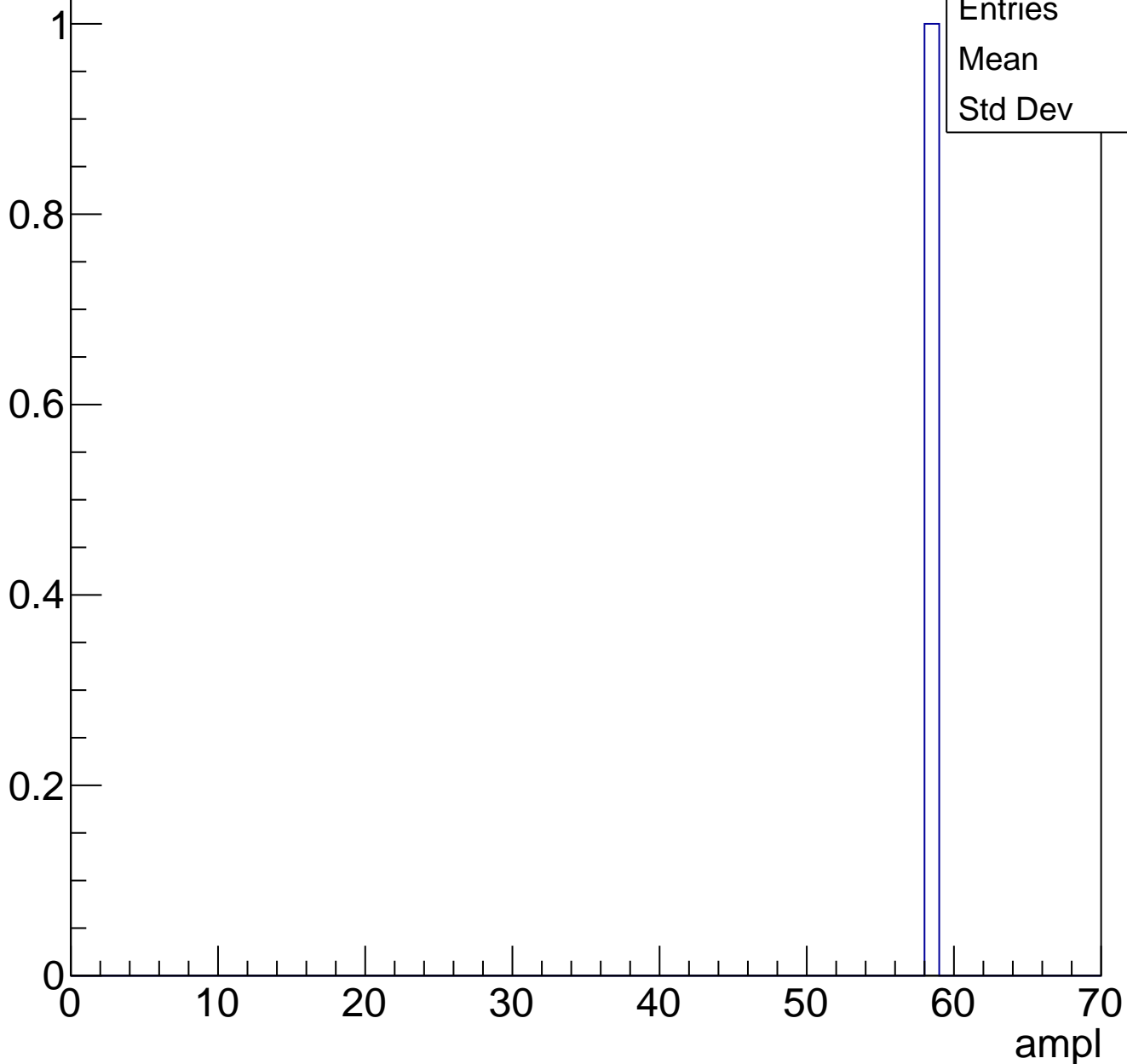
Entries	38
Mean	58.89
Std Dev	9.854



B0L002S, U18-ch65, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch65, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch66, adc0

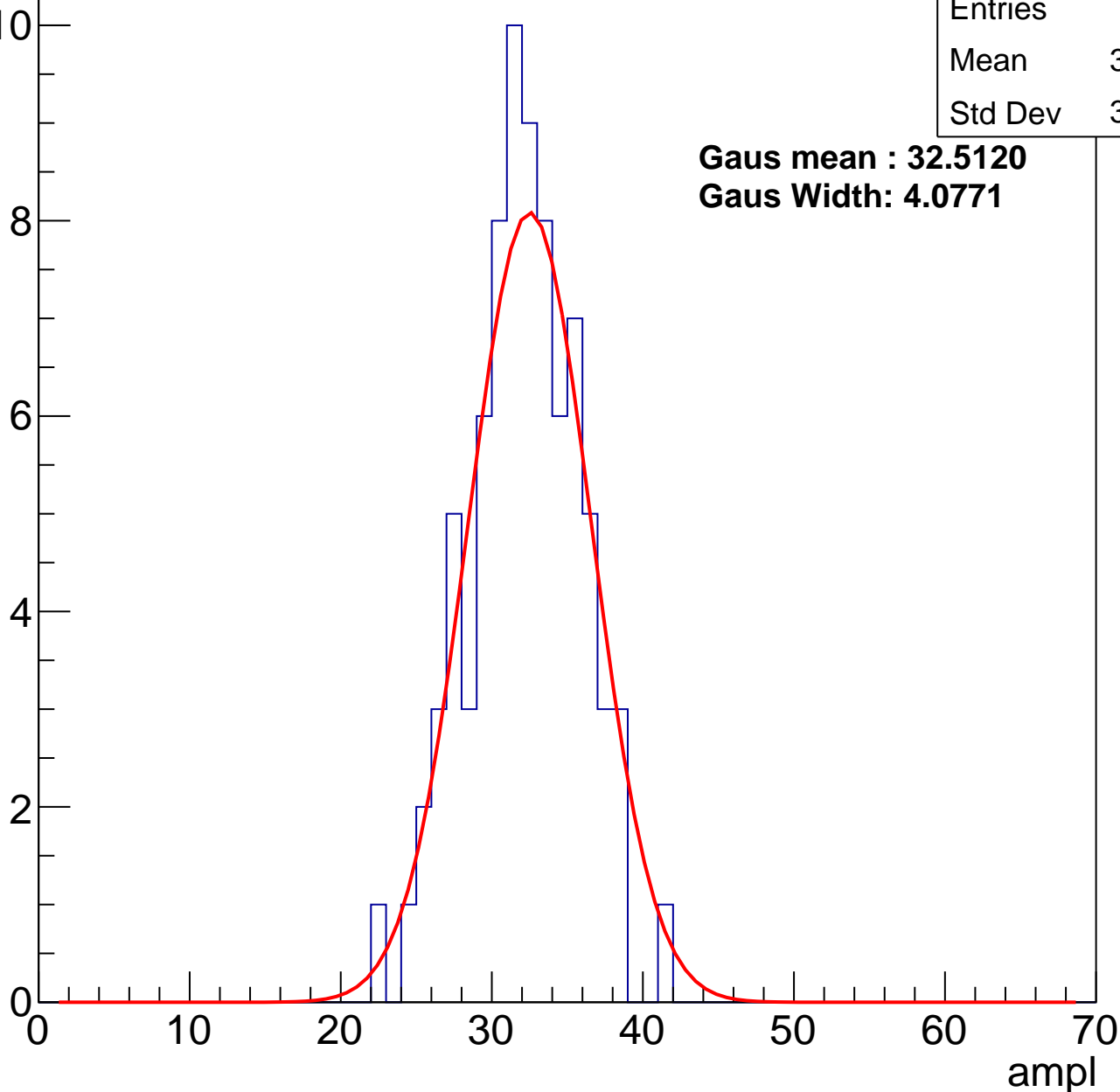
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	81
Mean	31.65
Std Dev	3.632

Gaus mean : 32.5120

Gaus Width: 4.0771



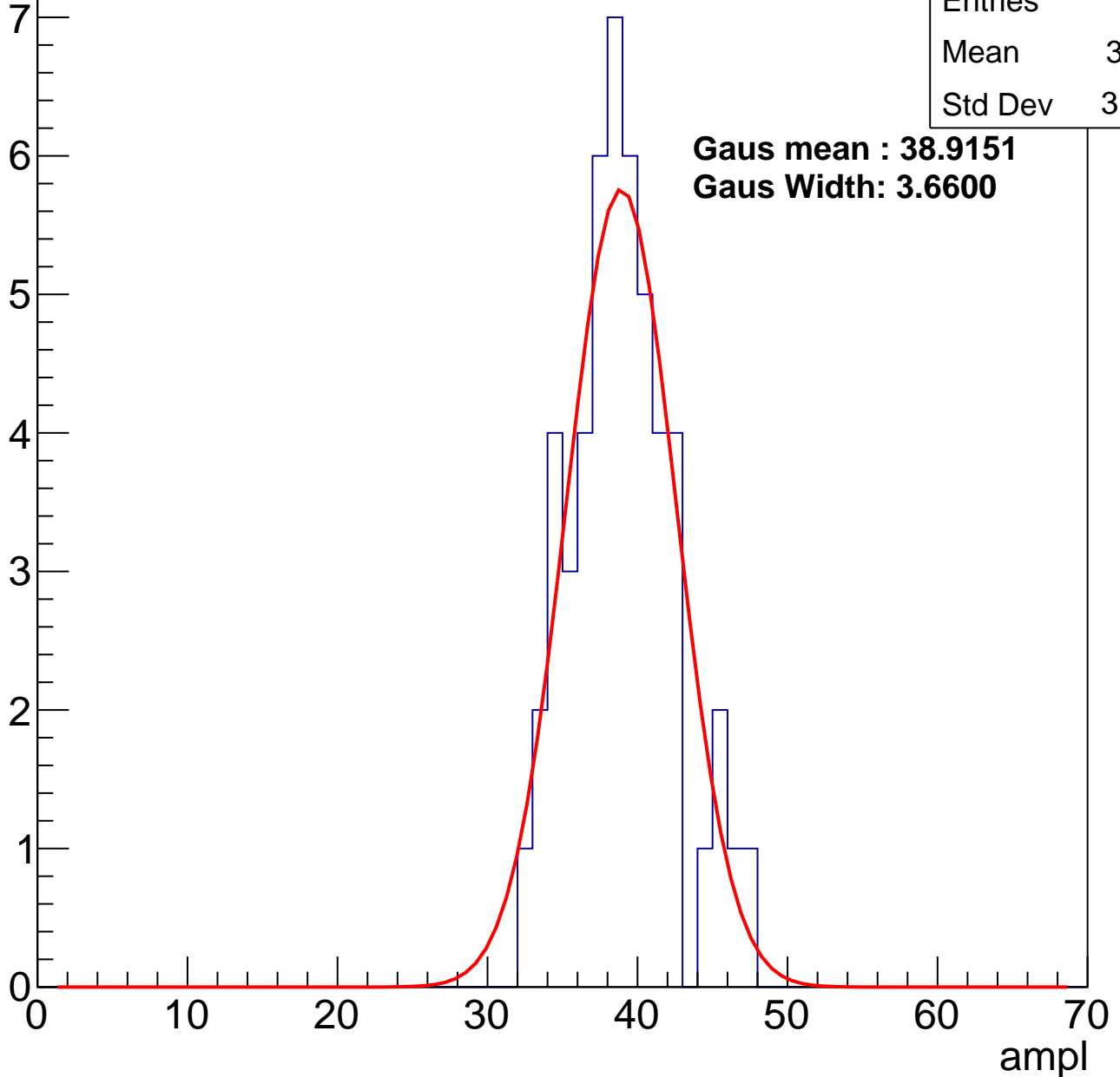
B0L002S, U18-ch66, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	38.51
Std Dev	3.403

Gaus mean : 38.9151
Gaus Width: 3.6600



B0L002S, U18-ch66, adc2

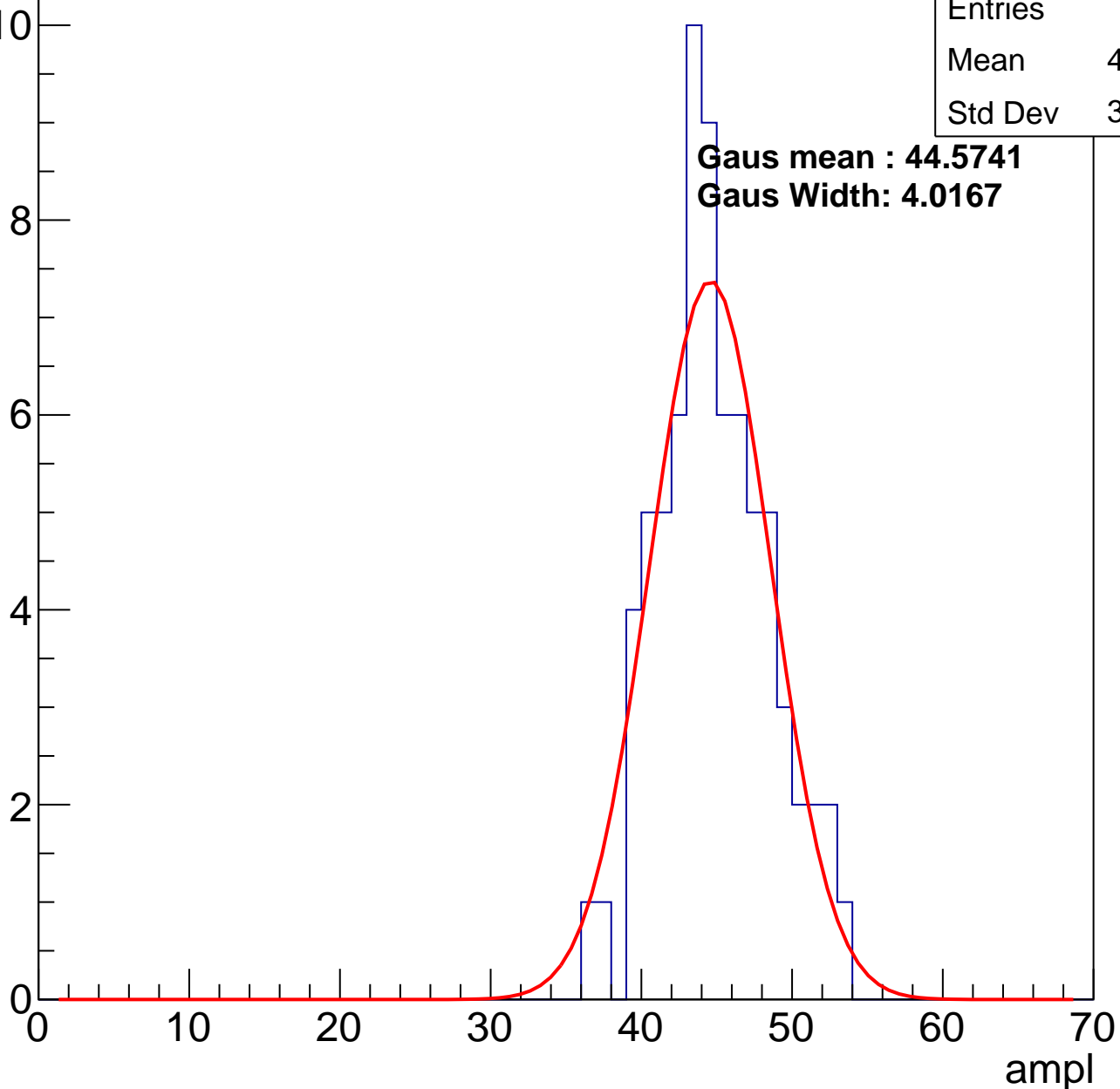
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.37
Std Dev	3.647

Gaus mean : 44.5741

Gaus Width: 4.0167

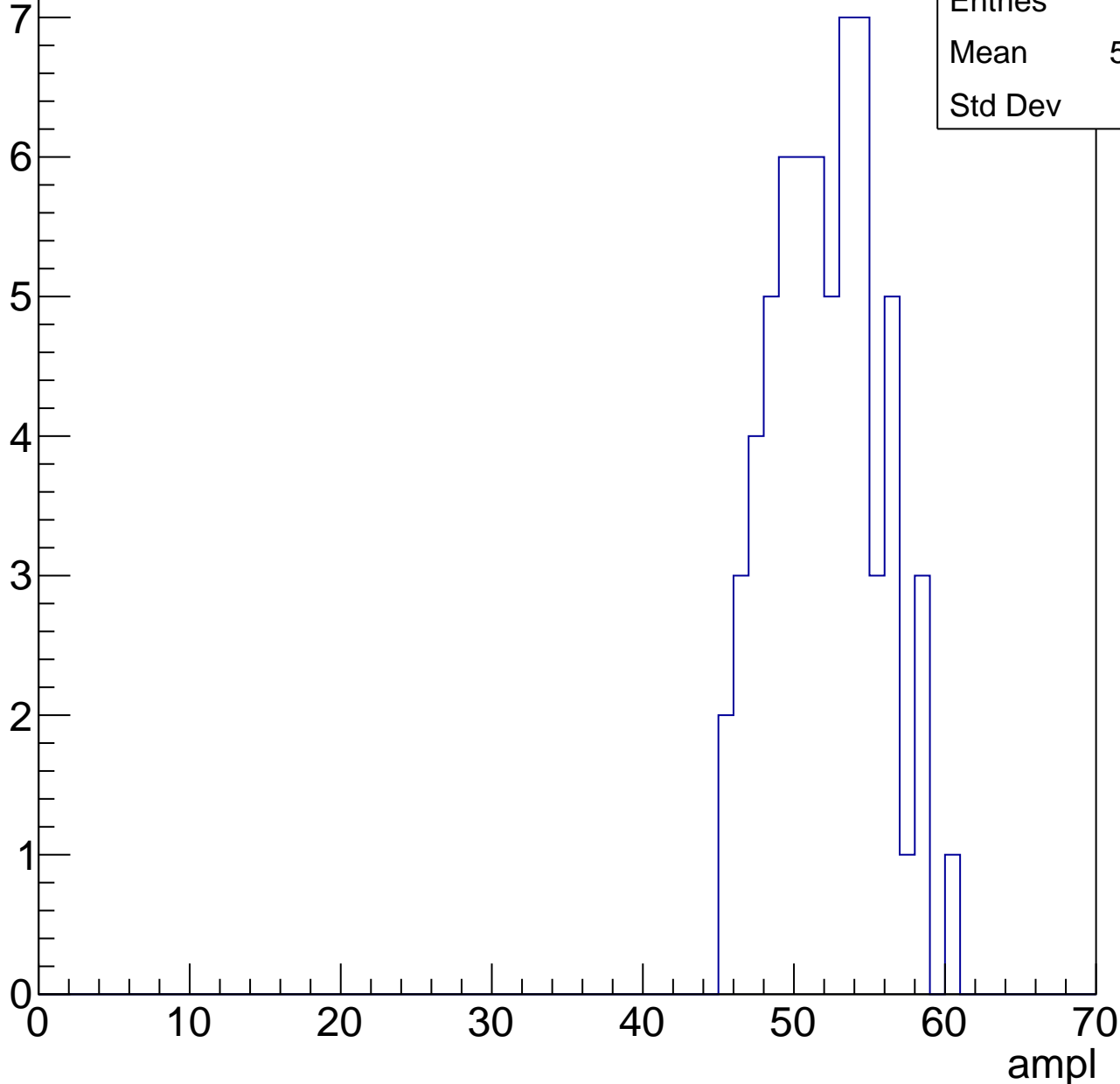


B0L002S, U18-ch66, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	51.58
Std Dev	3.53

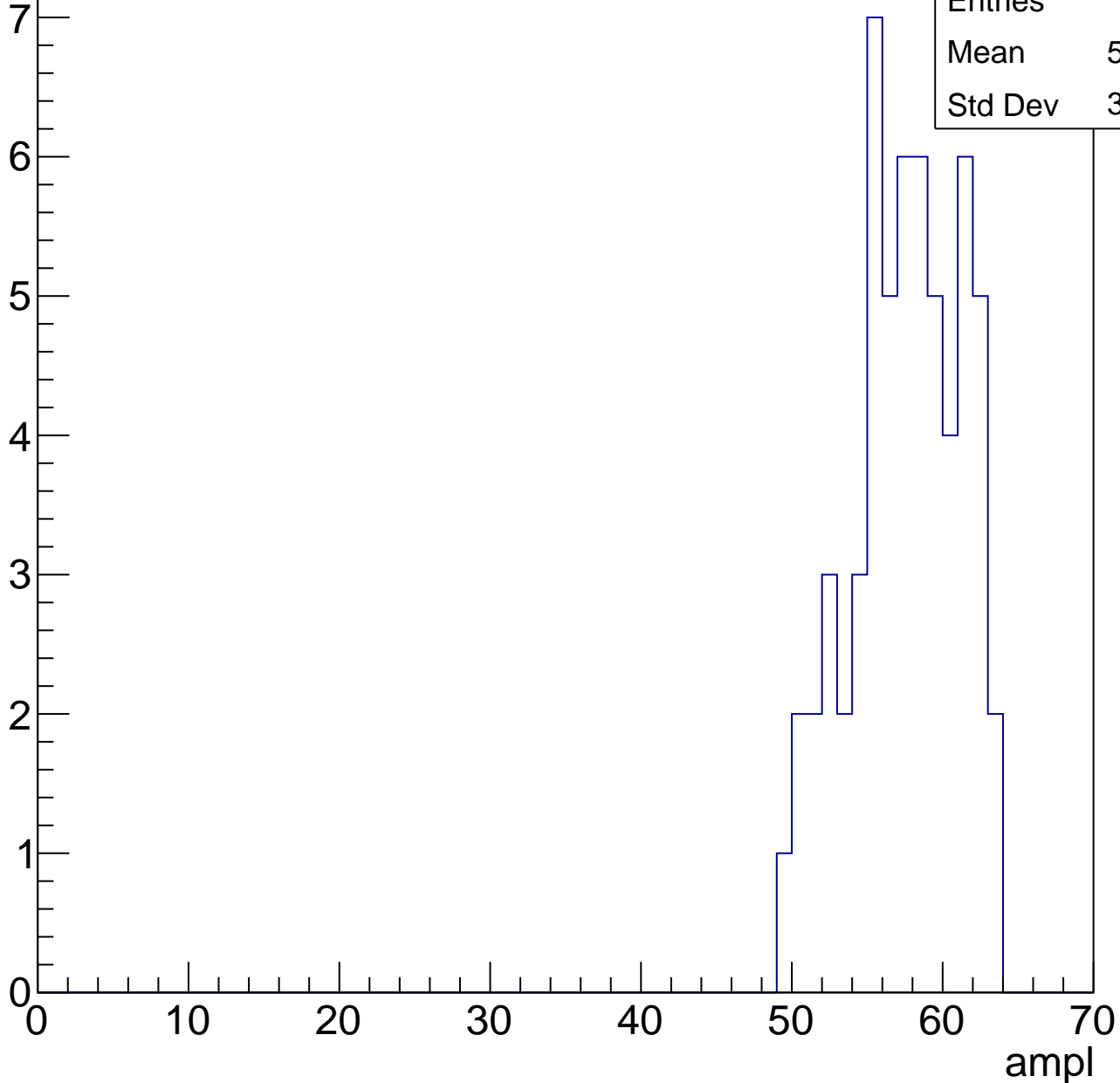


B0L002S, U18-ch66, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	57.07
Std Dev	3.579



B0L002S, U18-ch66, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

5

4

3

2

1

0

Entries

27

Mean

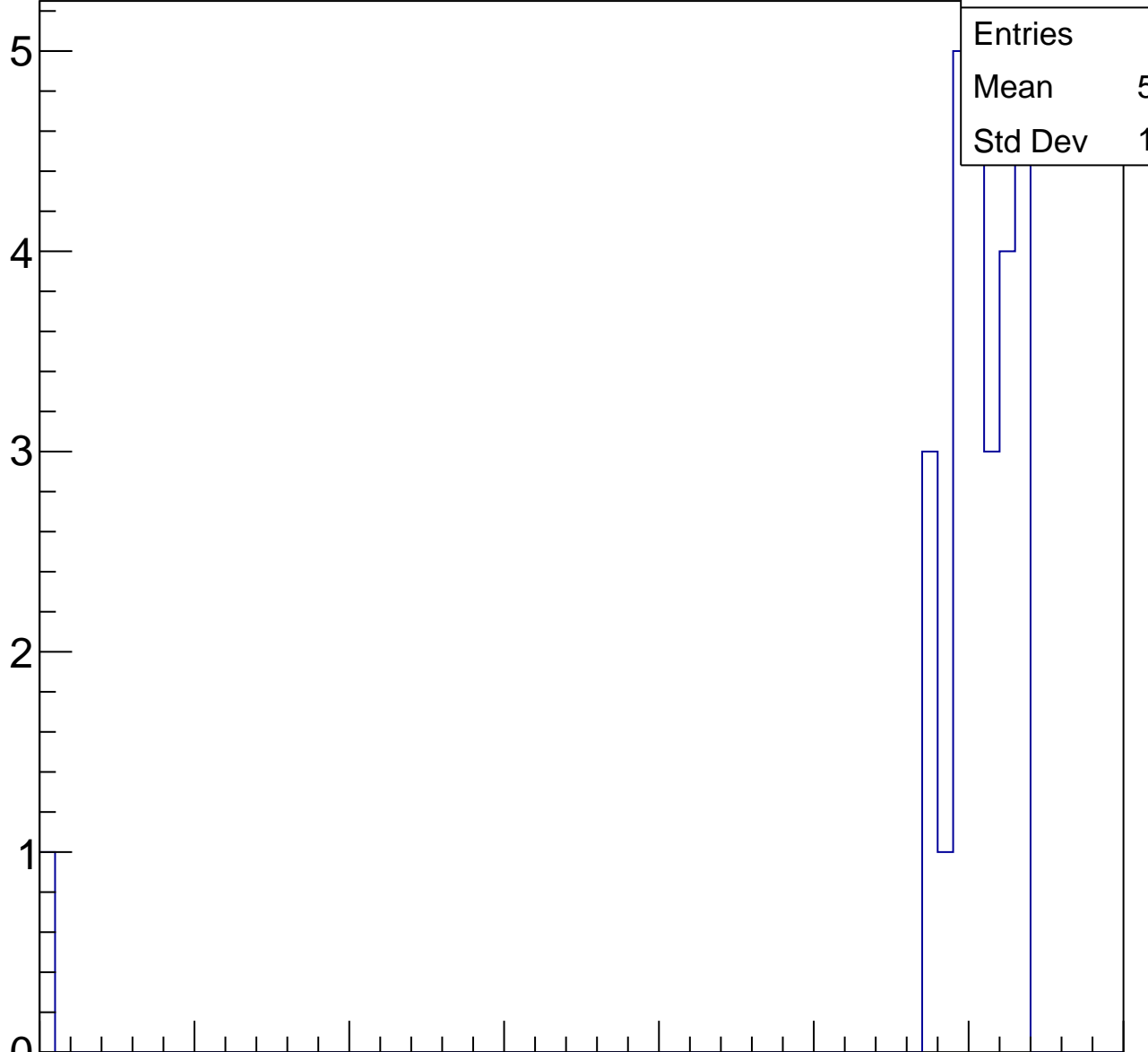
58.15

Std Dev

11.56

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch66, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch66, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U18-ch67, adc0

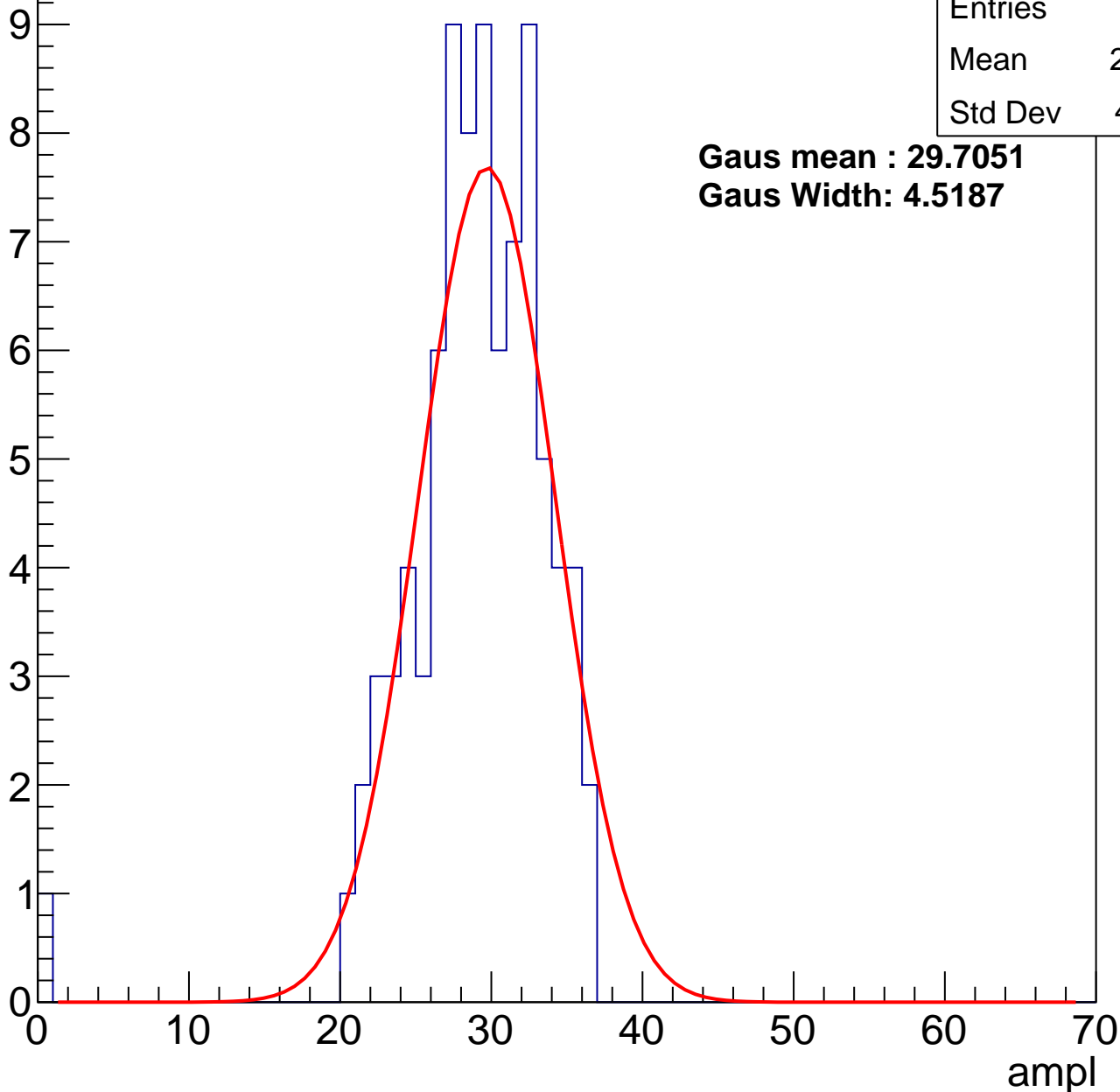
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	86
Mean	28.49
Std Dev	4.901

Gaus mean : 29.7051

Gaus Width: 4.5187



B0L002S, U18-ch67, adc1

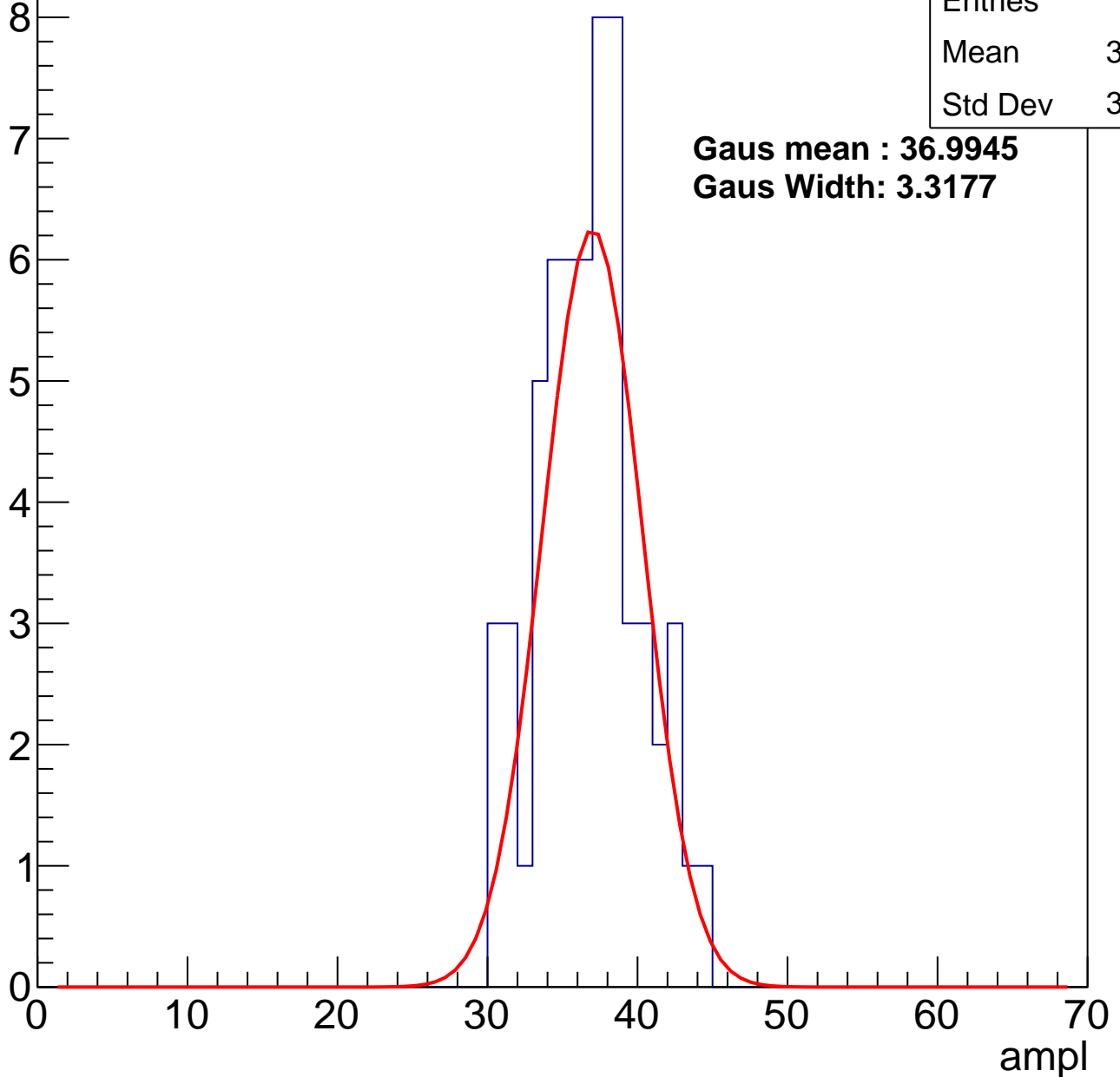
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.31
Std Dev	3.331

Gaus mean : 36.9945

Gaus Width: 3.3177



B0L002S, U18-ch67, adc2

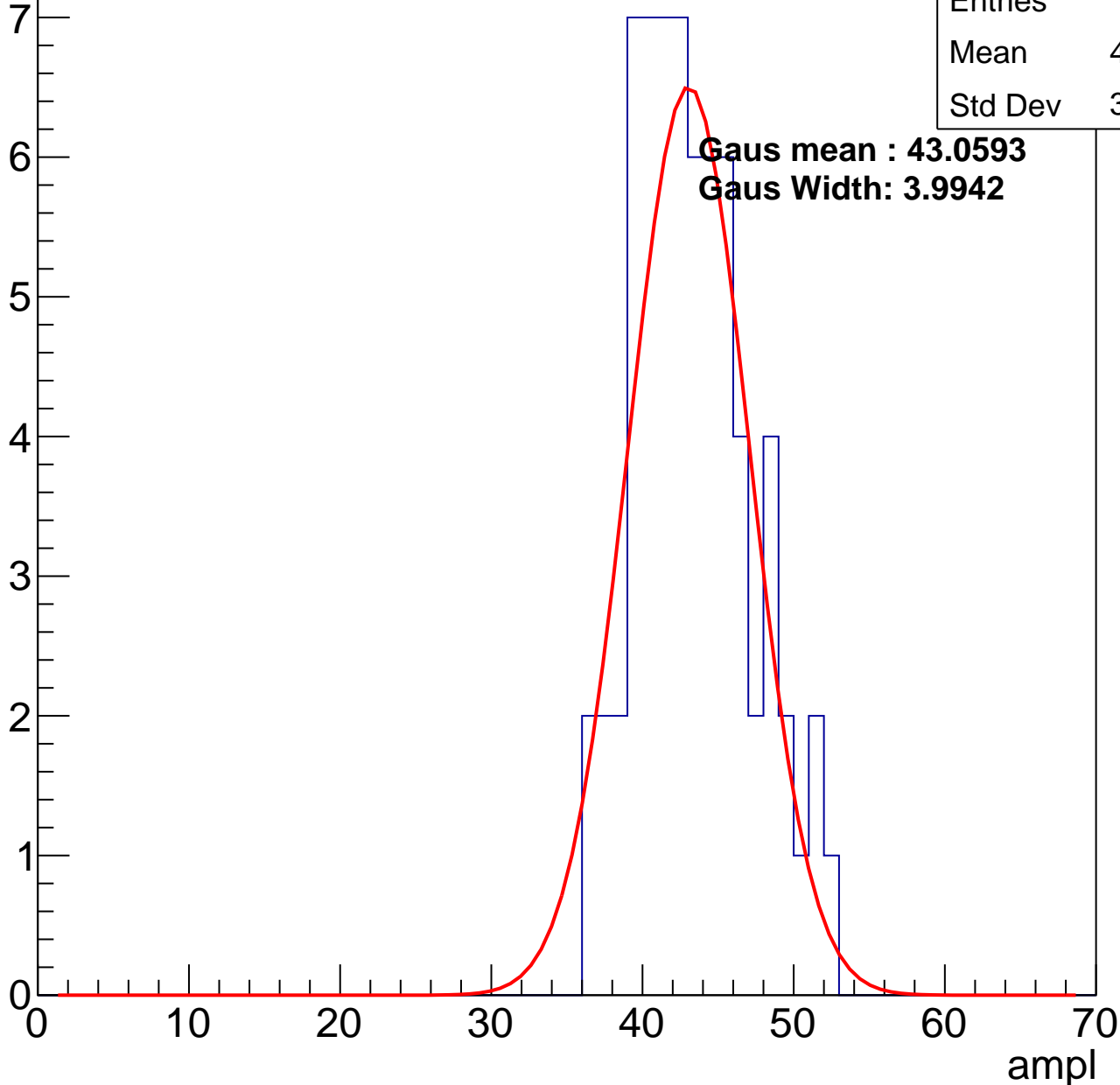
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	42.94
Std Dev	3.753

Gaus mean : 43.0593

Gaus Width: 3.9942

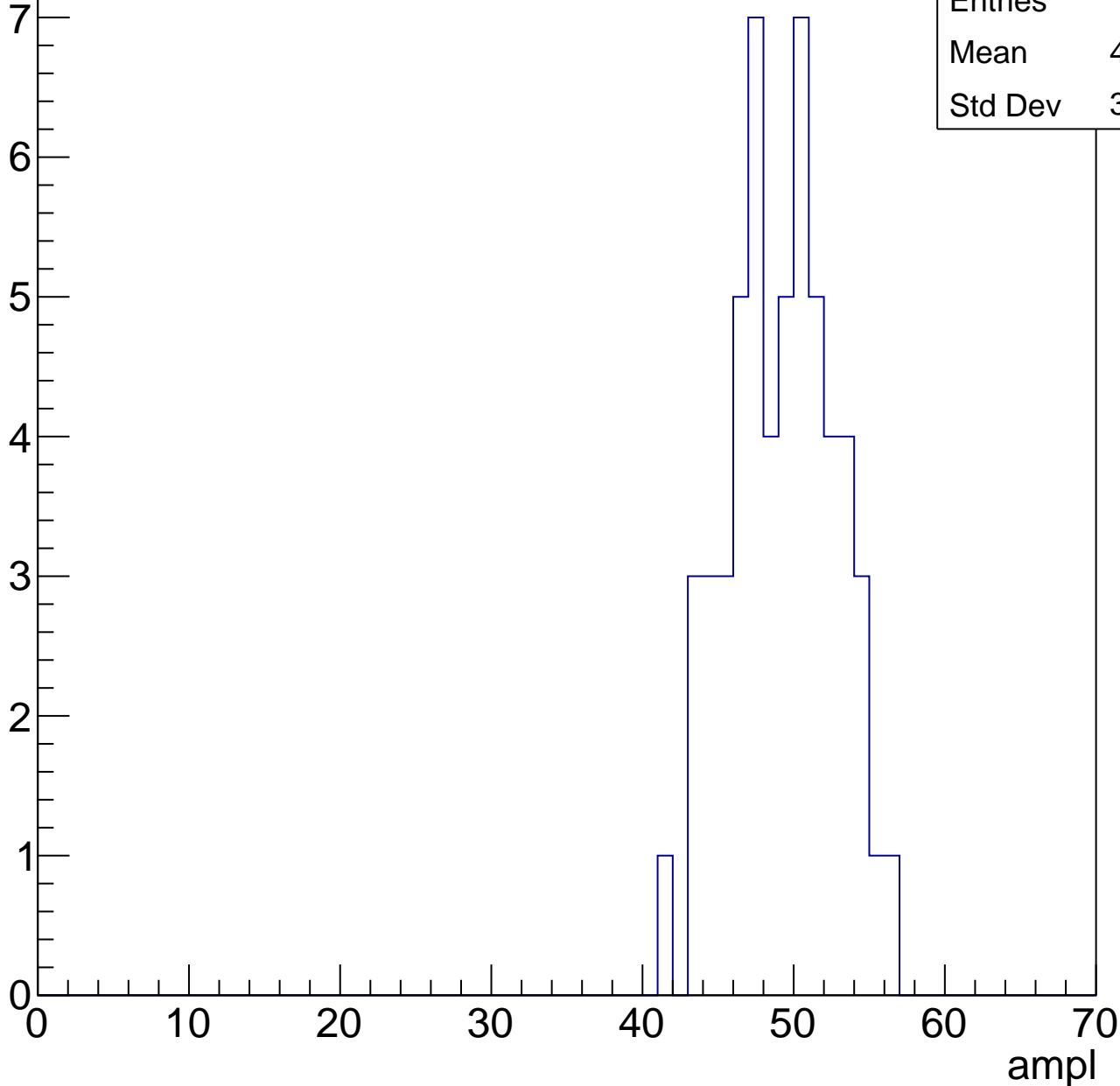


B0L002S, U18-ch67, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	48.77
Std Dev	3.423

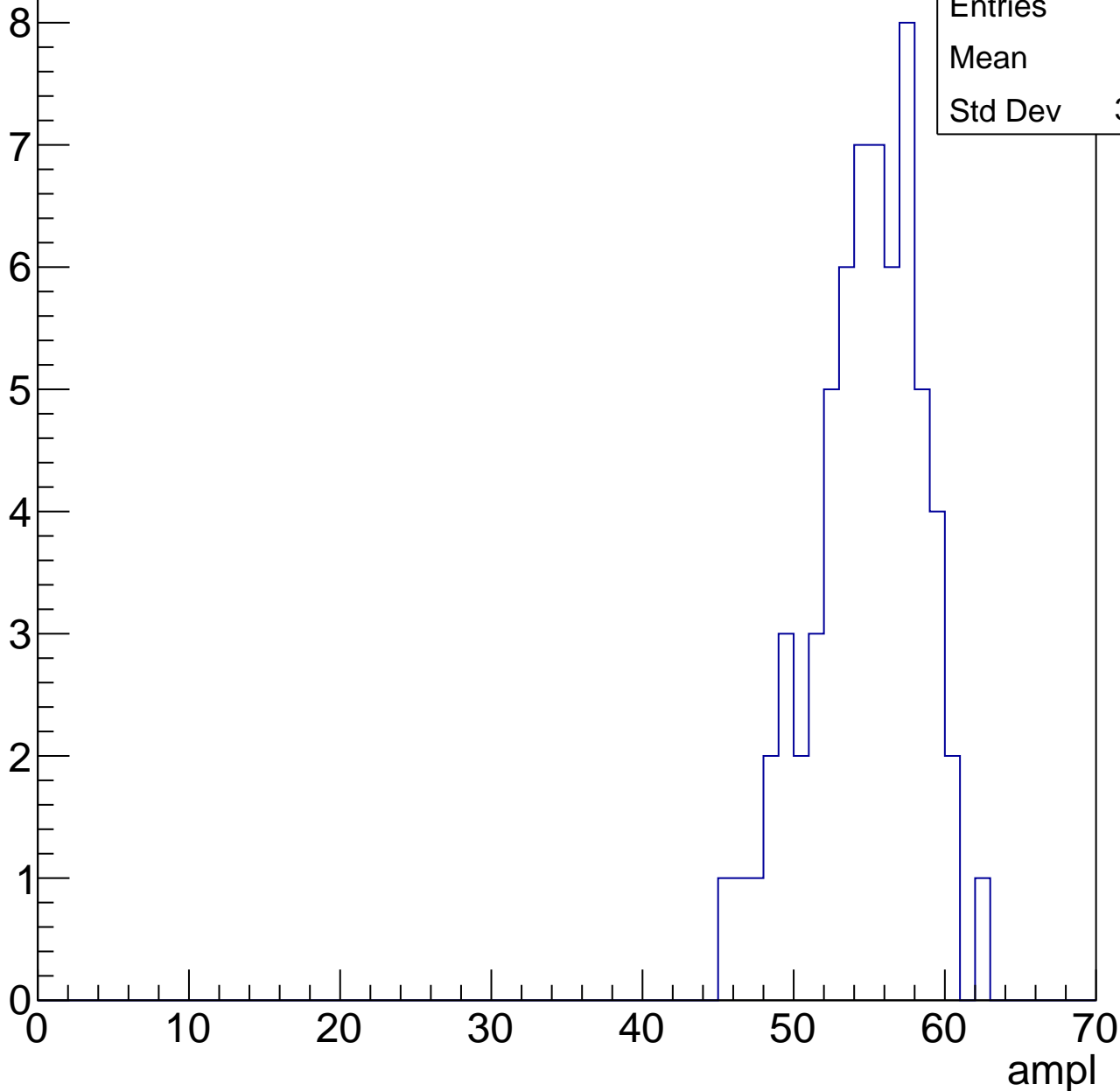


B0L002S, U18-ch67, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	54.3
Std Dev	3.621



B0L002S, U18-ch67, adc5

calib_packv5_042523_0143.root, FC#8, port C1

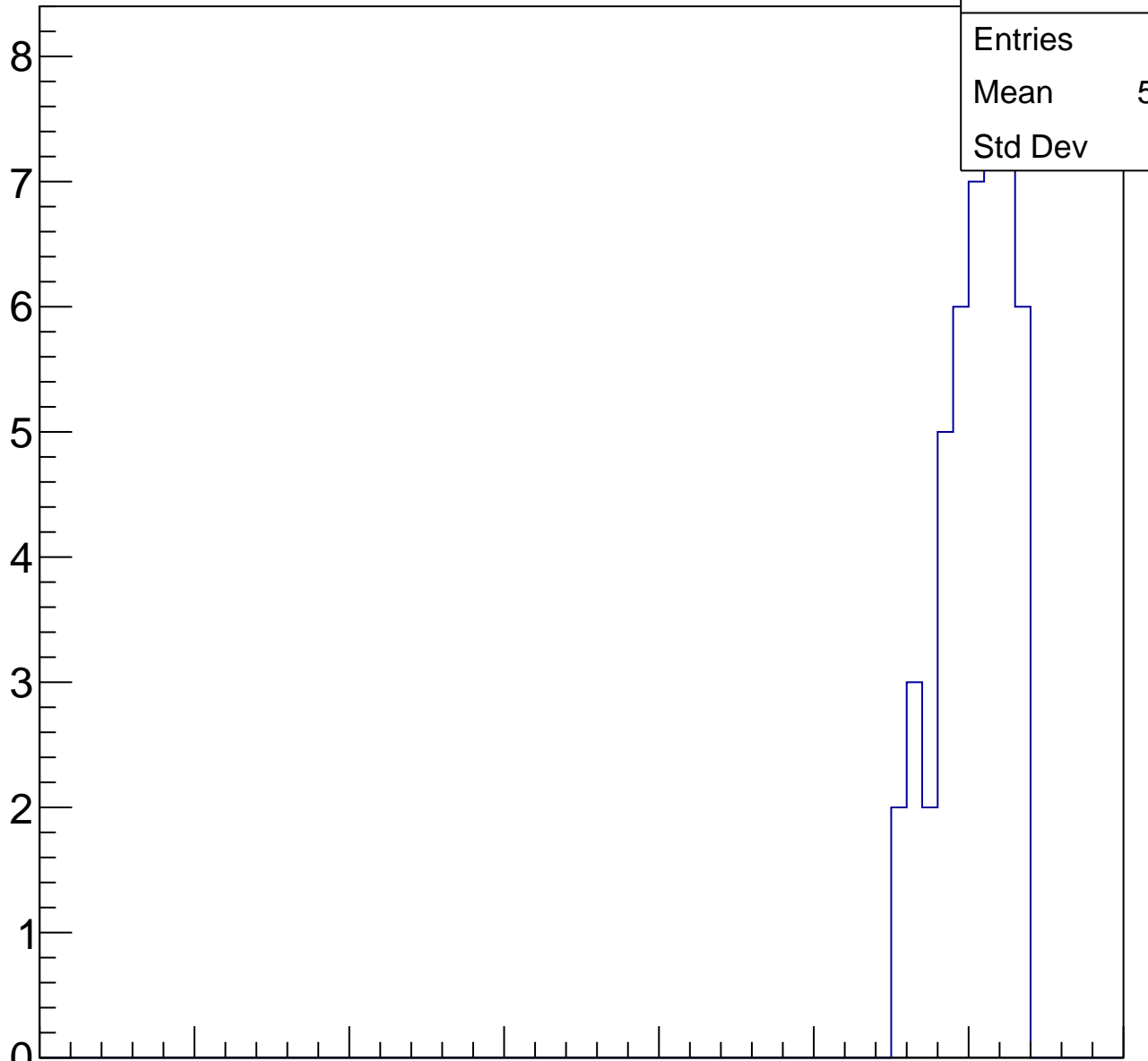
Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.96
Std Dev	2.24

ampl

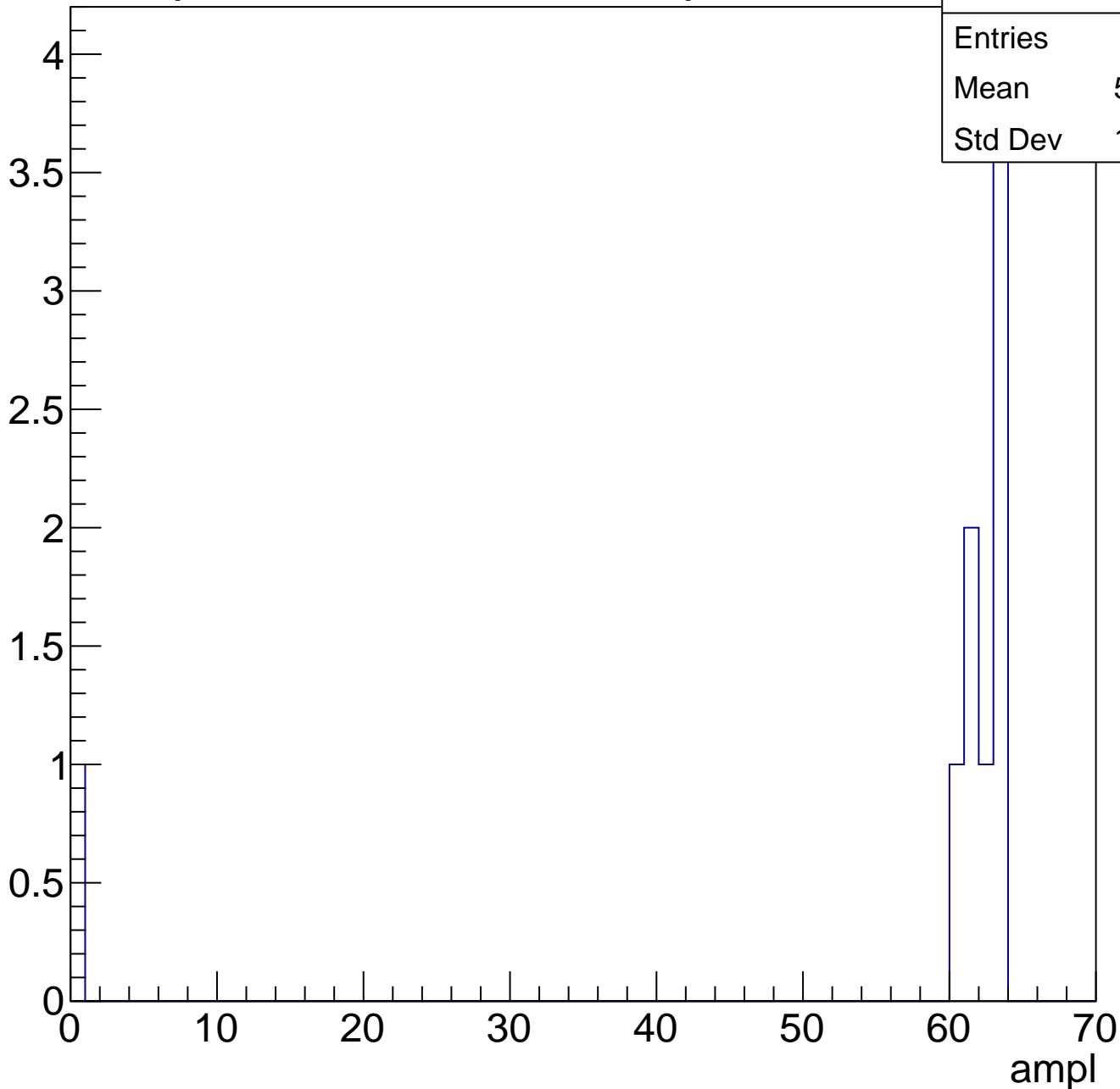
0 10 20 30 40 50 60 70



B0L002S, U18-ch67, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch67, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	6
Std Dev	8.485

B0L002S, U18-ch68, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	84
Mean	27.95
Std Dev	5.619

Gaus mean : 29.0821

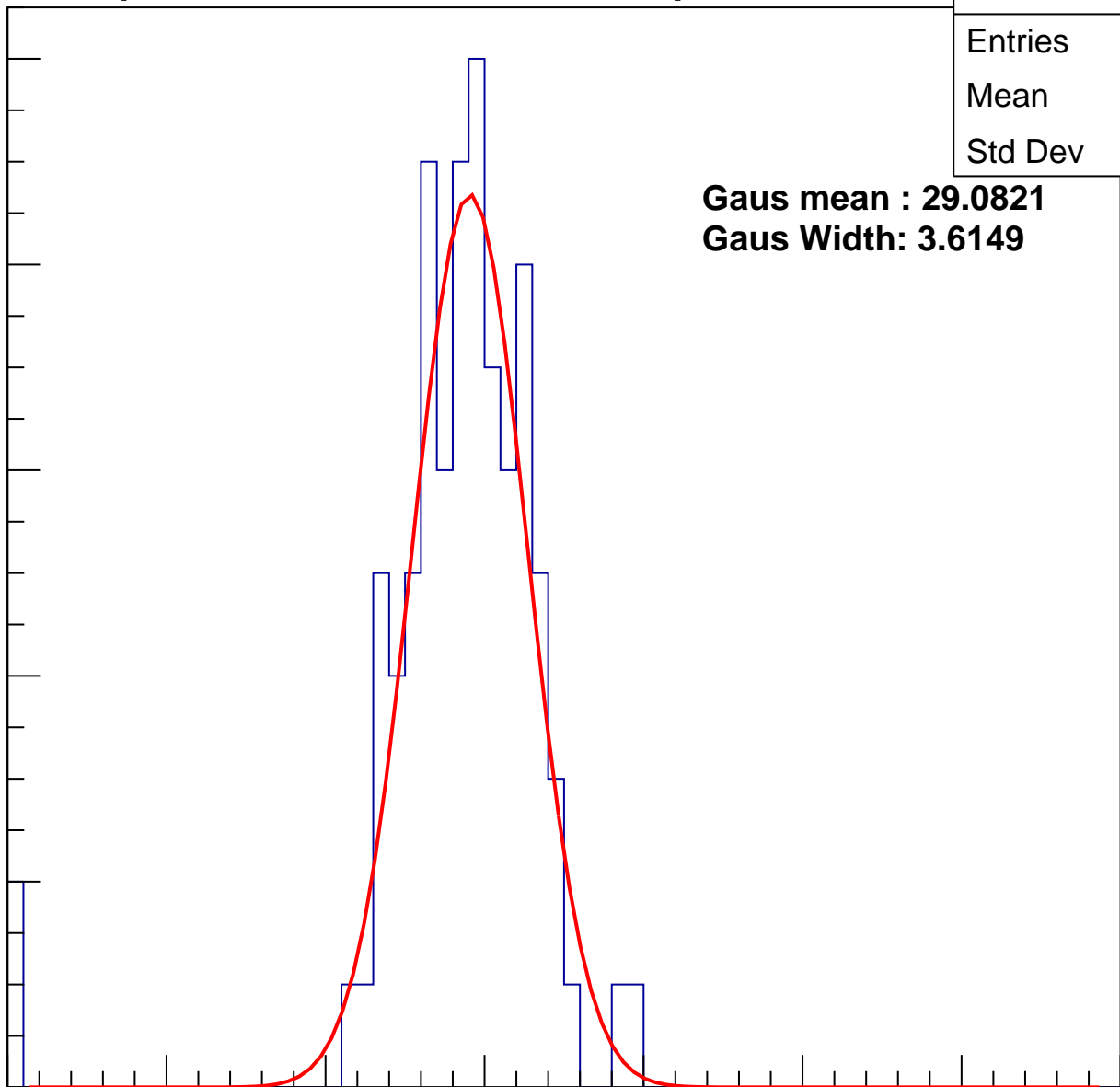
Gaus Width: 3.6149

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



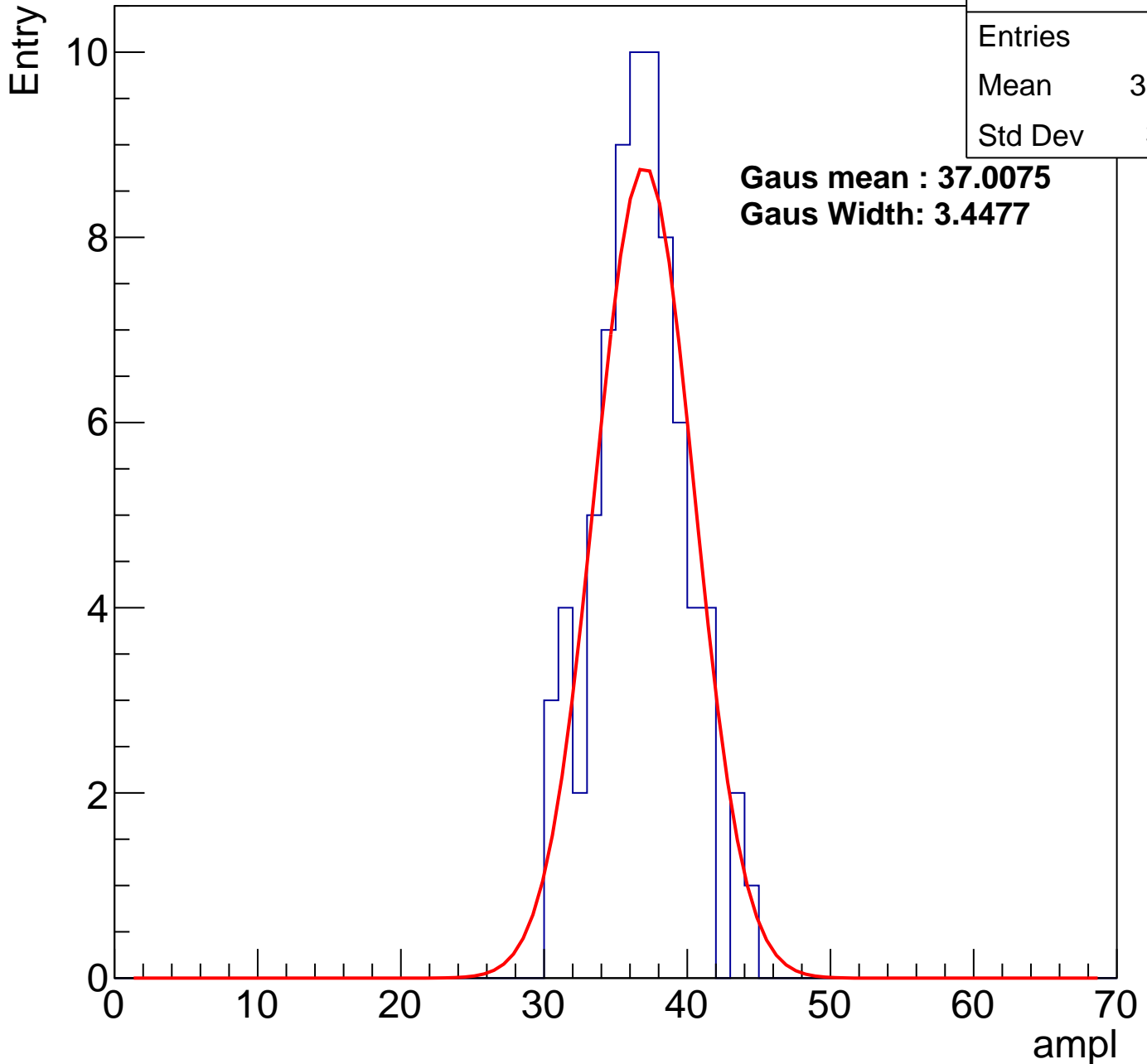
B0L002S, U18-ch68, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	75
Mean	36.24
Std Dev	3.14

Gaus mean : 37.0075

Gaus Width: 3.4477



B0L002S, U18-ch68, adc2

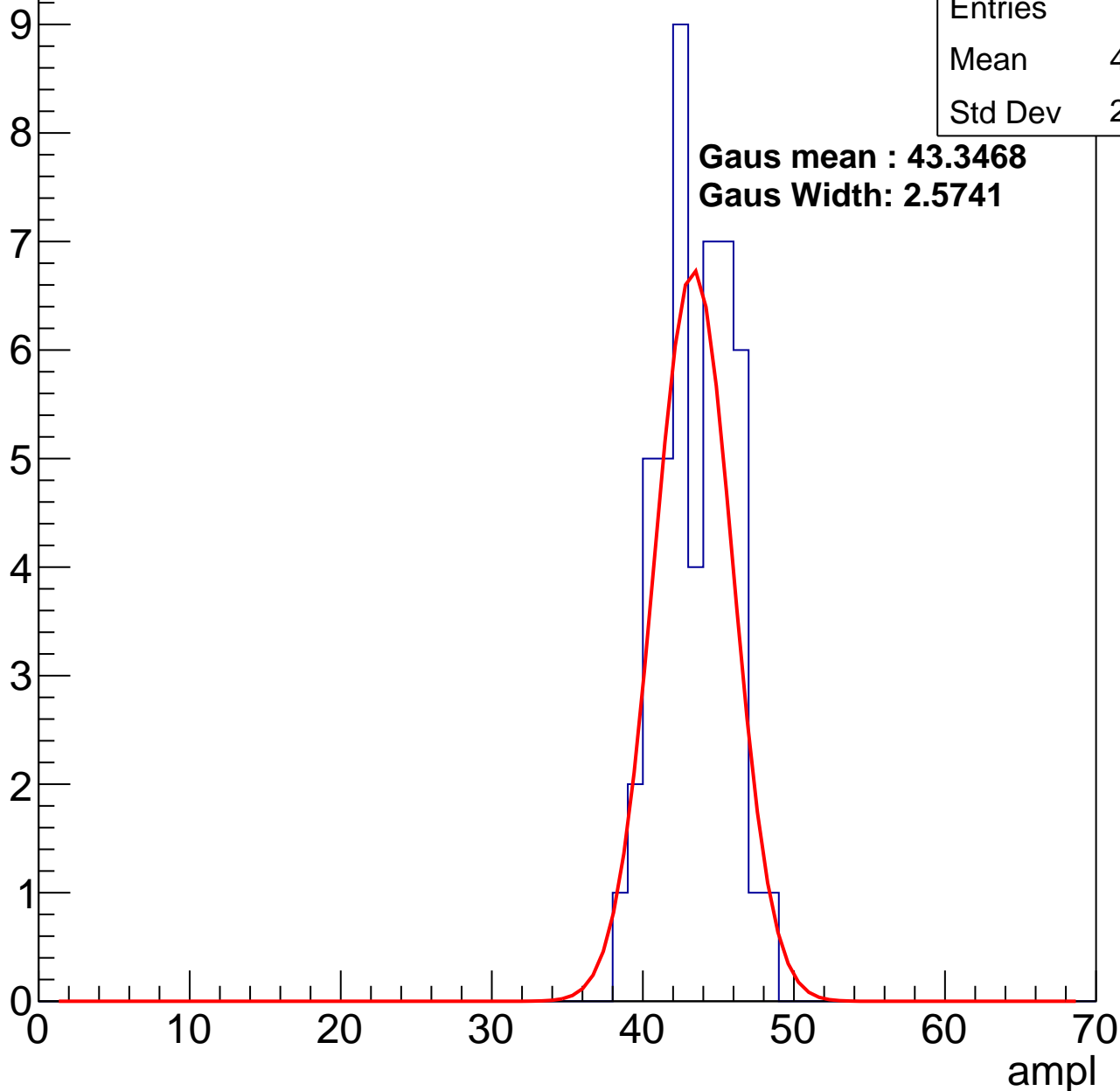
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.02
Std Dev	2.332

Gaus mean : 43.3468

Gaus Width: 2.5741



B0L002S, U18-ch68, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	49.28
Std Dev	3.963

Entry

10

8

6

4

2

0

0

10

20

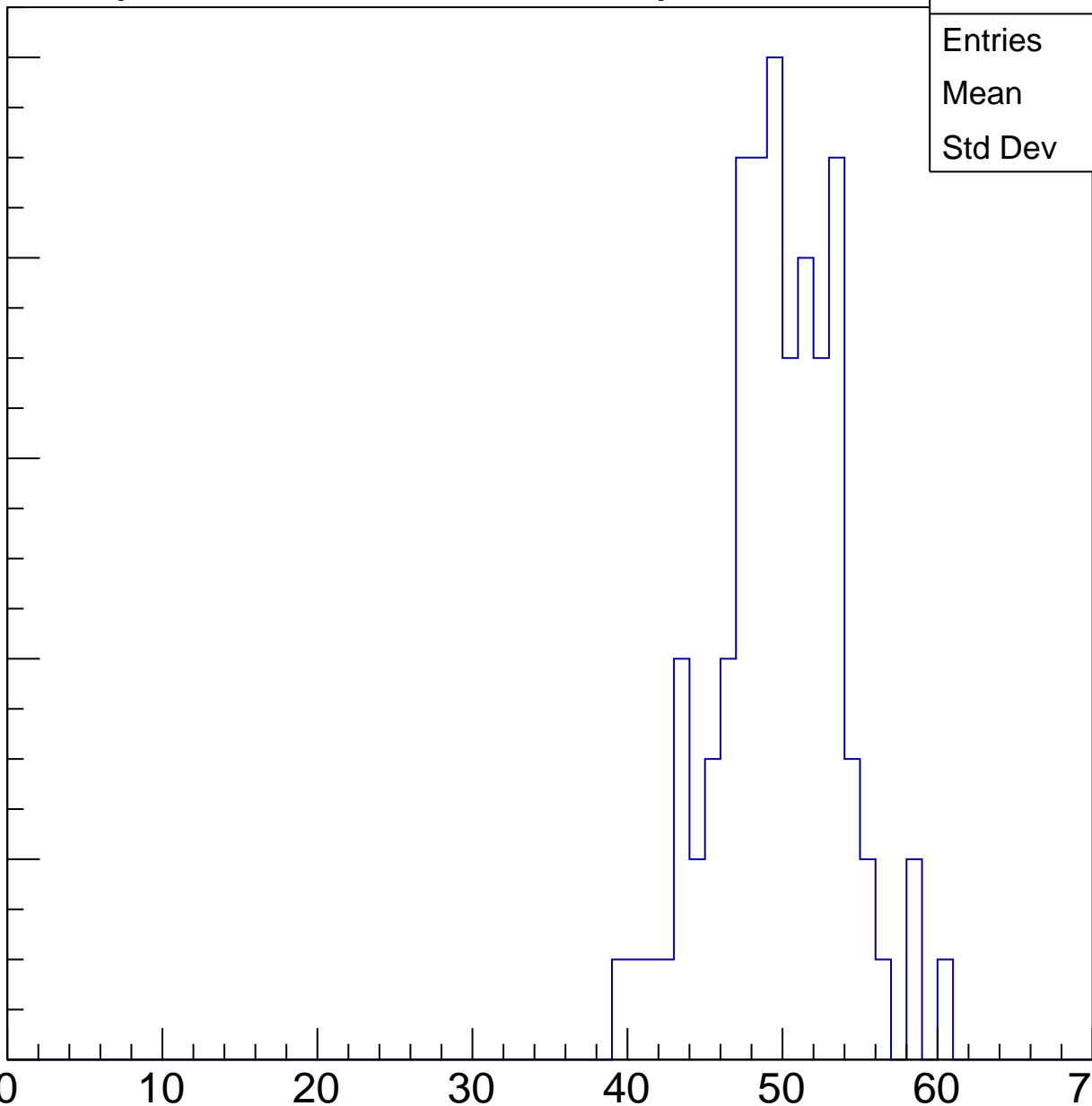
30

40

50

60

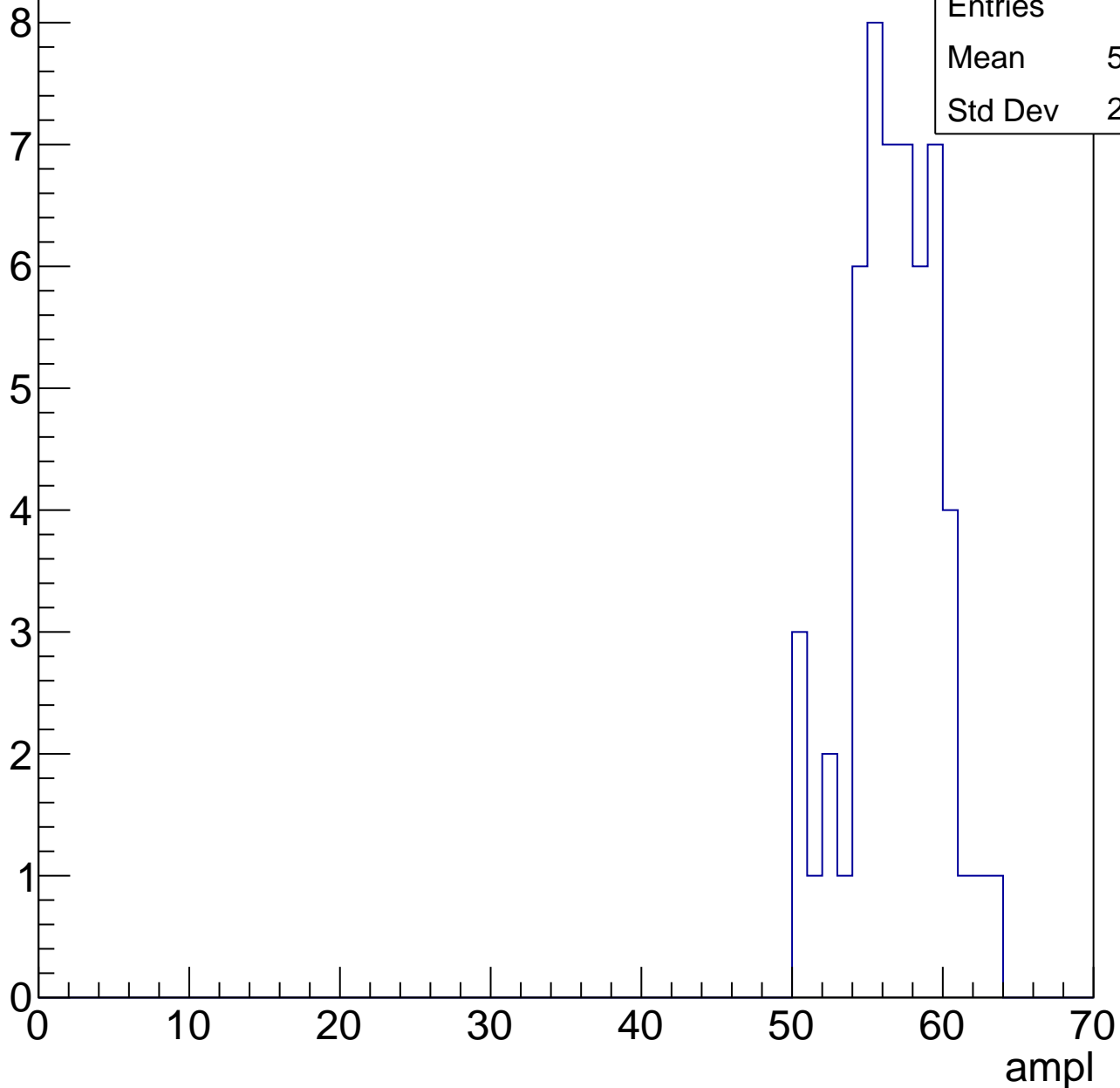
ampl



B0L002S, U18-ch68, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

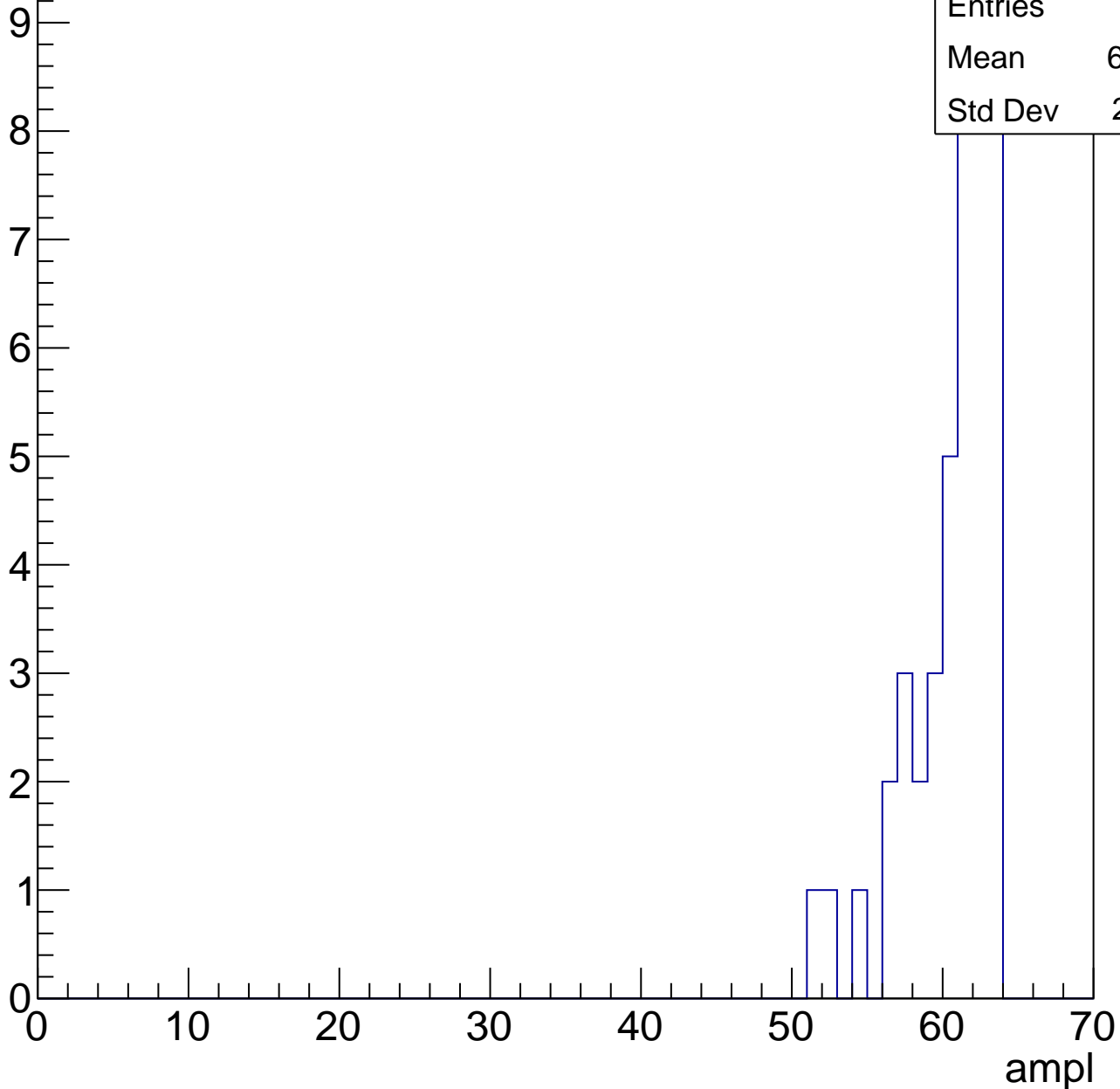


B0L002S, U18-ch68, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	60.07
Std Dev	2.921



B0L002S, U18-ch68, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch68, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch69, adc0

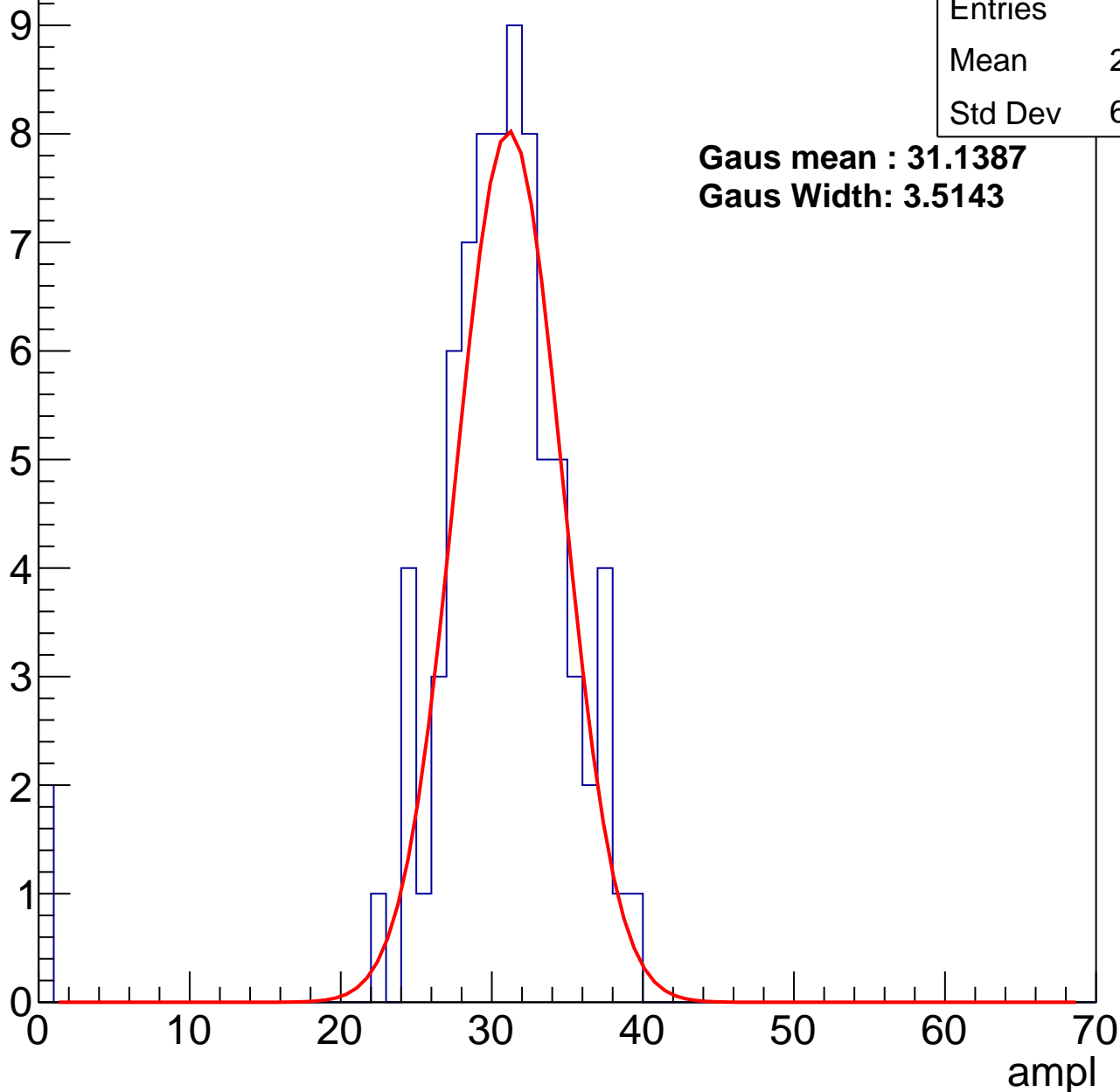
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	29.78
Std Dev	6.014

Gaus mean : 31.1387

Gaus Width: 3.5143



B0L002S, U18-ch69, adc1

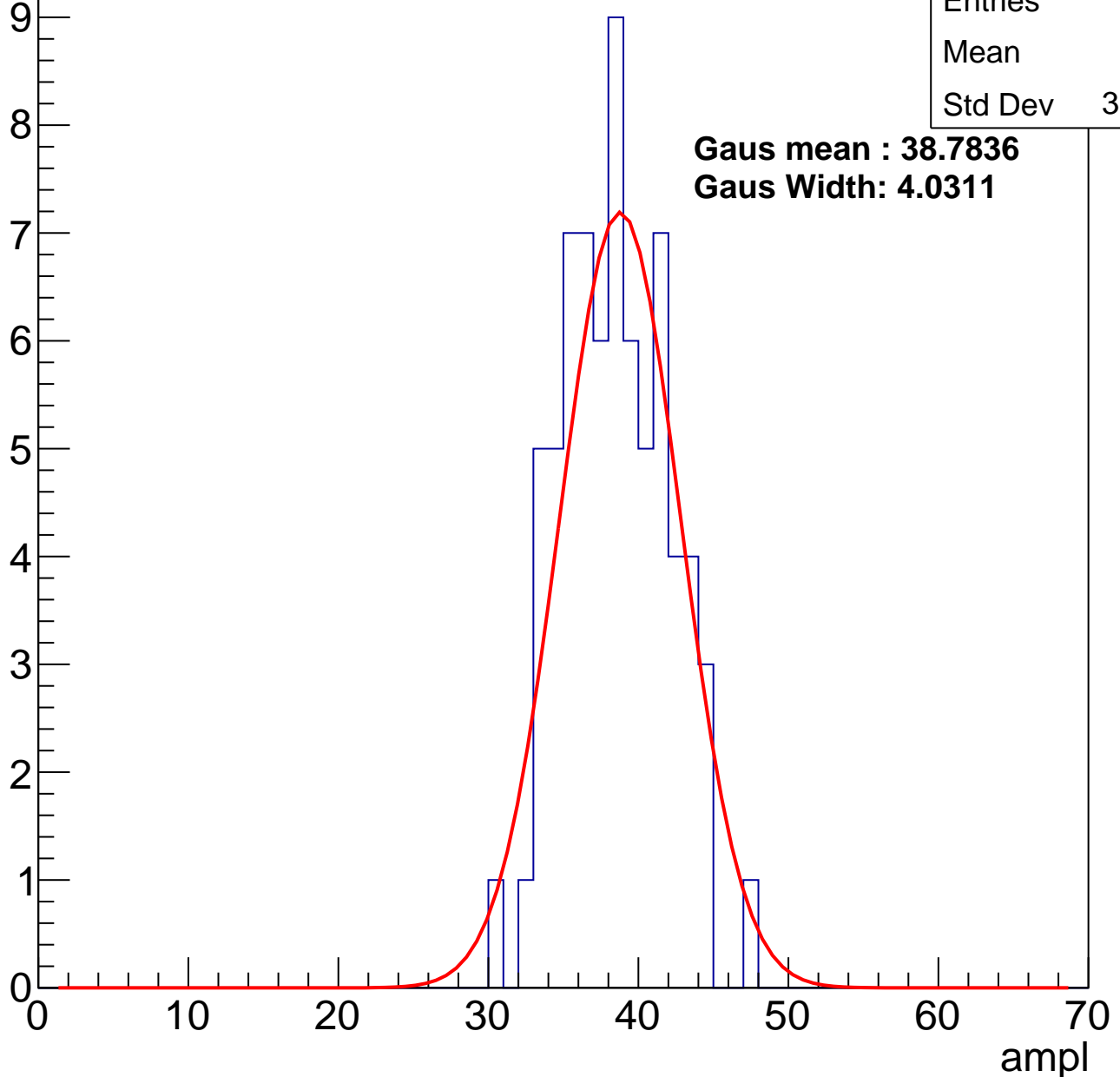
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	38
Std Dev	3.448

Gaus mean : 38.7836

Gaus Width: 4.0311



B0L002S, U18-ch69, adc2

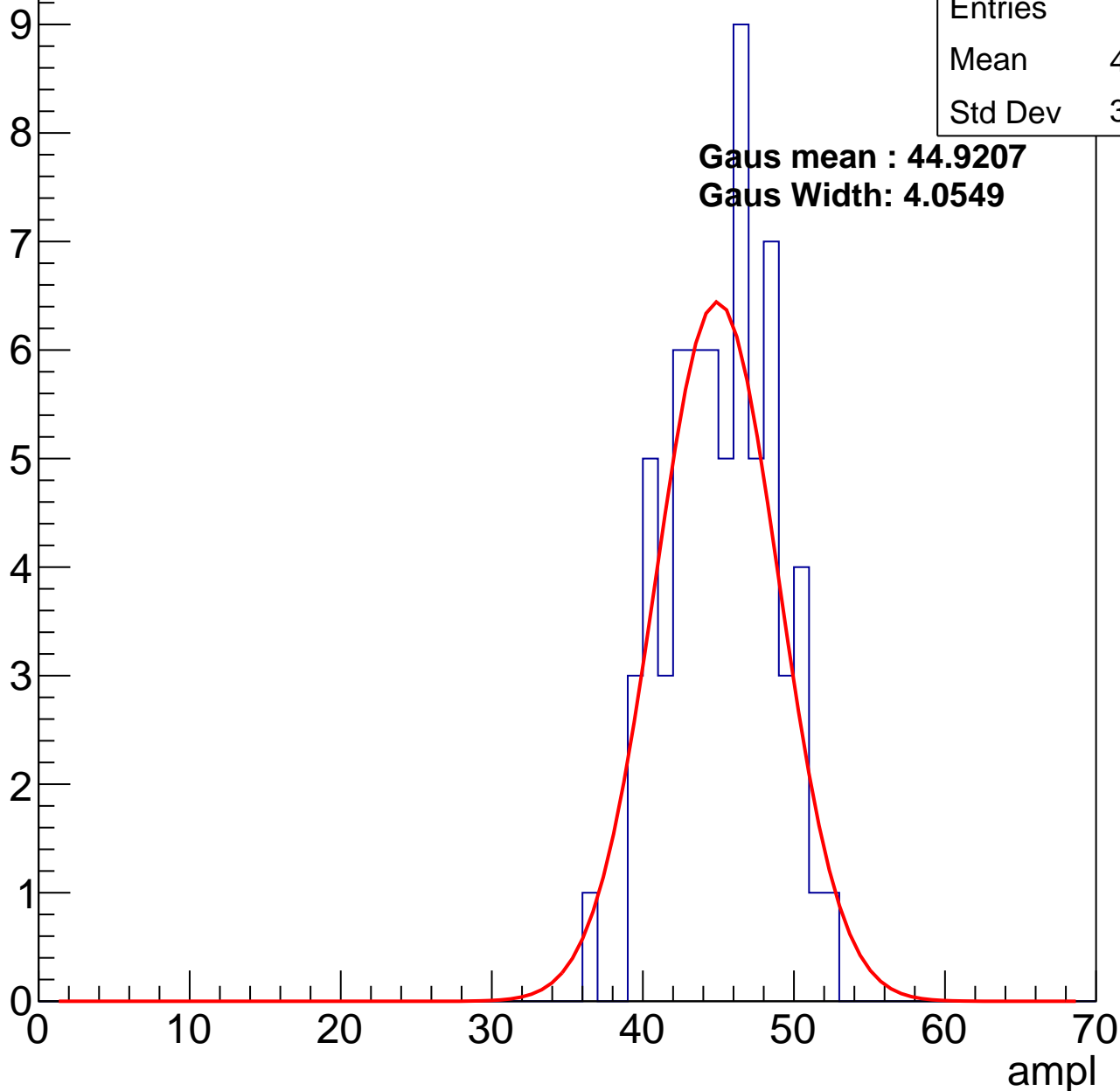
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	44.77
Std Dev	3.436

Gaus mean : 44.9207

Gaus Width: 4.0549

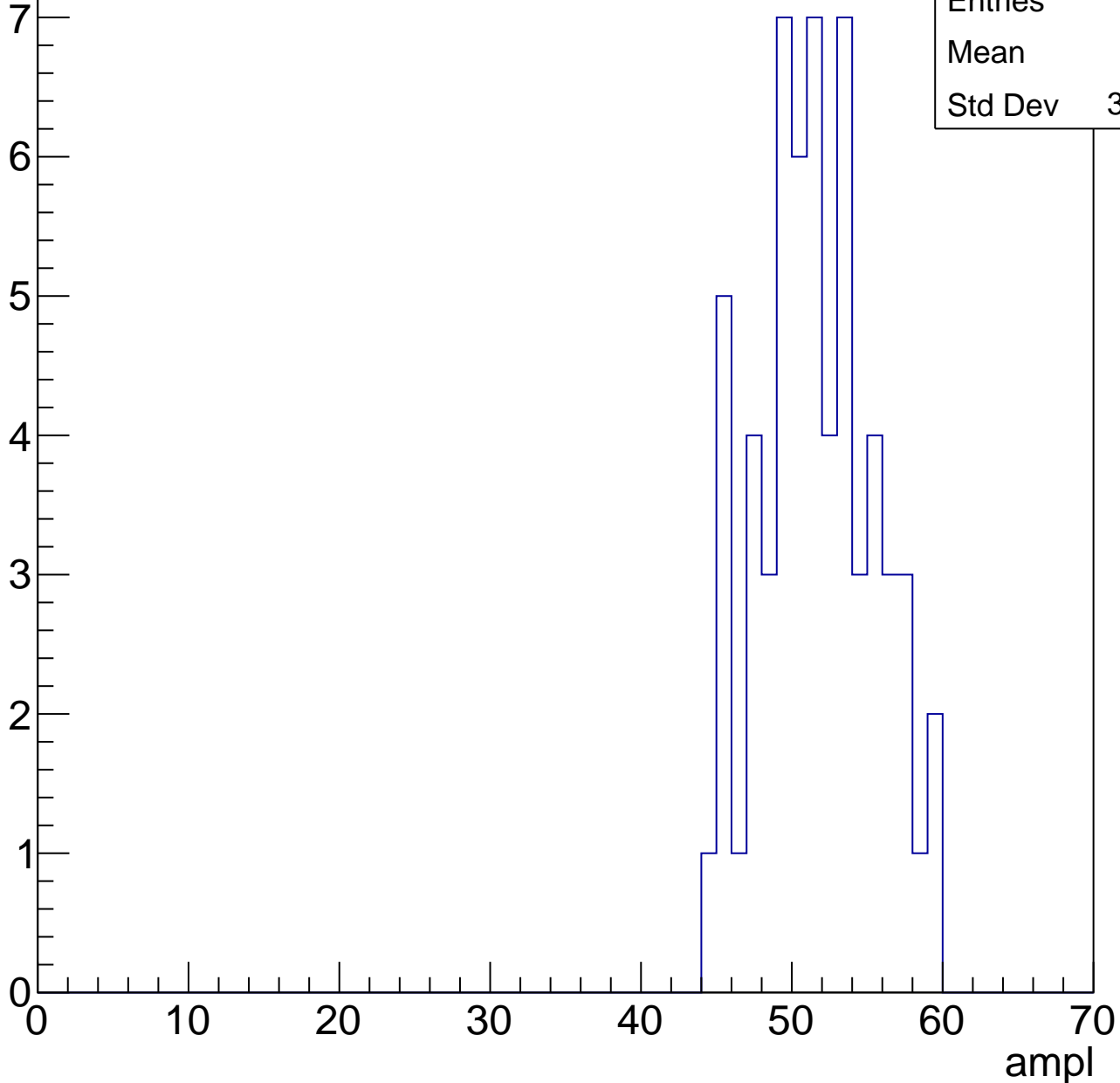


B0L002S, U18-ch69, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	51.2
Std Dev	3.754

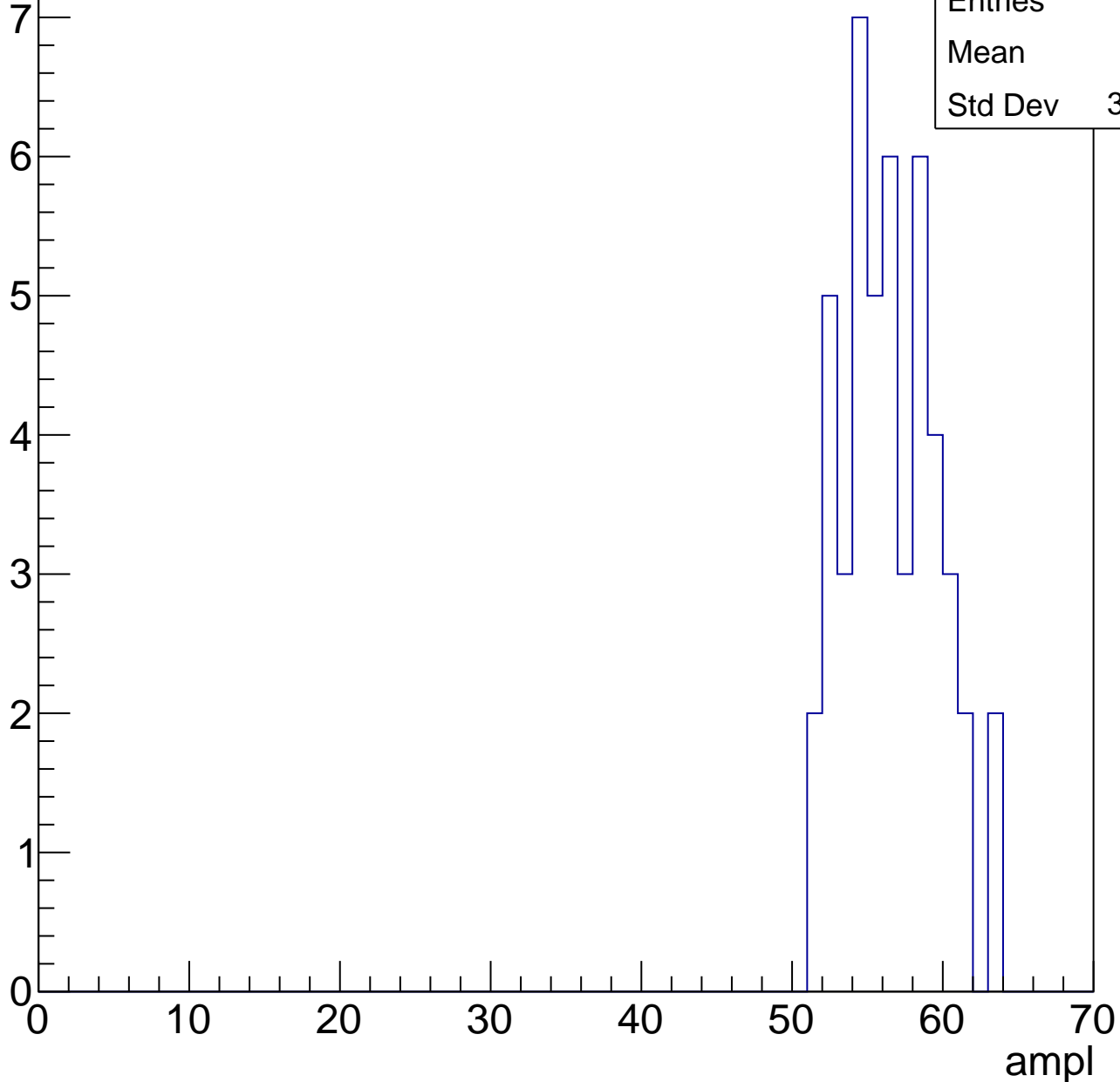


B0L002S, U18-ch69, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	56.1
Std Dev	3.057

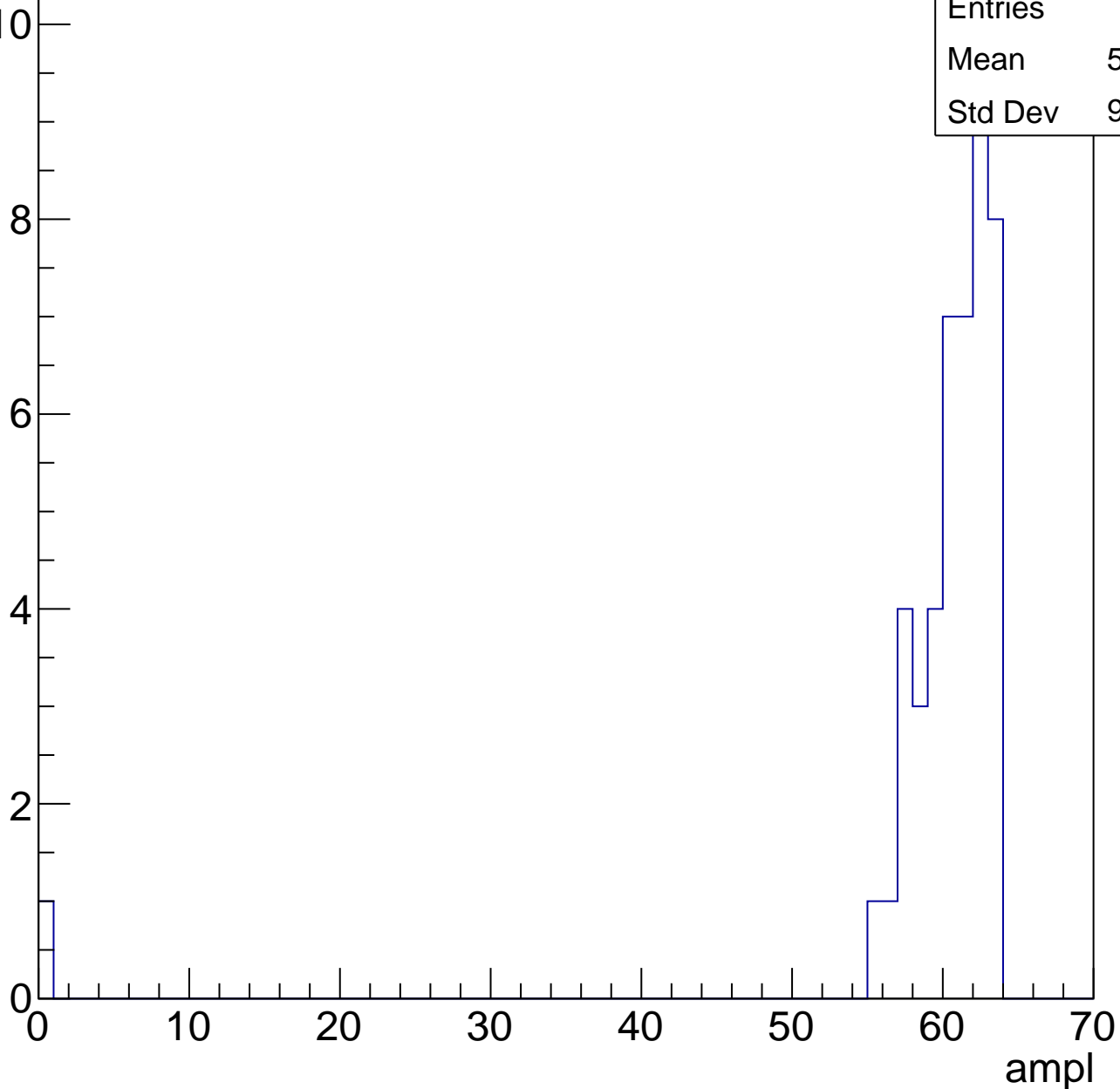


B0L002S, U18-ch69, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	59.13
Std Dev	9.062



B0L002S, U18-ch69, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch69, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch70, adc0

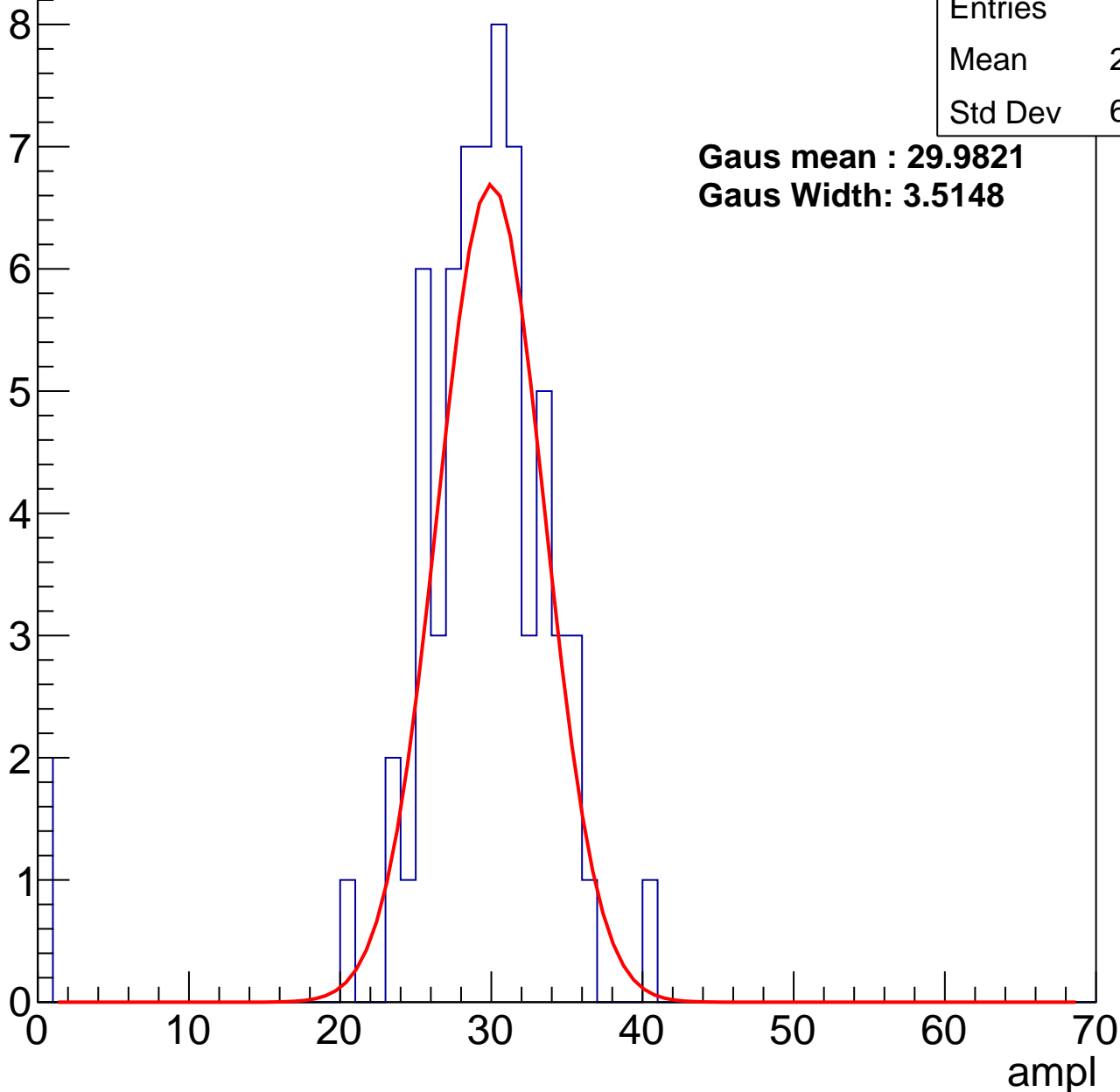
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	28.48
Std Dev	6.145

Gaus mean : 29.9821

Gaus Width: 3.5148



B0L002S, U18-ch70, adc1

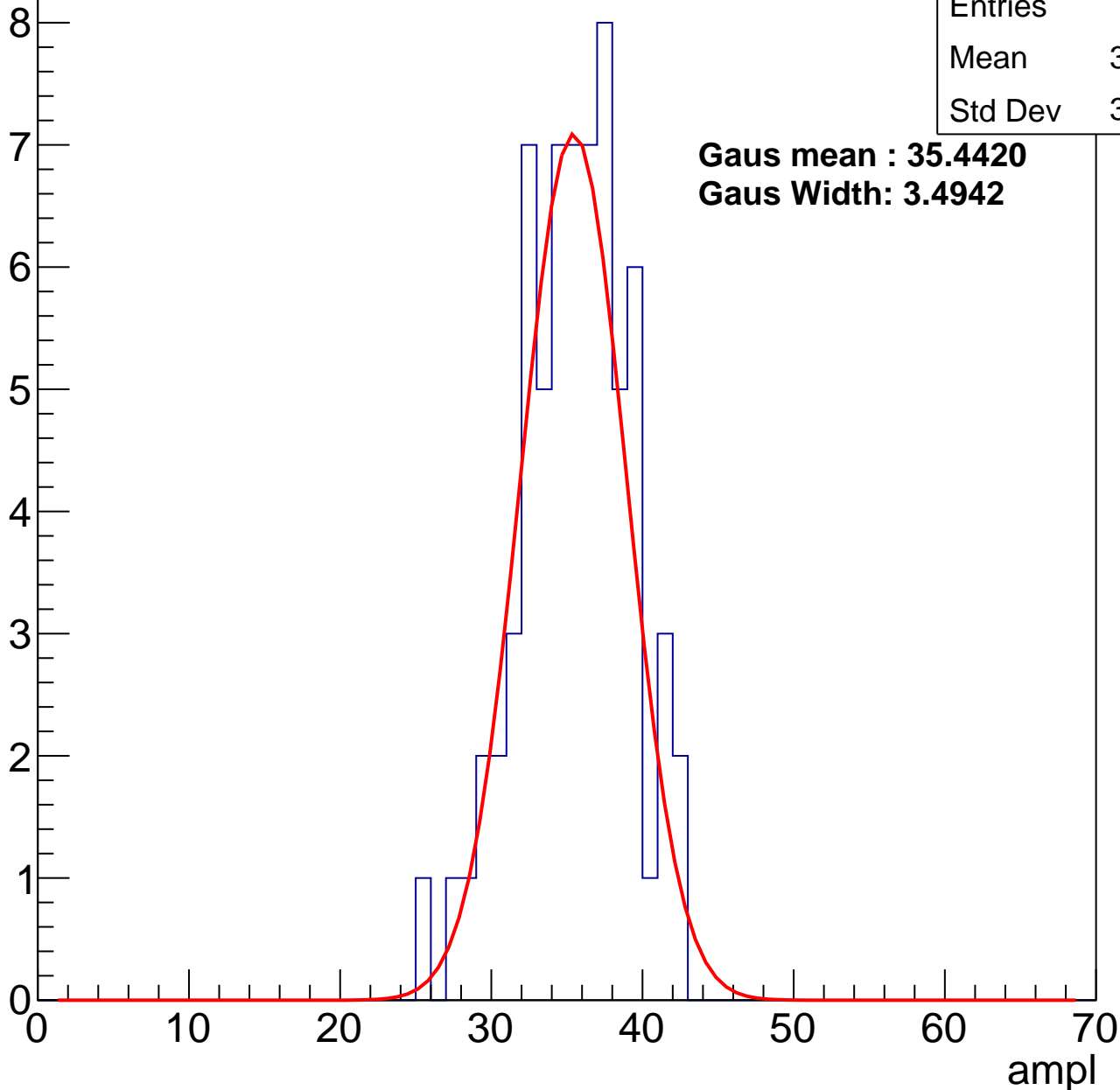
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	35.03
Std Dev	3.605

Gaus mean : 35.4420

Gaus Width: 3.4942



B0L002S, U18-ch70, adc2

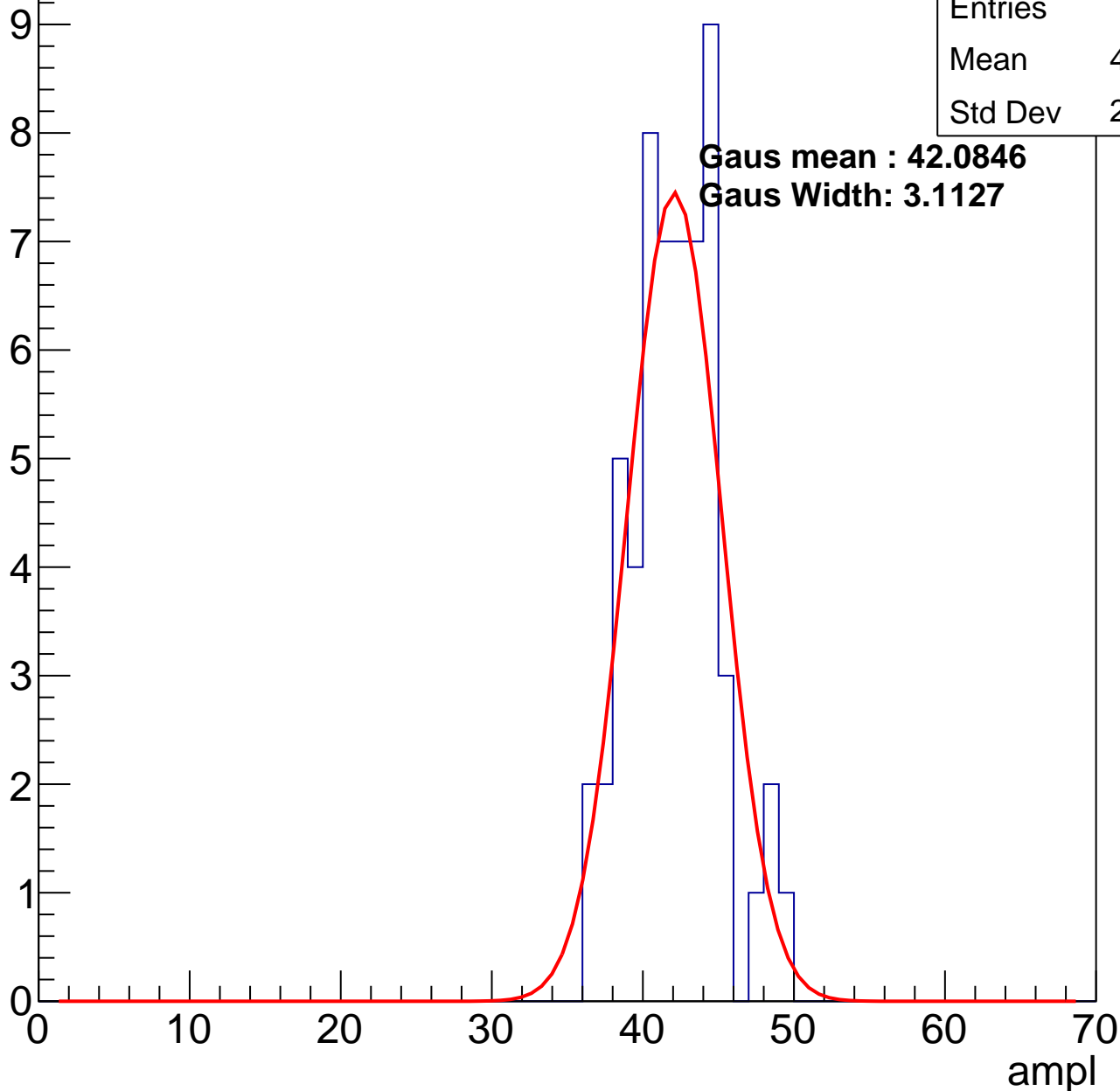
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	41.67
Std Dev	2.897

Gaus mean : 42.0846

Gaus Width: 3.1127

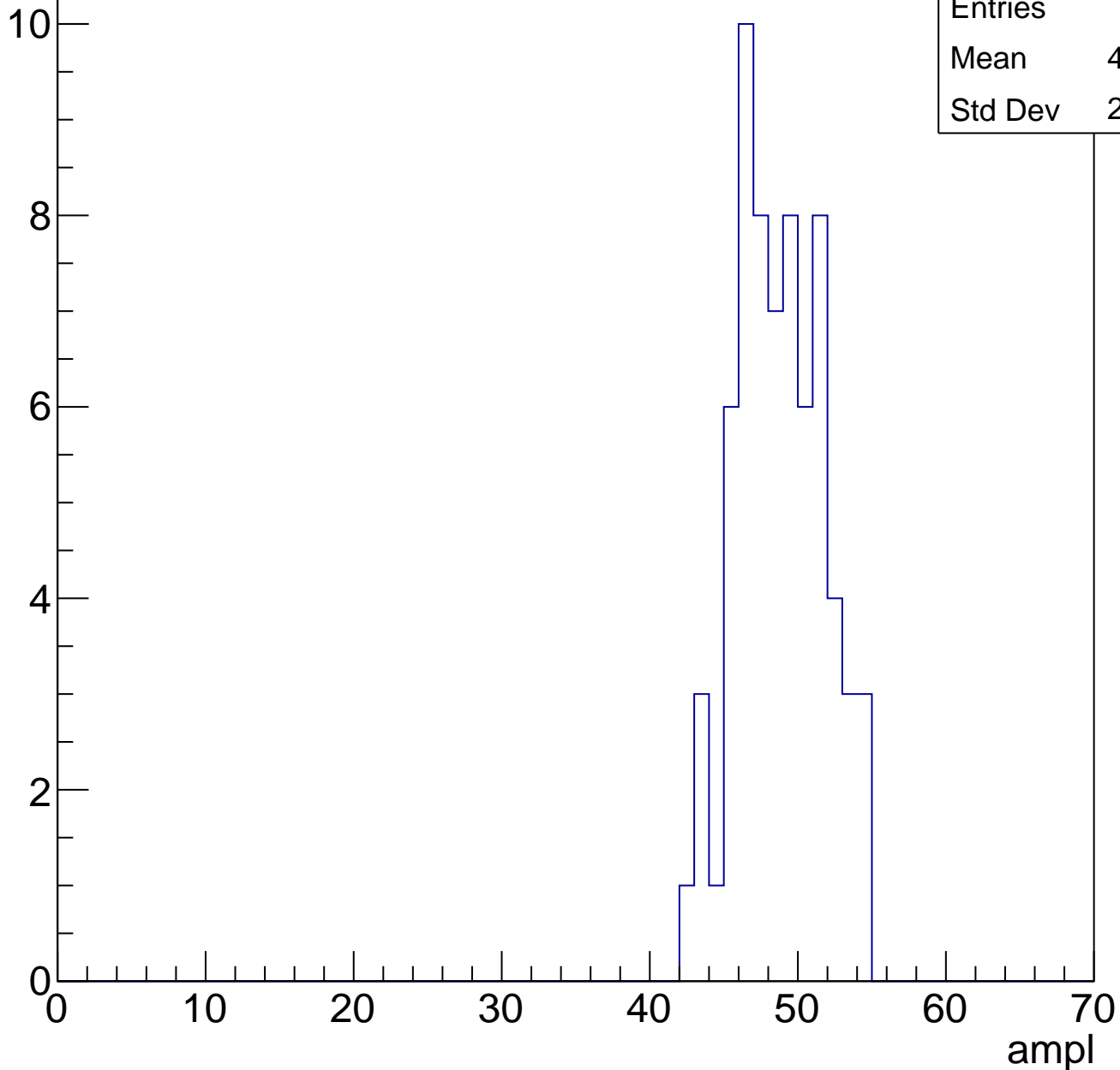


B0L002S, U18-ch70, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	68
Mean	48.32
Std Dev	2.903

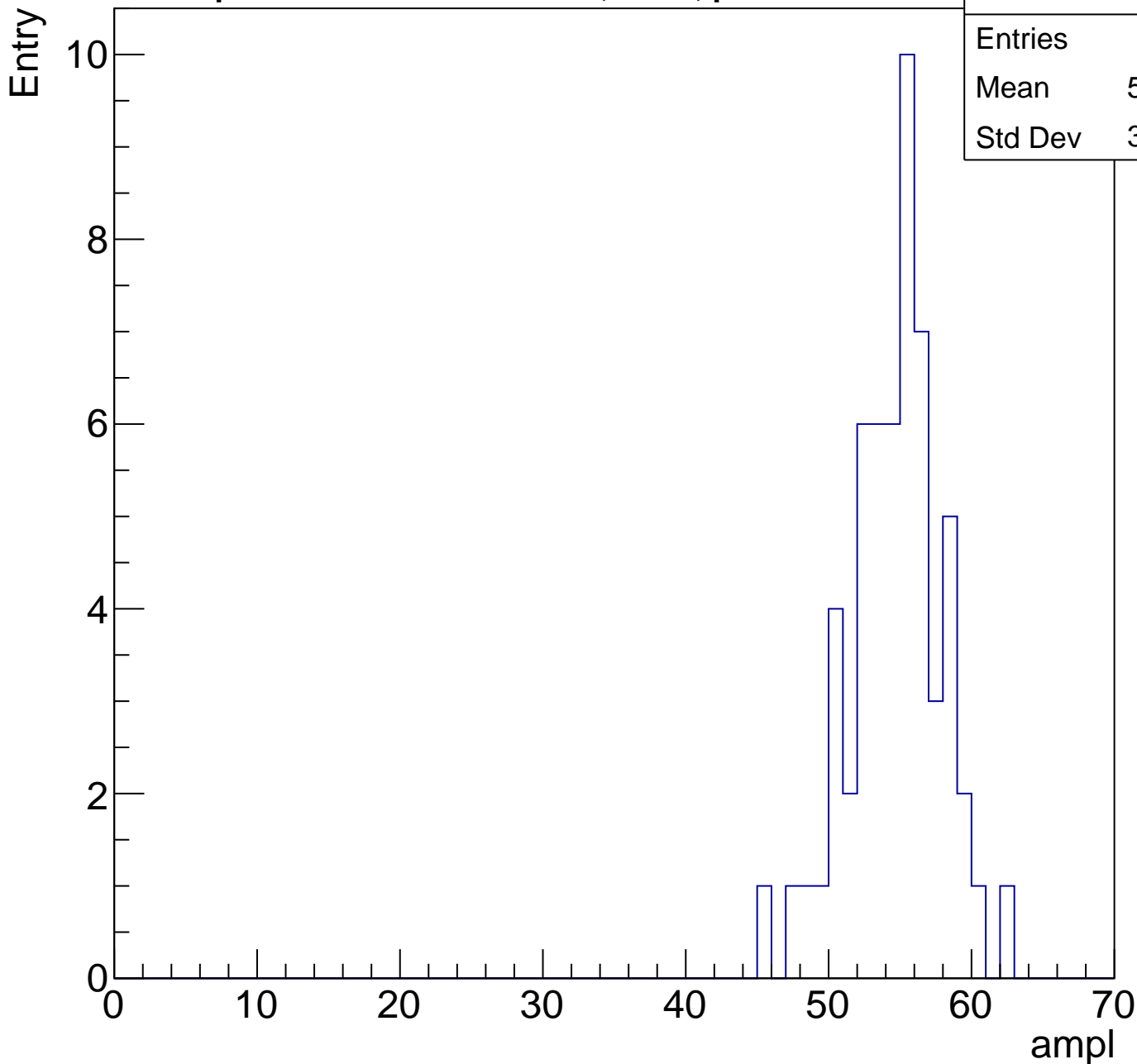
Entry



B0L002S, U18-ch70, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	57
Mean	54.18
Std Dev	3.234

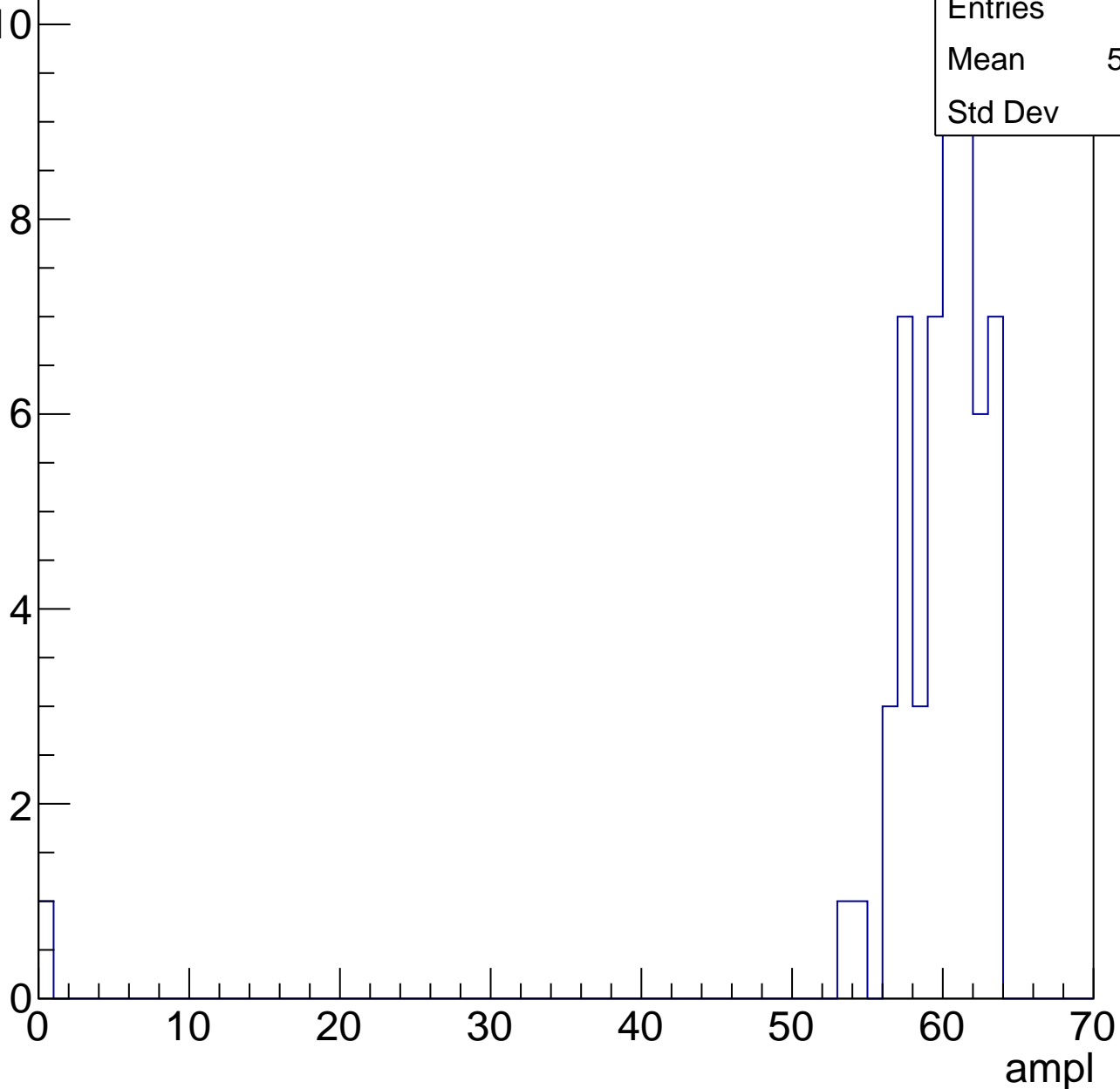


B0L002S, U18-ch70, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

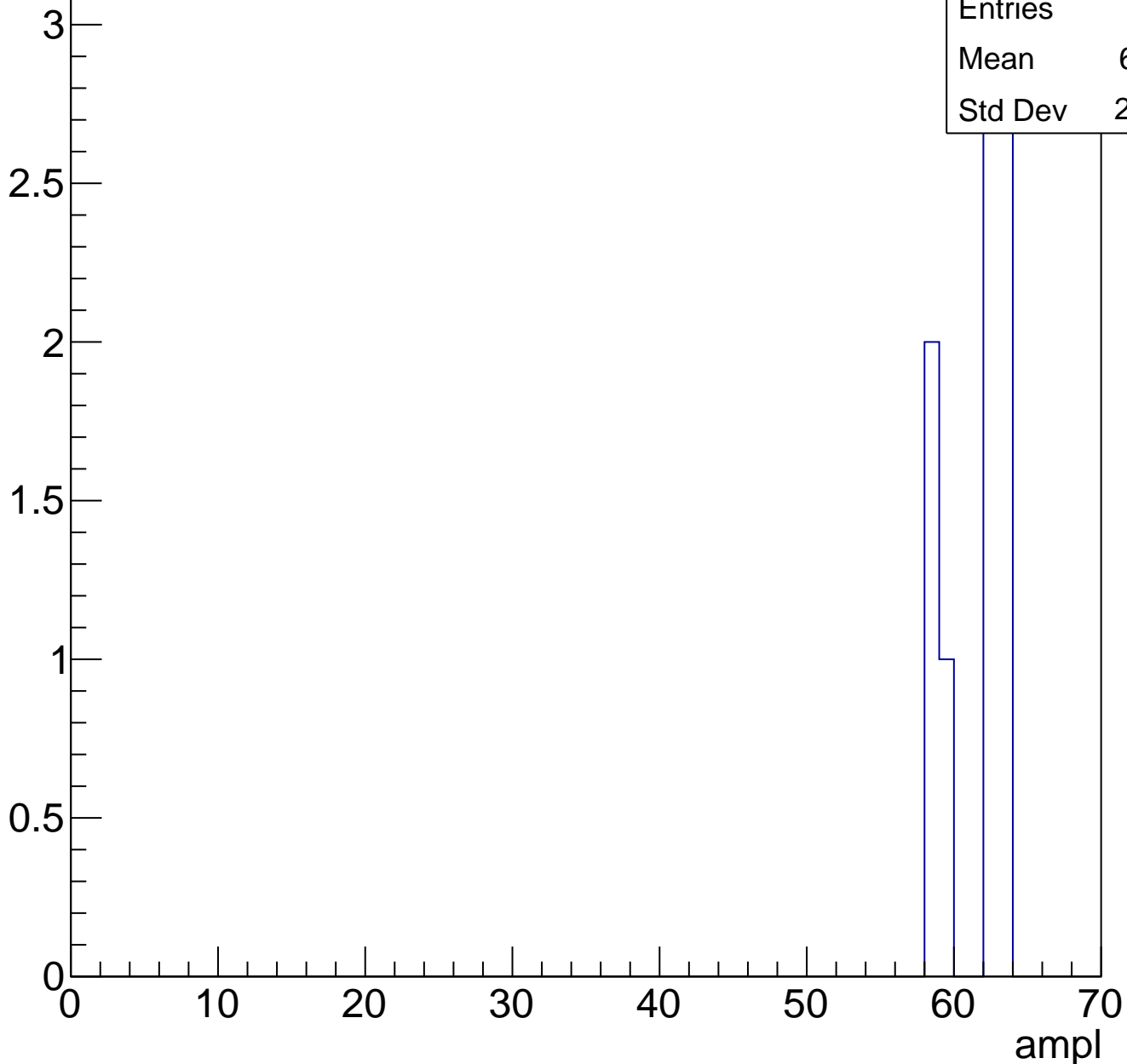
Entries	55
Mean	58.62
Std Dev	8.32



B0L002S, U18-ch70, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch70, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch71, adc0

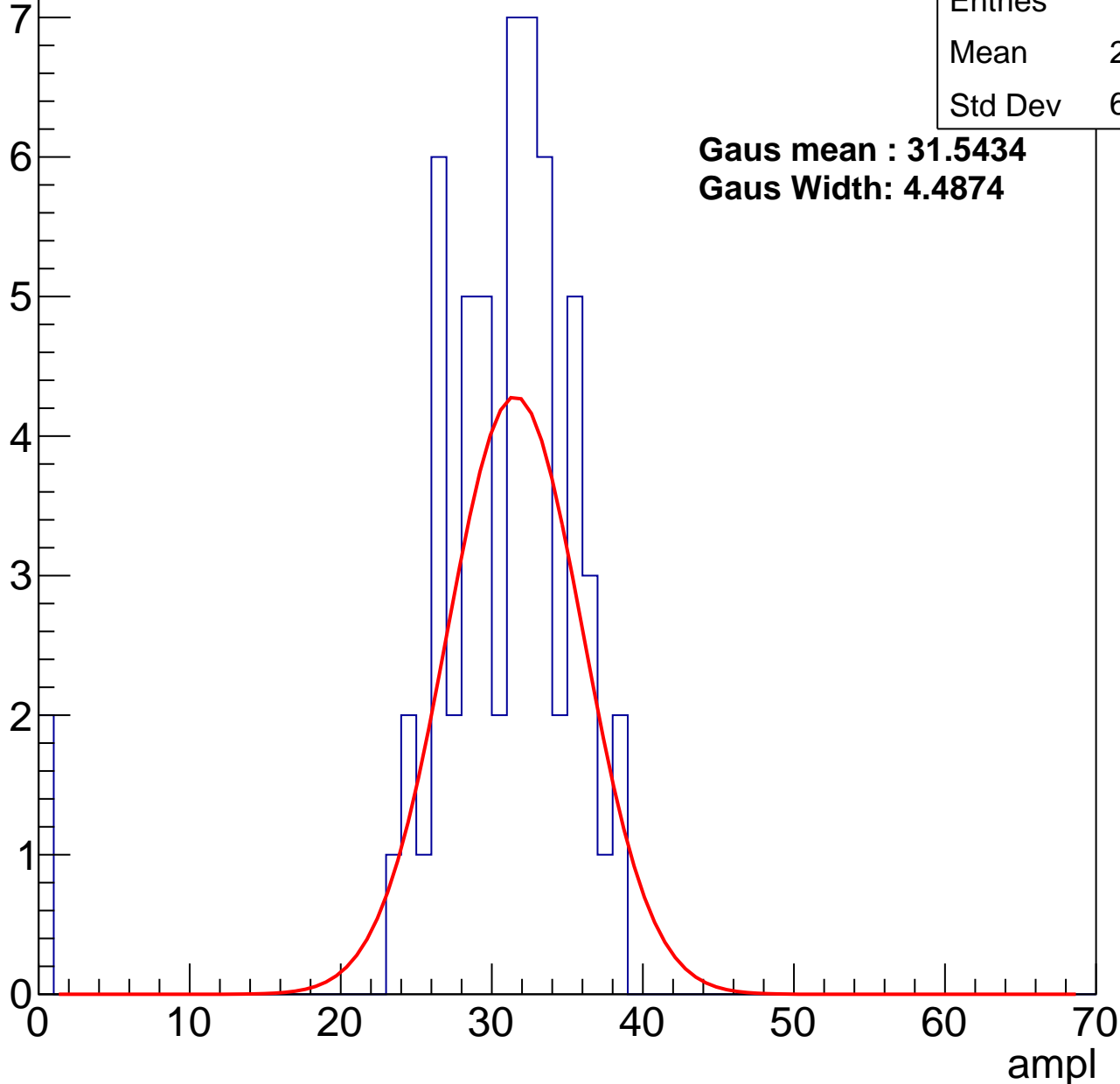
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.73
Std Dev	6.653

Gaus mean : 31.5434

Gaus Width: 4.4874



B0L002S, U18-ch71, adc1

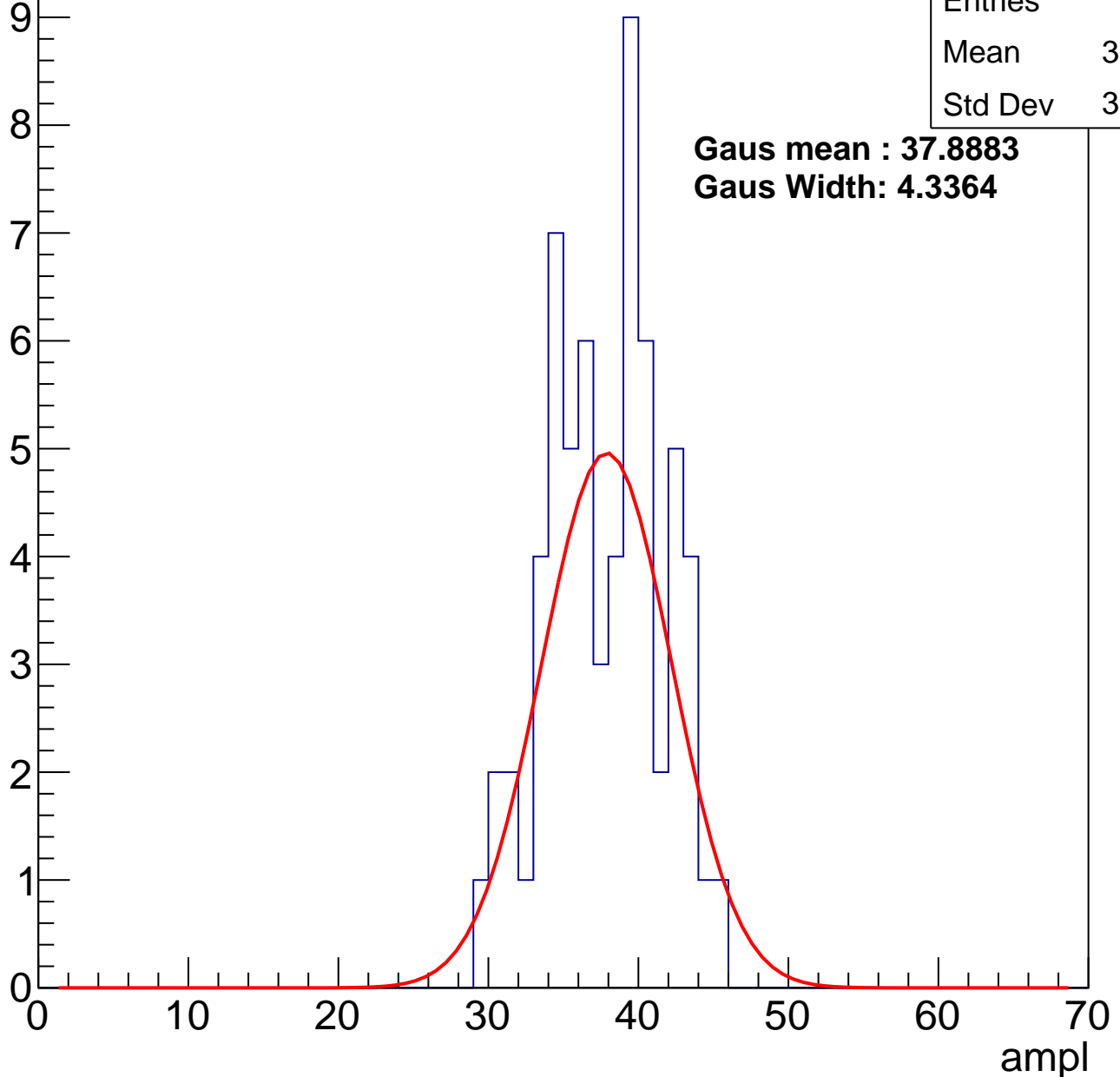
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	37.32
Std Dev	3.808

Gaus mean : 37.8883

Gaus Width: 4.3364



B0L002S, U18-ch71, adc2

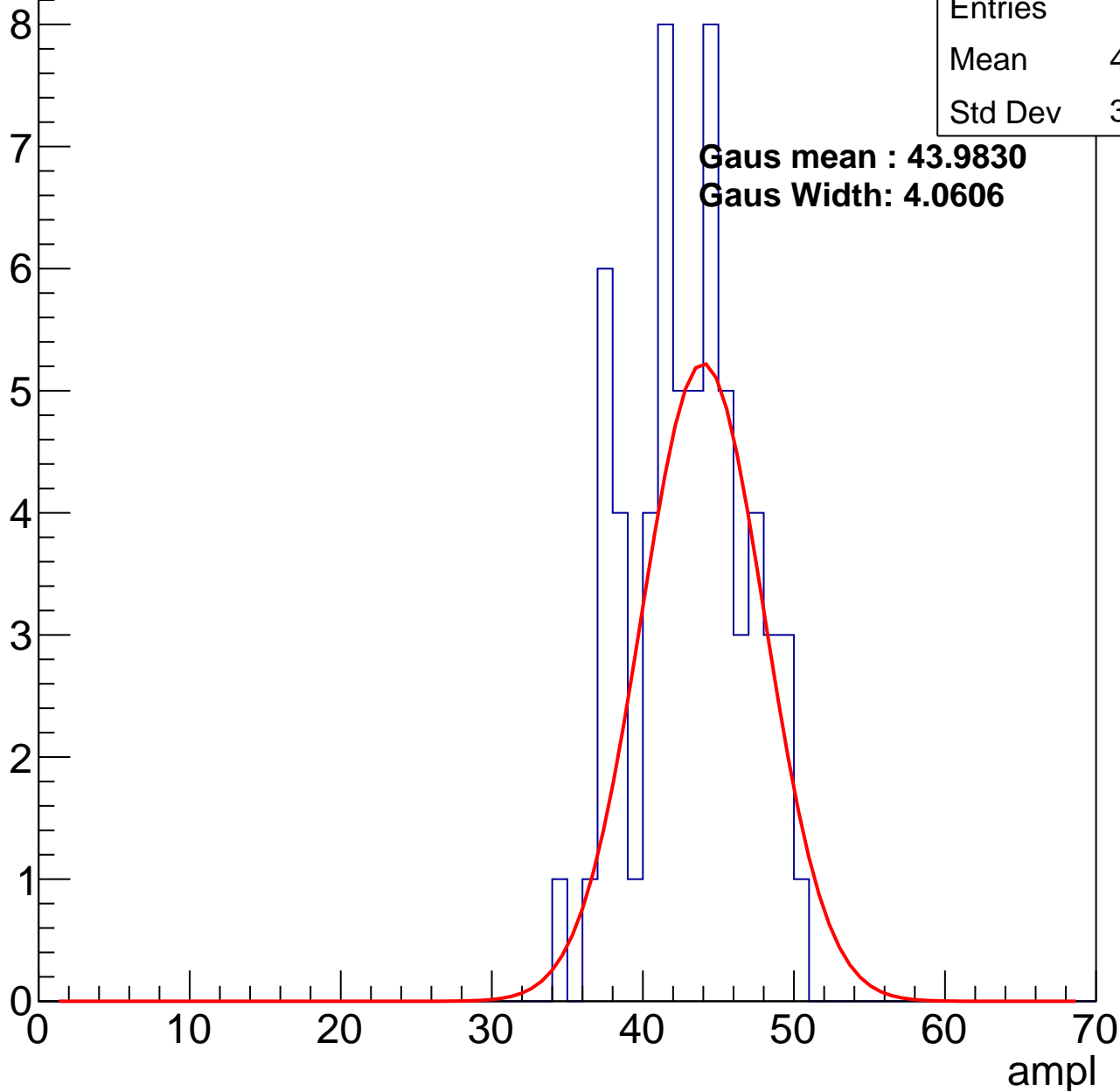
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	42.58
Std Dev	3.757

Gaus mean : 43.9830

Gaus Width: 4.0606

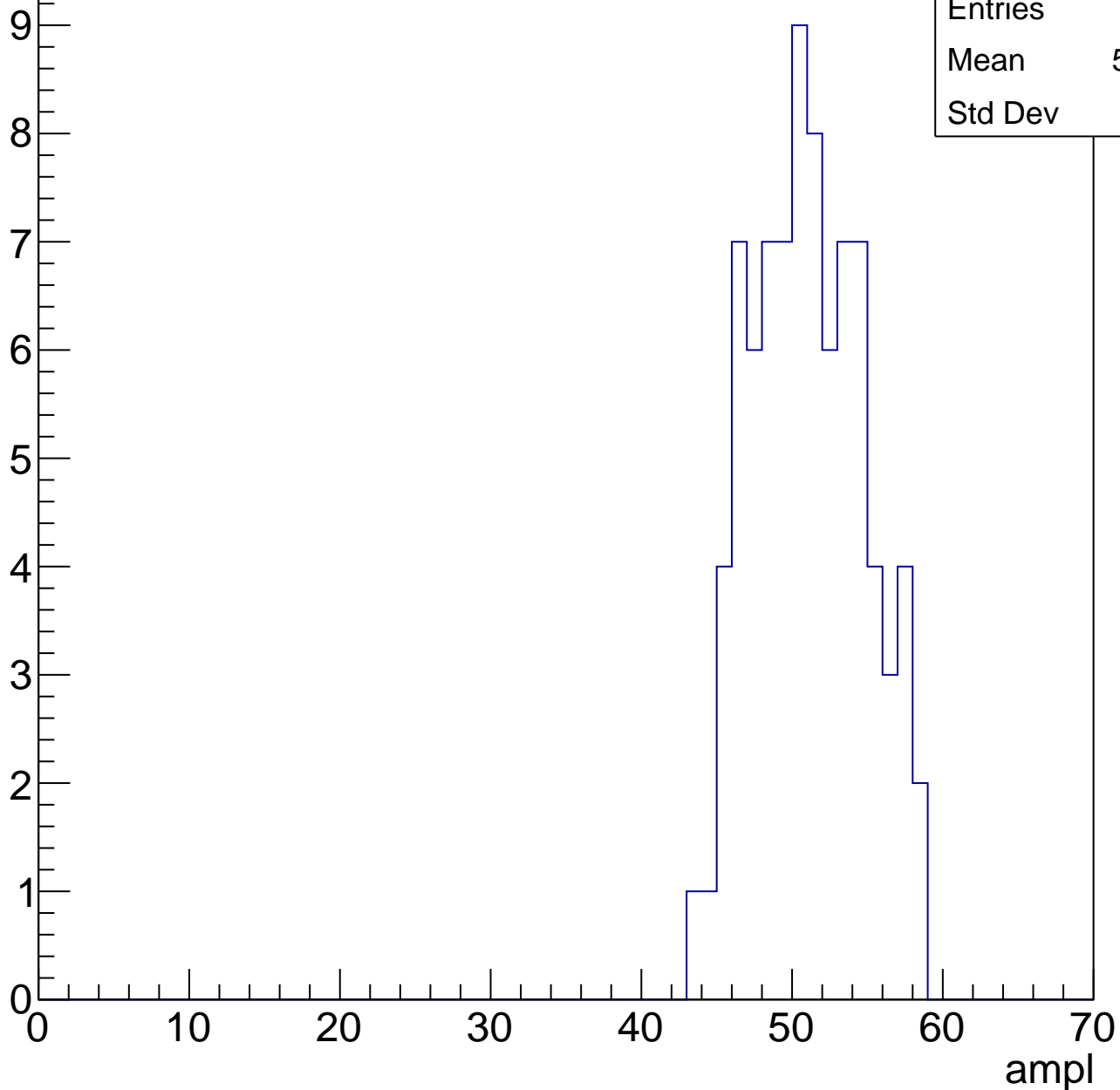


B0L002S, U18-ch71, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

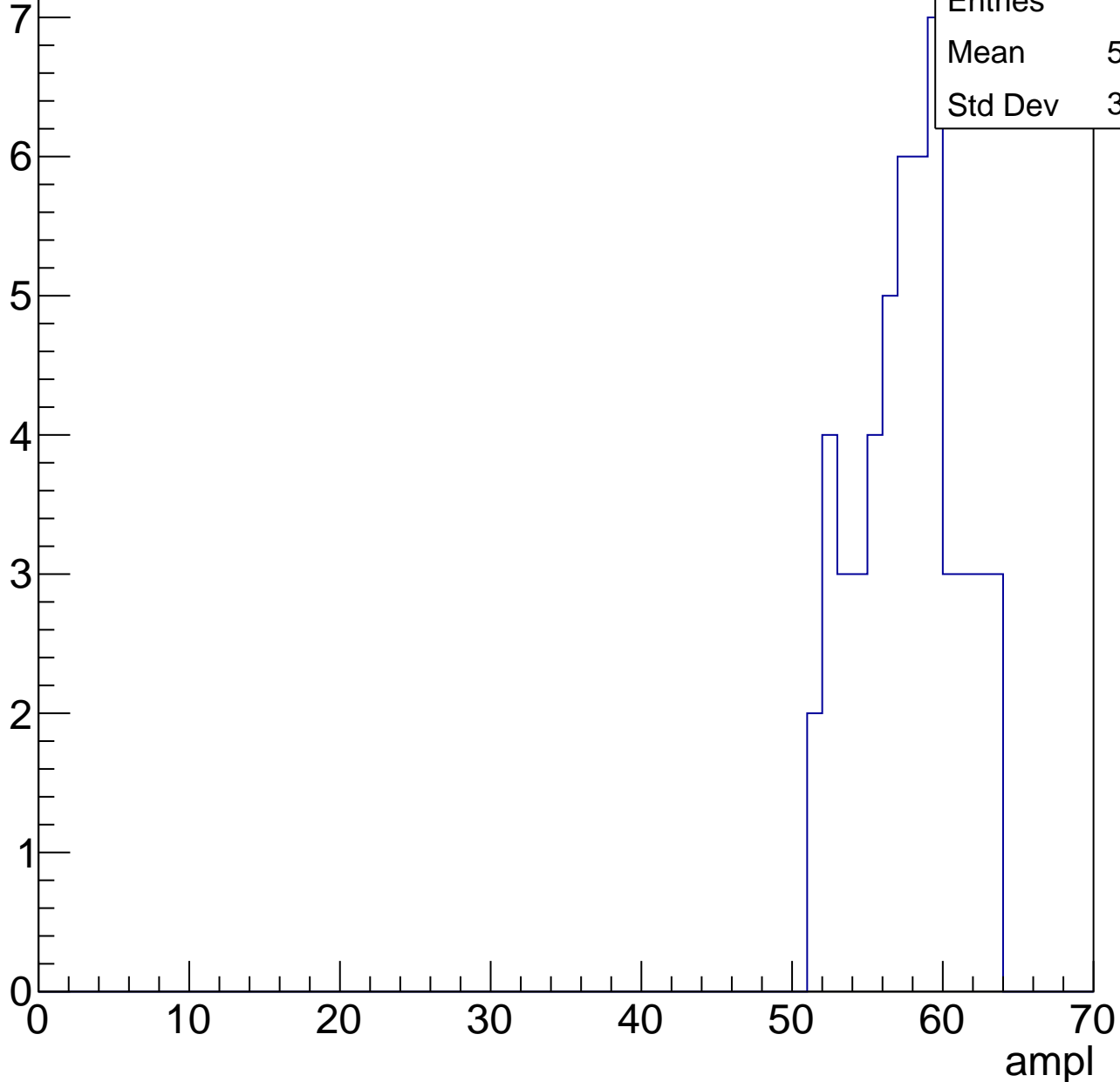
Entries	83
Mean	50.61
Std Dev	3.63



B0L002S, U18-ch71, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



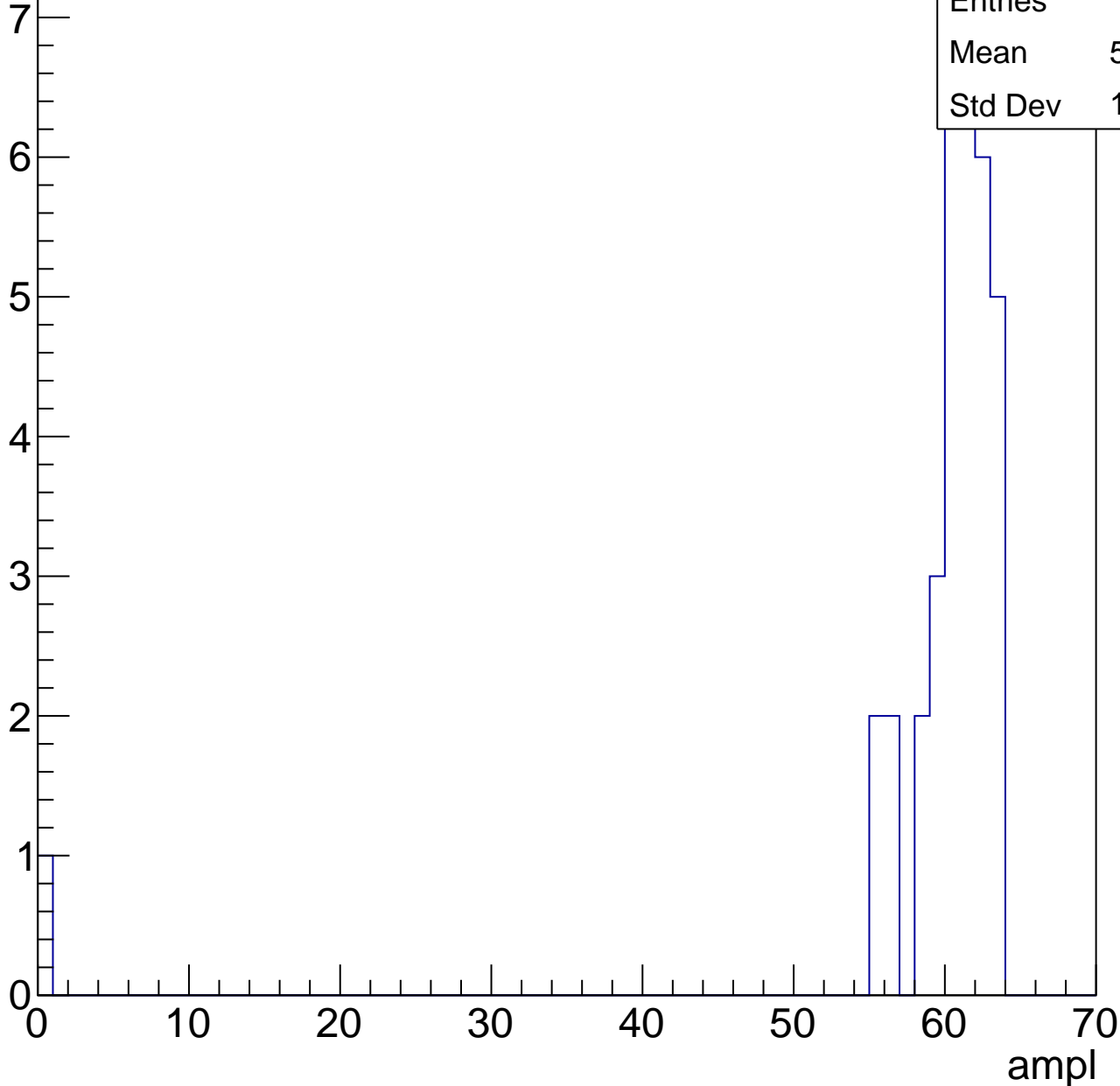
Entries	52
Mean	57.15
Std Dev	3.278

B0L002S, U18-ch71, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.54
Std Dev	10.27



B0L002S, U18-ch71, adc6

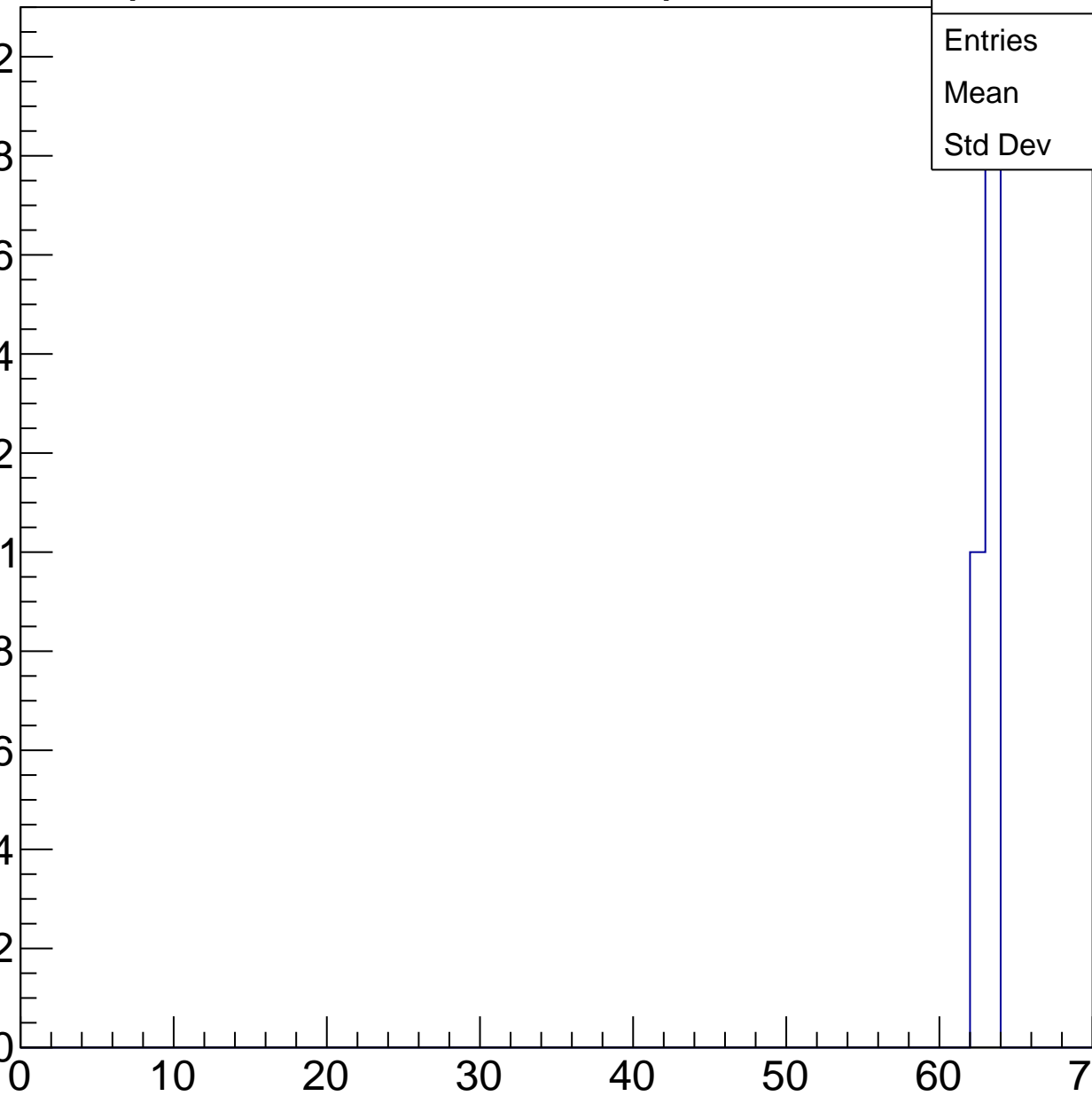
calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.67
Std Dev	0.4714

ampl



B0L002S, U18-ch71, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch72, adc0

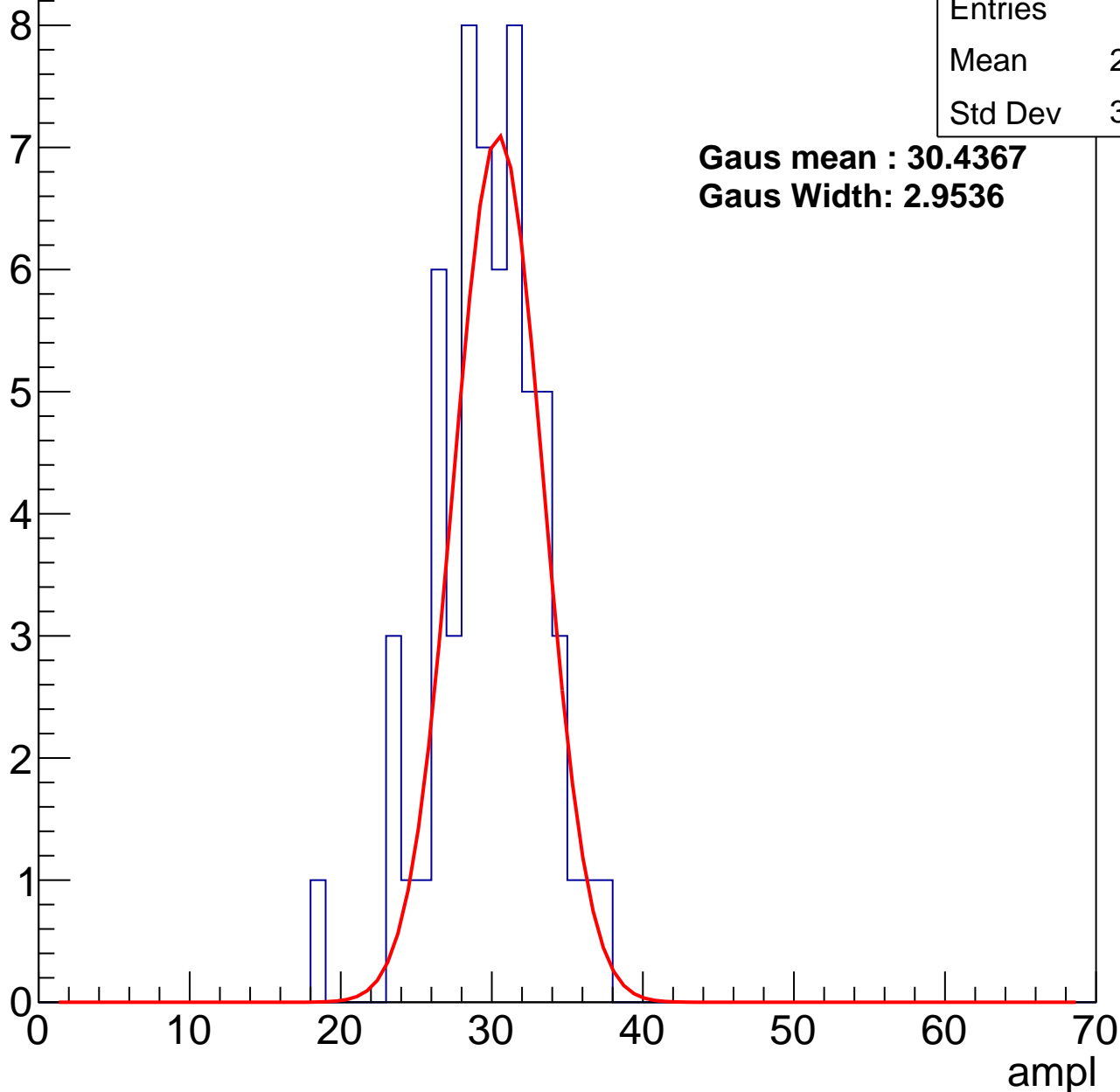
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	29.38
Std Dev	3.469

Gaus mean : 30.4367

Gaus Width: 2.9536



B0L002S, U18-ch72, adc1

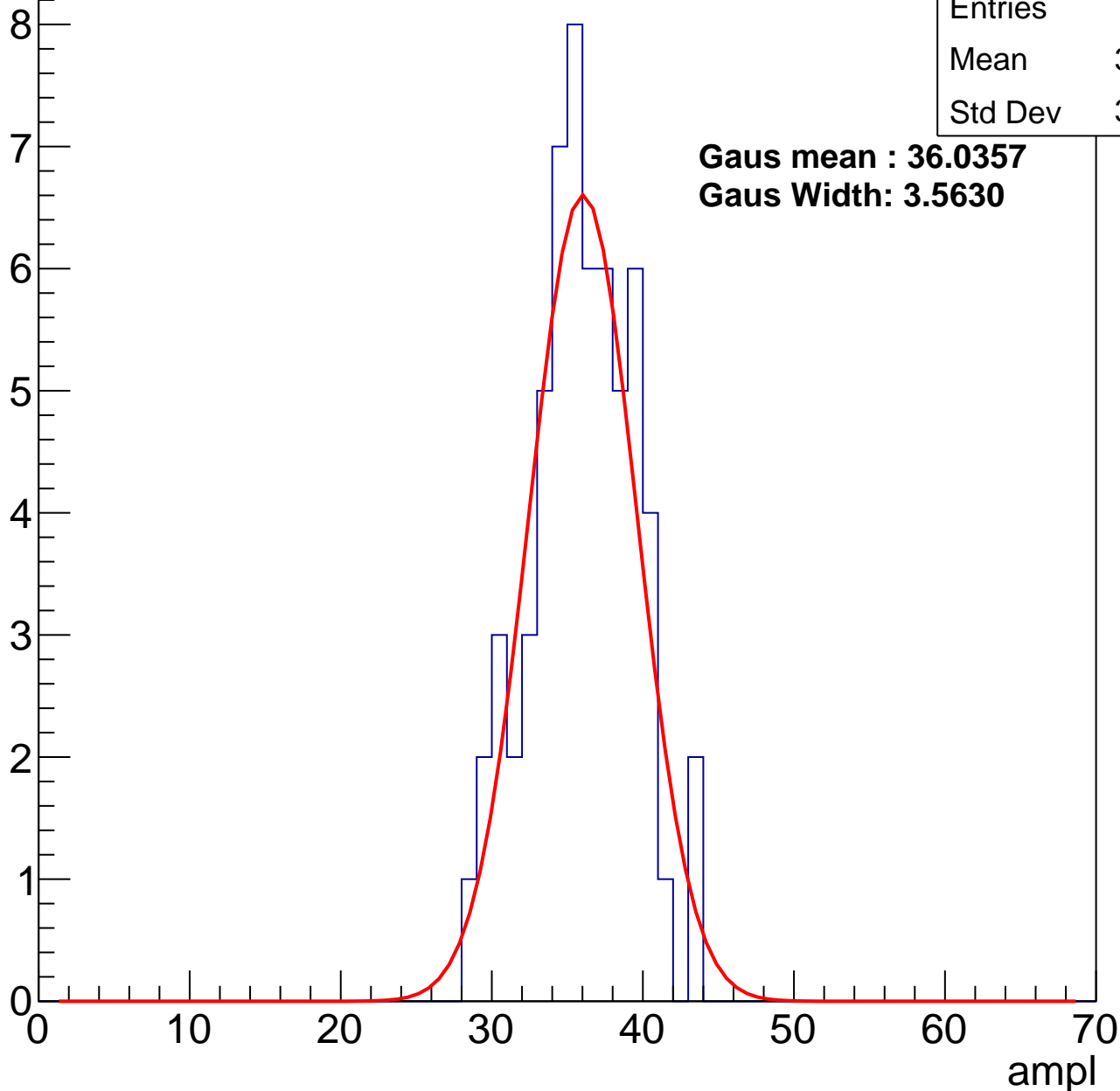
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	35.51
Std Dev	3.391

Gaus mean : 36.0357

Gaus Width: 3.5630



B0L002S, U18-ch72, adc2

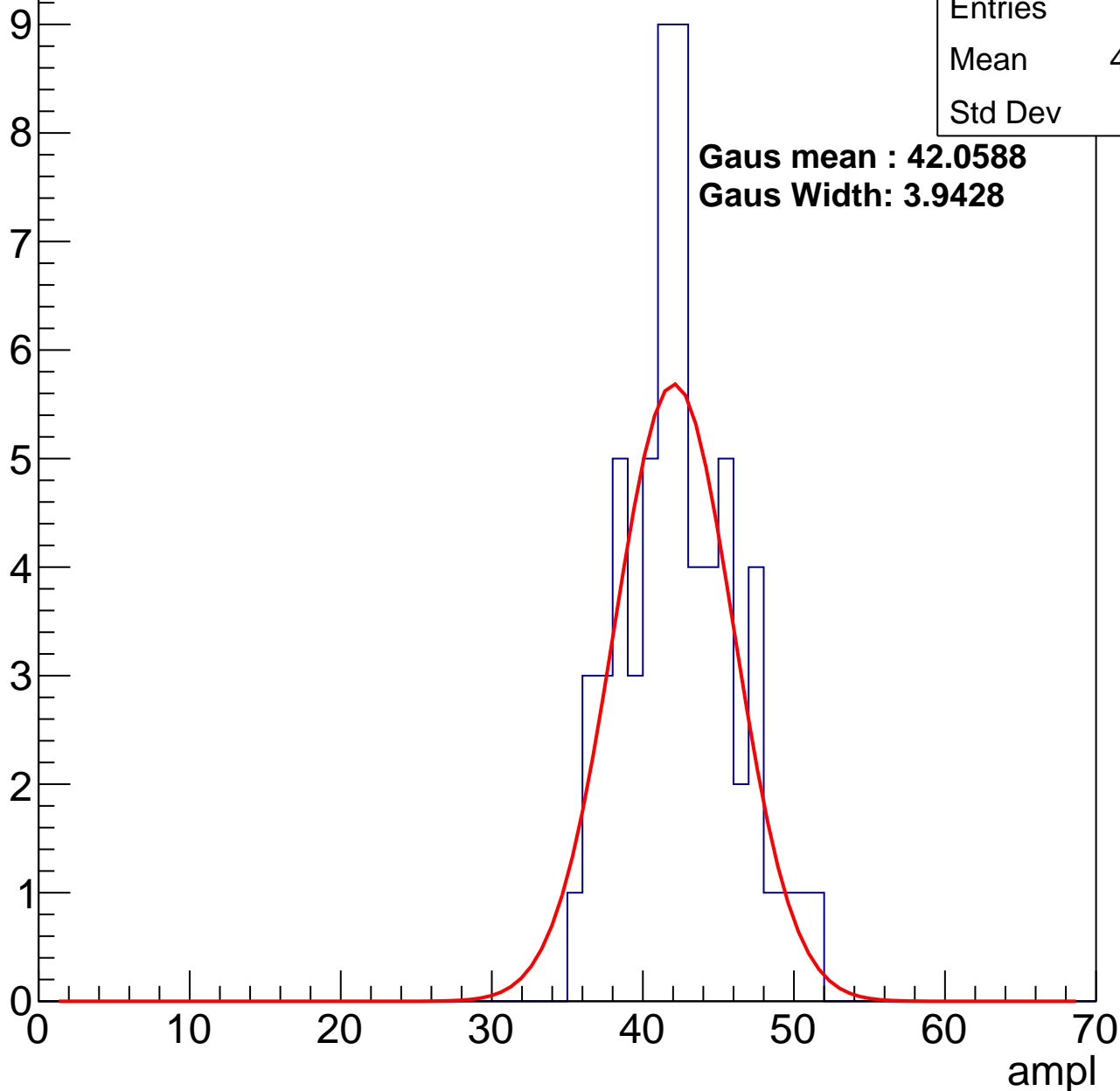
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	41.95
Std Dev	3.61

Gaus mean : 42.0588

Gaus Width: 3.9428

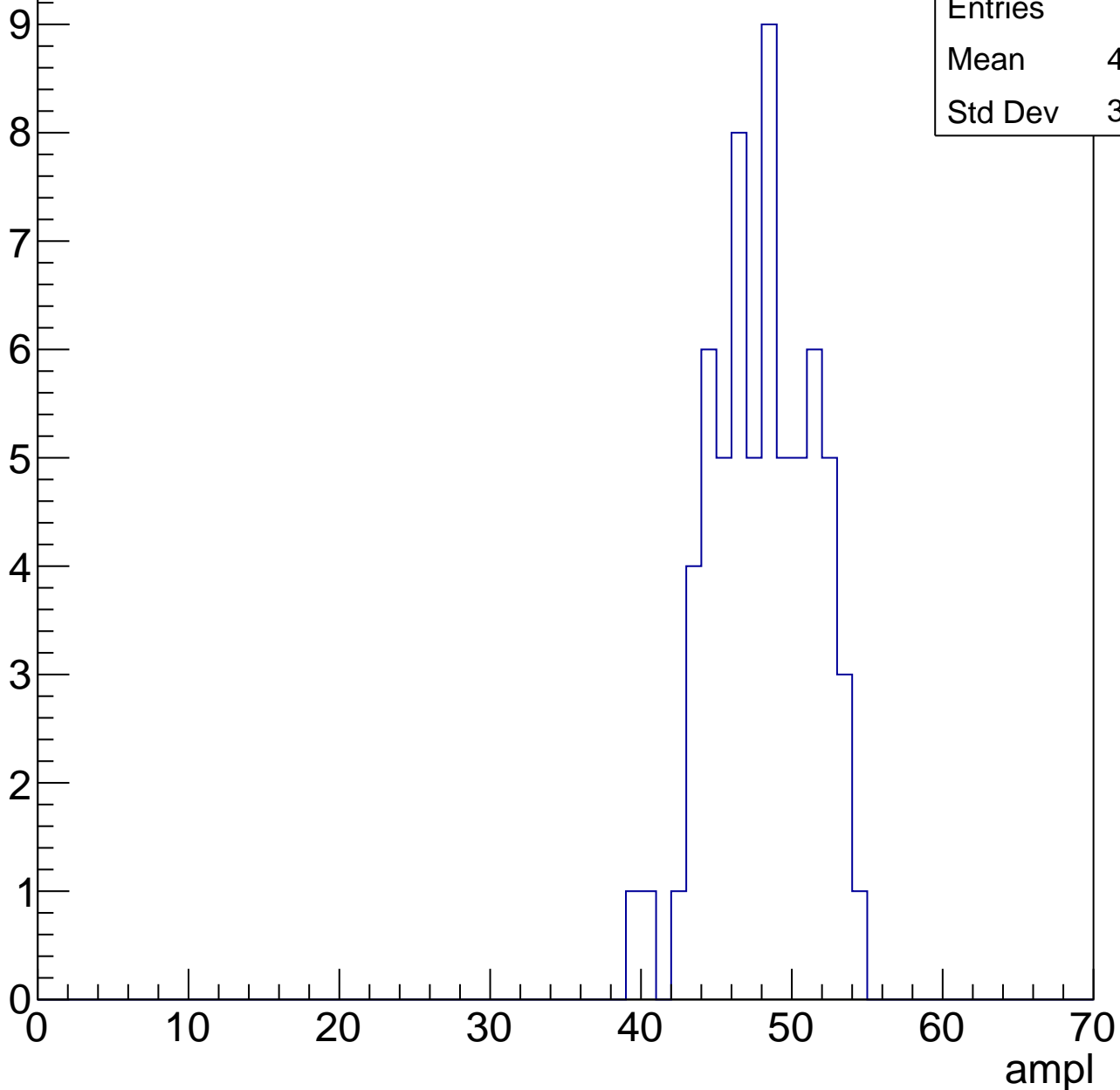


B0L002S, U18-ch72, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	47.55
Std Dev	3.319

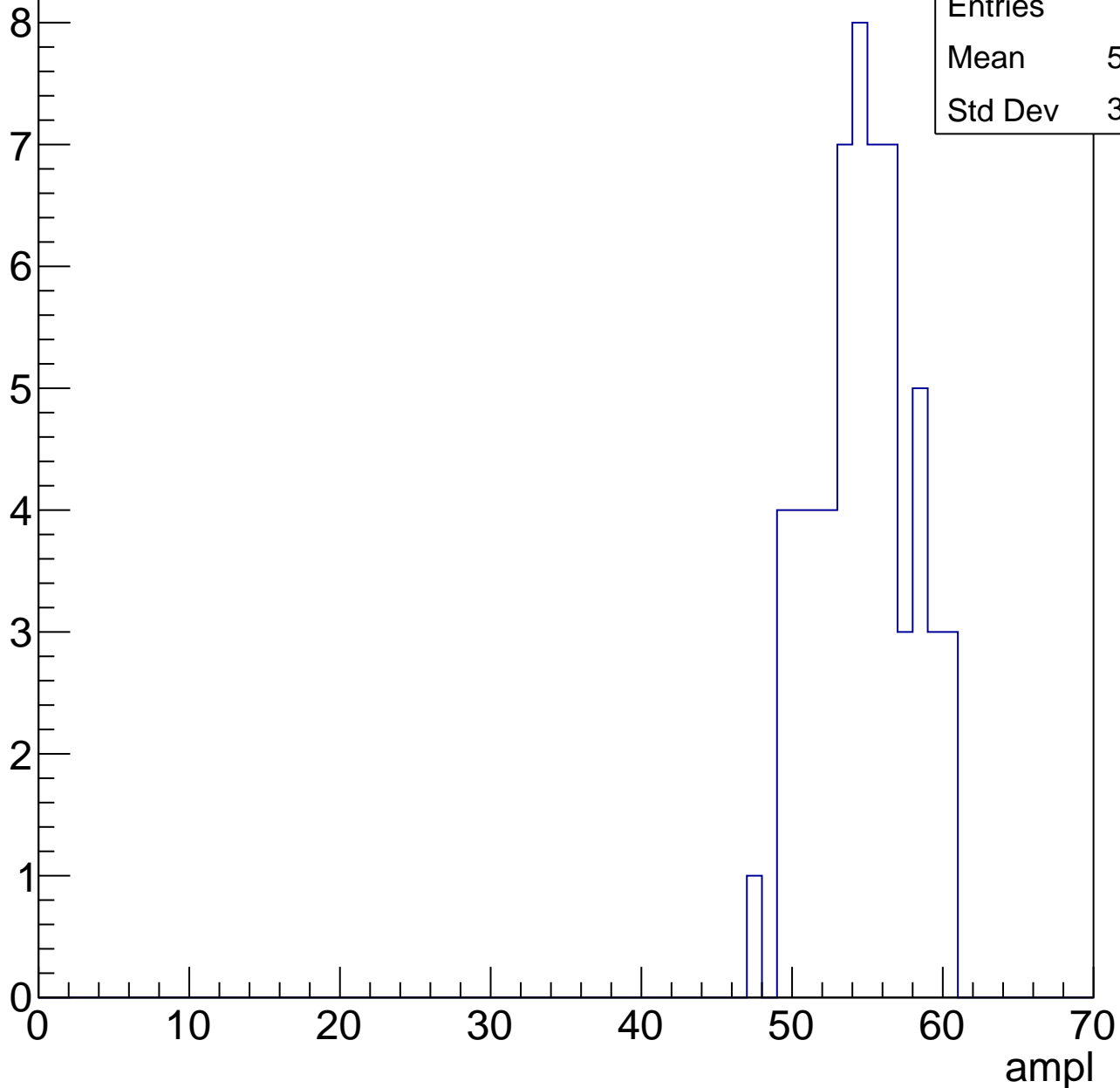


B0L002S, U18-ch72, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	54.22
Std Dev	3.147

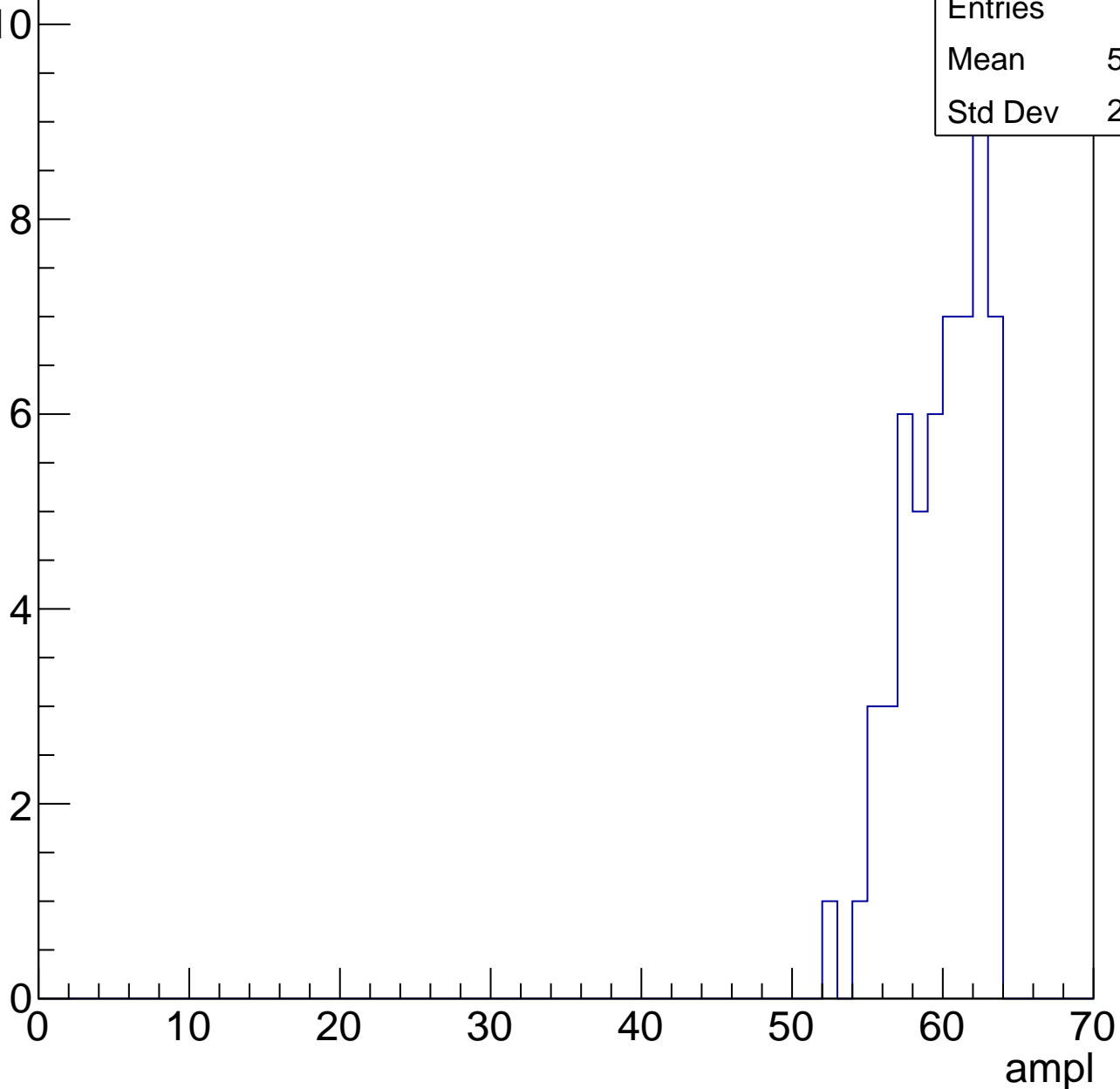


B0L002S, U18-ch72, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

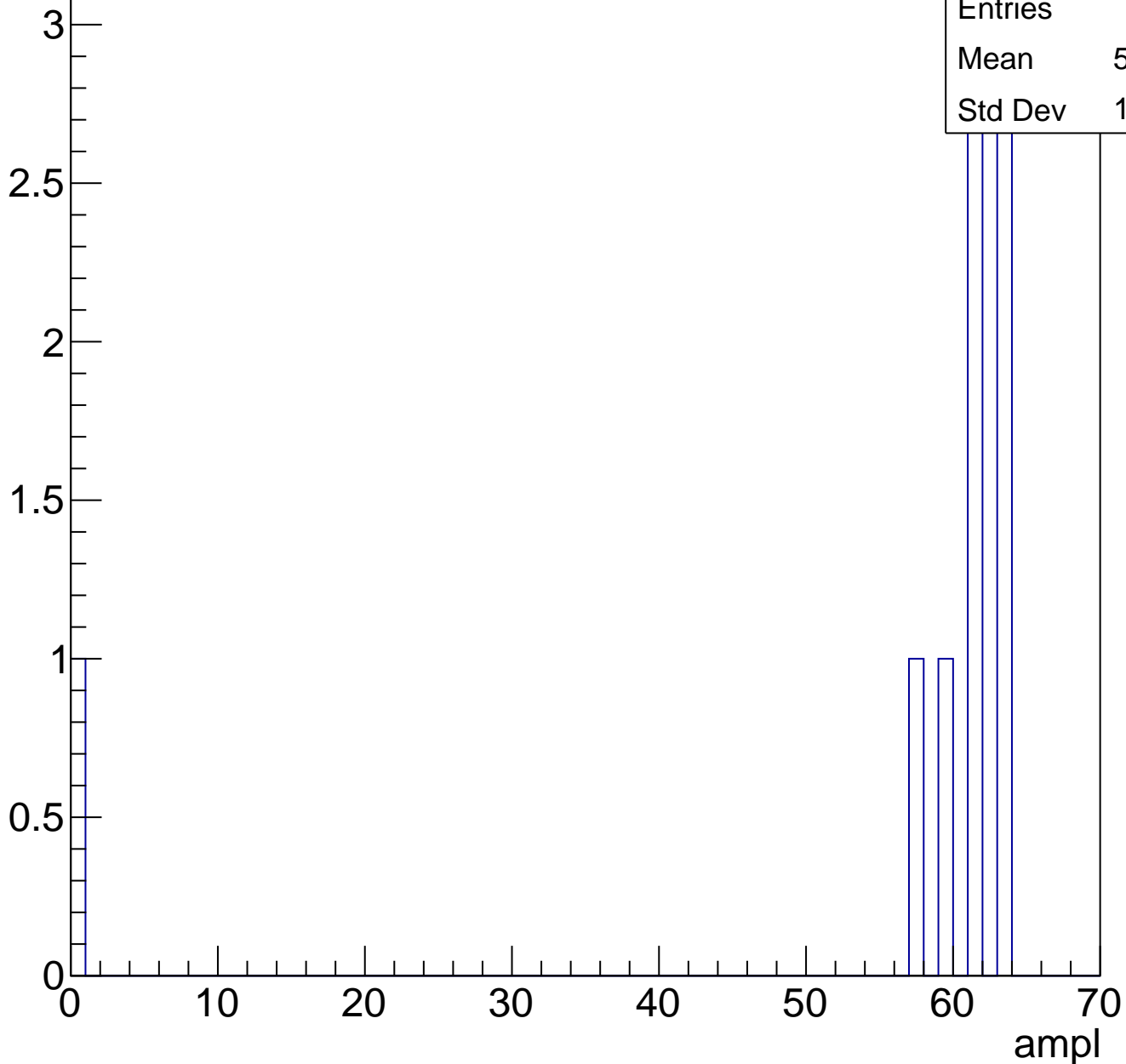
Entries	56
Mean	59.52
Std Dev	2.673



B0L002S, U18-ch72, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch72, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch73, adc0

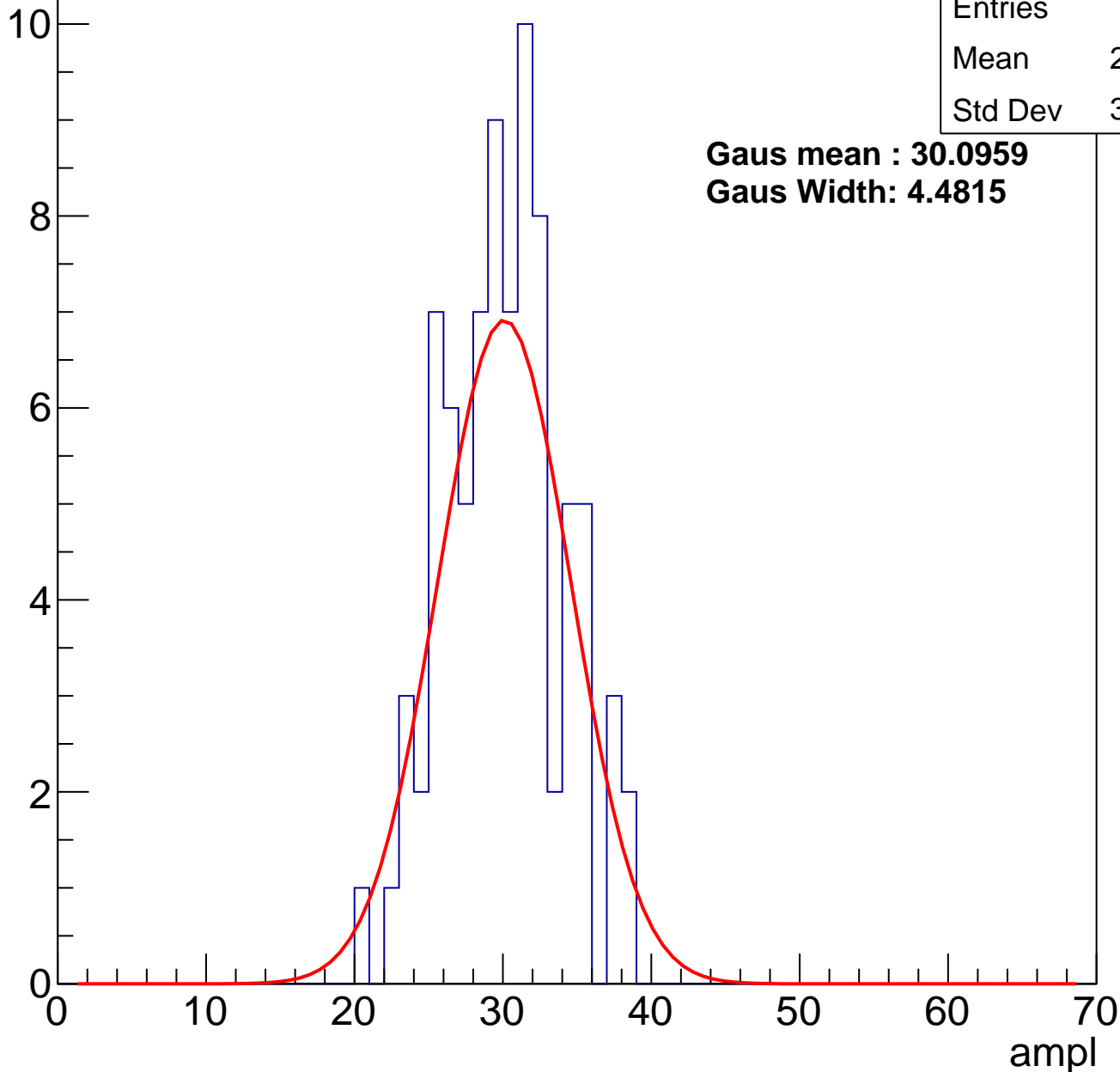
calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	29.59
Std Dev	3.899

Gaus mean : 30.0959

Gaus Width: 4.4815

Entry



B0L002S, U18-ch73, adc1

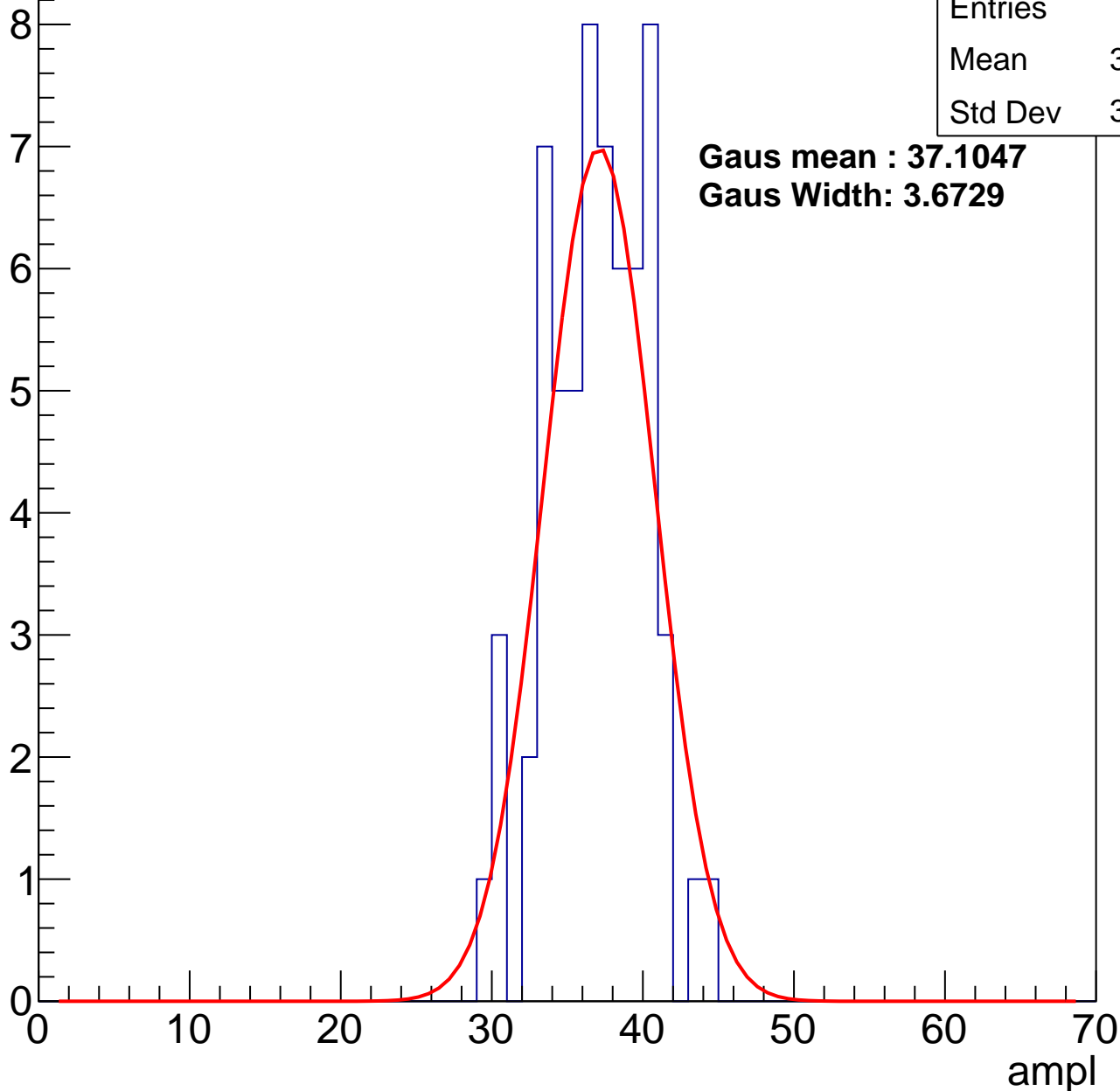
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	36.48
Std Dev	3.246

Gaus mean : 37.1047

Gaus Width: 3.6729



B0L002S, U18-ch73, adc2

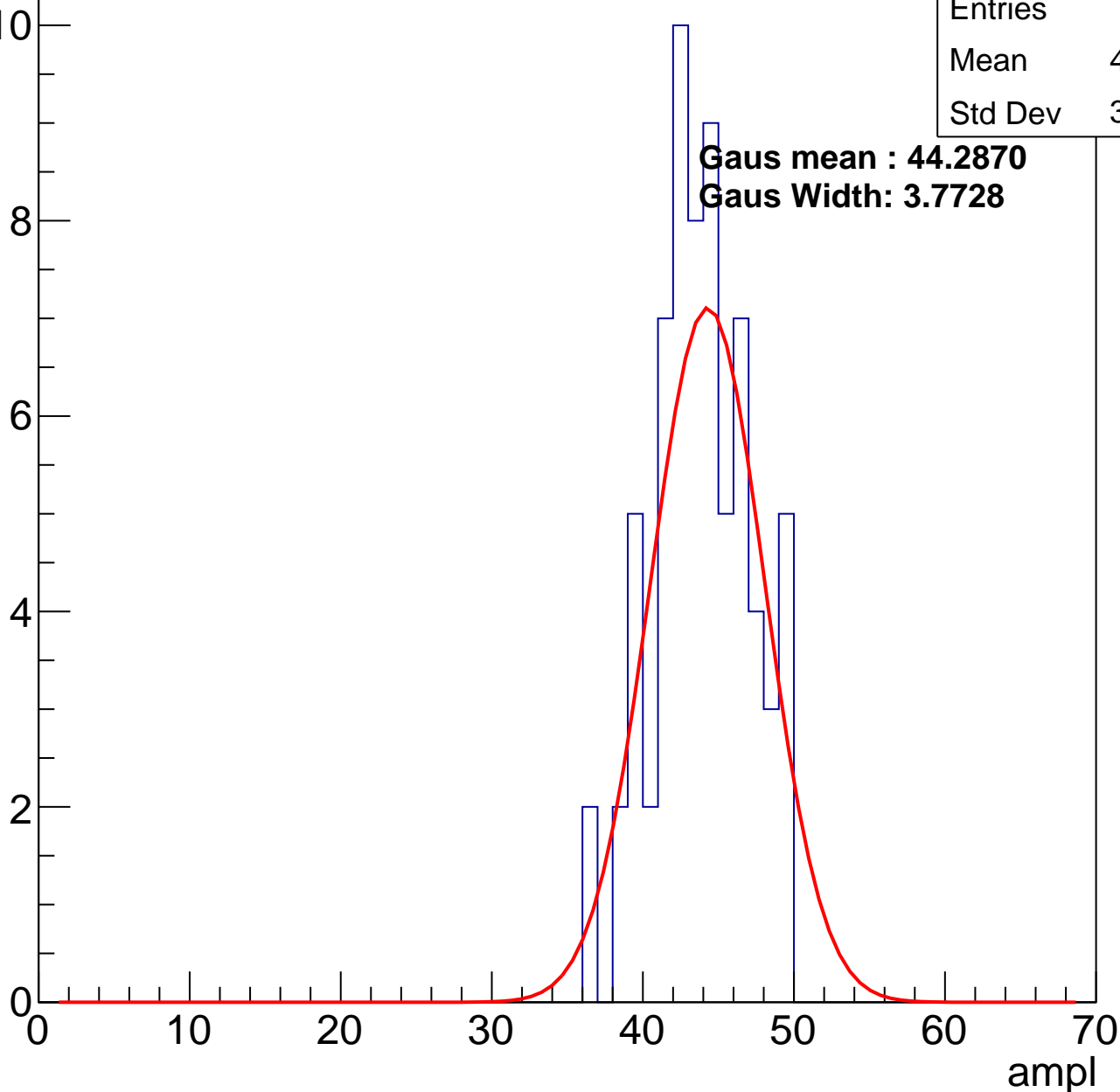
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	43.39
Std Dev	3.149

Gaus mean : 44.2870

Gaus Width: 3.7728

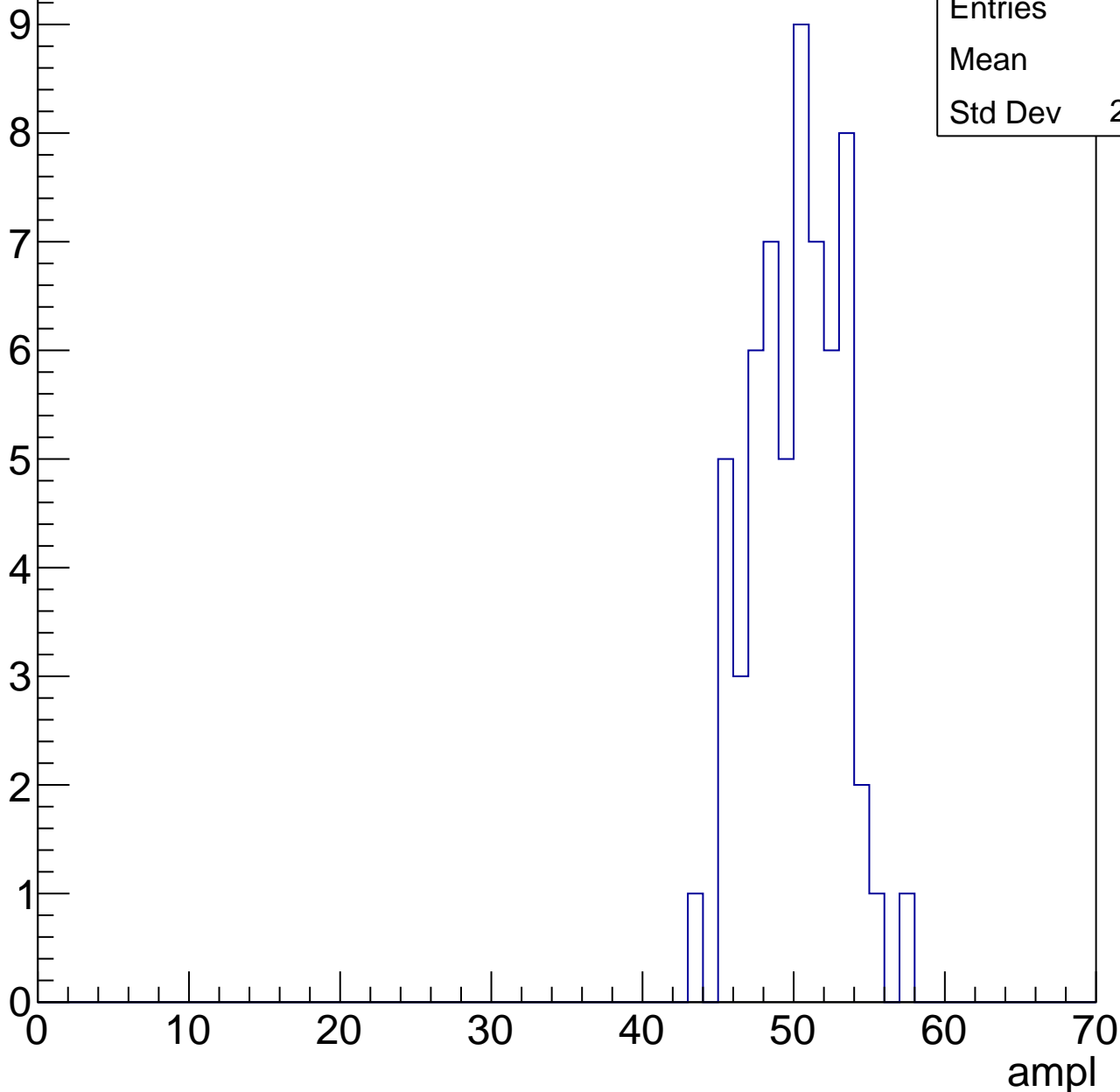


B0L002S, U18-ch73, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.7
Std Dev	2.899

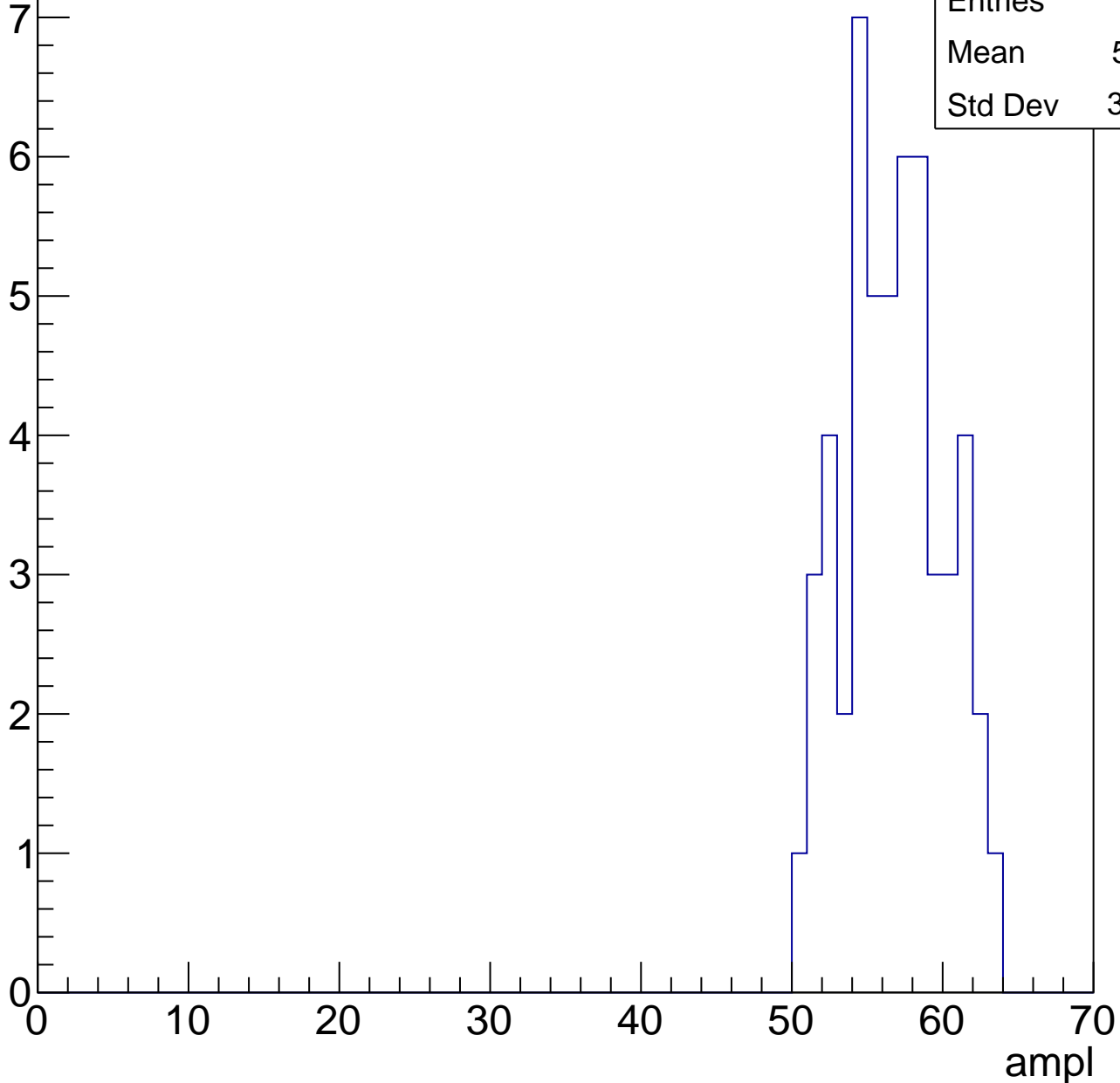


B0L002S, U18-ch73, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.31
Std Dev	3.244

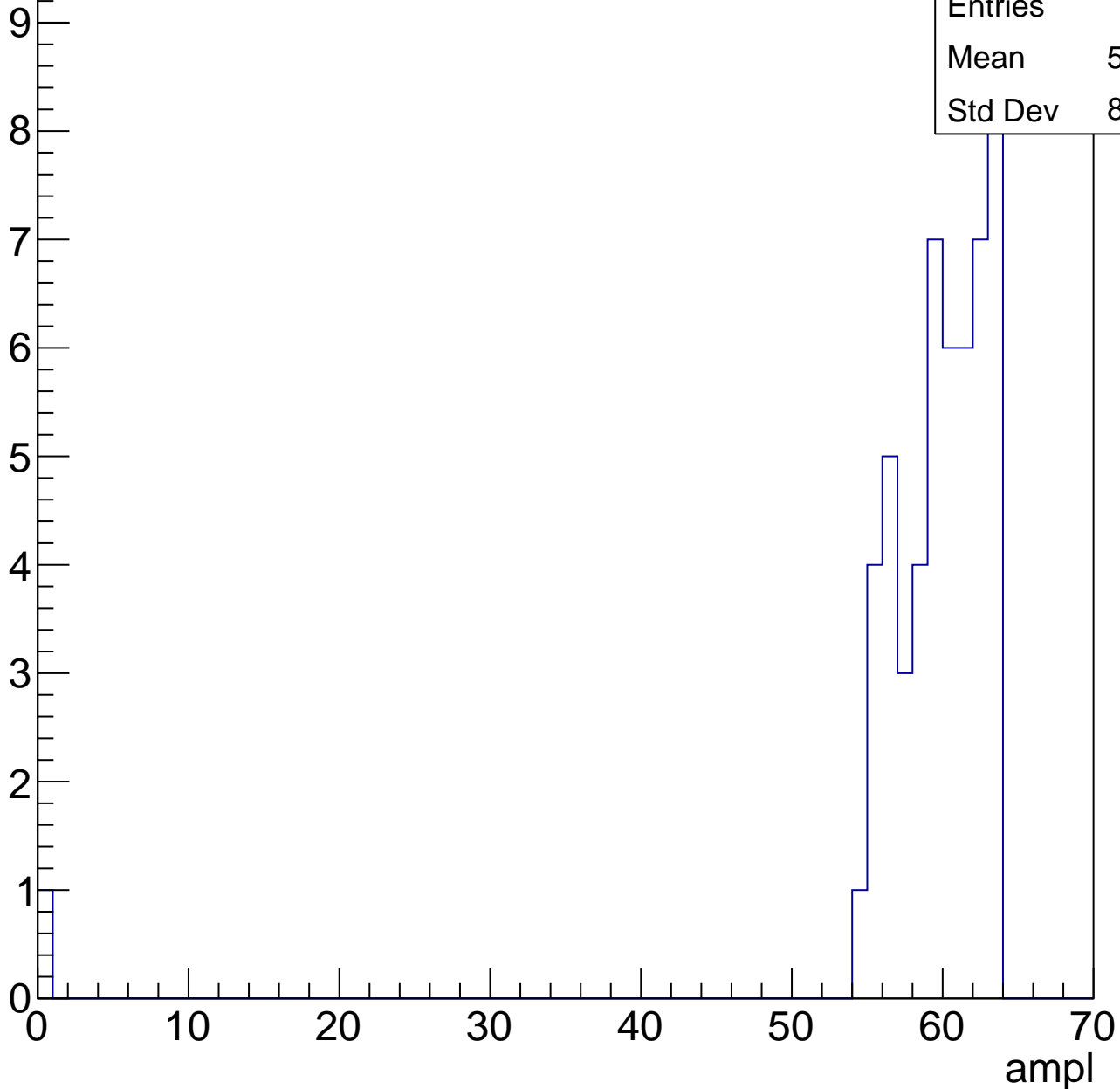


B0L002S, U18-ch73, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	58.43
Std Dev	8.524



B0L002S, U18-ch73, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch73, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch74, adc0

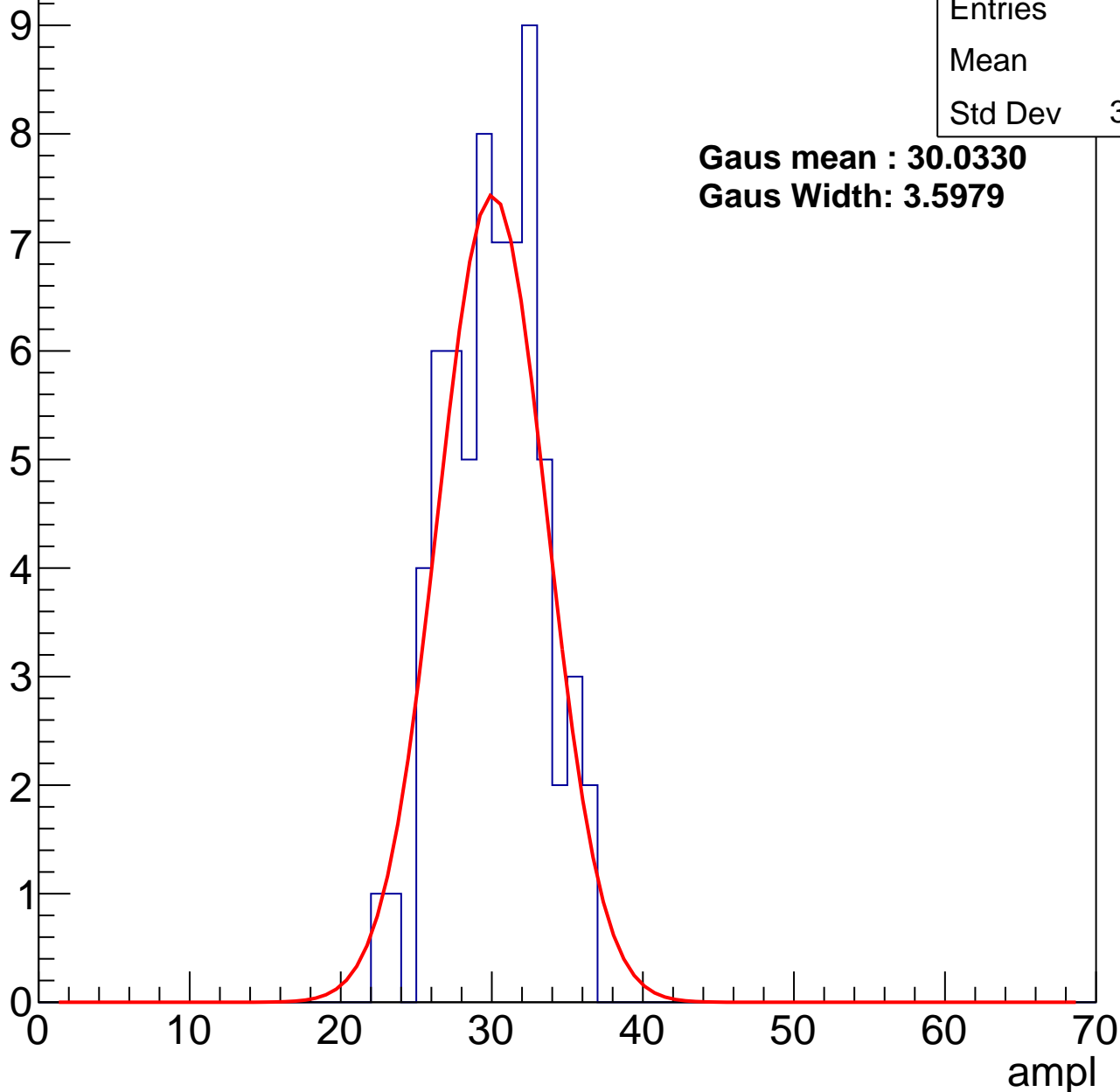
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	29.7
Std Dev	3.143

Gaus mean : 30.0330

Gaus Width: 3.5979



B0L002S, U18-ch74, adc1

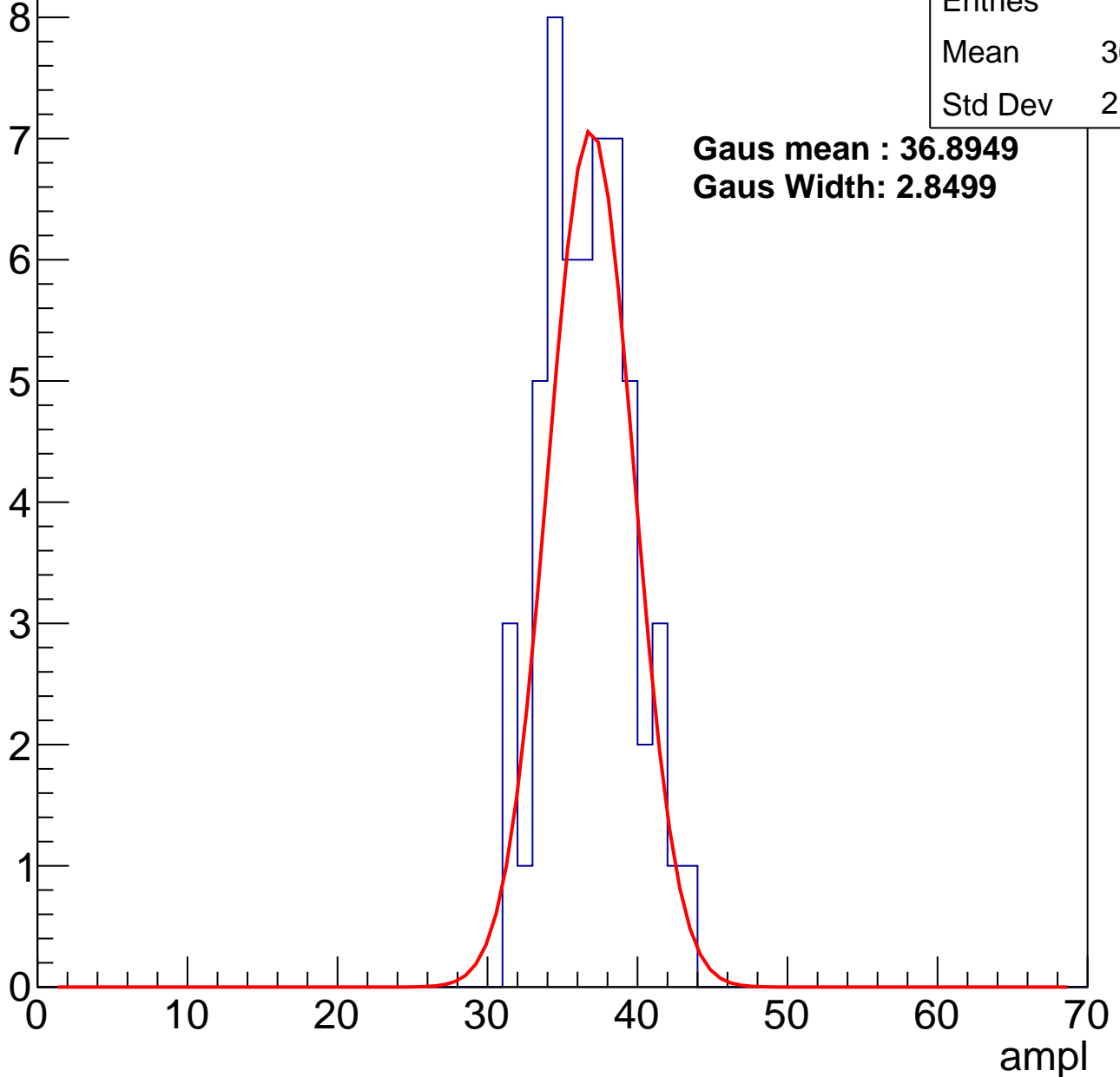
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	36.29
Std Dev	2.833

Gaus mean : 36.8949

Gaus Width: 2.8499



B0L002S, U18-ch74, adc2

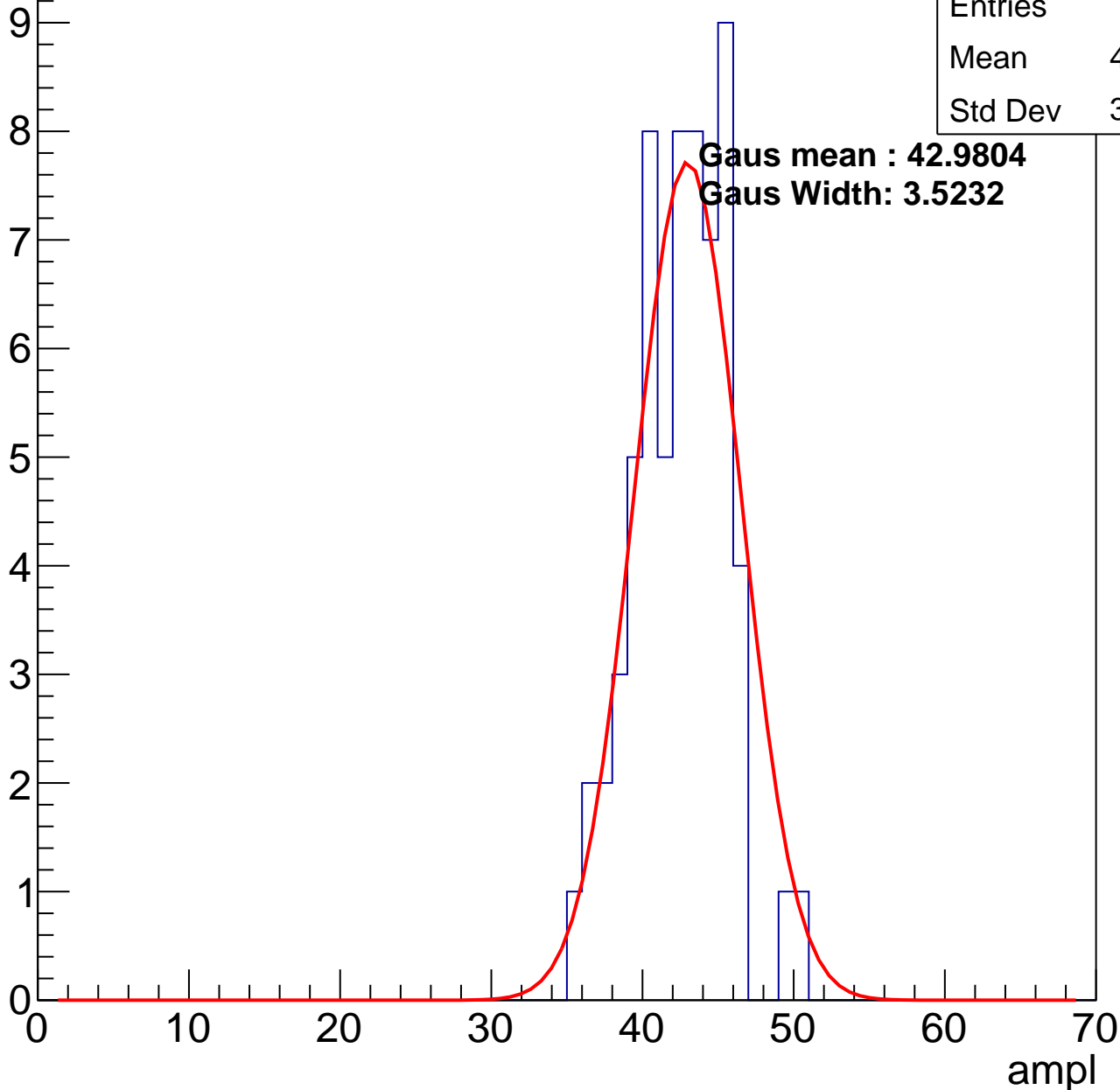
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.05
Std Dev	3.049

Gaus mean : 42.9804

Gaus Width: 3.5232



B0L002S, U18-ch74, adc3

calib_packv5_042523_0143.root, FC#8, port C1

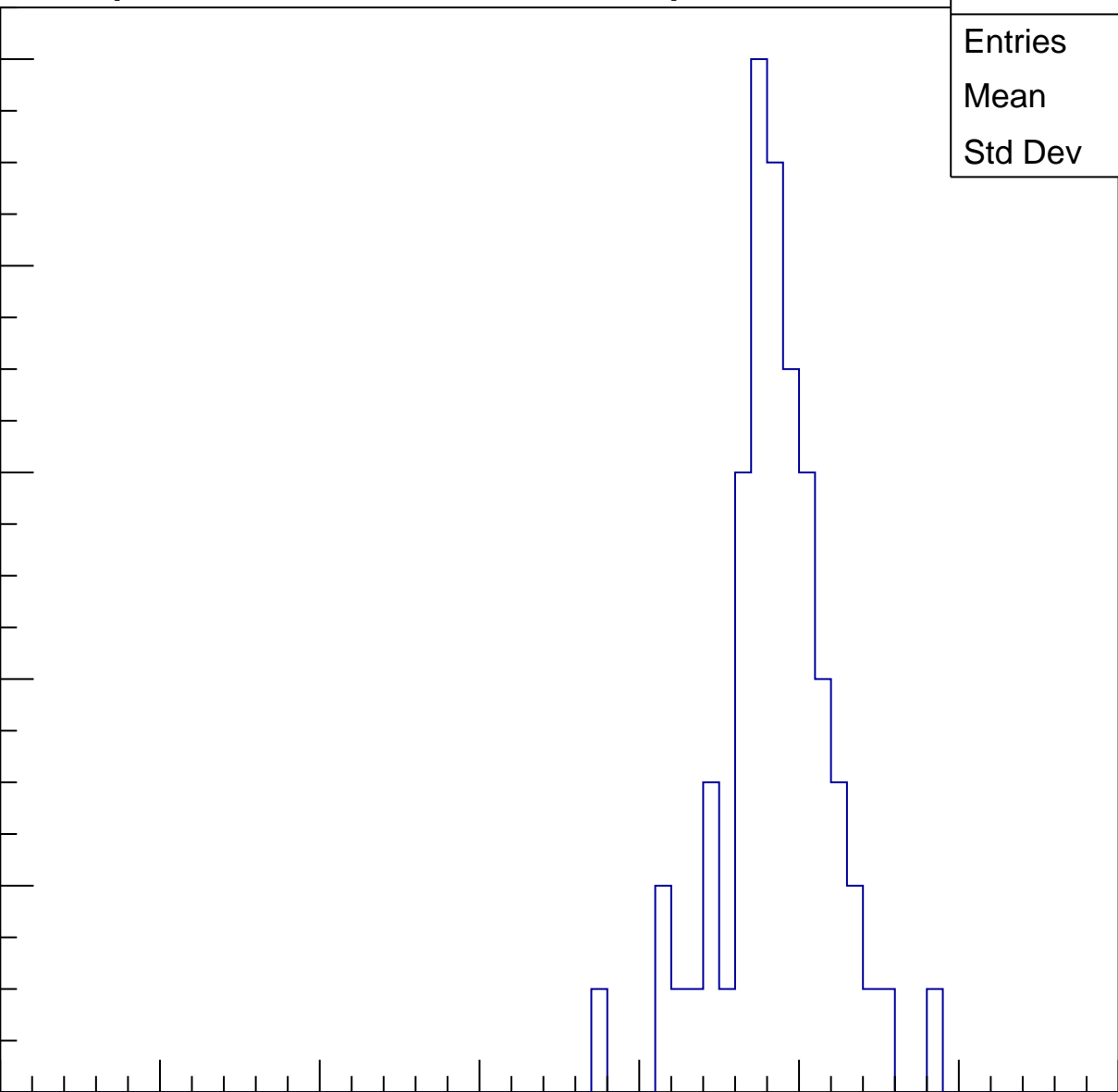
Entries	59
Mean	48.05
Std Dev	3.495

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch74, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	54.89
Std Dev	3.012

Entry

10

8

6

4

2

0

0

10

20

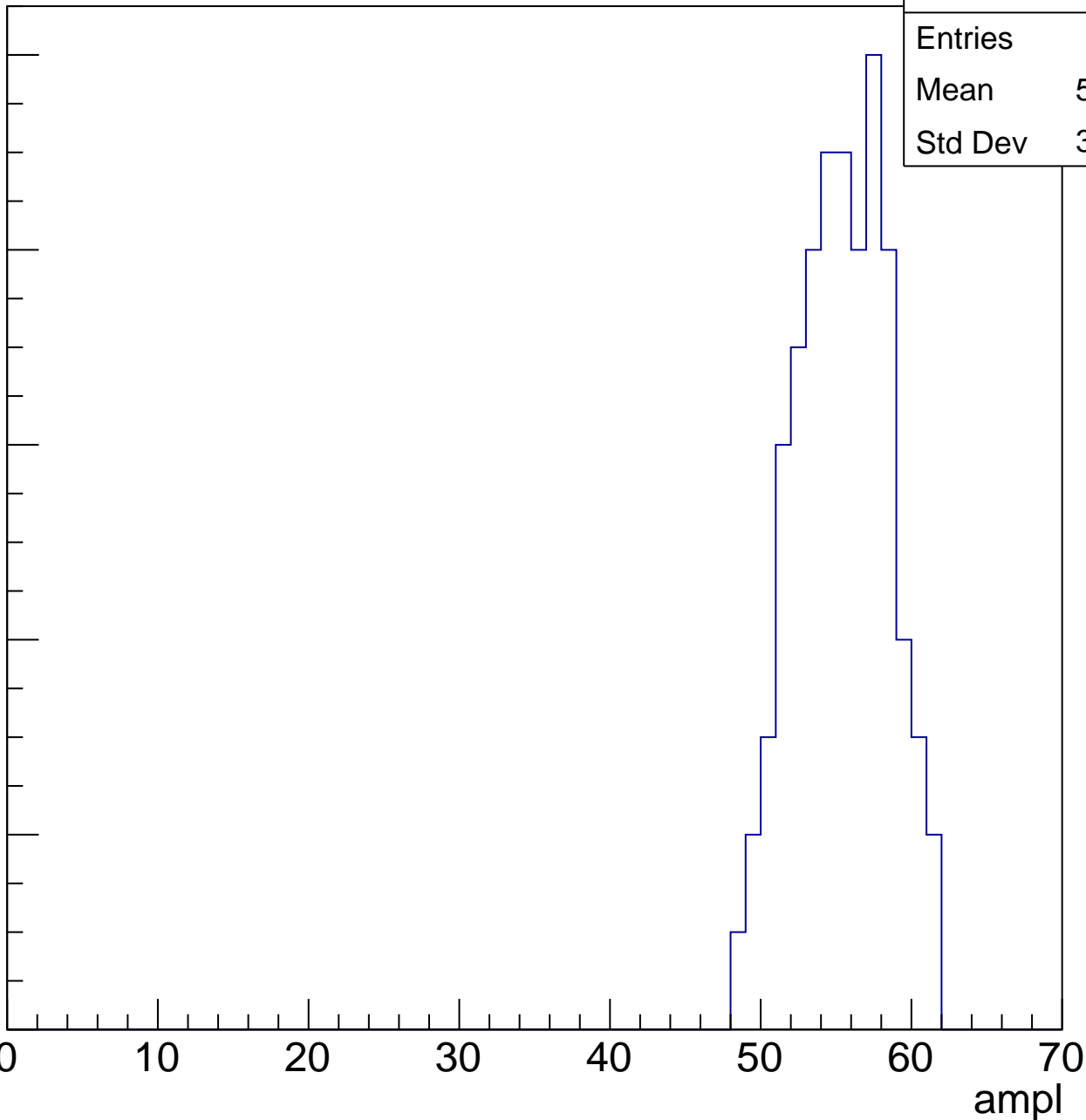
30

40

50

60

ampl

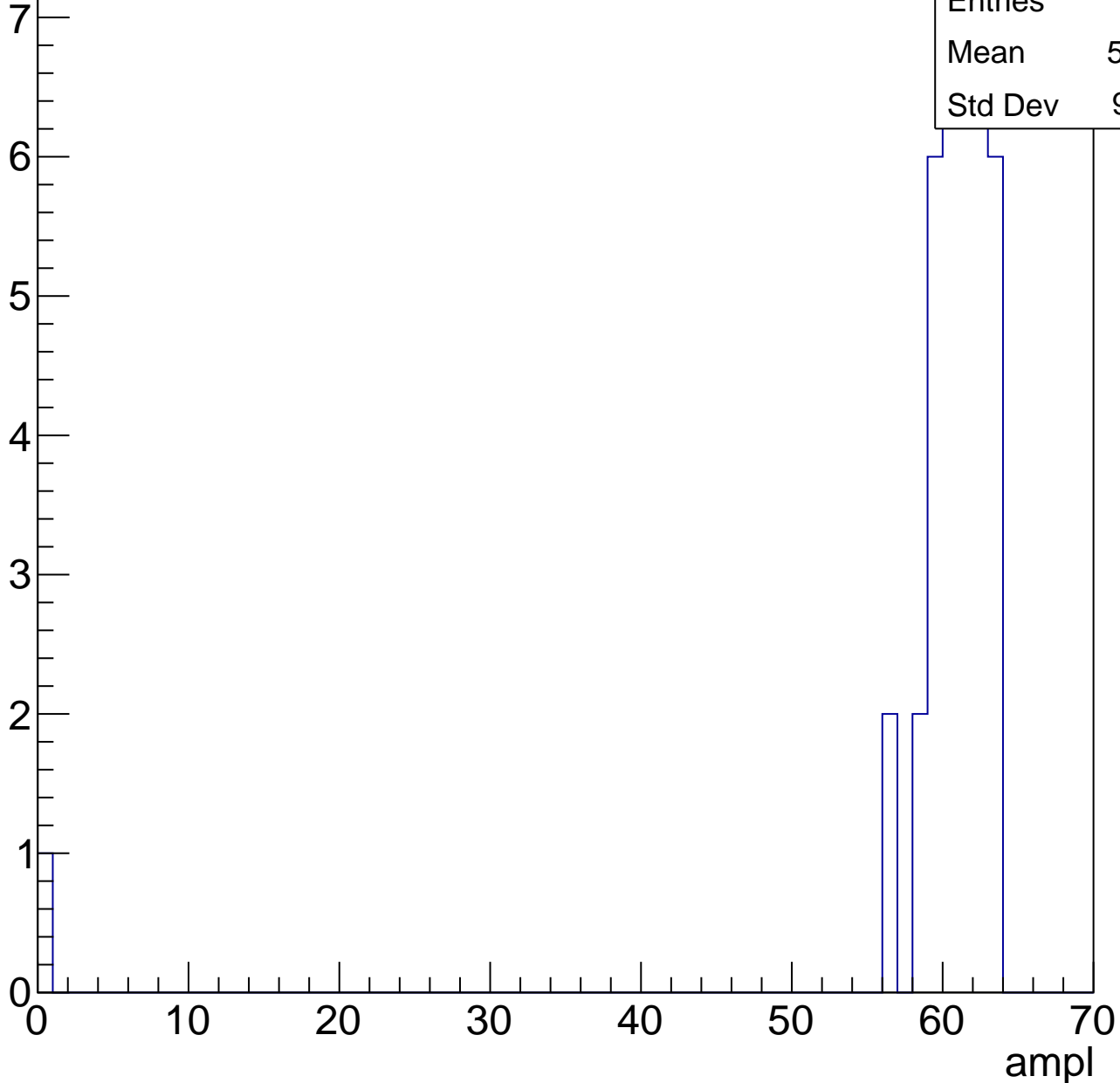


B0L002S, U18-ch74, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.97
Std Dev	9.861



B0L002S, U18-ch74, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch74, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch75, adc0

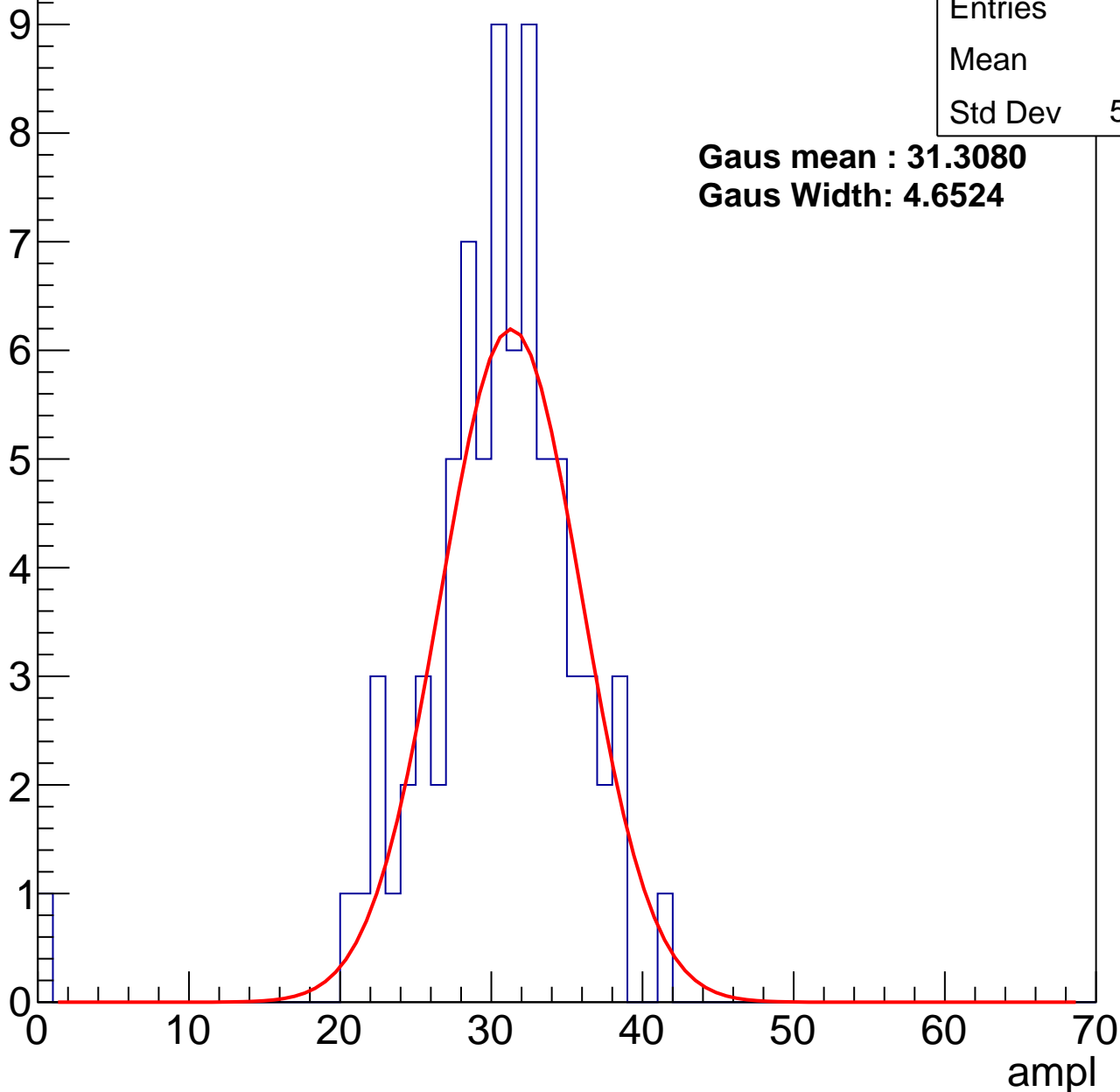
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	29.9
Std Dev	5.512

Gaus mean : 31.3080

Gaus Width: 4.6524



B0L002S, U18-ch75, adc1

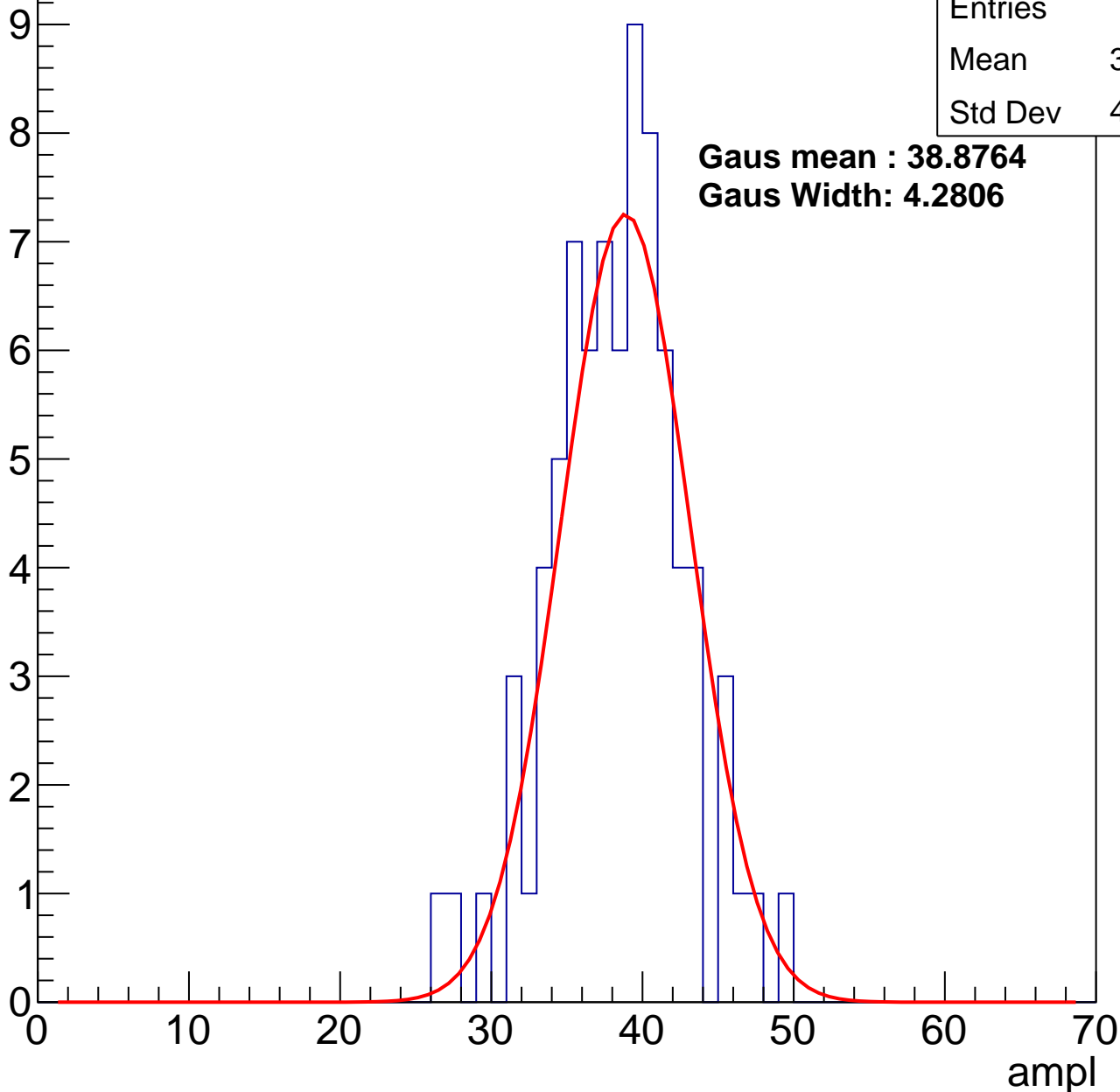
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	37.86
Std Dev	4.319

Gaus mean : 38.8764

Gaus Width: 4.2806

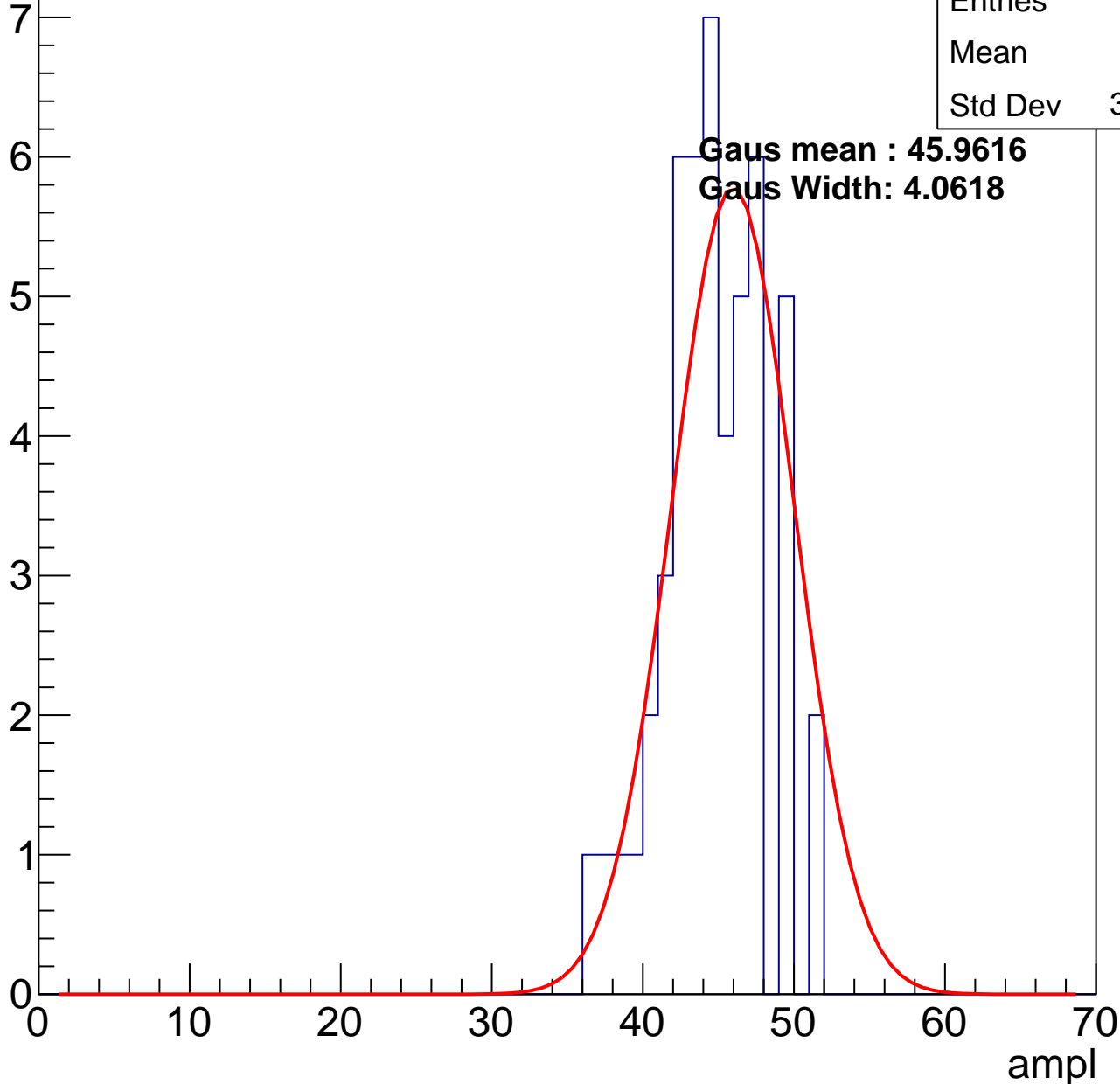


B0L002S, U18-ch75, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	44.2
Std Dev	3.353

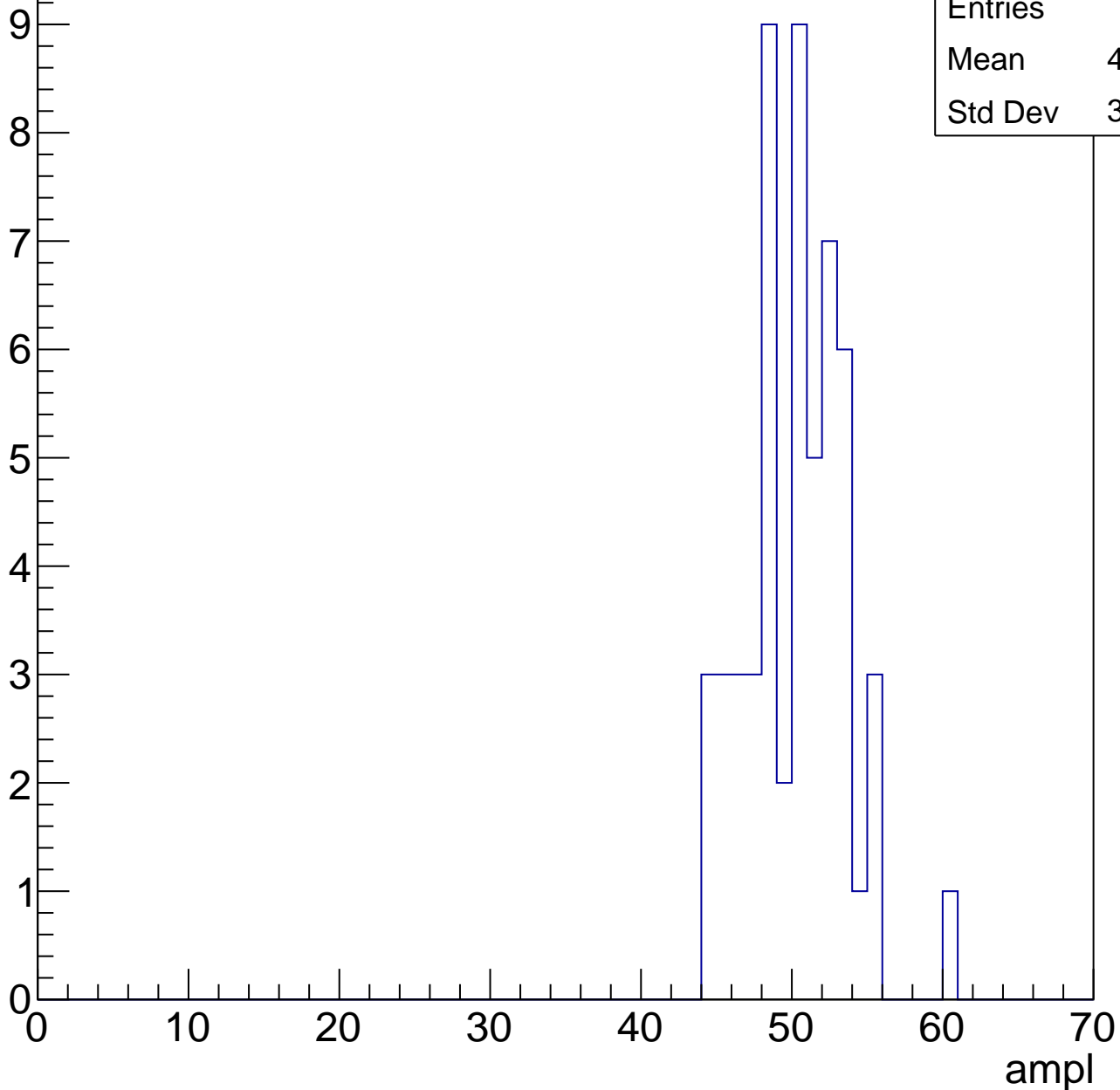


B0L002S, U18-ch75, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	49.85
Std Dev	3.227

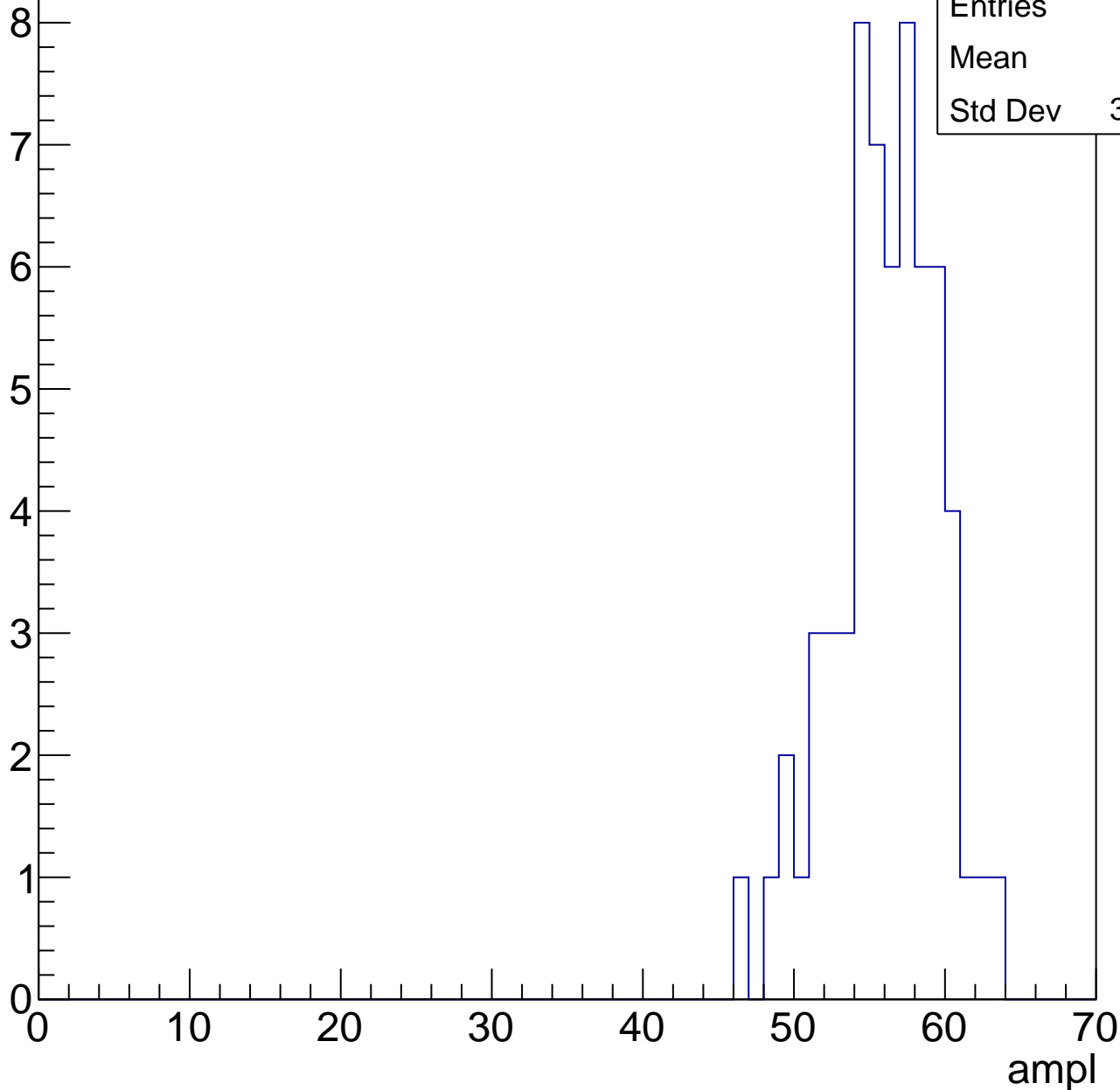


B0L002S, U18-ch75, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	55.6
Std Dev	3.448

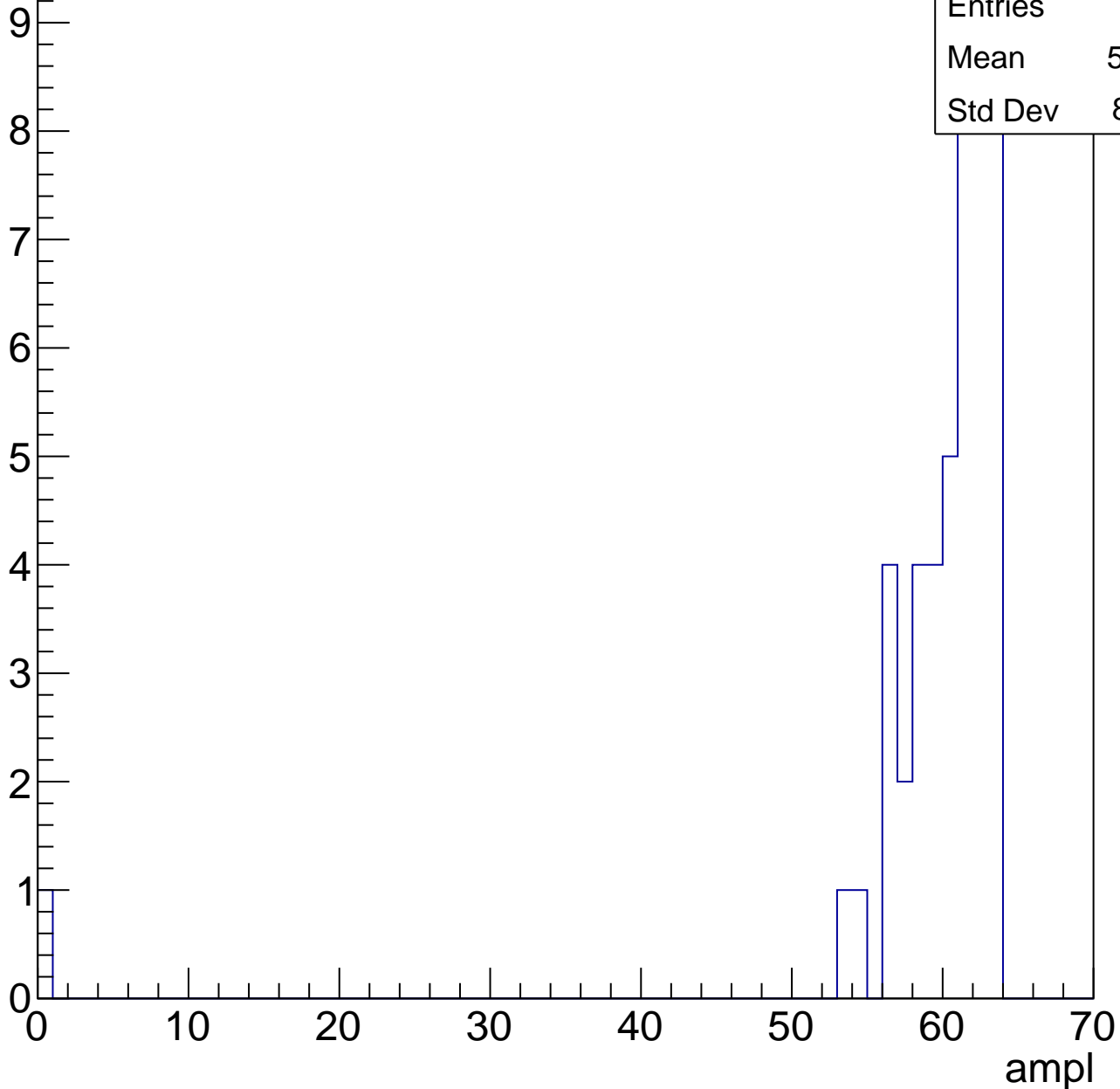


B0L002S, U18-ch75, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	58.92
Std Dev	8.871



B0L002S, U18-ch75, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch75, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch76, adc0

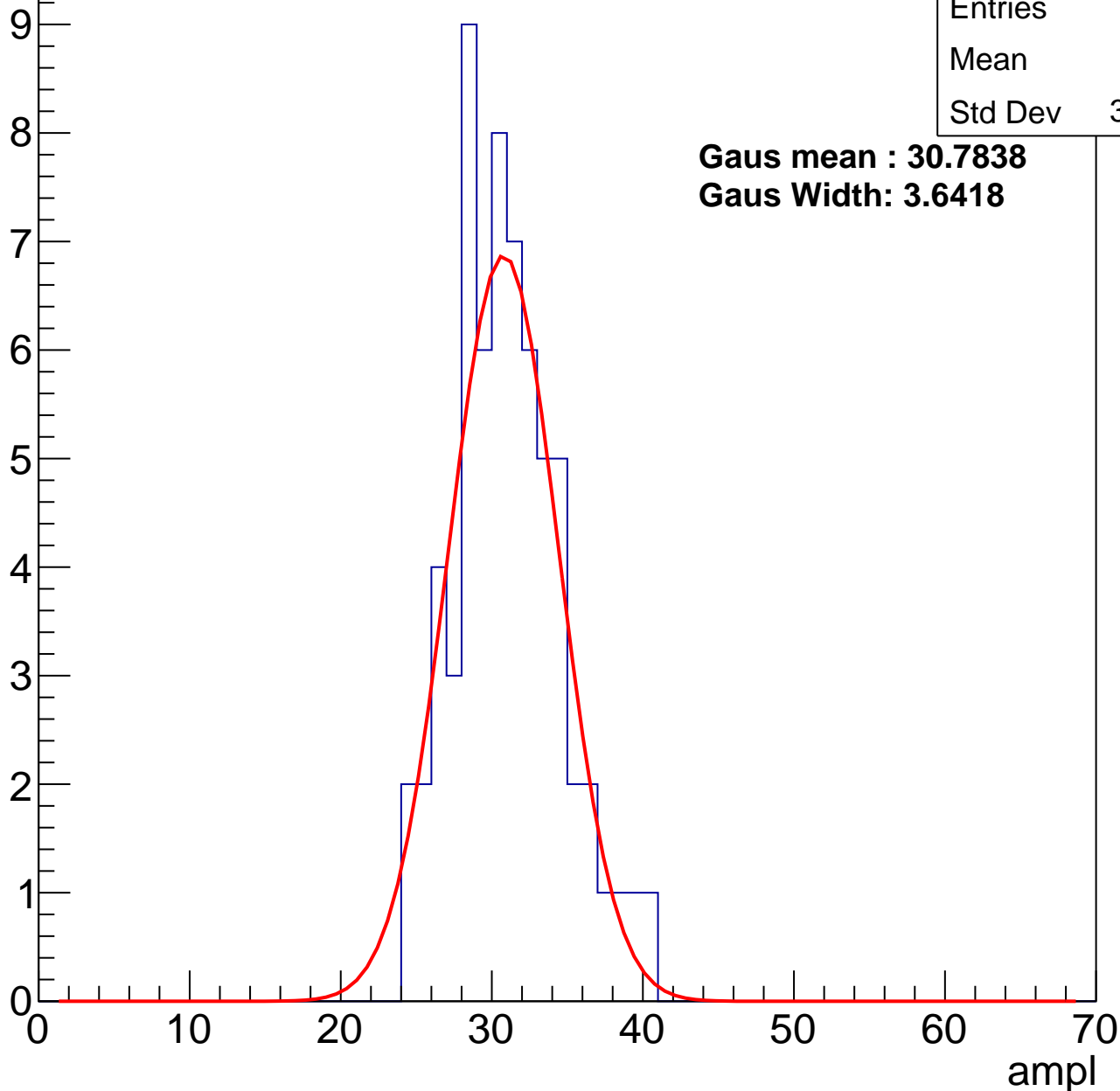
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.6
Std Dev	3.516

Gaus mean : 30.7838

Gaus Width: 3.6418



B0L002S, U18-ch76, adc1

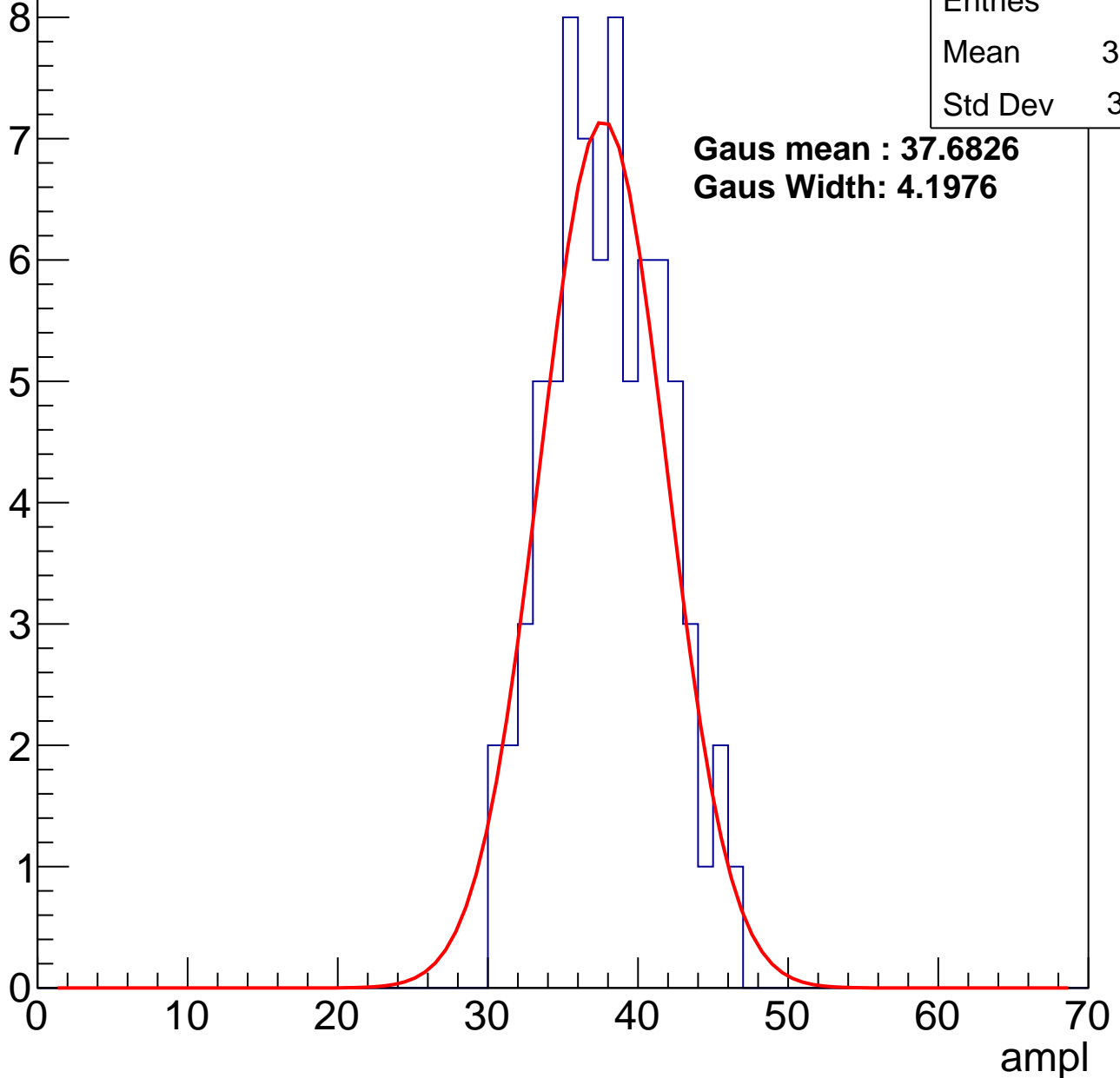
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	37.48
Std Dev	3.761

Gaus mean : 37.6826

Gaus Width: 4.1976



B0L002S, U18-ch76, adc2

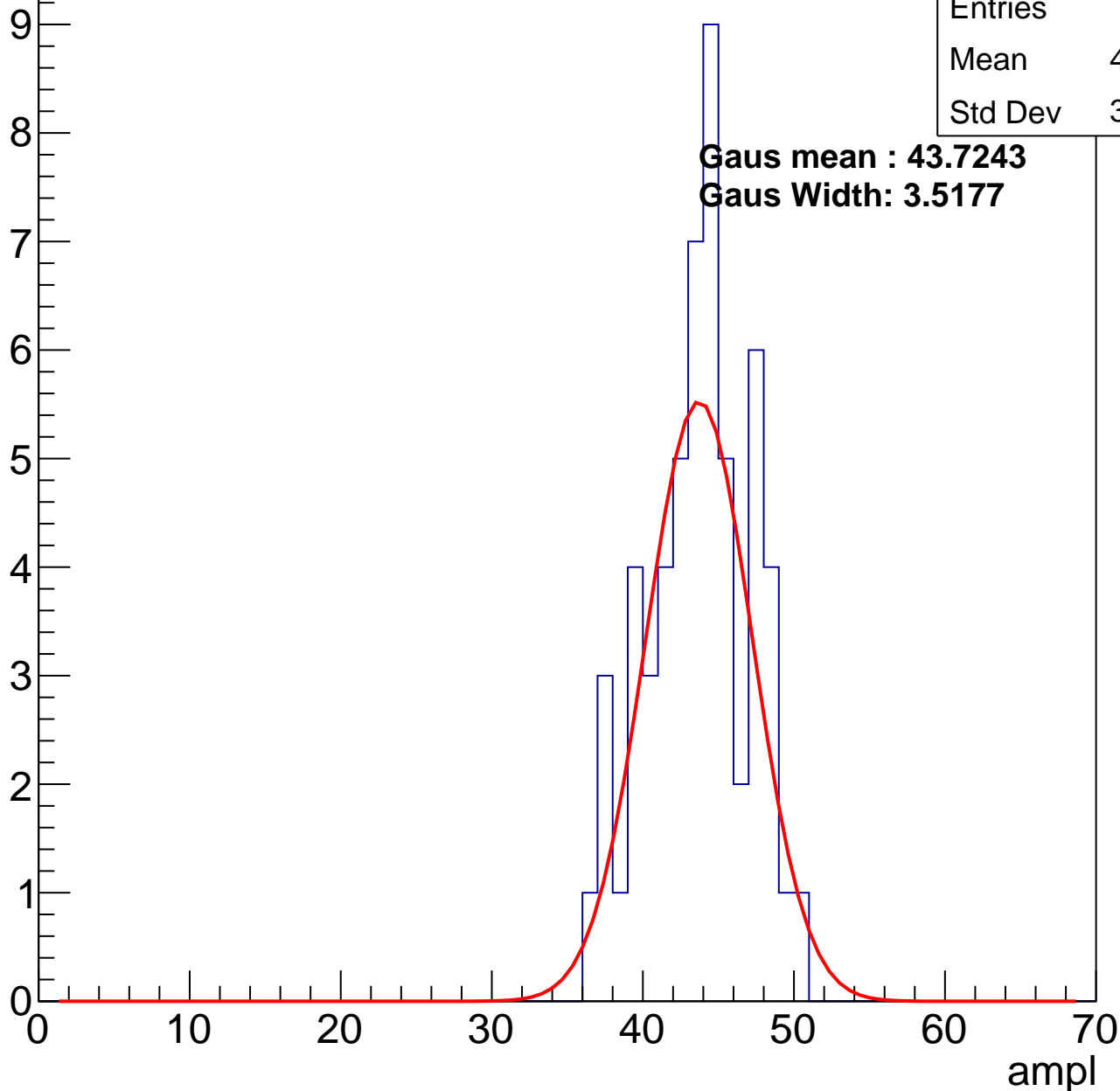
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	43.25
Std Dev	3.323

Gaus mean : 43.7243

Gaus Width: 3.5177

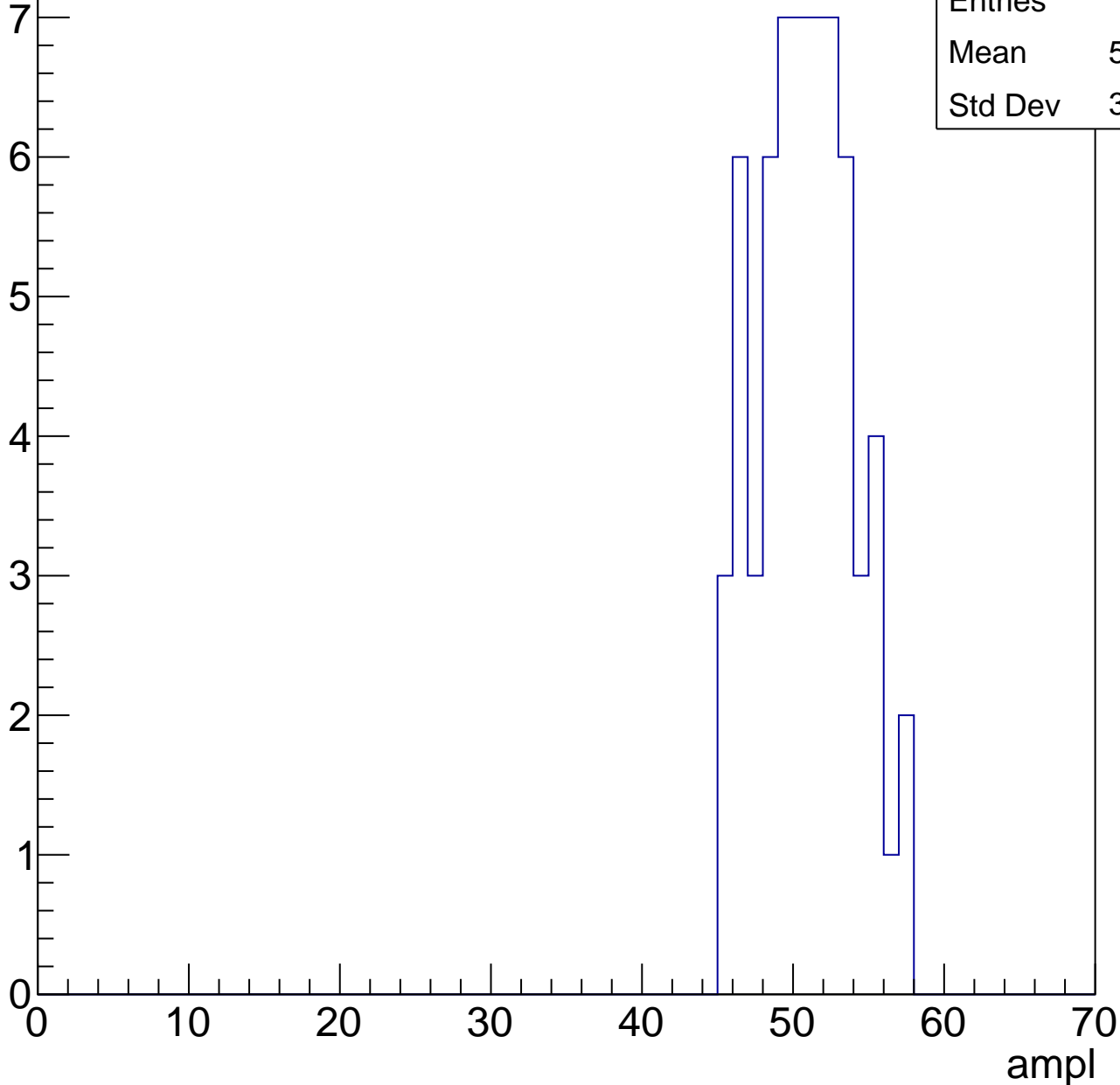


B0L002S, U18-ch76, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	50.39
Std Dev	3.087

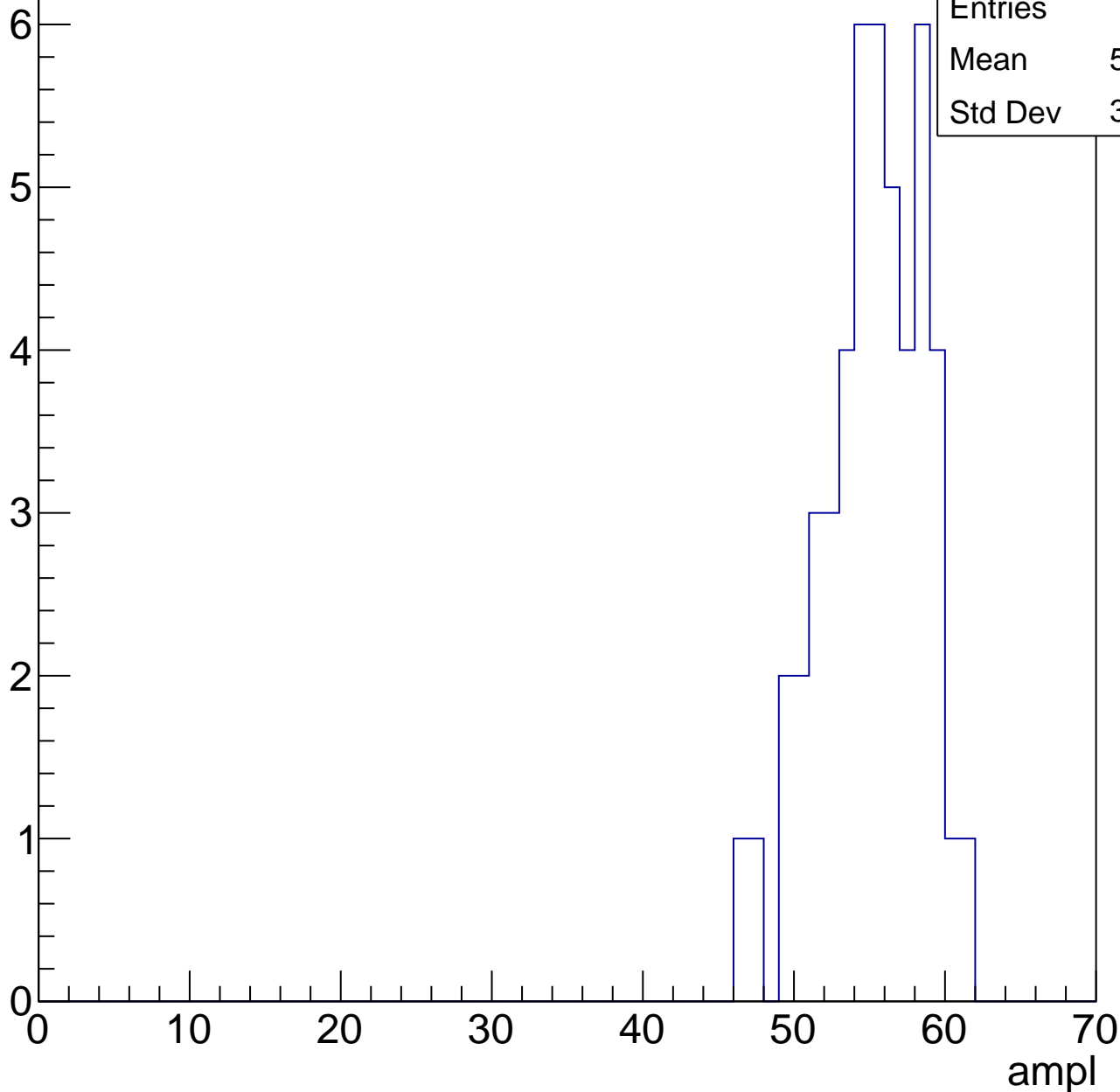


B0L002S, U18-ch76, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	54.67
Std Dev	3.377



B0L002S, U18-ch76, adc5

calib_packv5_042523_0143.root, FC#8, port C1

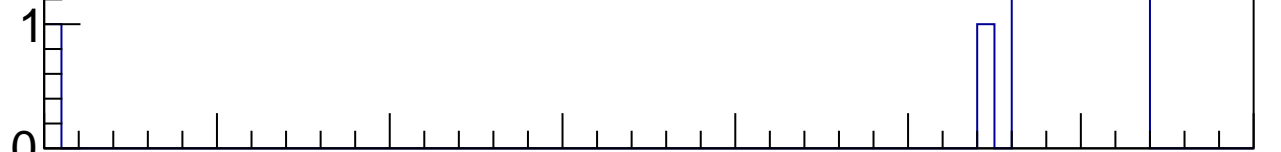
Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.98
Std Dev	8.418

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch76, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch76, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch77, adc0

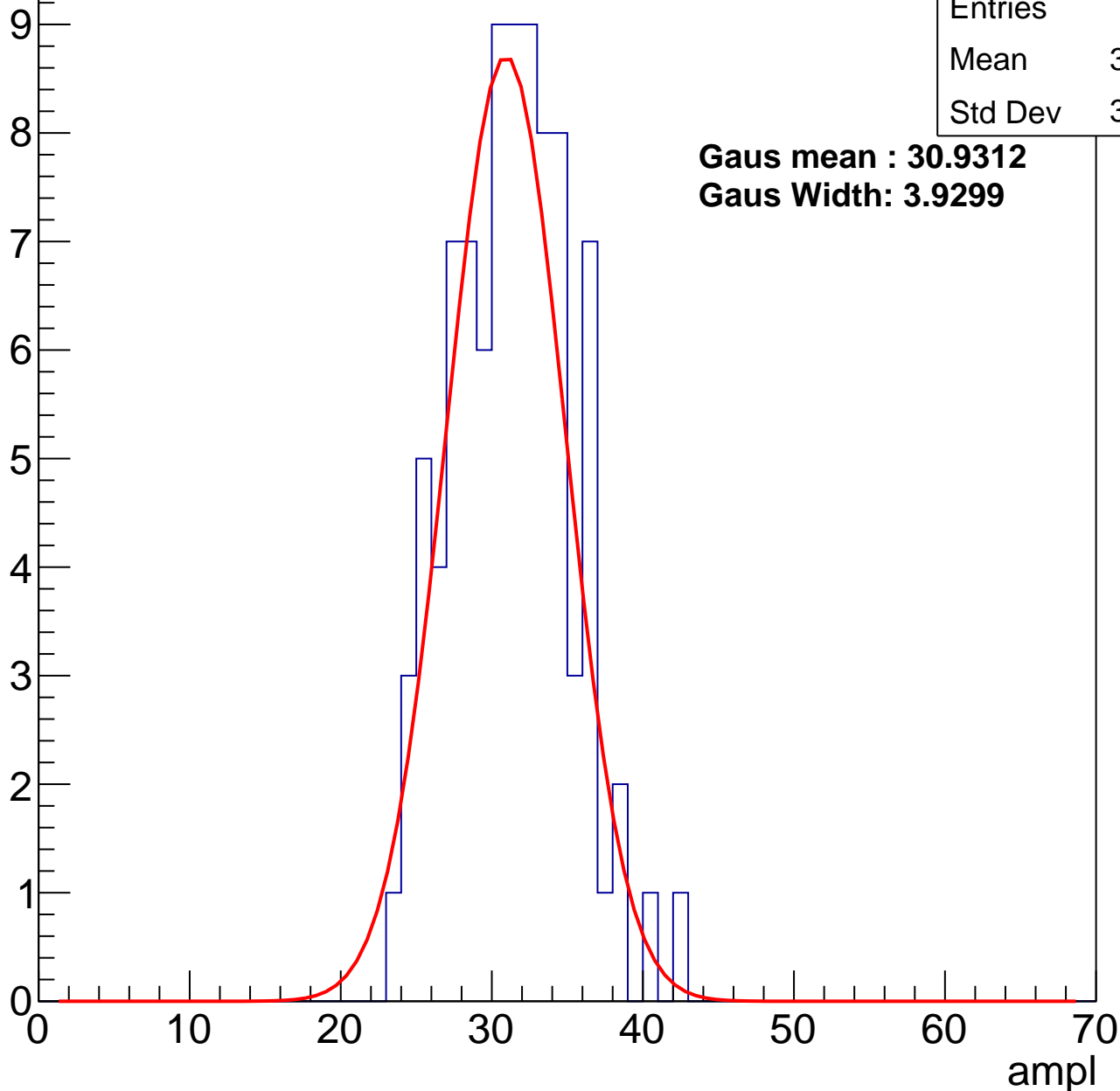
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	91
Mean	30.86
Std Dev	3.879

Gaus mean : 30.9312

Gaus Width: 3.9299



B0L002S, U18-ch77, adc1

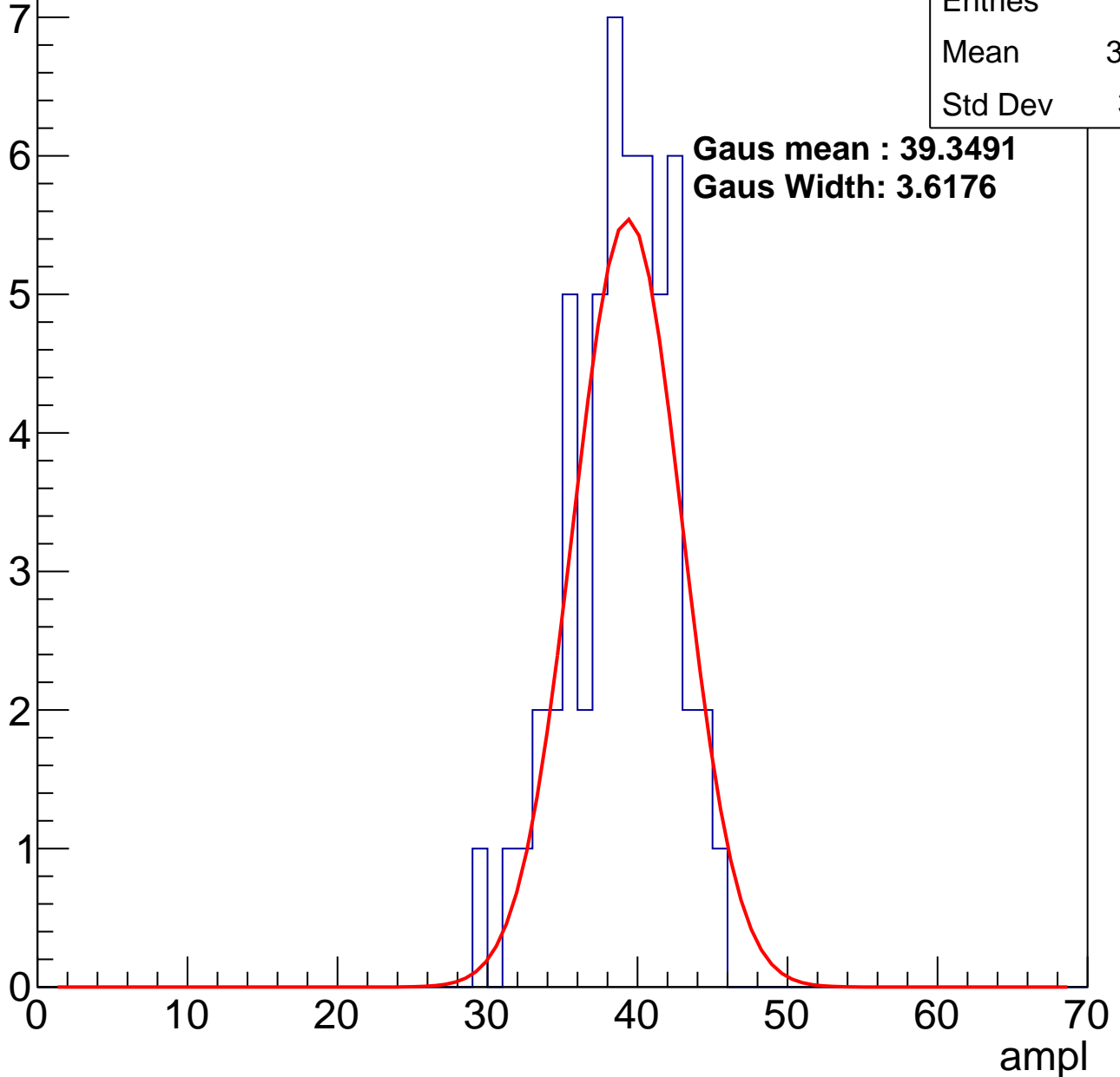
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	38.41
Std Dev	3.44

Gaus mean : 39.3491

Gaus Width: 3.6176



B0L002S, U18-ch77, adc2

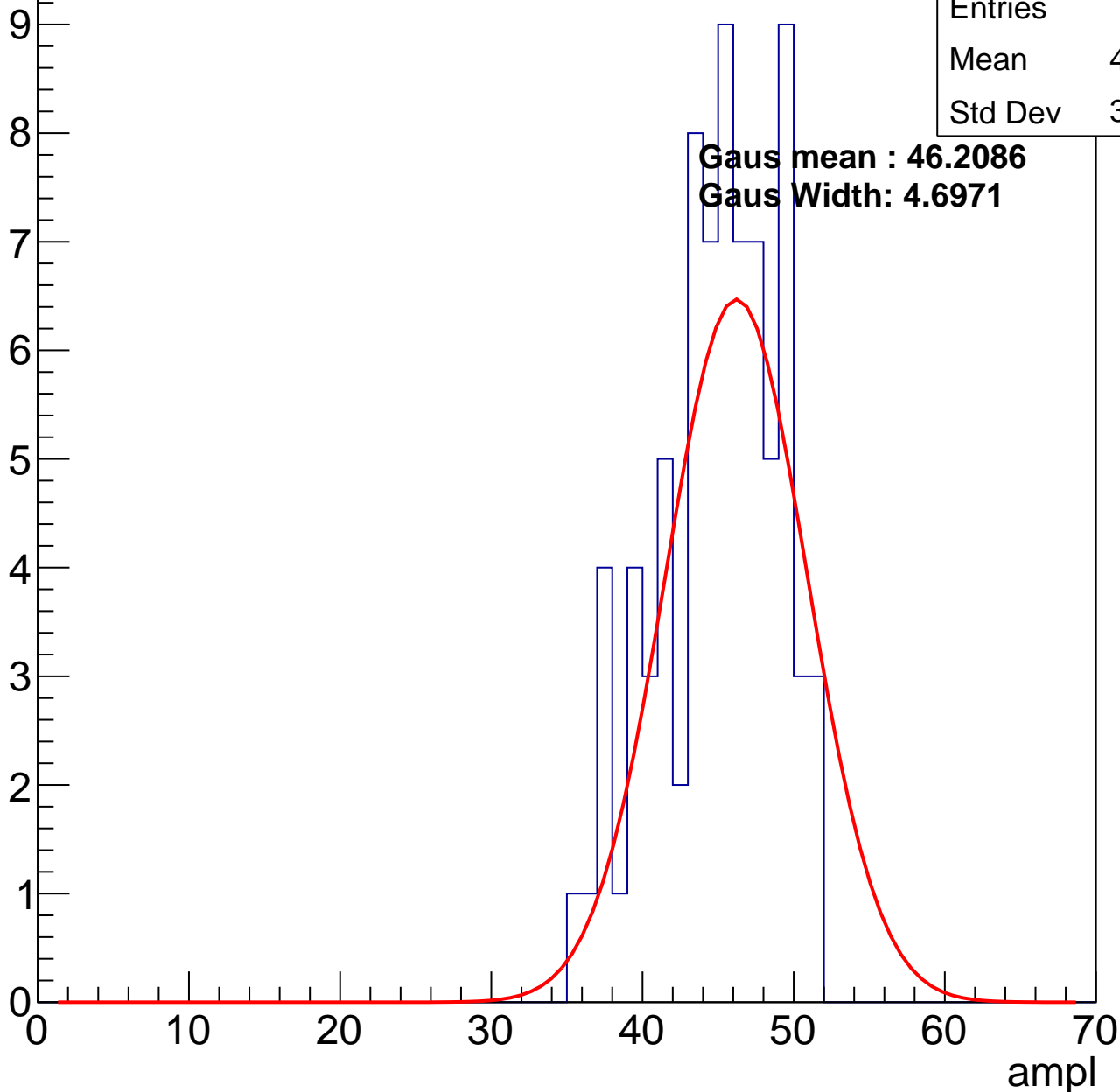
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	79
Mean	44.48
Std Dev	3.942

Gaus mean : 46.2086

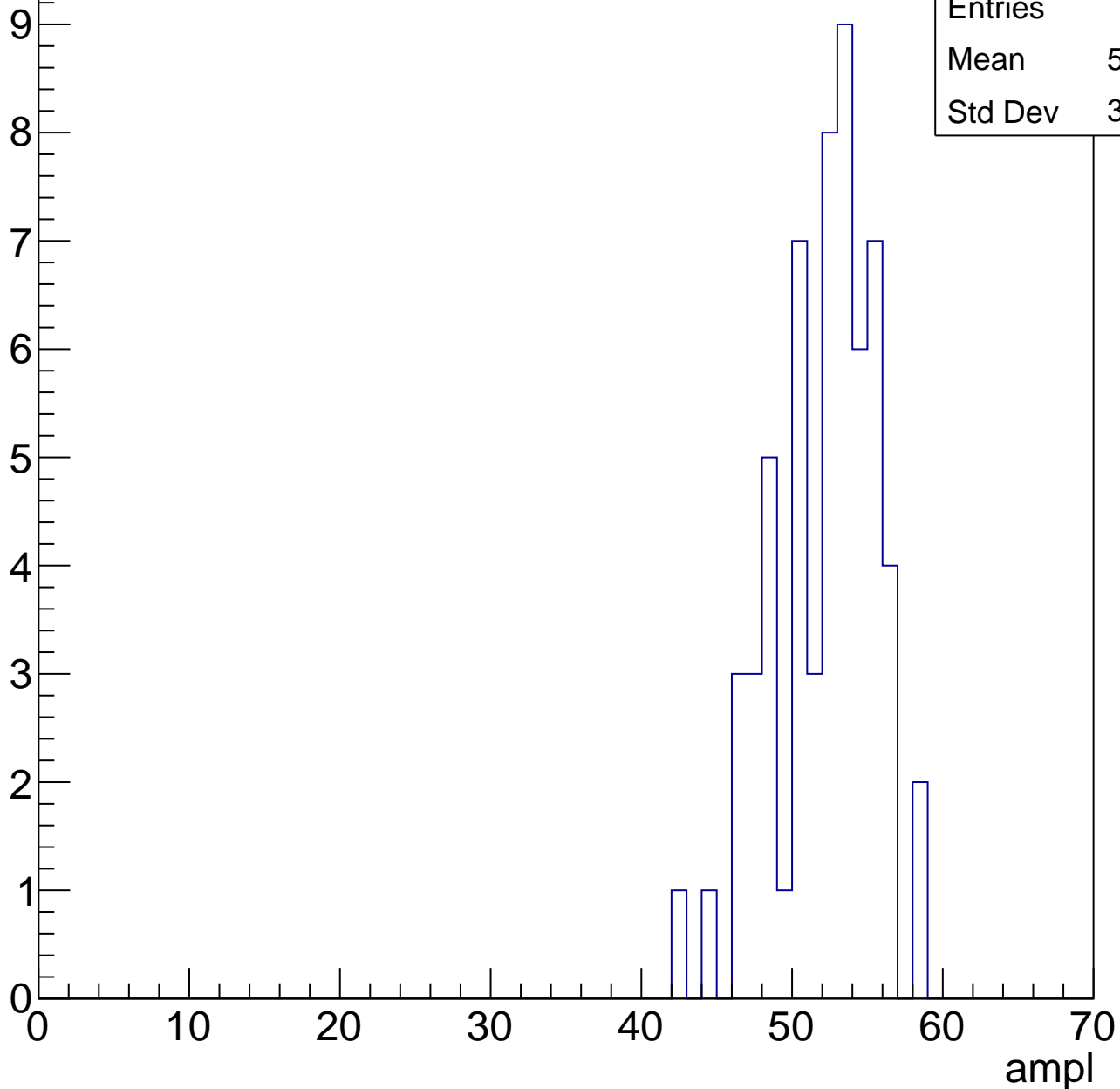
Gaus Width: 4.6971



B0L002S, U18-ch77, adc3

calib_packv5_042523_0143.root, FC#8, port C1

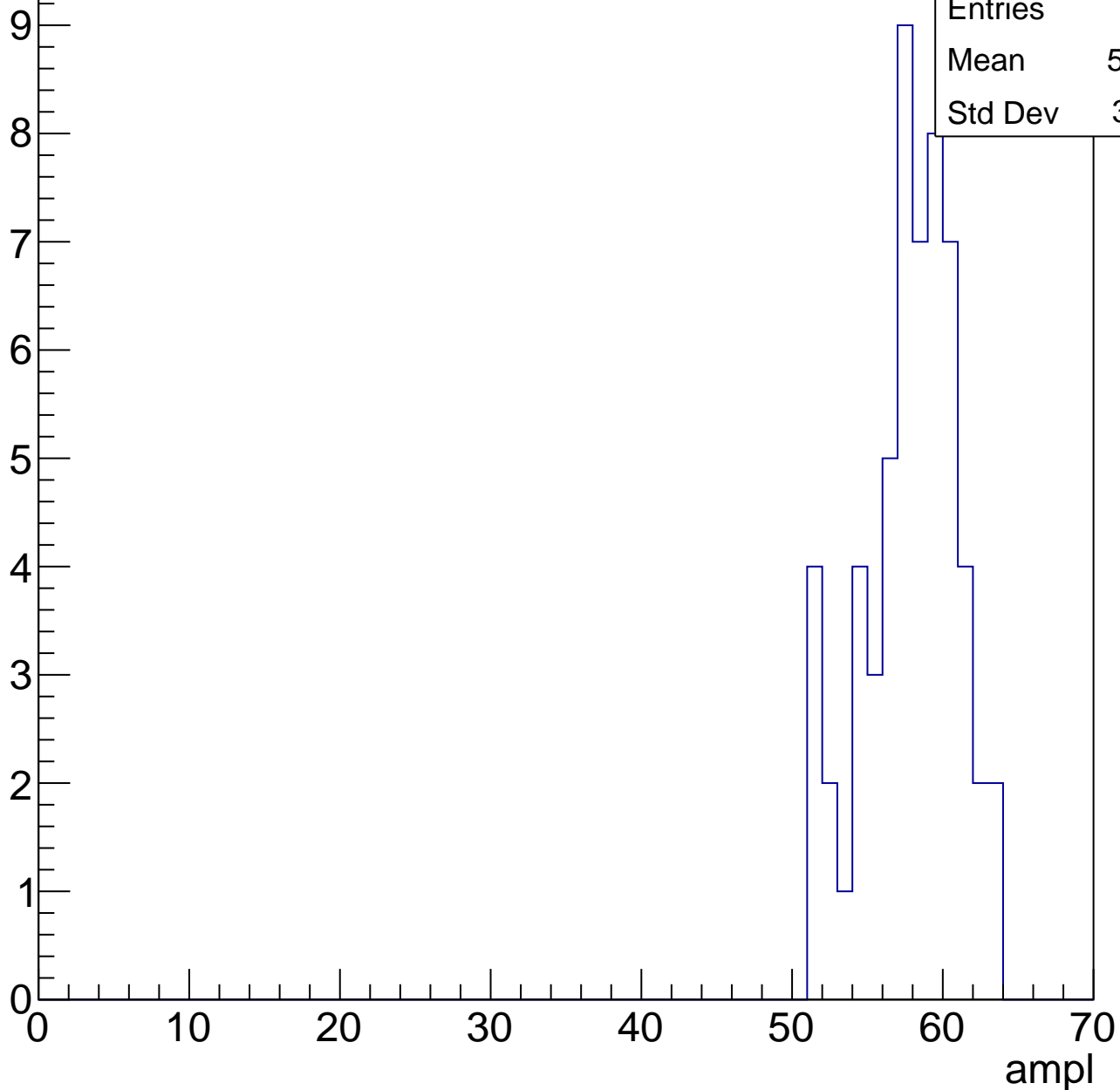
Entry



B0L002S, U18-ch77, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



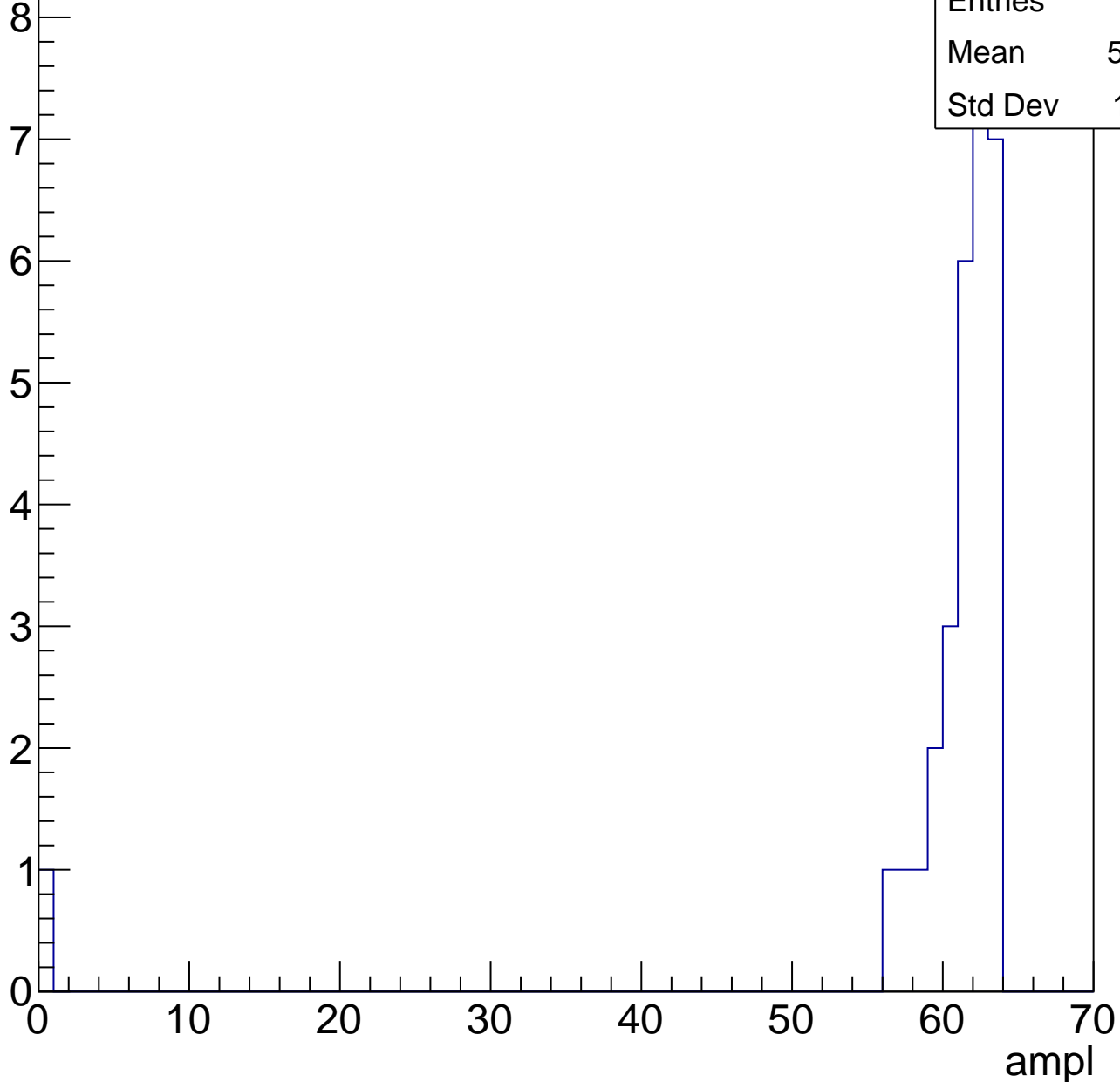
Entries	58
Mean	57.36
Std Dev	3.061

B0L002S, U18-ch77, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	30
Mean	59.07
Std Dev	11.11



B0L002S, U18-ch77, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	63
Std Dev	0

B0L002S, U18-ch77, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch78, adc0

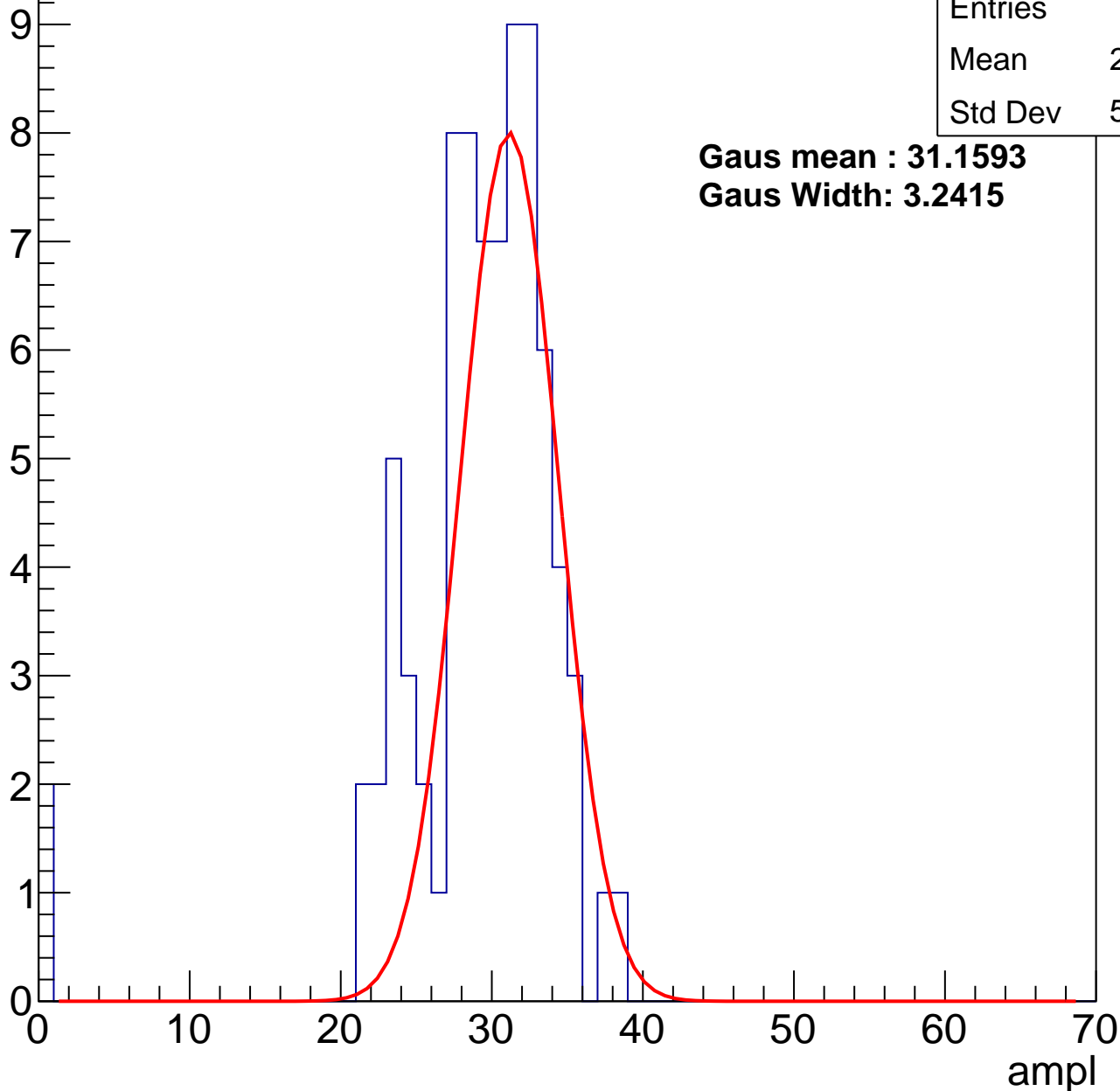
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	28.54
Std Dev	5.916

Gaus mean : 31.1593

Gaus Width: 3.2415



B0L002S, U18-ch78, adc1

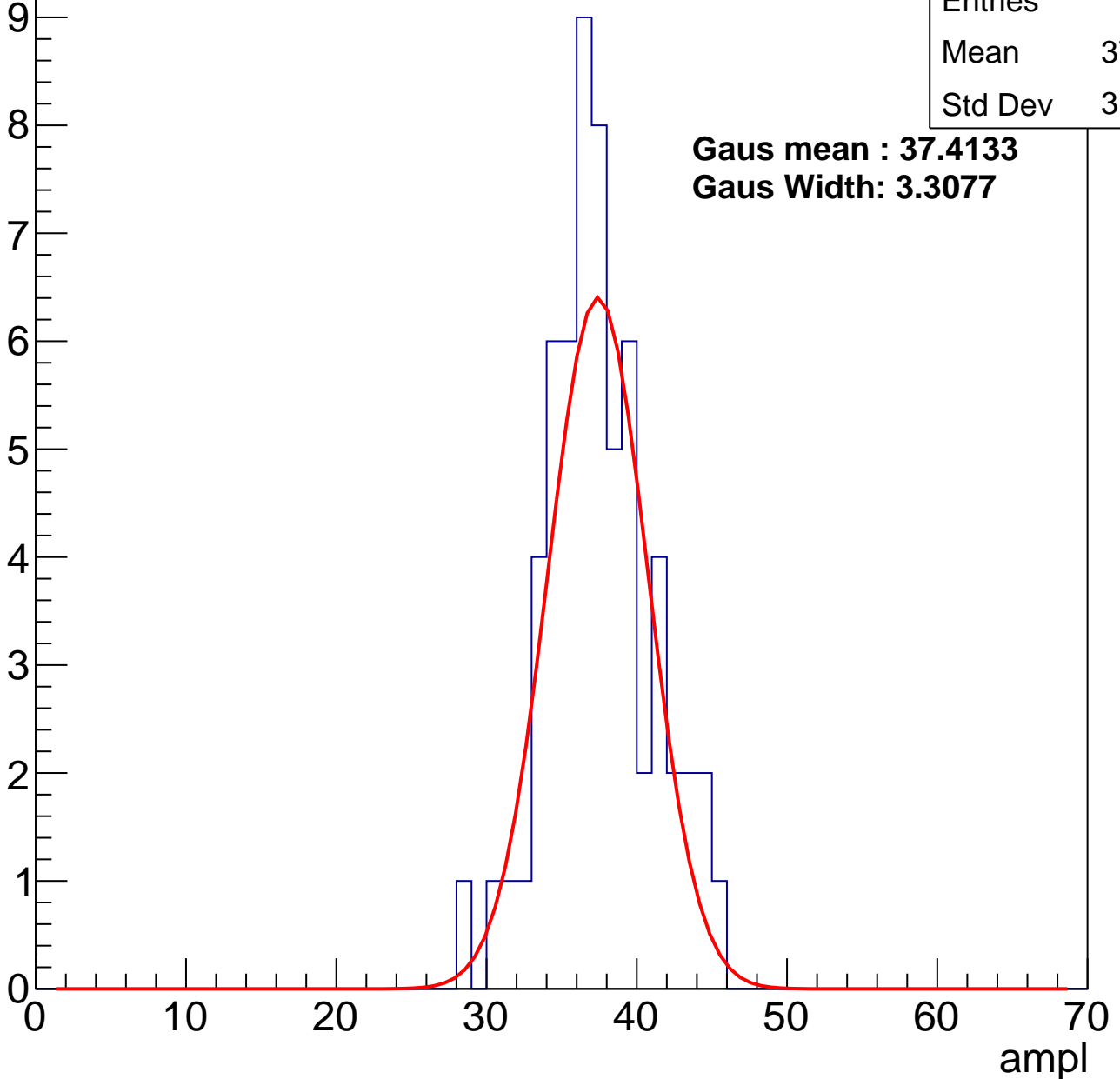
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	37.02
Std Dev	3.476

Gaus mean : 37.4133

Gaus Width: 3.3077



B0L002S, U18-ch78, adc2

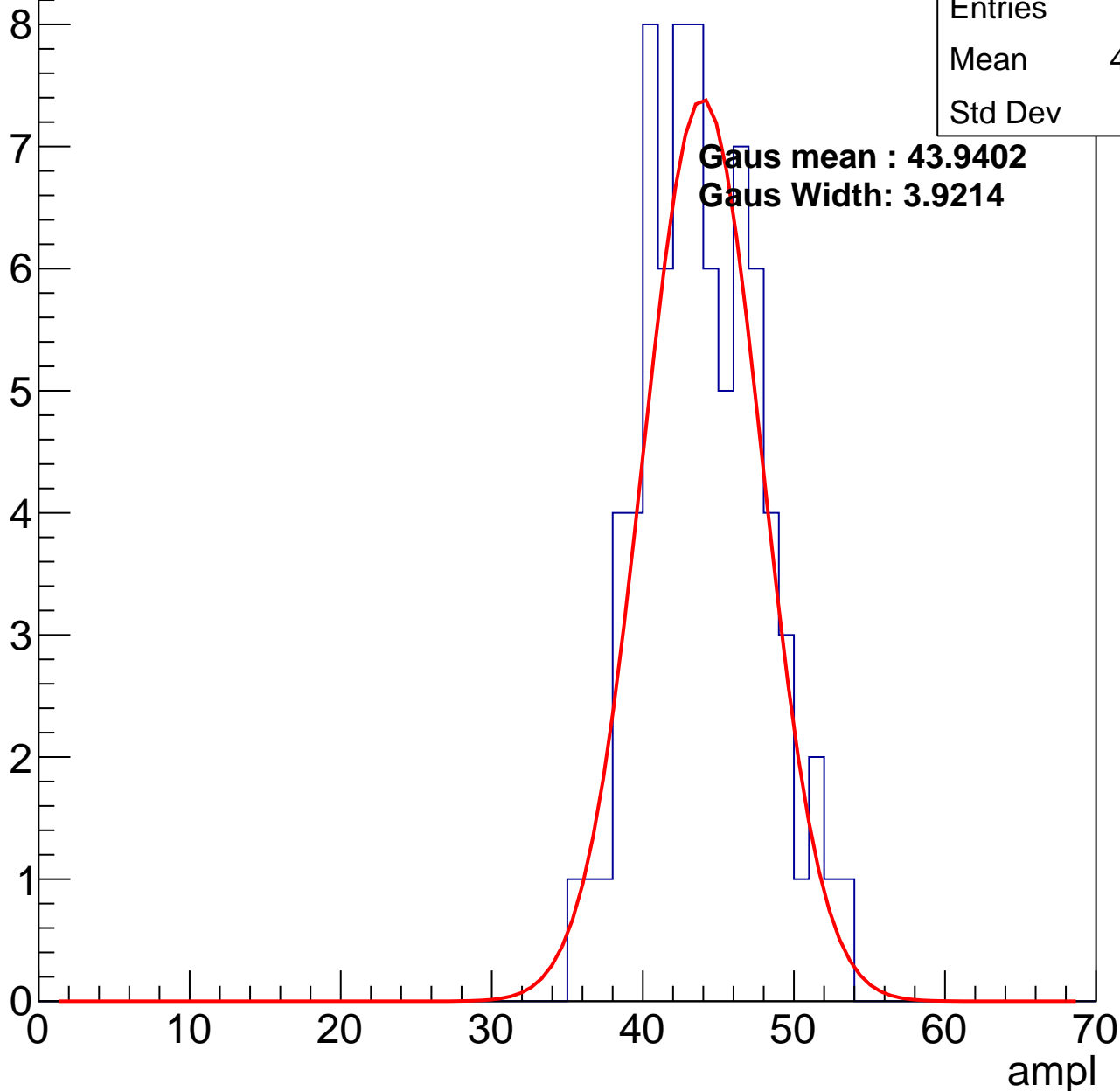
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.52
Std Dev	3.86

Gaus mean : 43.9402

Gaus Width: 3.9214

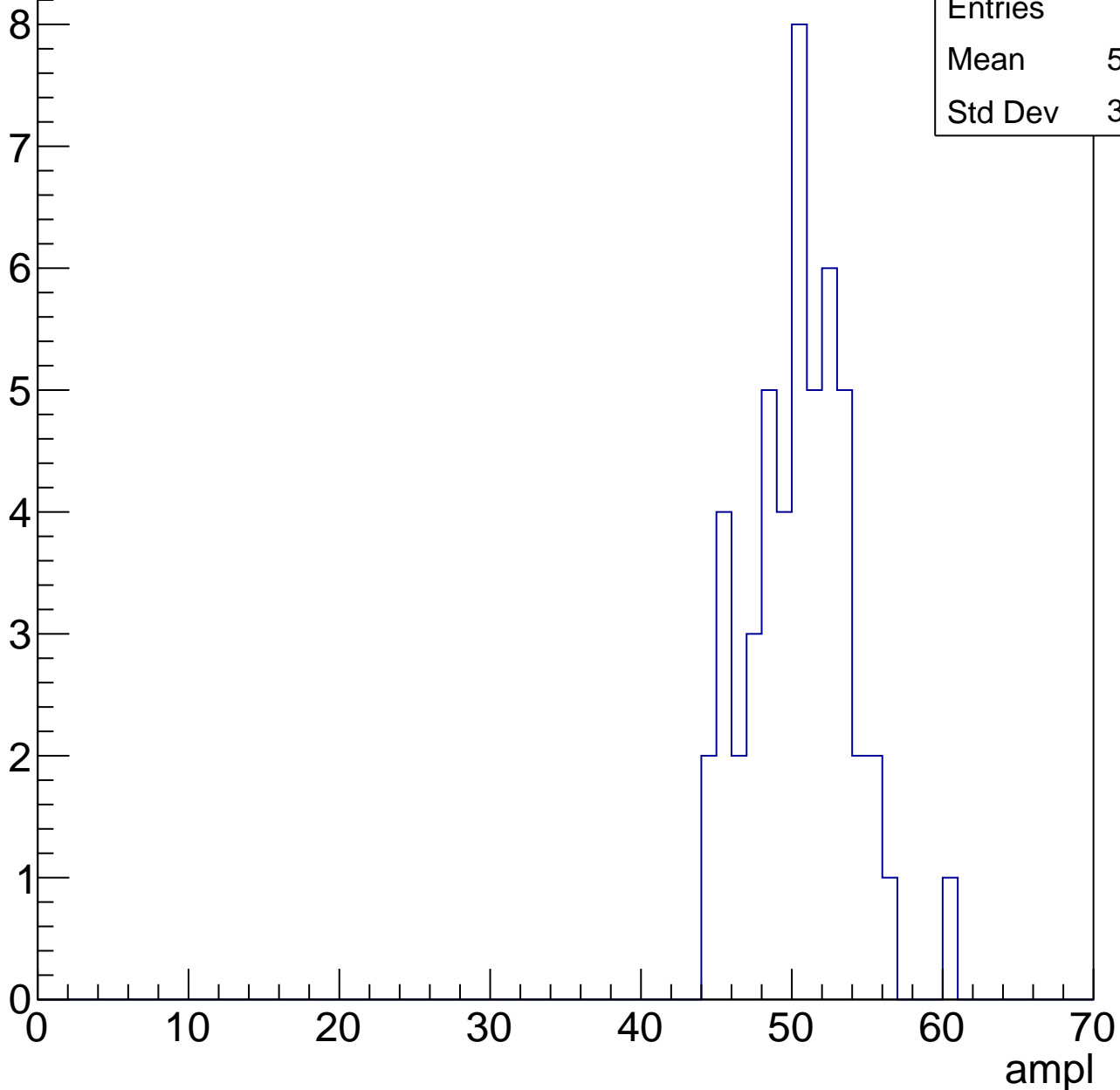


B0L002S, U18-ch78, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	50.06
Std Dev	3.307

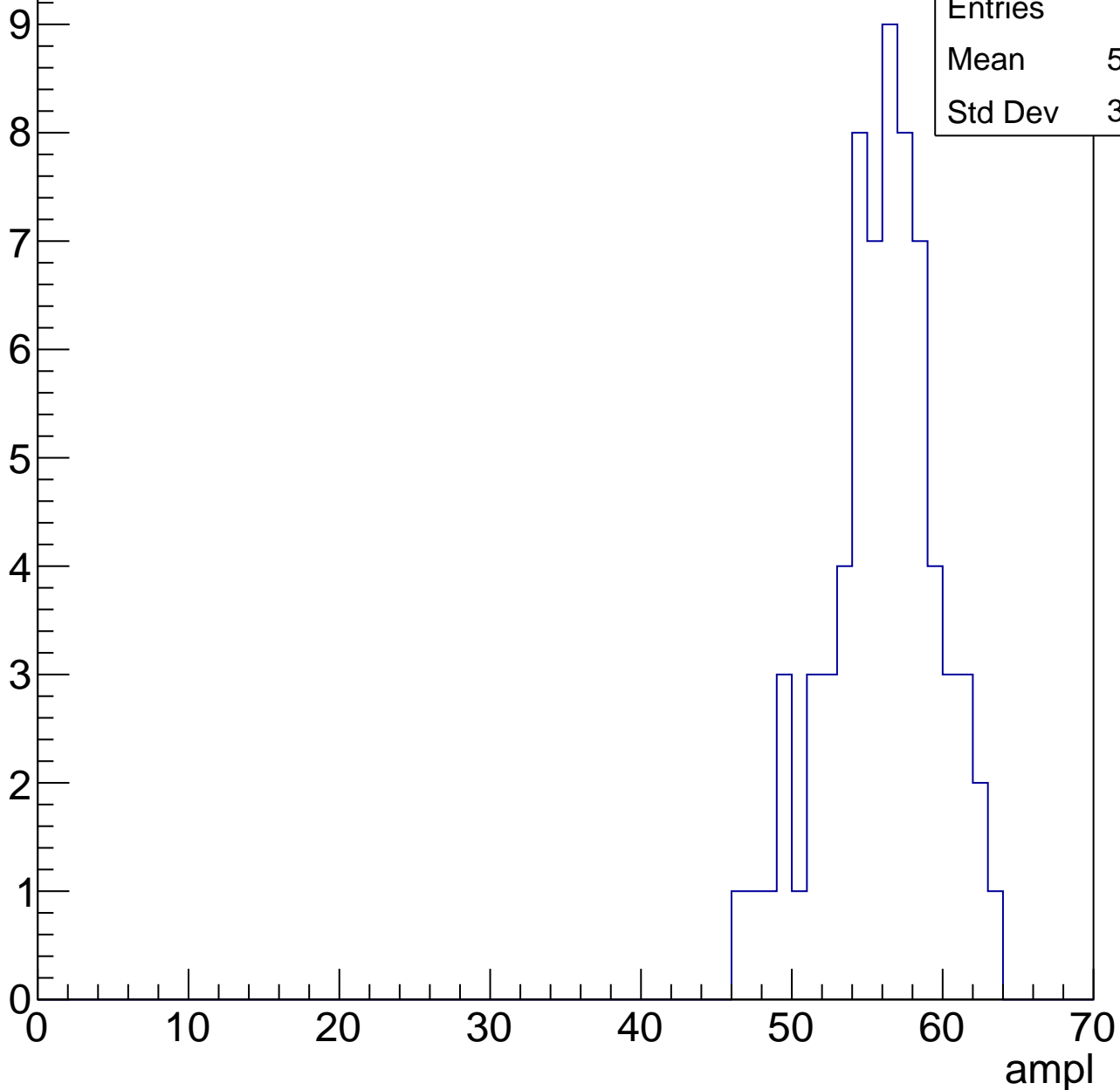


B0L002S, U18-ch78, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	55.48
Std Dev	3.658

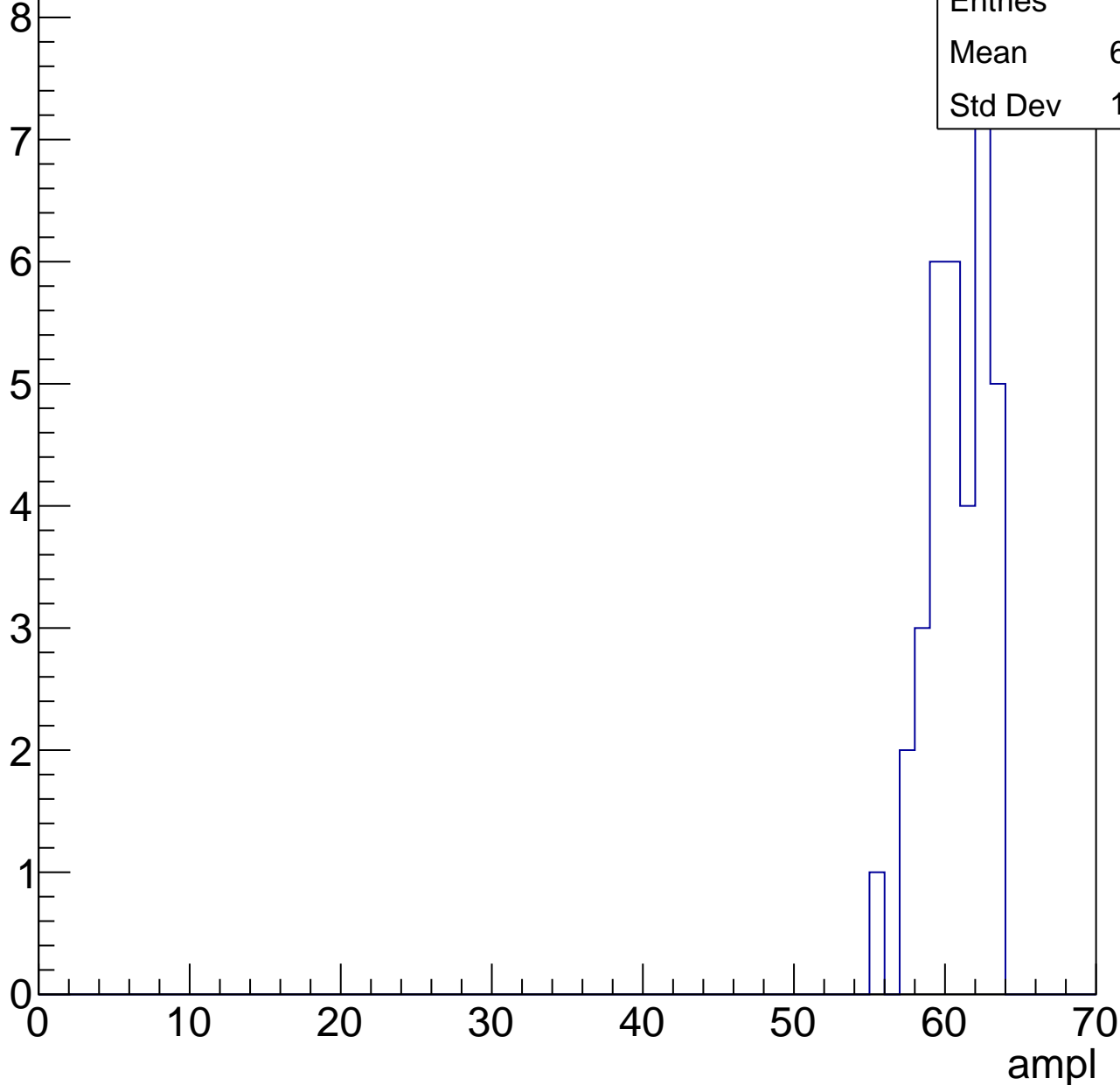


B0L002S, U18-ch78, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

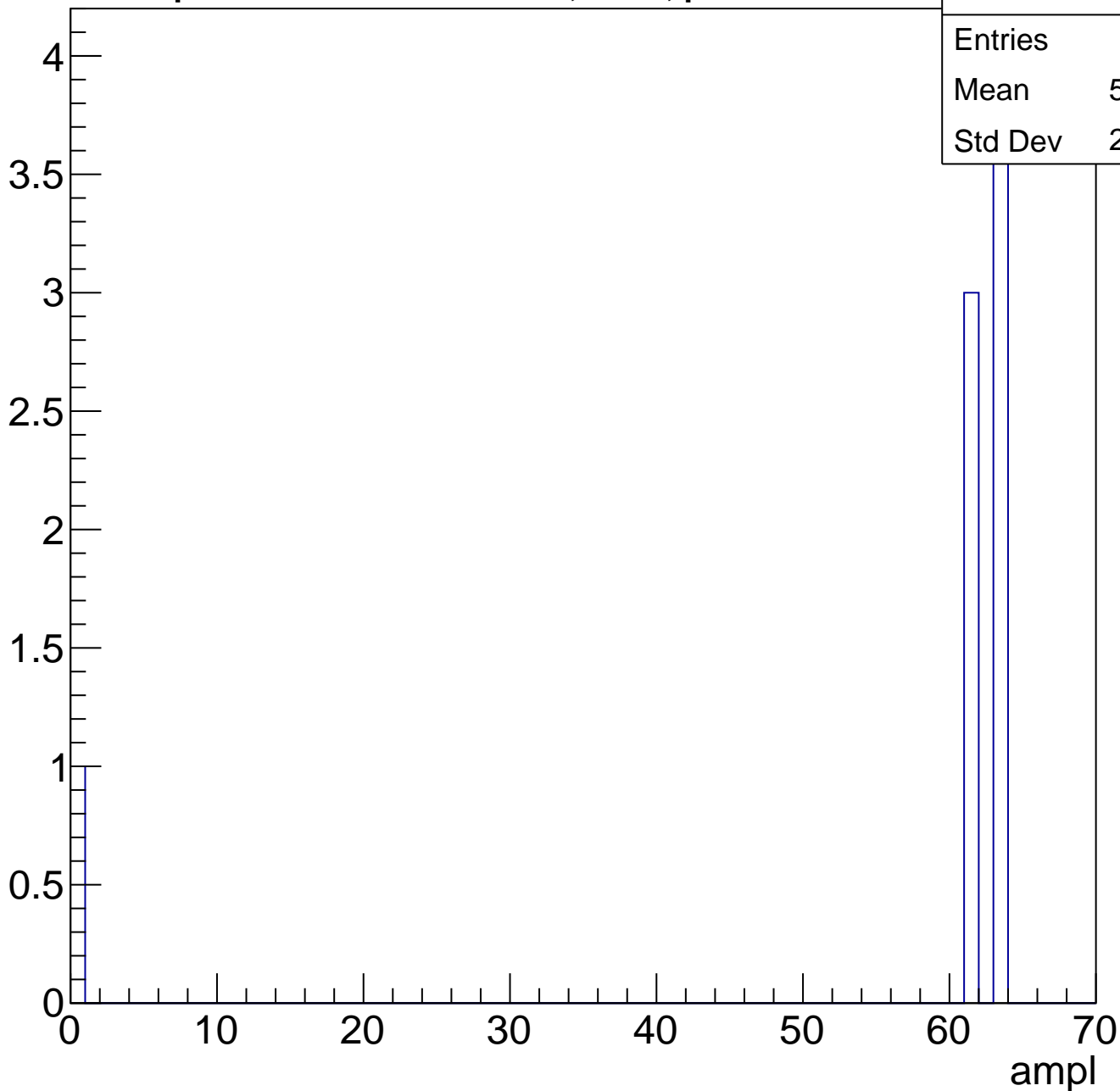
Entries	35
Mean	60.34
Std Dev	1.985



B0L002S, U18-ch78, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch78, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch79, adc0

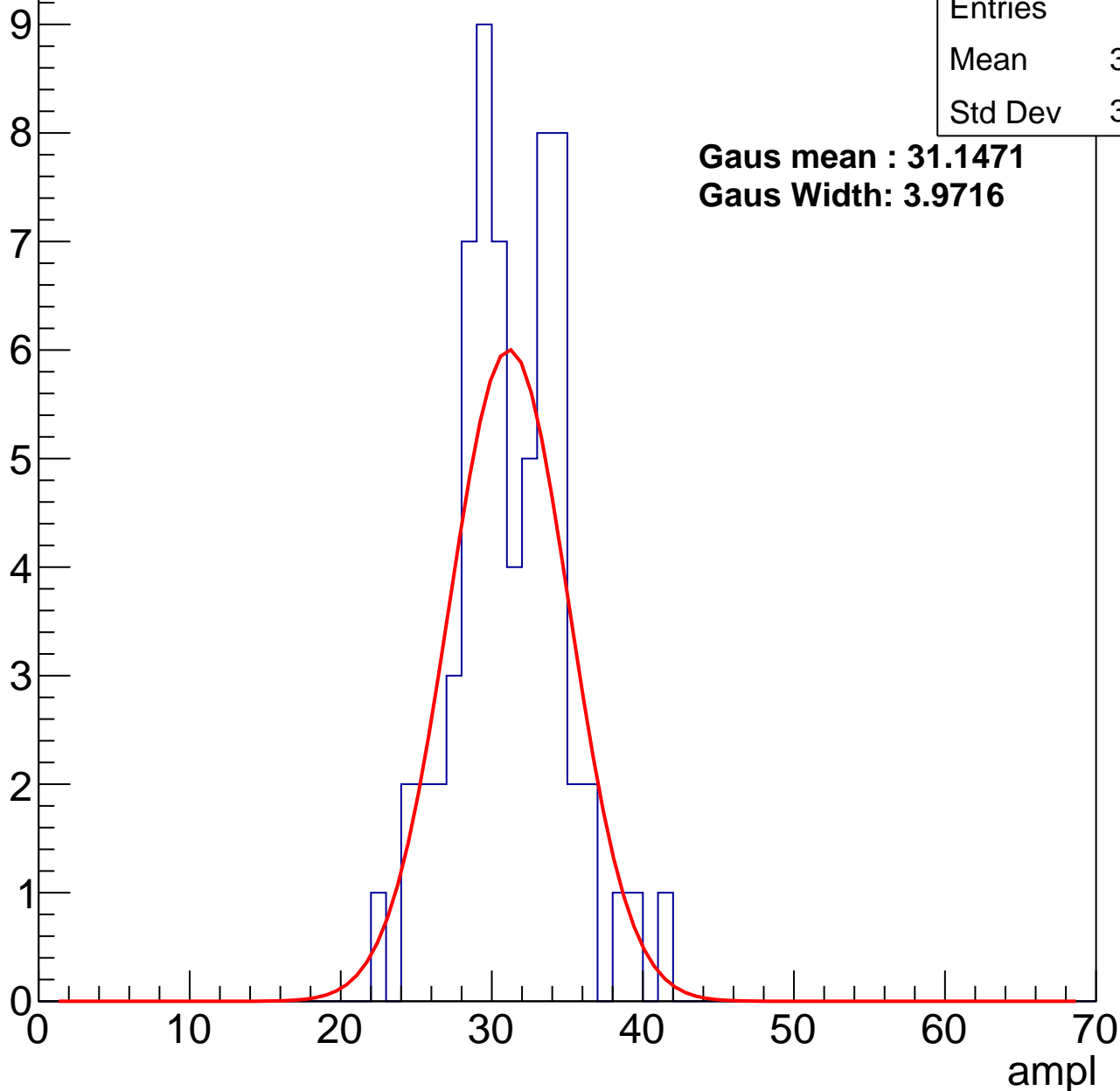
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	30.77
Std Dev	3.628

Gaus mean : 31.1471

Gaus Width: 3.9716



B0L002S, U18-ch79, adc1

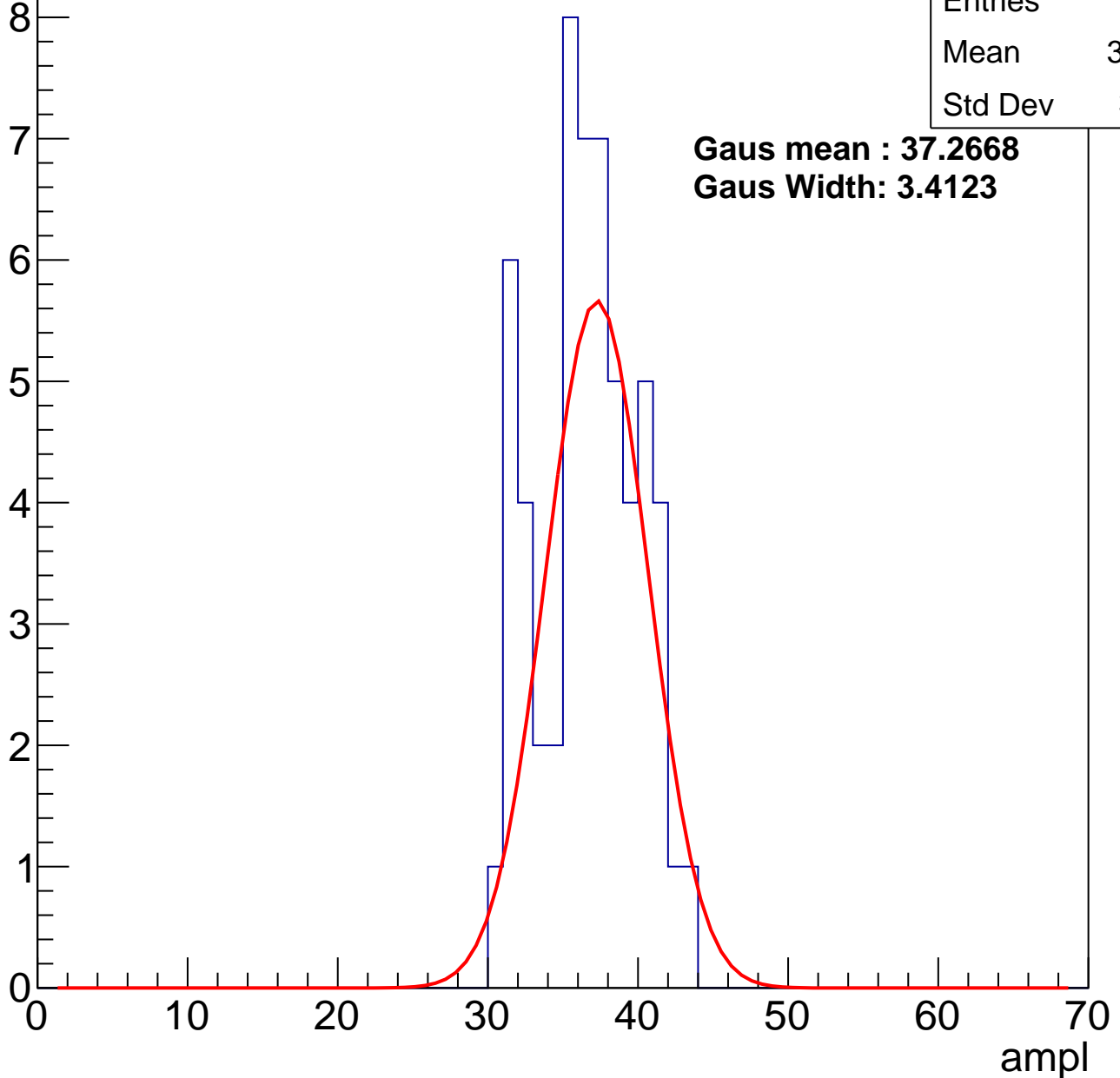
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	36.21
Std Dev	3.27

Gaus mean : 37.2668

Gaus Width: 3.4123



B0L002S, U18-ch79, adc2

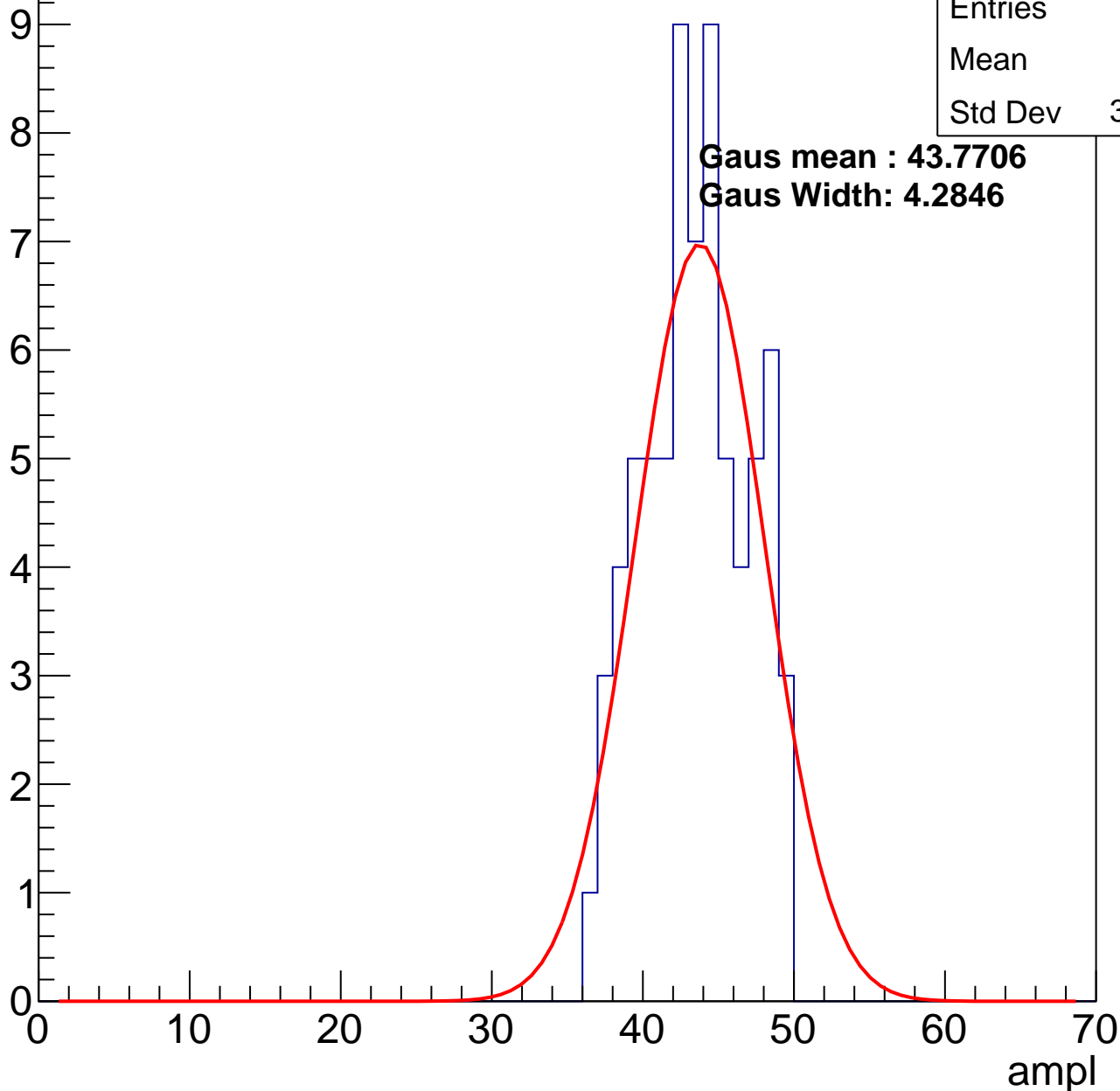
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43
Std Dev	3.386

Gaus mean : 43.7706

Gaus Width: 4.2846

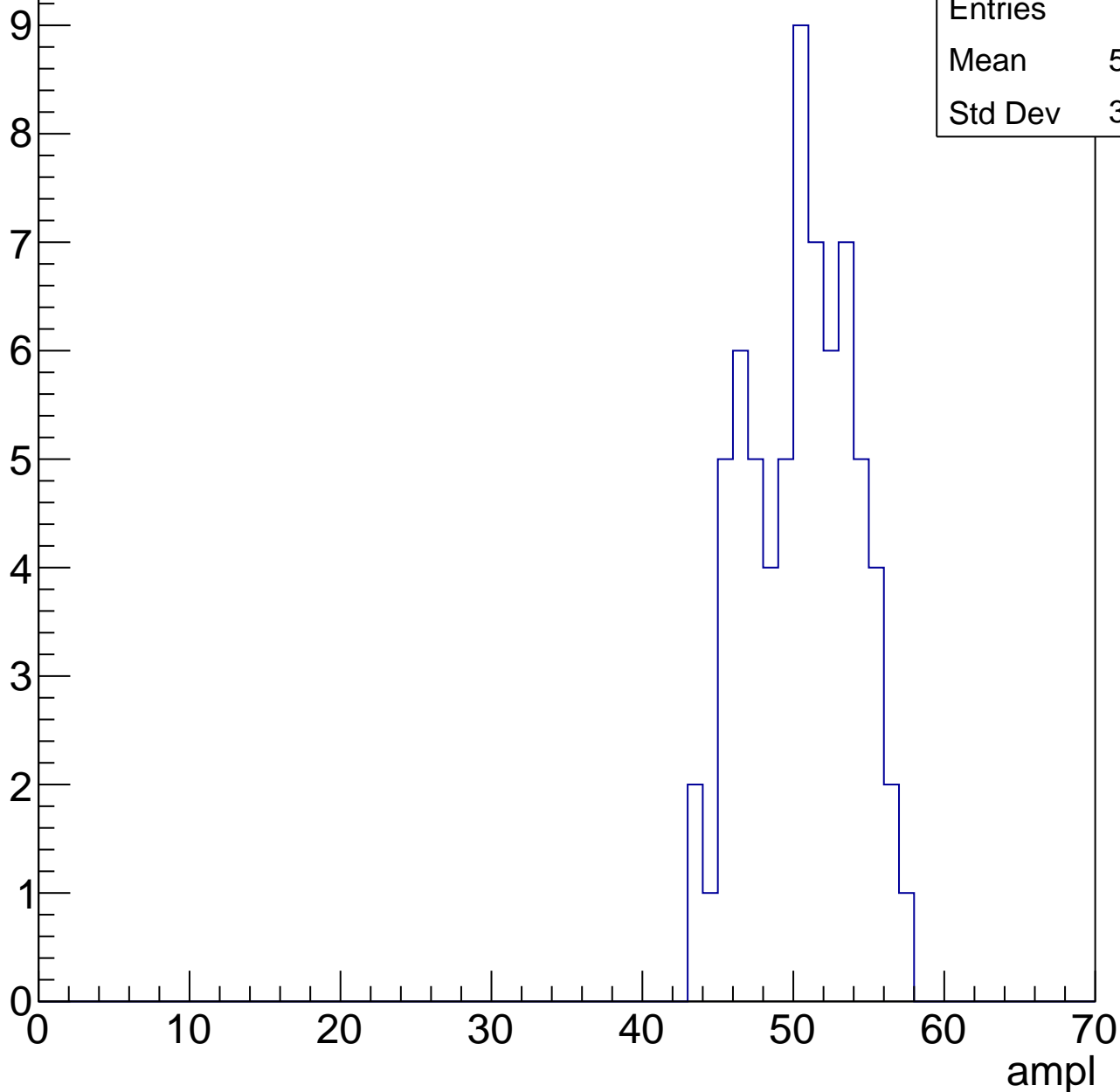


B0L002S, U18-ch79, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	50.03
Std Dev	3.439

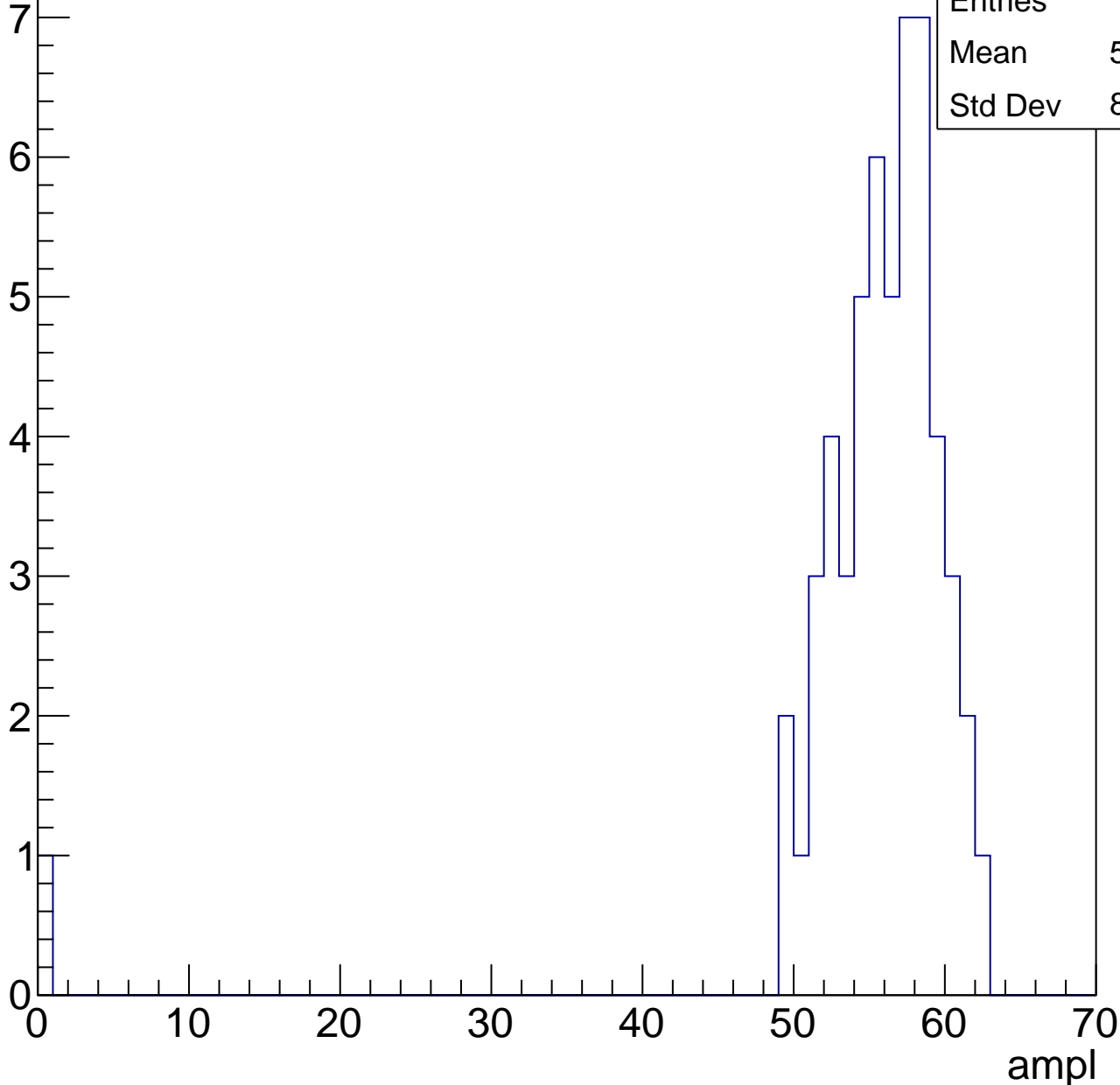


B0L002S, U18-ch79, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

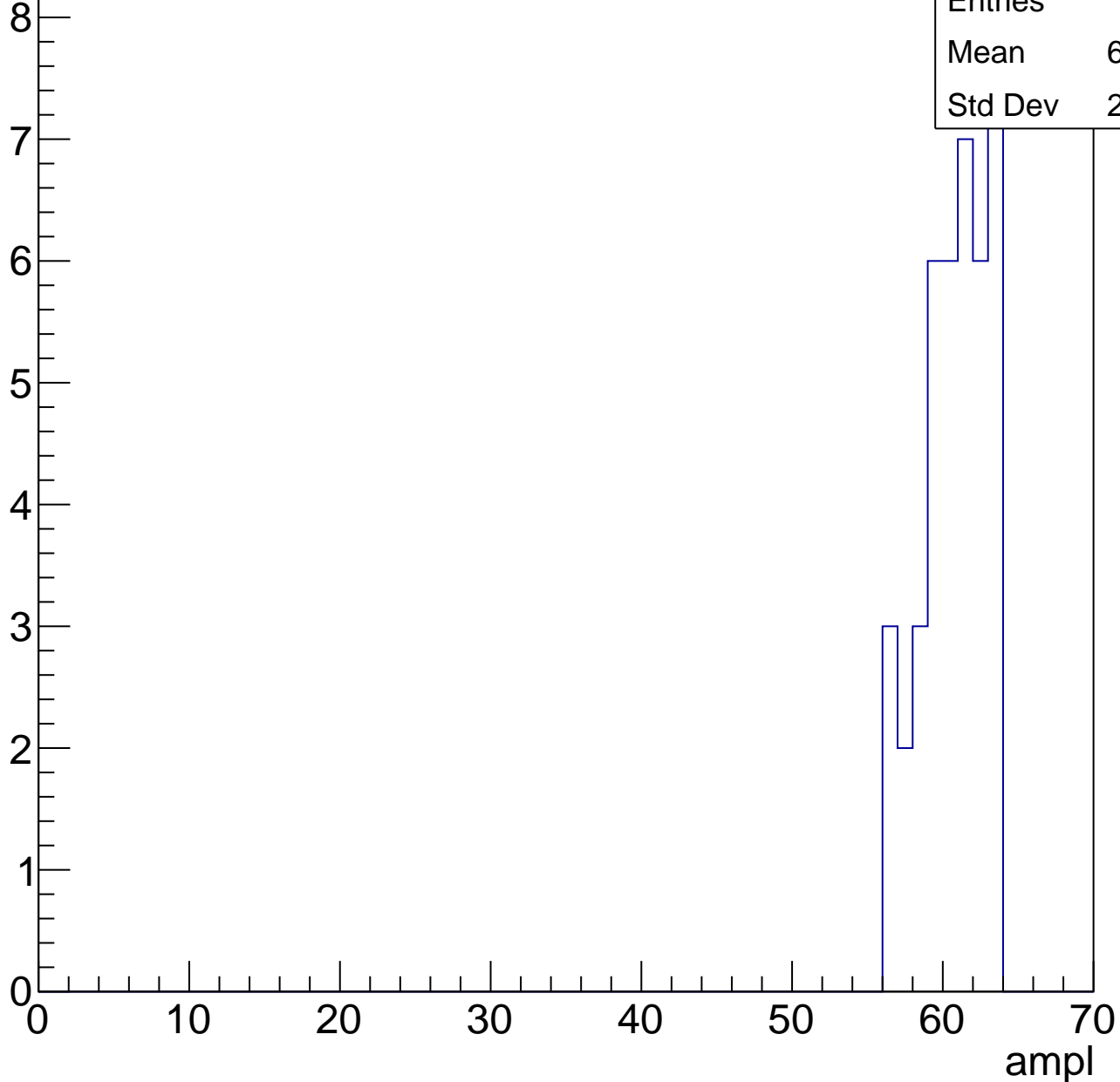
Entries	54
Mean	54.69
Std Dev	8.135



B0L002S, U18-ch79, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

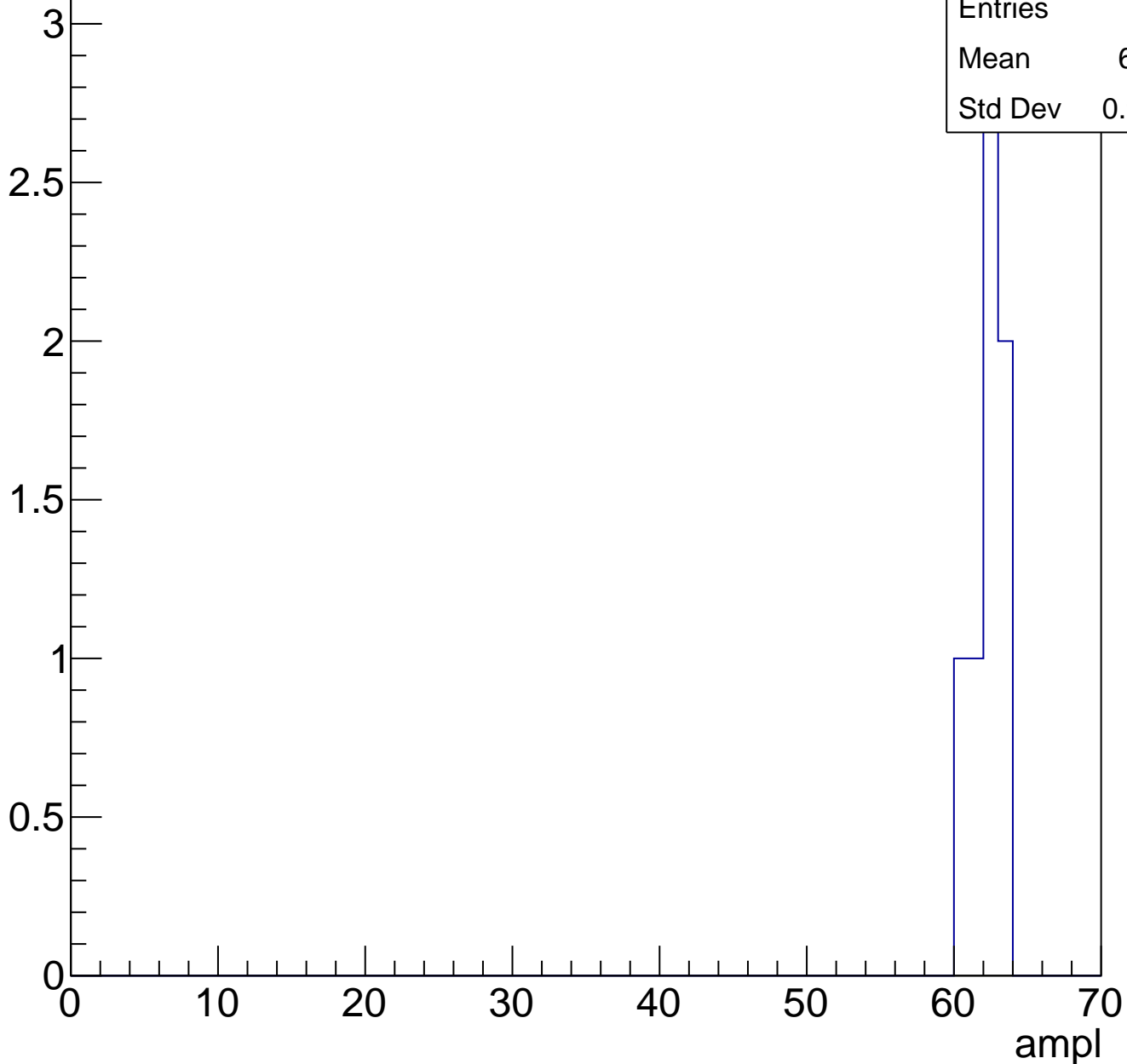


Entries	41
Mean	60.32
Std Dev	2.112

B0L002S, U18-ch79, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch79, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch80, adc0

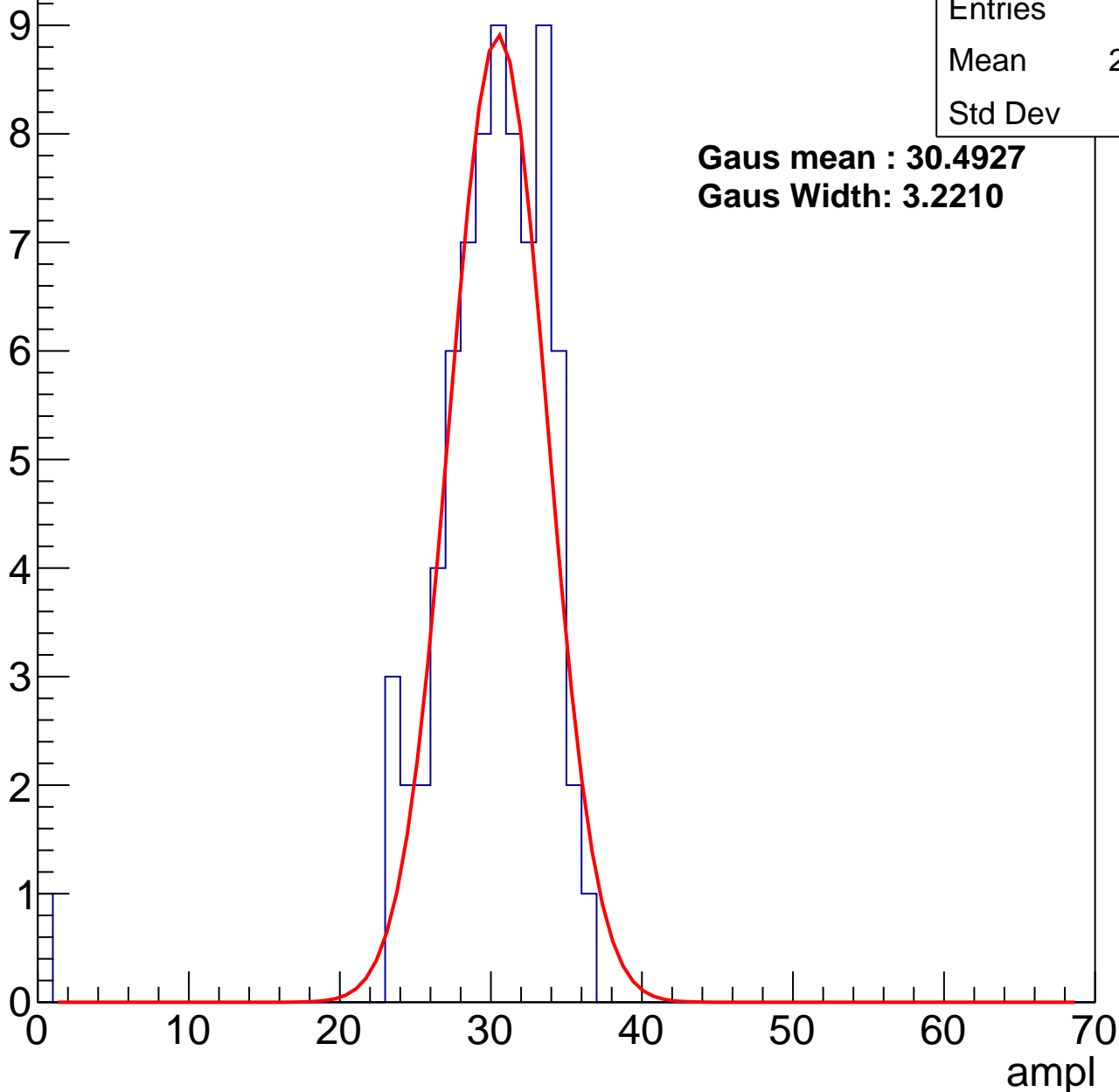
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	29.47
Std Dev	4.62

Gaus mean : 30.4927

Gaus Width: 3.2210



B0L002S, U18-ch80, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	37.29
Std Dev	3.133

Gaus mean : 37.9350

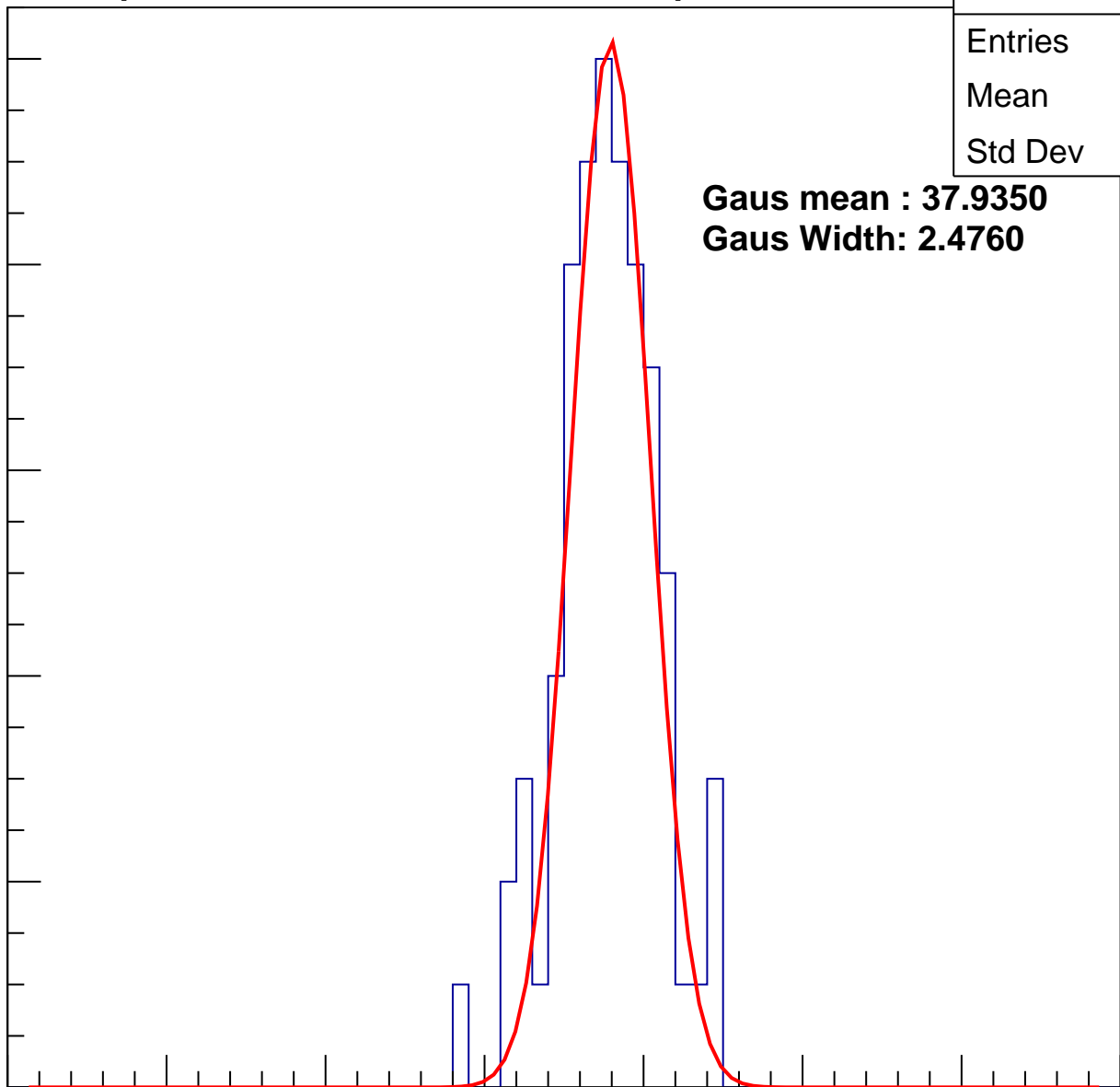
Gaus Width: 2.4760

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch80, adc2

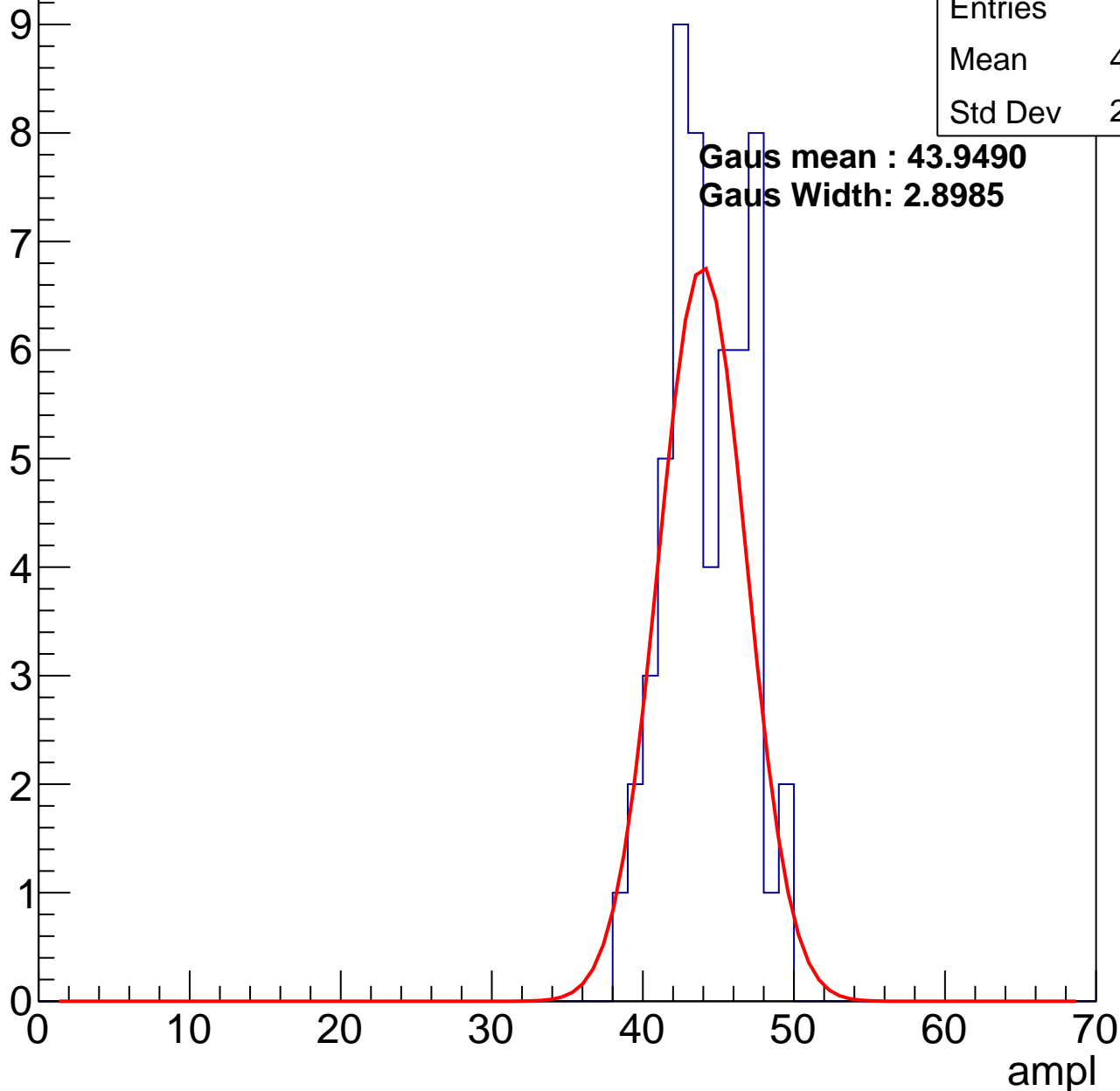
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	43.76
Std Dev	2.656

Gaus mean : 43.9490

Gaus Width: 2.8985

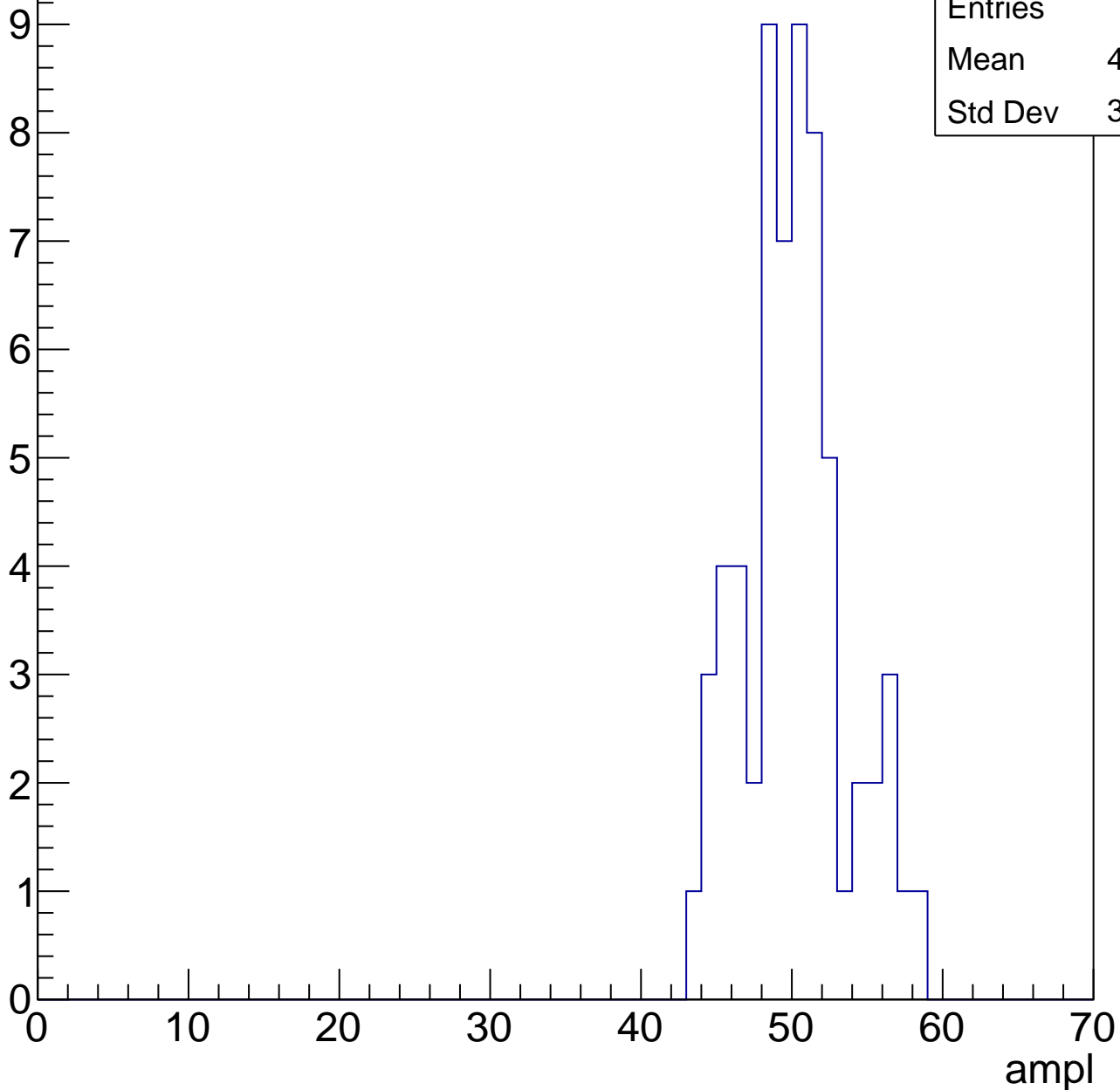


B0L002S, U18-ch80, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	49.68
Std Dev	3.397

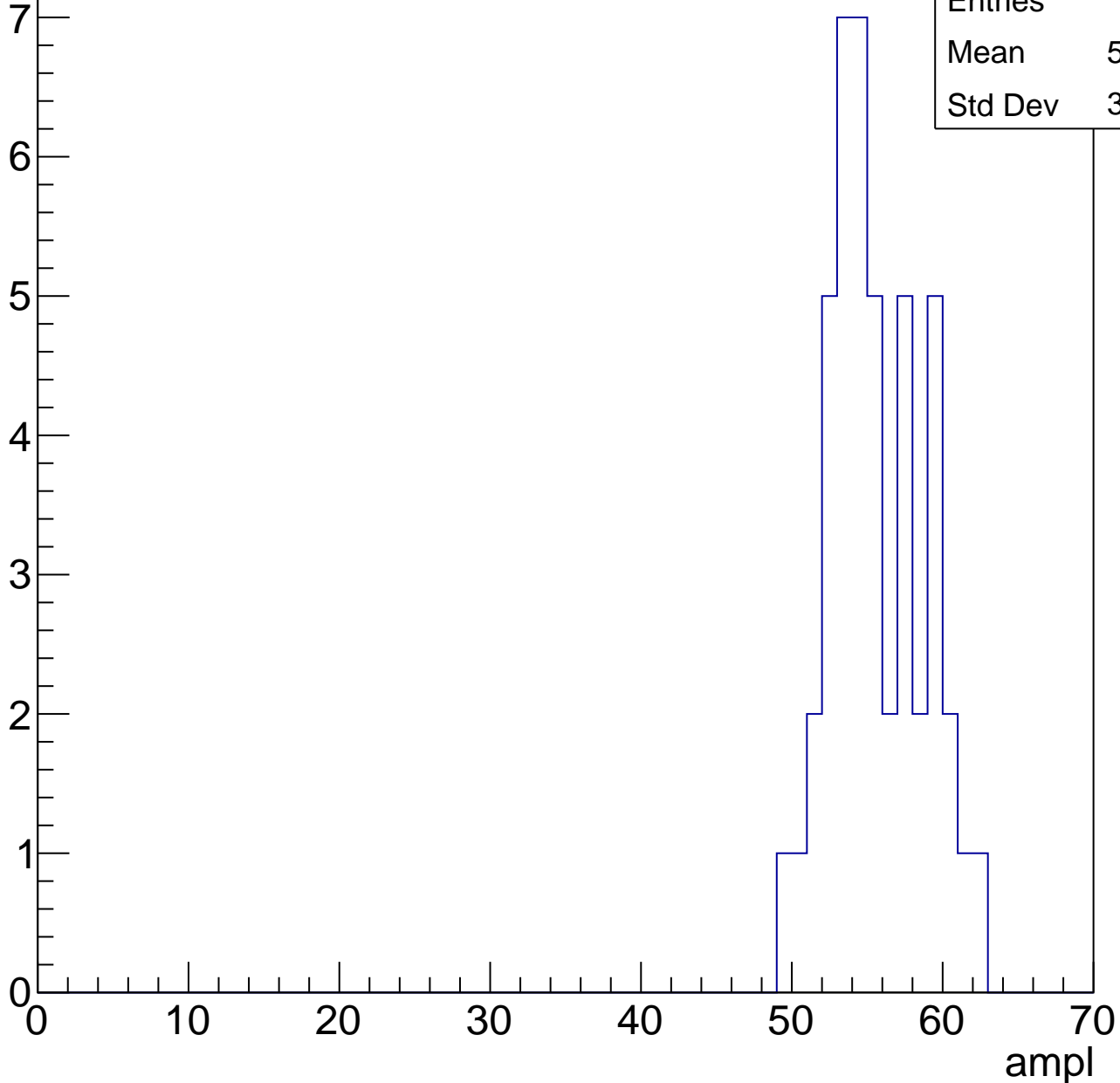


B0L002S, U18-ch80, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	55.13
Std Dev	3.048

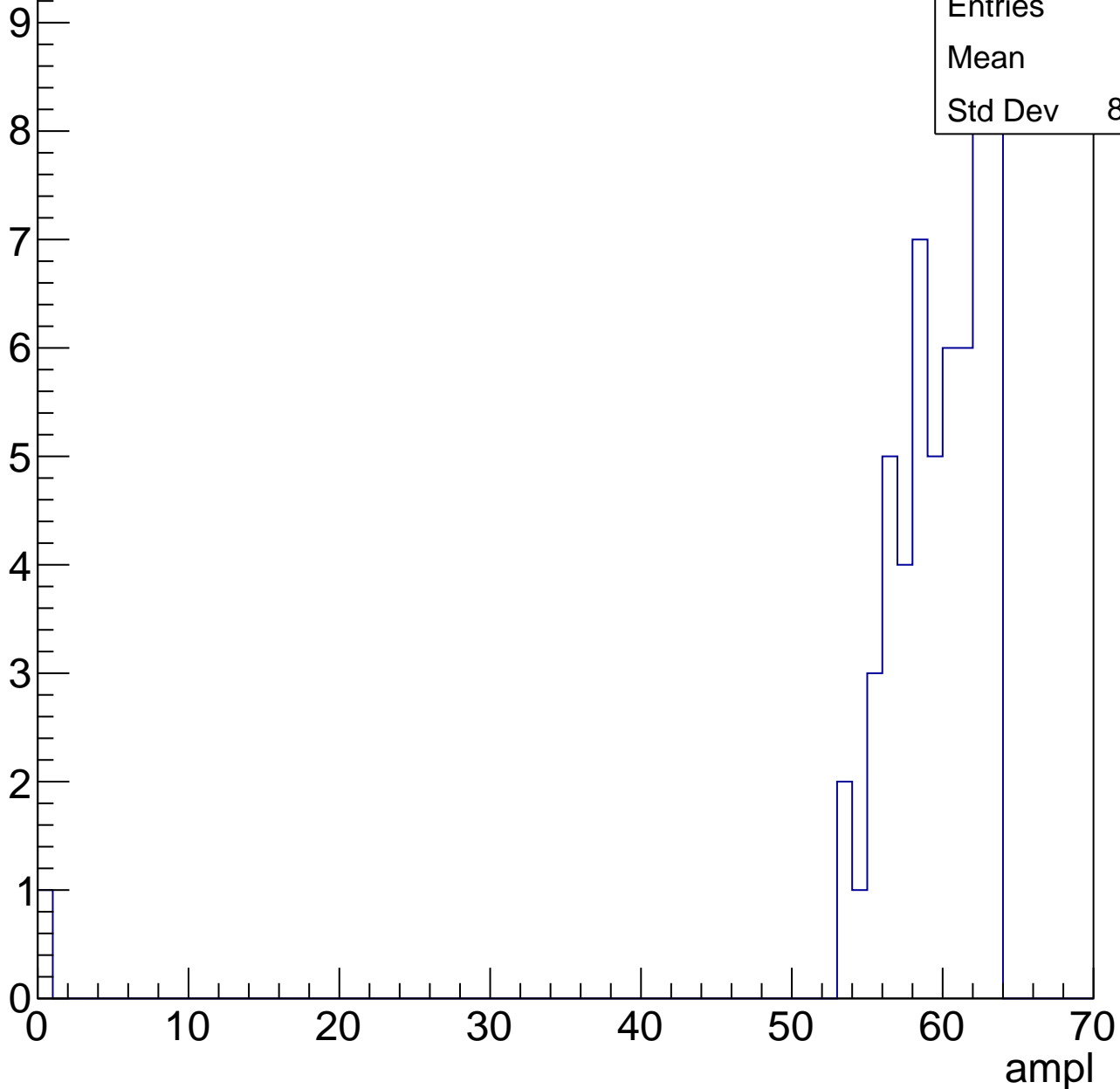


B0L002S, U18-ch80, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

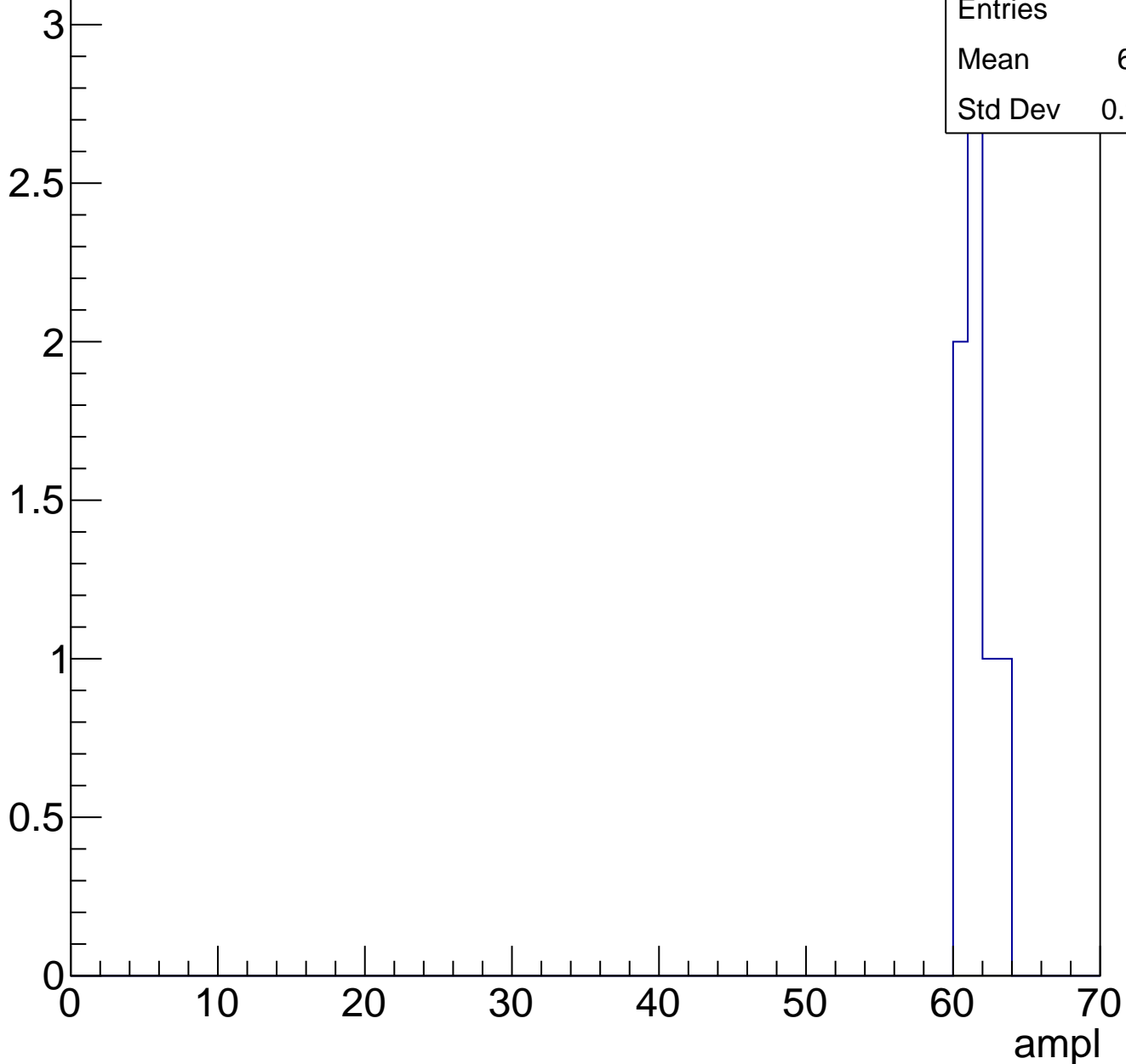
Entries	57
Mean	58.3
Std Dev	8.284



B0L002S, U18-ch80, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch80, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch81, adc0

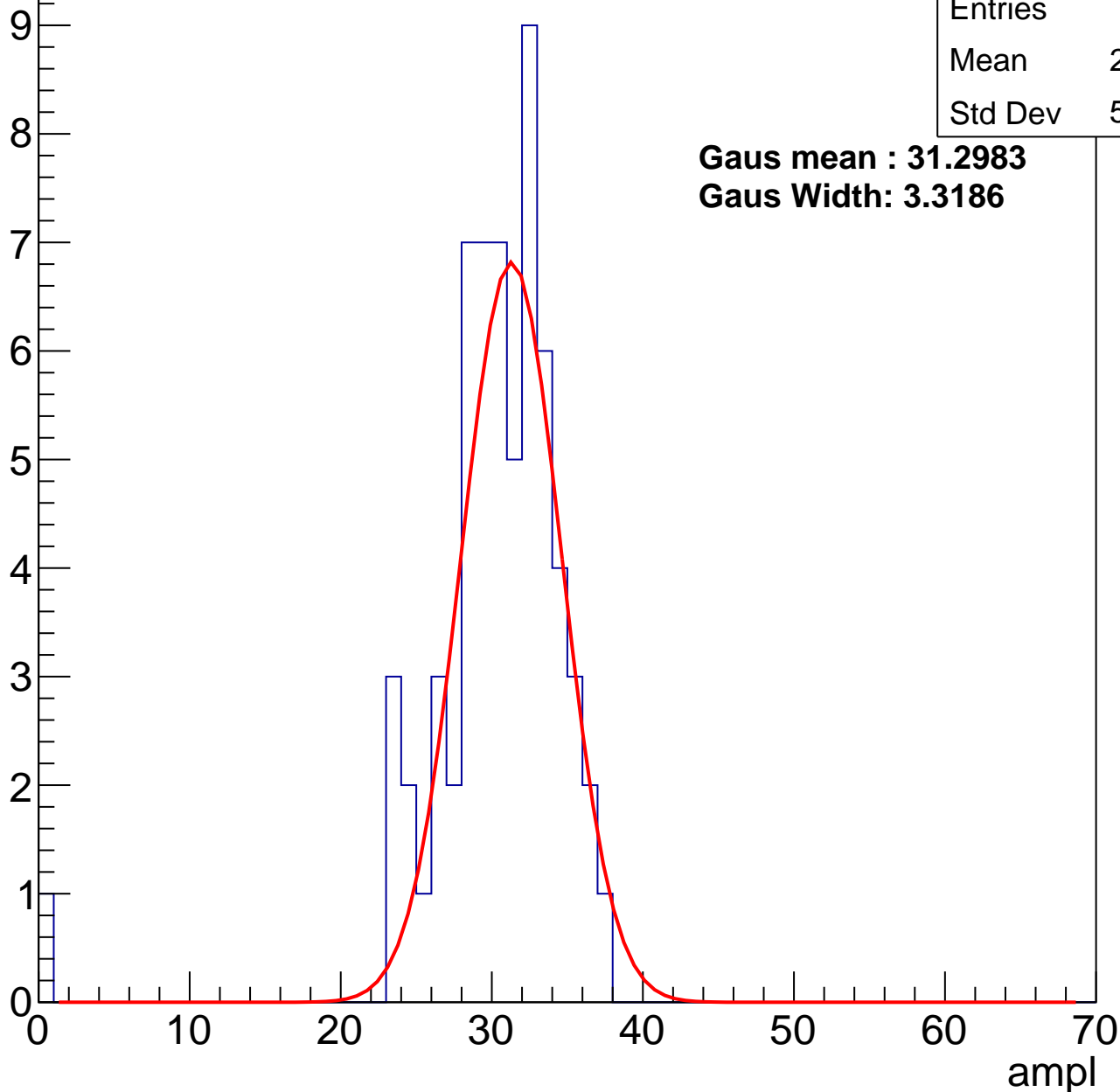
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	29.75
Std Dev	5.033

Gaus mean : 31.2983

Gaus Width: 3.3186



B0L002S, U18-ch81, adc1

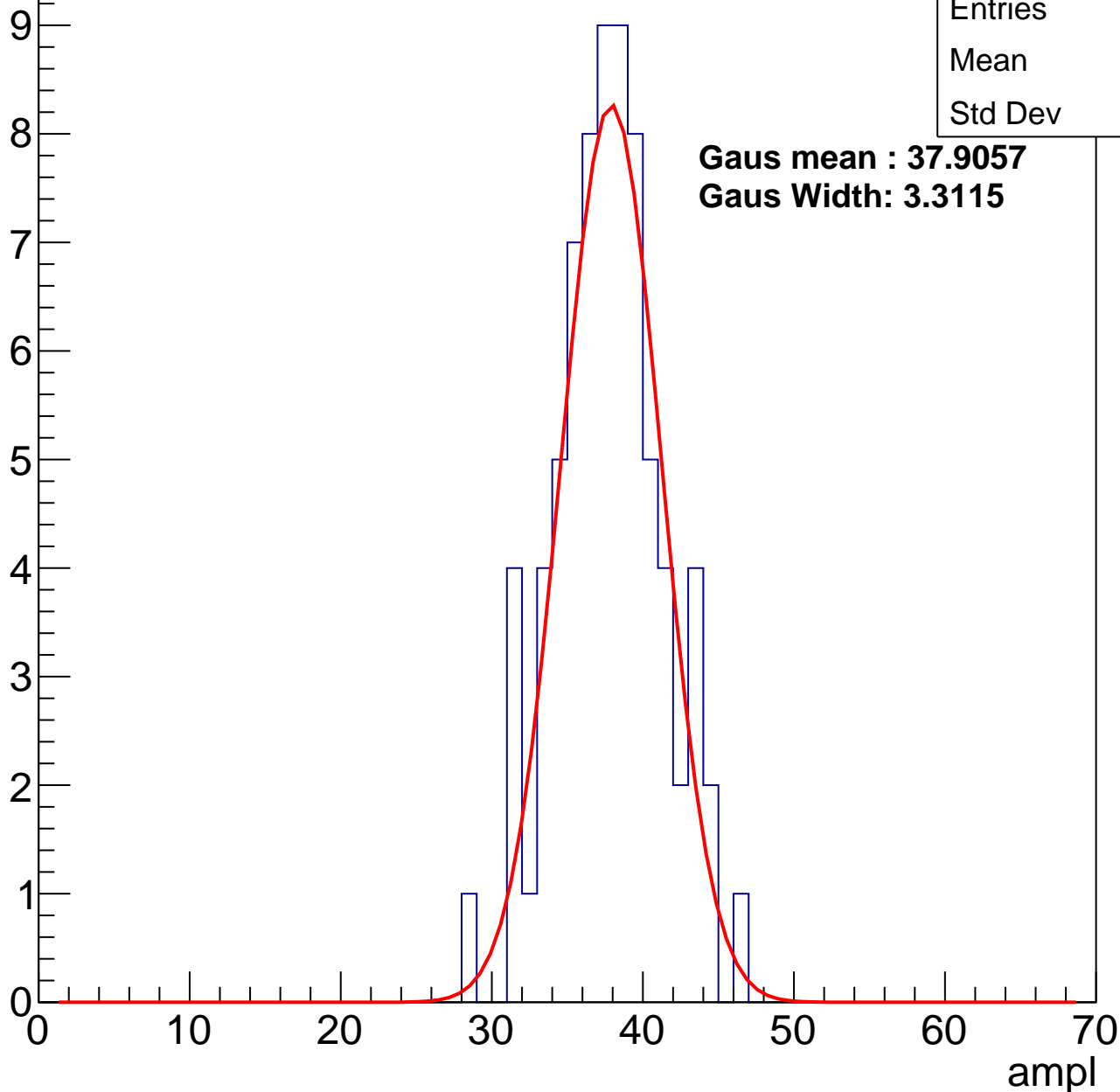
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	37.3
Std Dev	3.51

Gaus mean : 37.9057

Gaus Width: 3.3115



B0L002S, U18-ch81, adc2

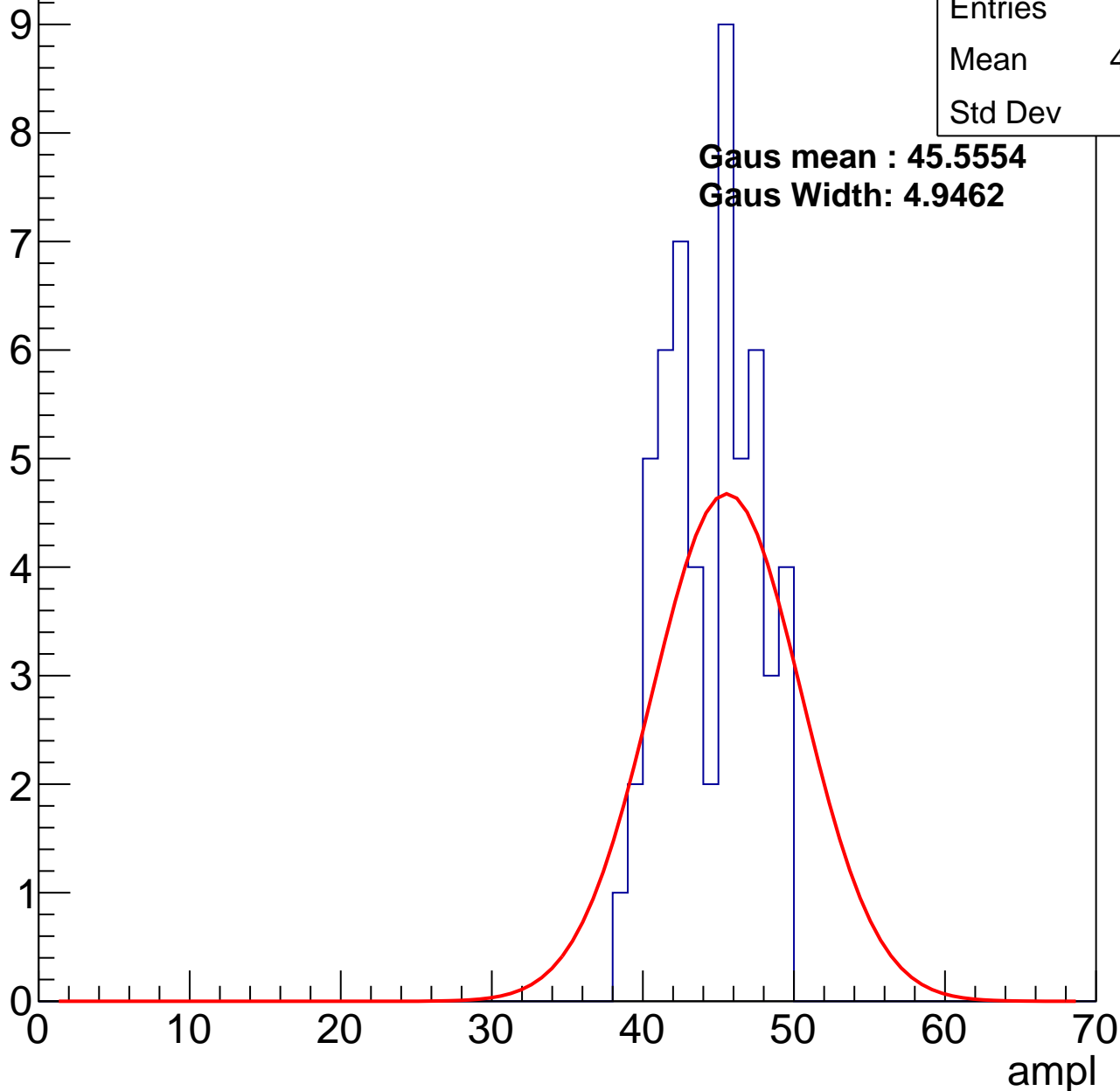
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	43.94
Std Dev	2.99

Gaus mean : 45.5554

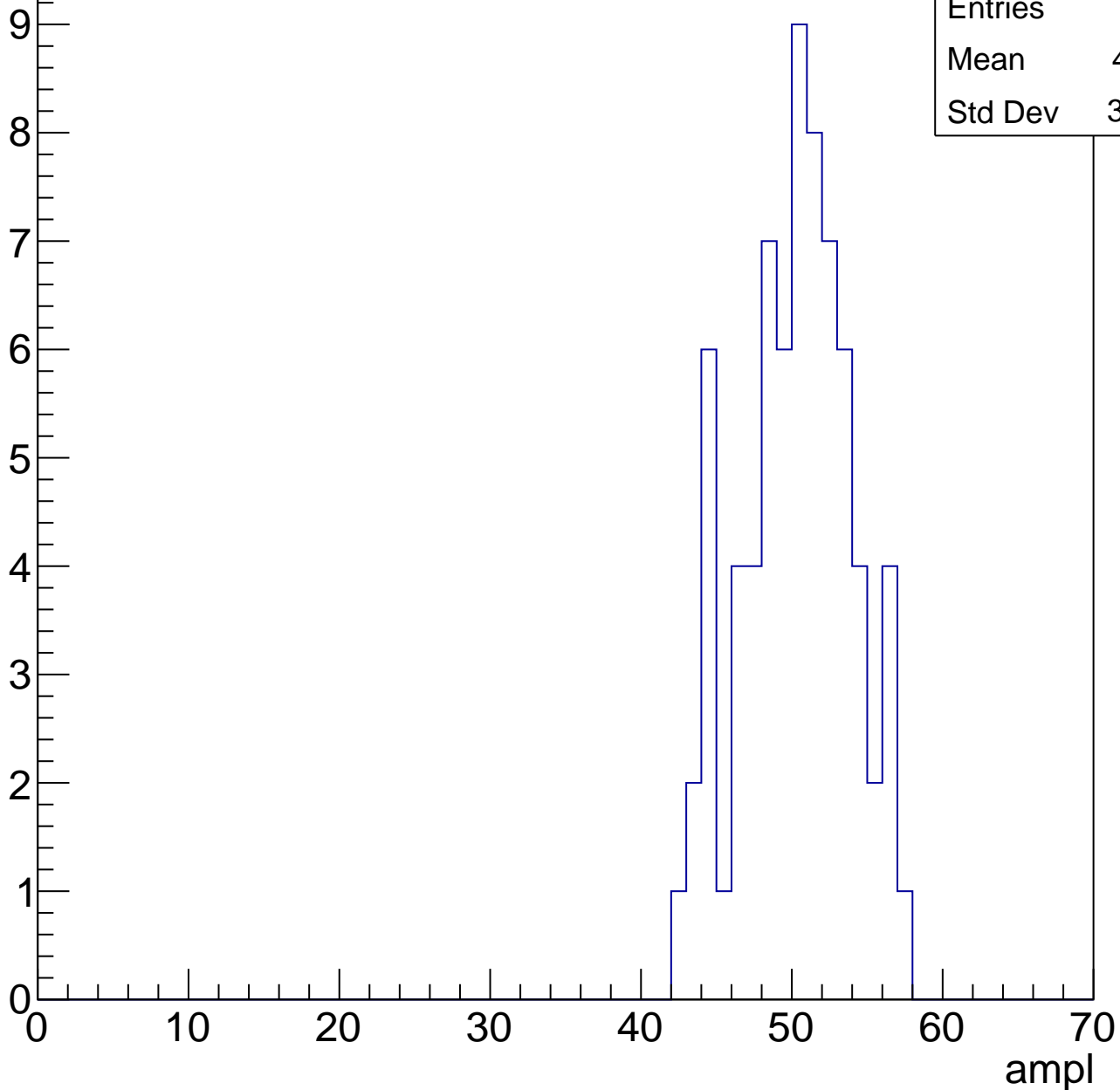
Gaus Width: 4.9462



B0L002S, U18-ch81, adc3

calib_packv5_042523_0143.root, FC#8, port C1

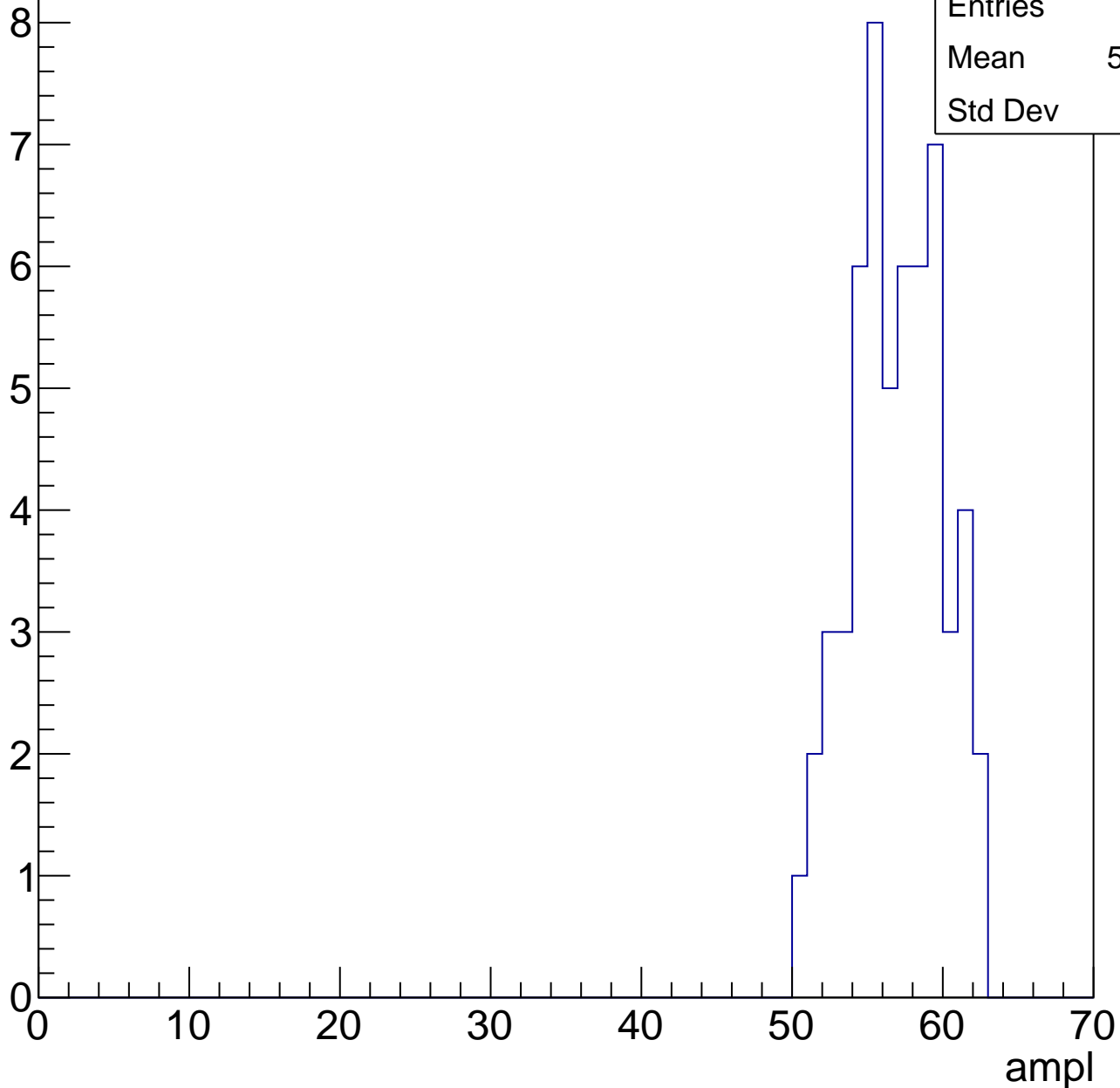
Entry



B0L002S, U18-ch81, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



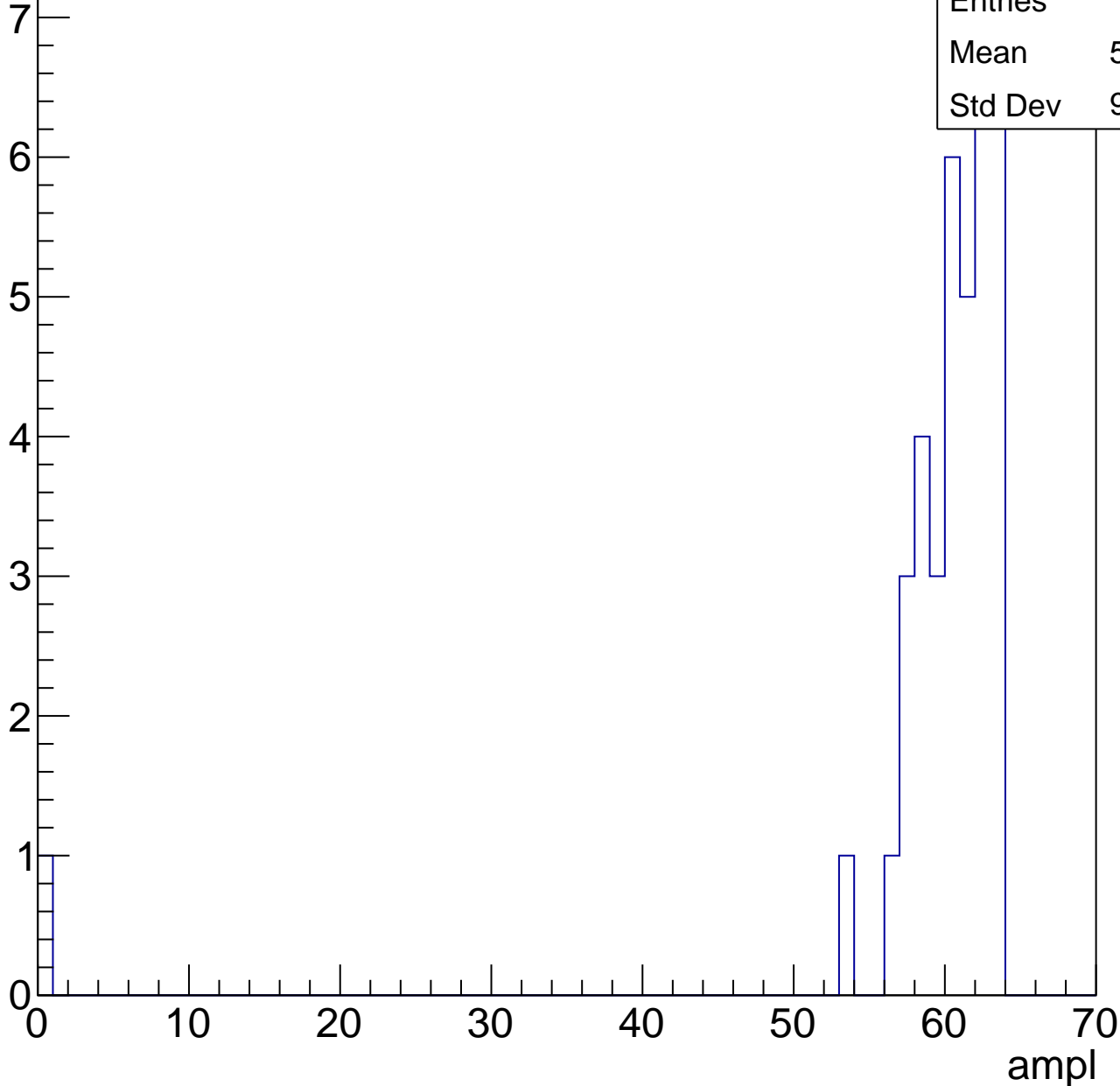
Entries	56
Mean	56.46
Std Dev	2.97

B0L002S, U18-ch81, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	38
Mean	58.66
Std Dev	9.919



B0L002S, U18-ch81, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch81, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch82, adc0

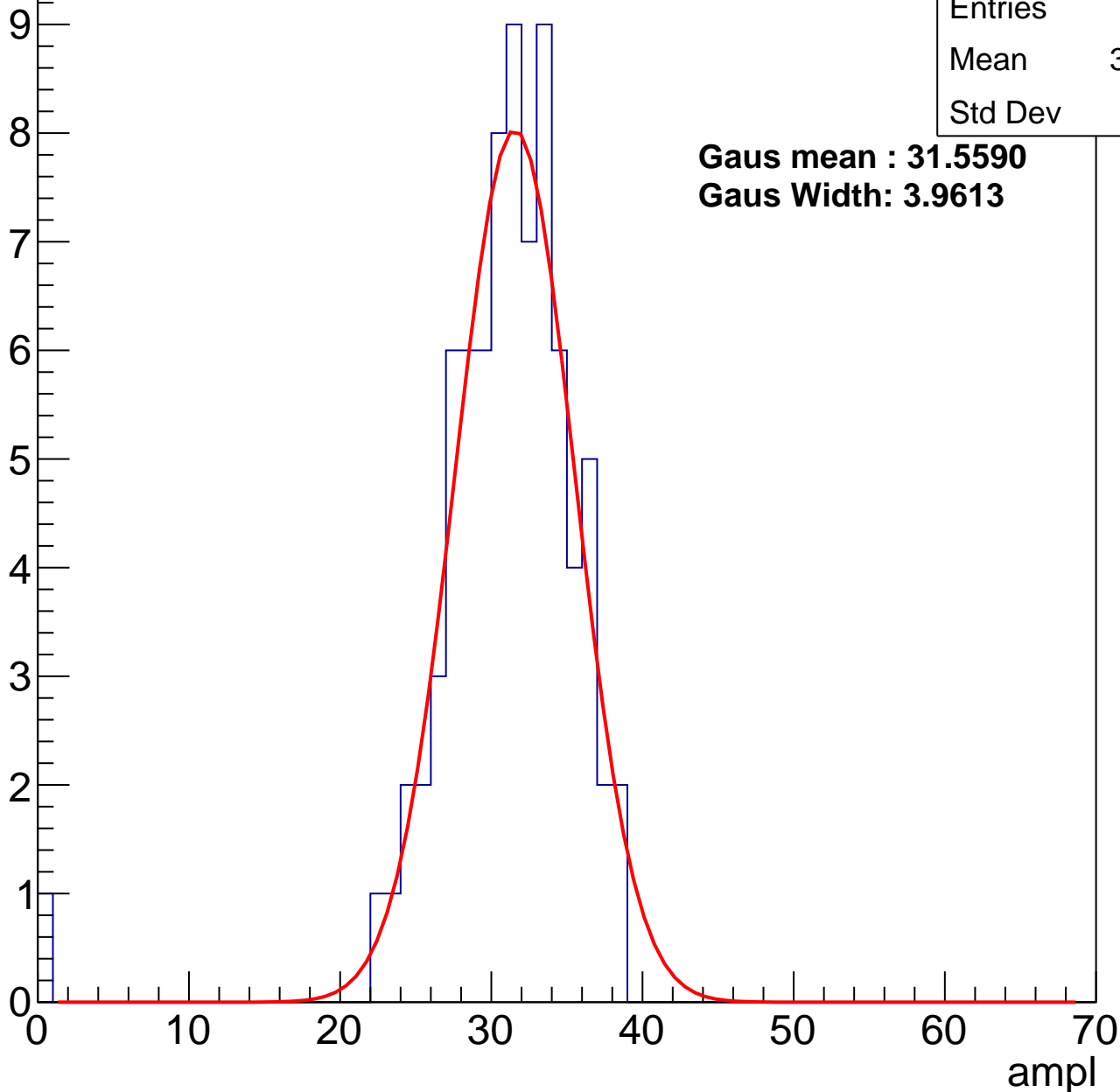
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	30.49
Std Dev	4.96

Gaus mean : 31.5590

Gaus Width: 3.9613



B0L002S, U18-ch82, adc1

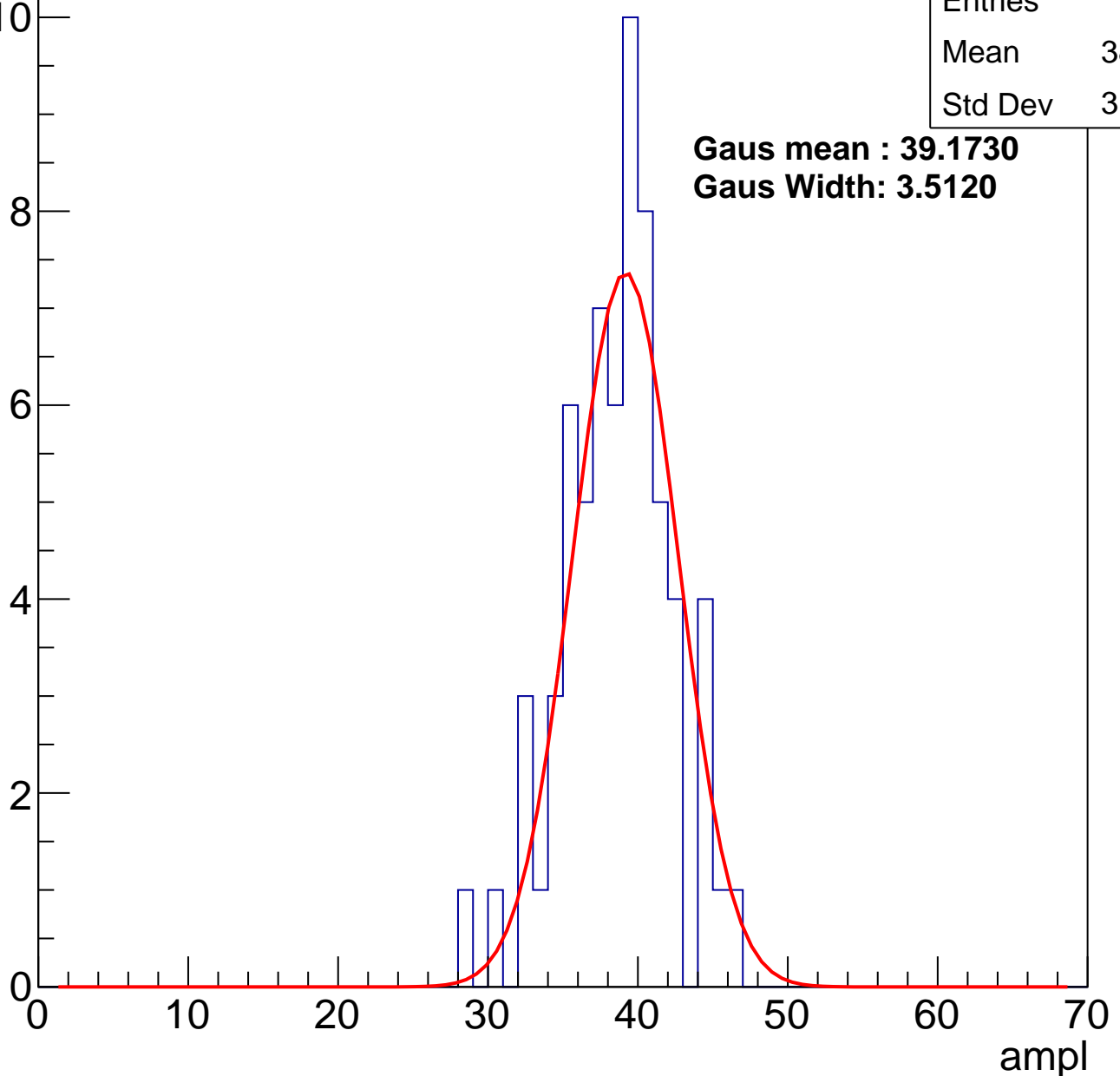
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	38.12
Std Dev	3.553

Gaus mean : 39.1730

Gaus Width: 3.5120



B0L002S, U18-ch82, adc2

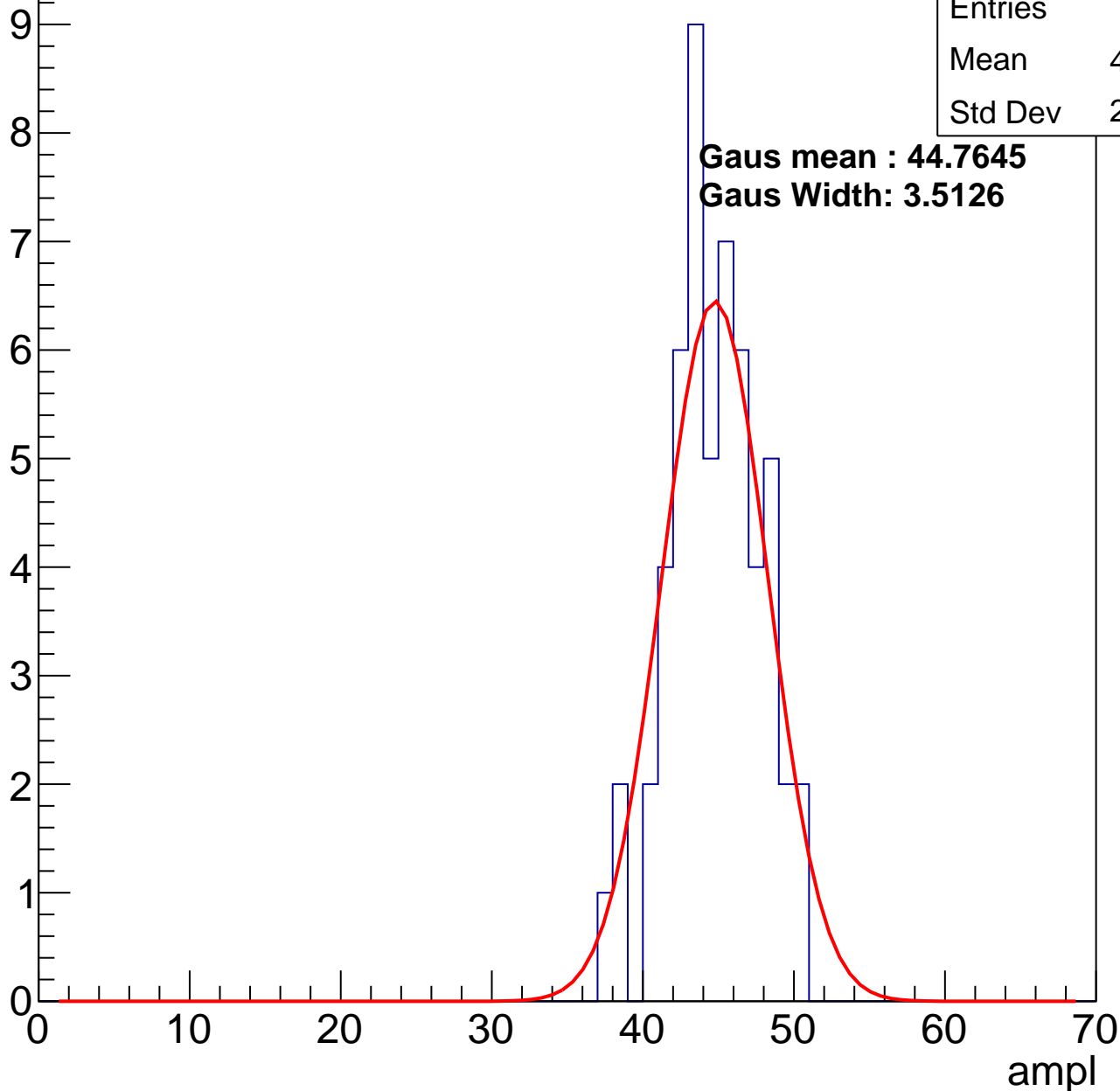
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	44.24
Std Dev	2.978

Gaus mean : 44.7645

Gaus Width: 3.5126

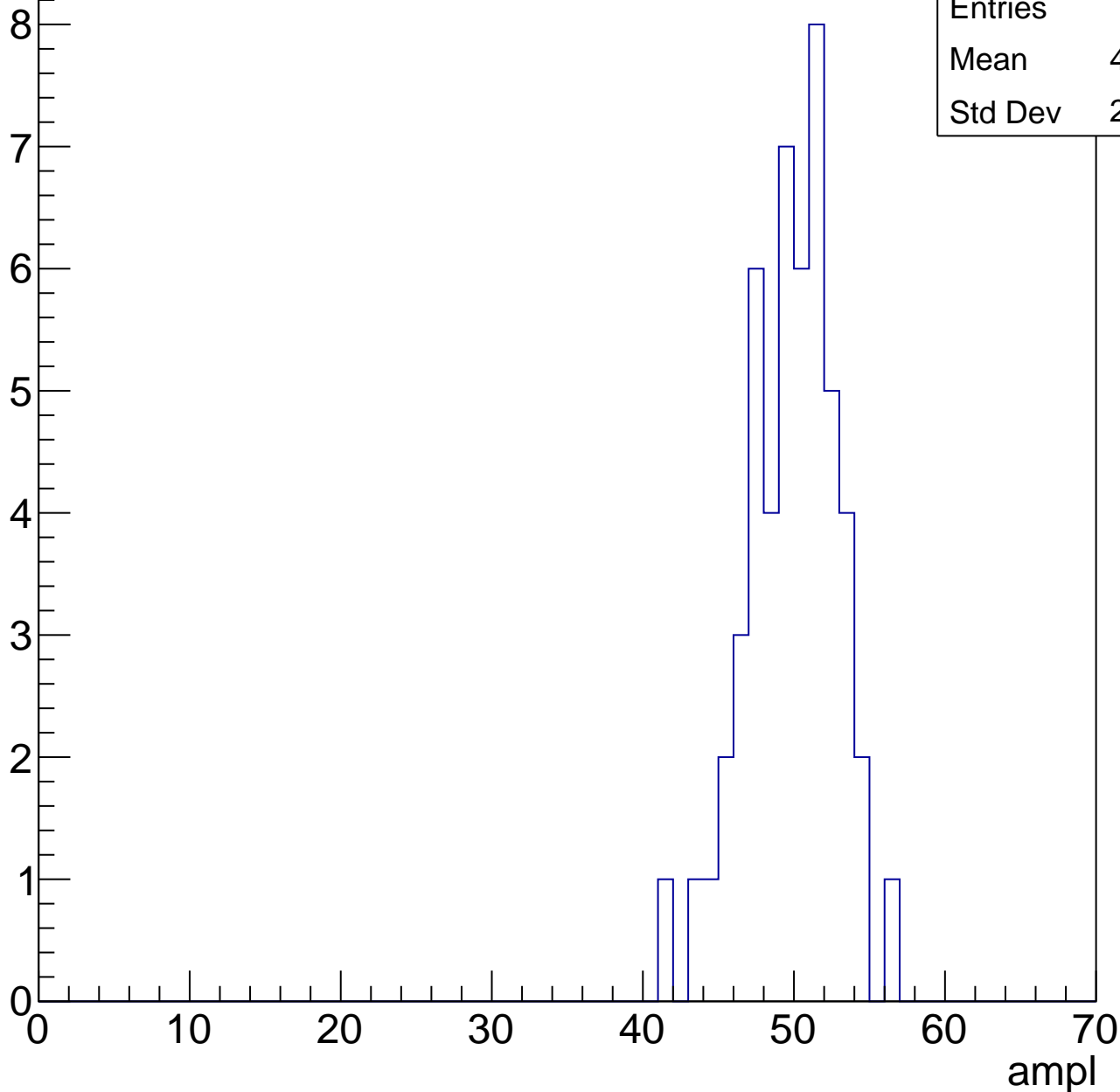


B0L002S, U18-ch82, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

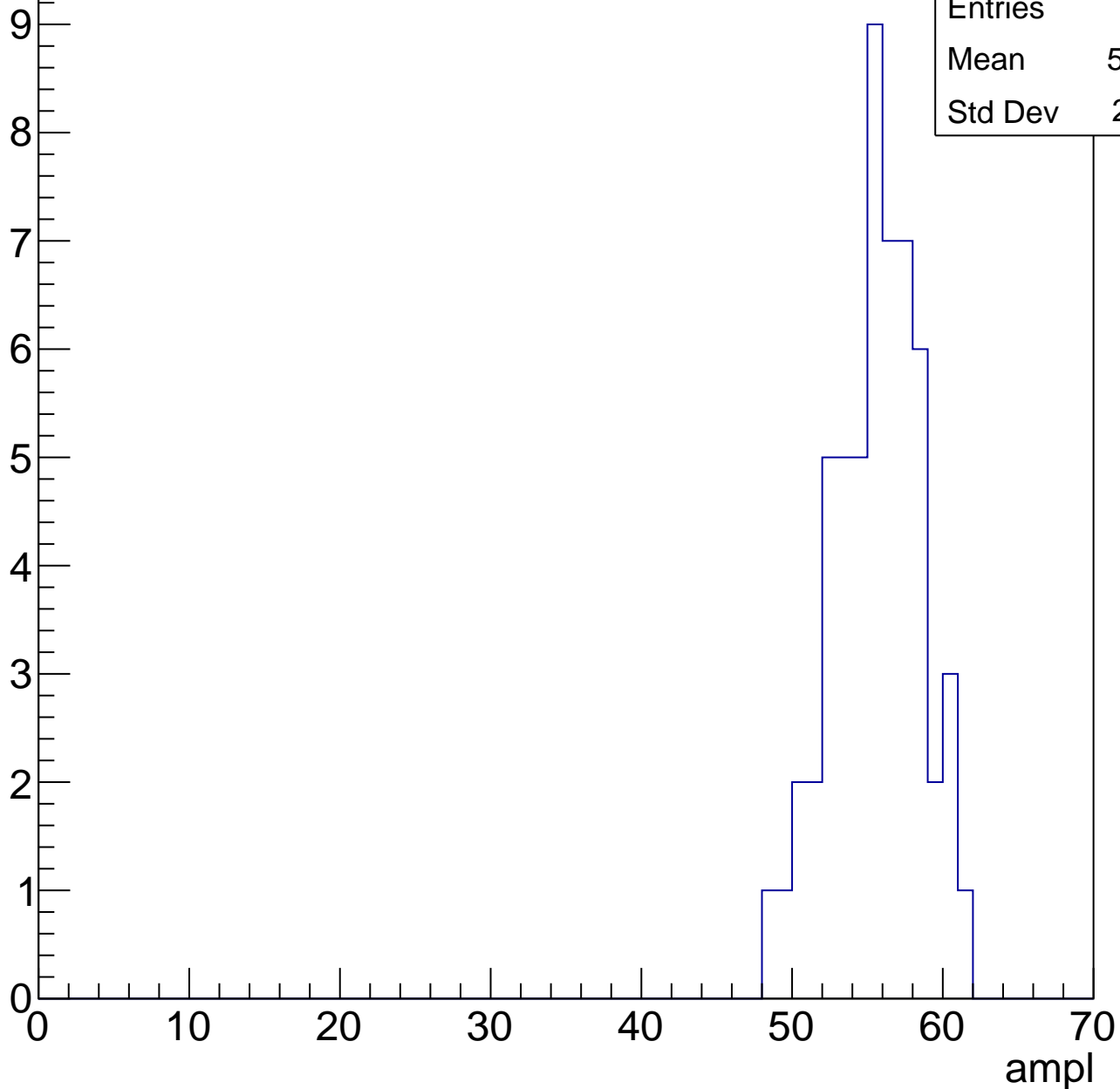
Entries	51
Mean	49.35
Std Dev	2.976



B0L002S, U18-ch82, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	56
Mean	55.12
Std Dev	2.891

B0L002S, U18-ch82, adc5

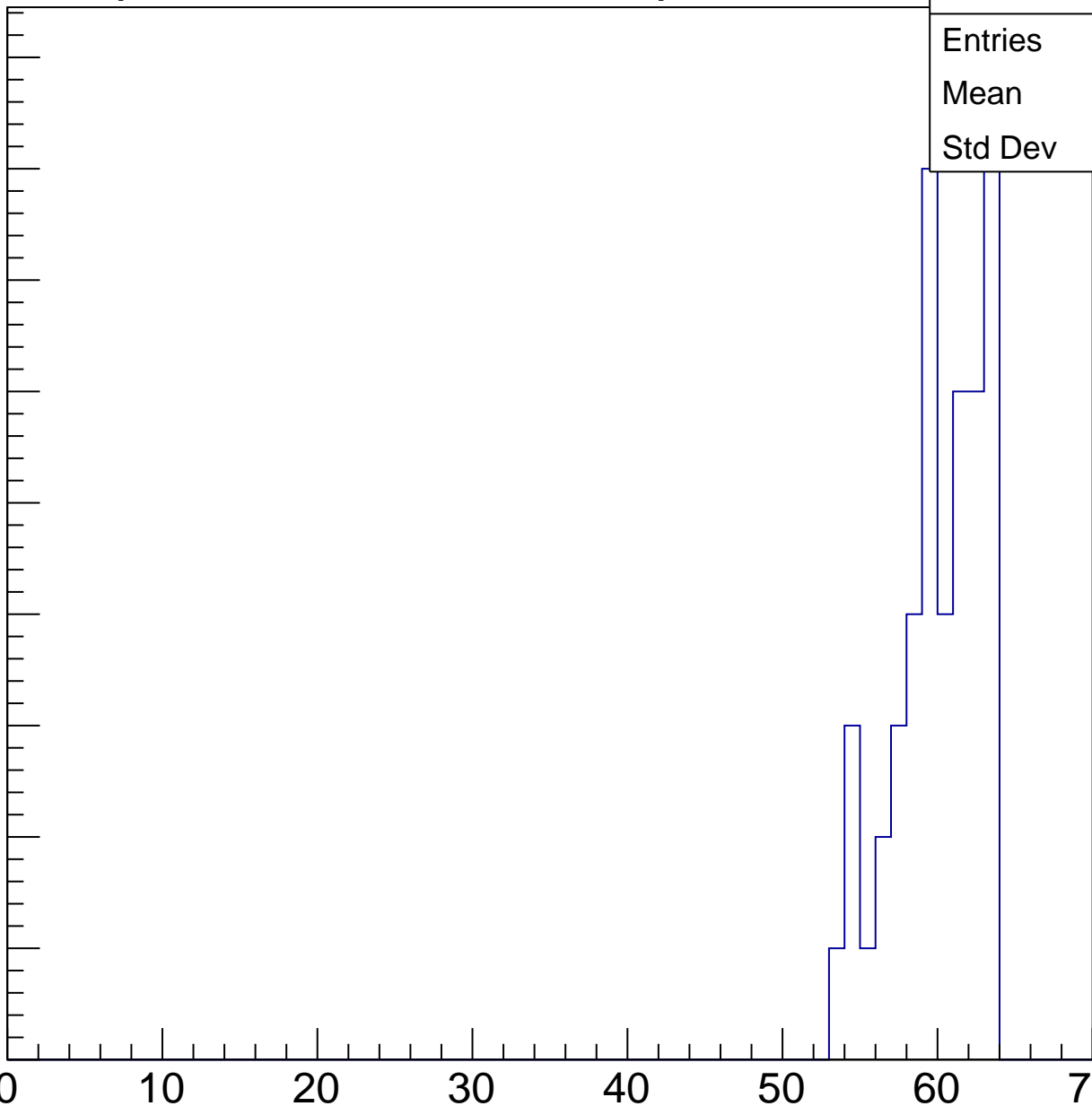
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	47
Mean	59.62
Std Dev	2.802

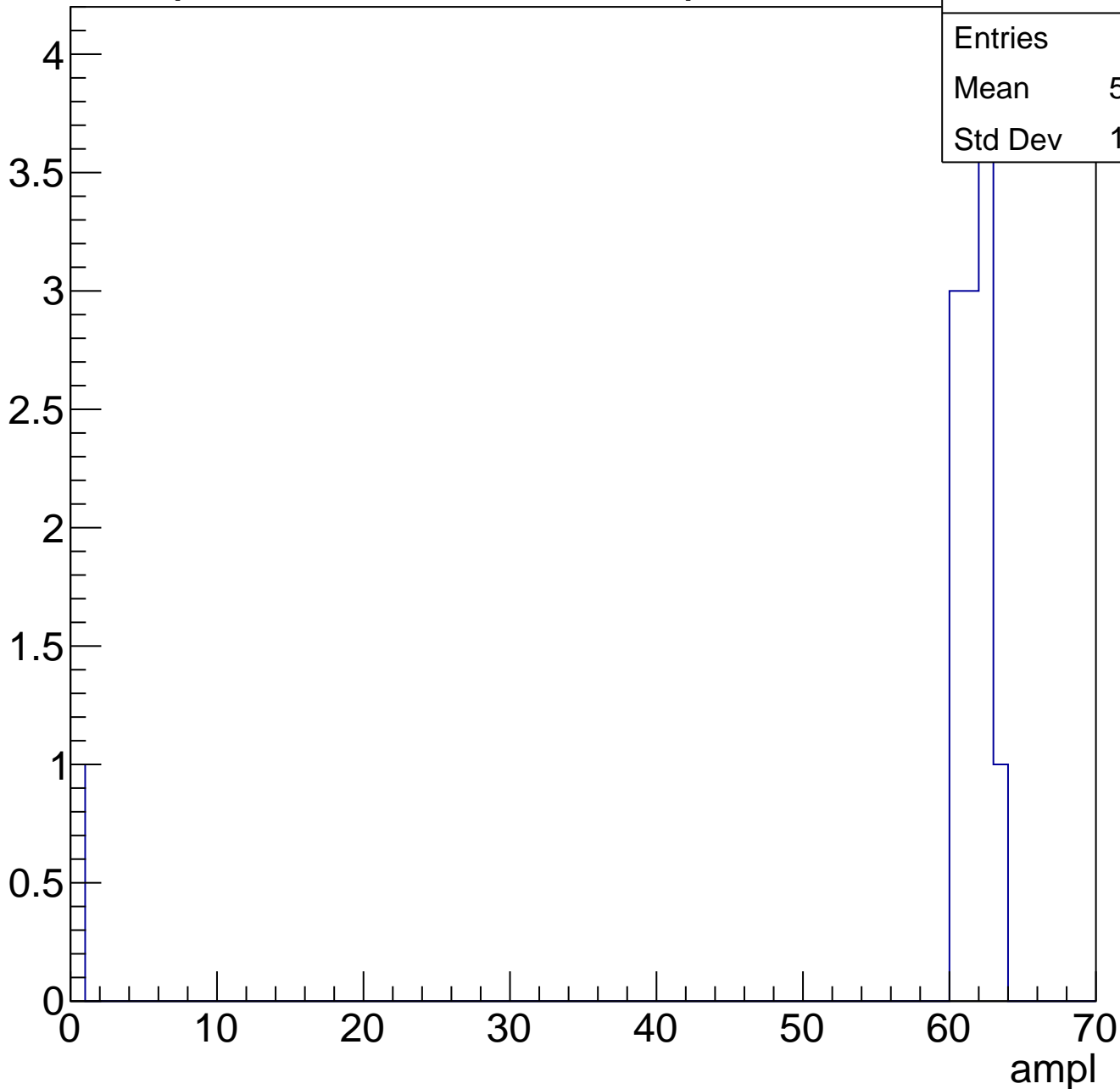
ampl



B0L002S, U18-ch82, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch82, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch83, adc0

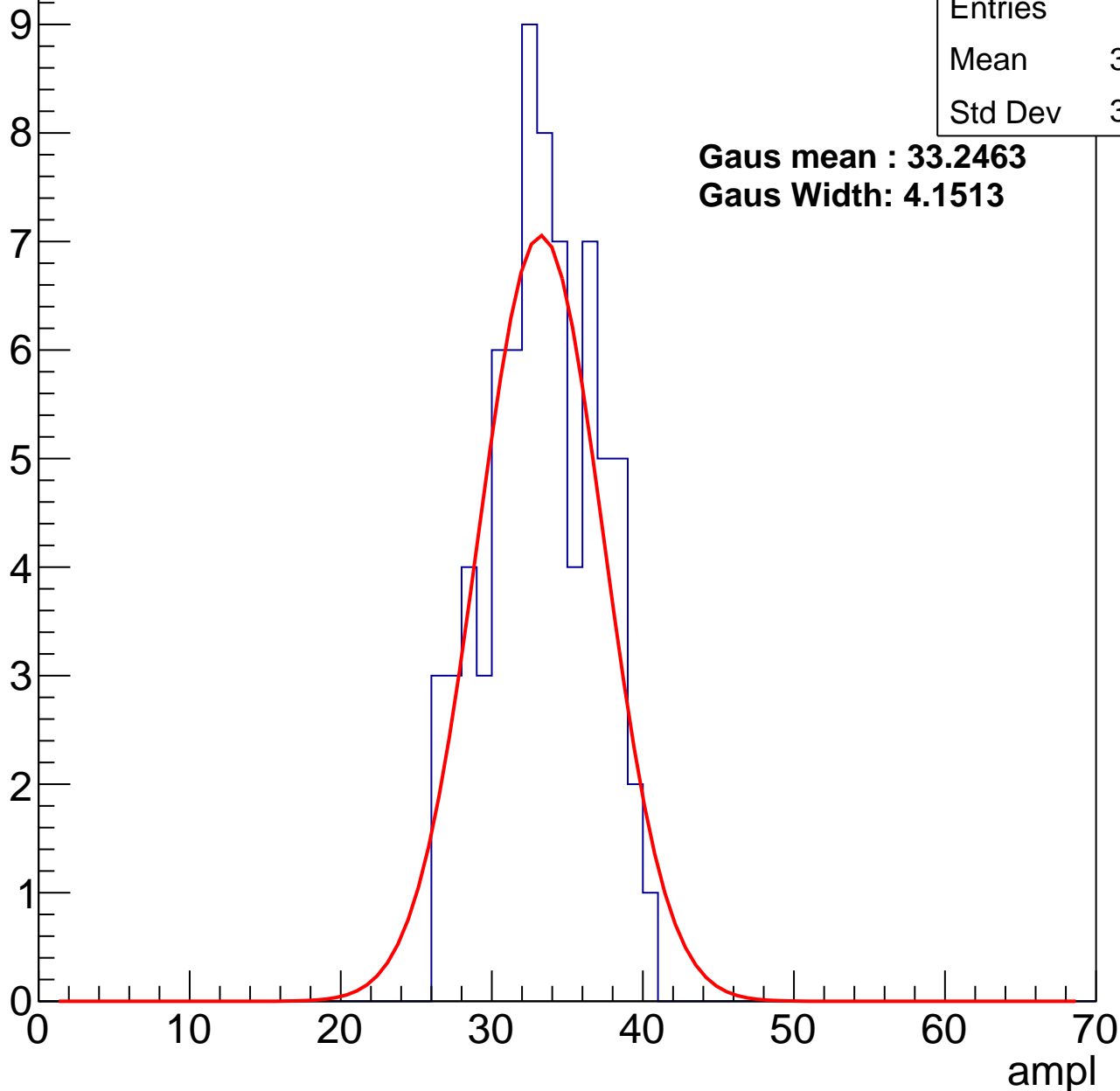
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	32.86
Std Dev	3.512

Gaus mean : 33.2463

Gaus Width: 4.1513



B0L002S, U18-ch83, adc1

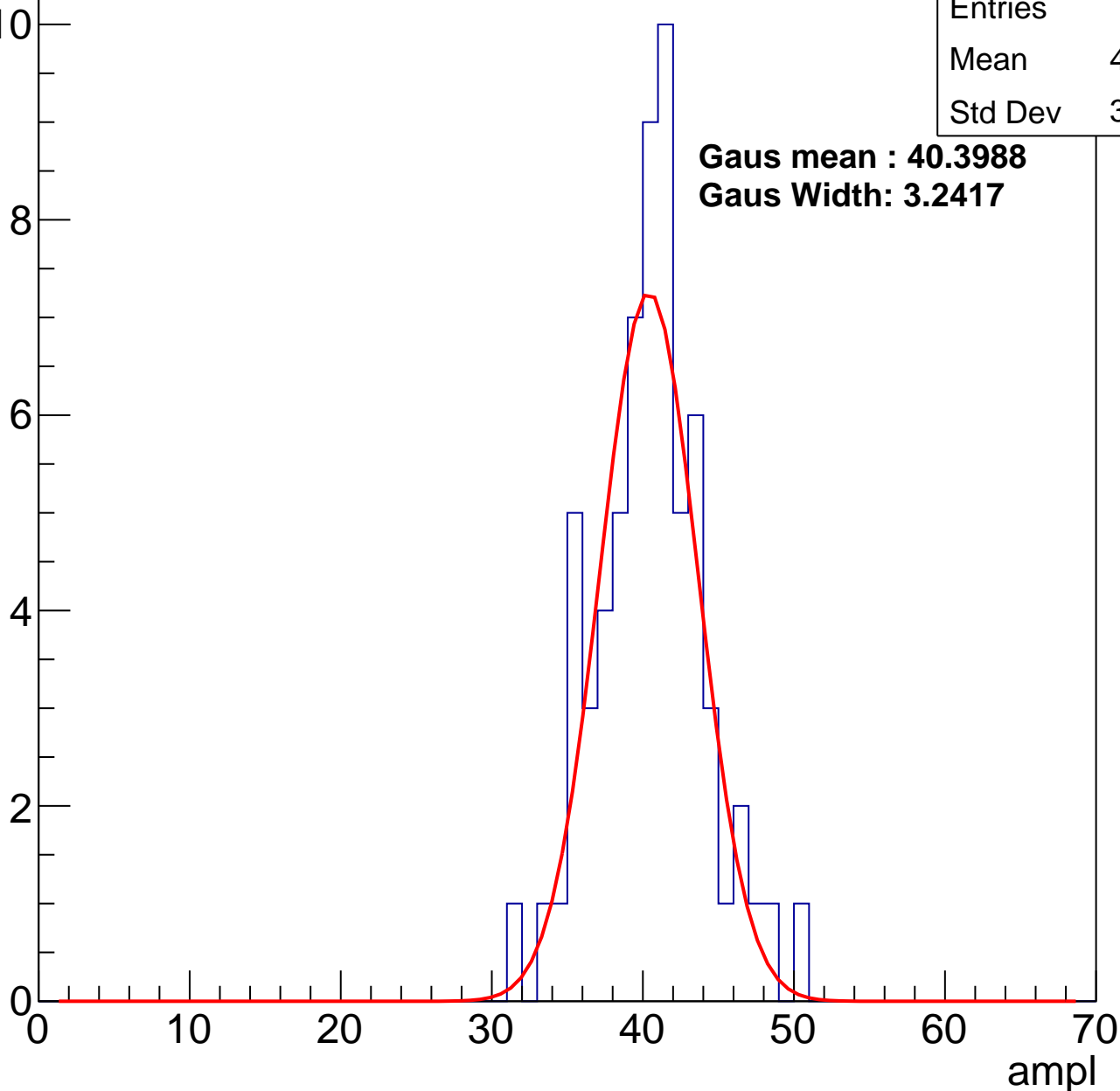
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	40.06
Std Dev	3.576

Gaus mean : 40.3988

Gaus Width: 3.2417



B0L002S, U18-ch83, adc2

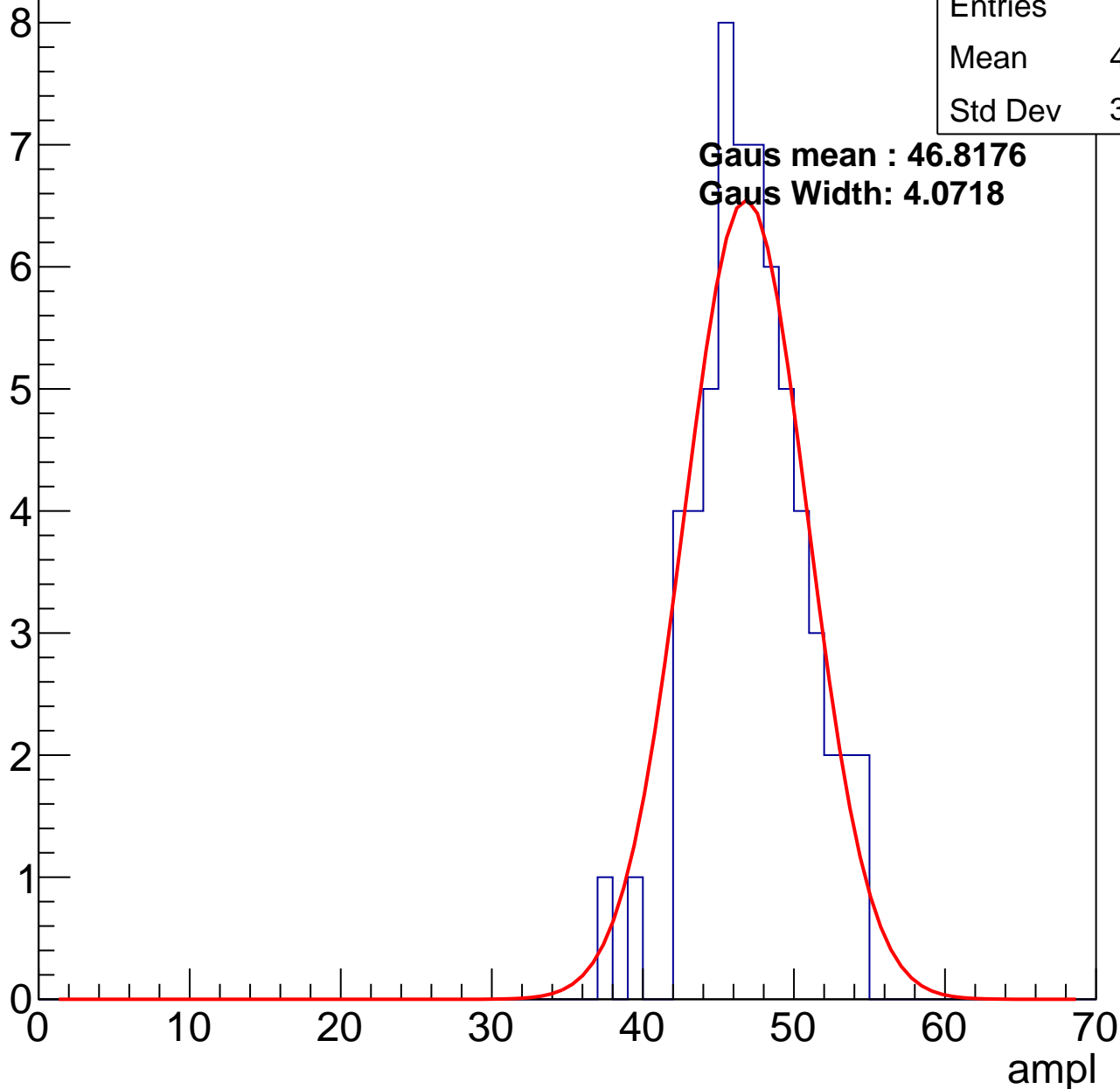
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	46.74
Std Dev	3.478

Gaus mean : 46.8176

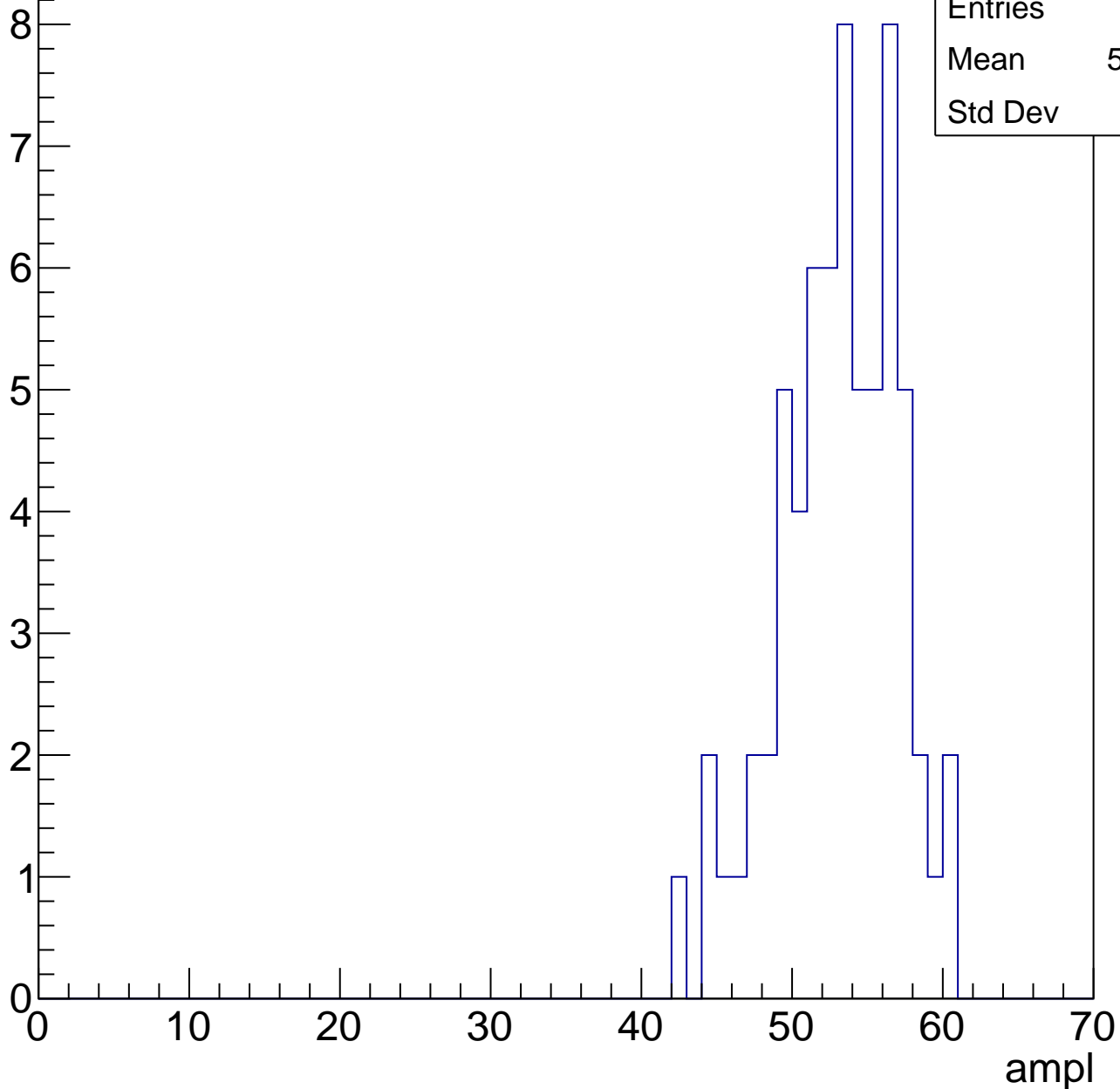
Gaus Width: 4.0718



B0L002S, U18-ch83, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



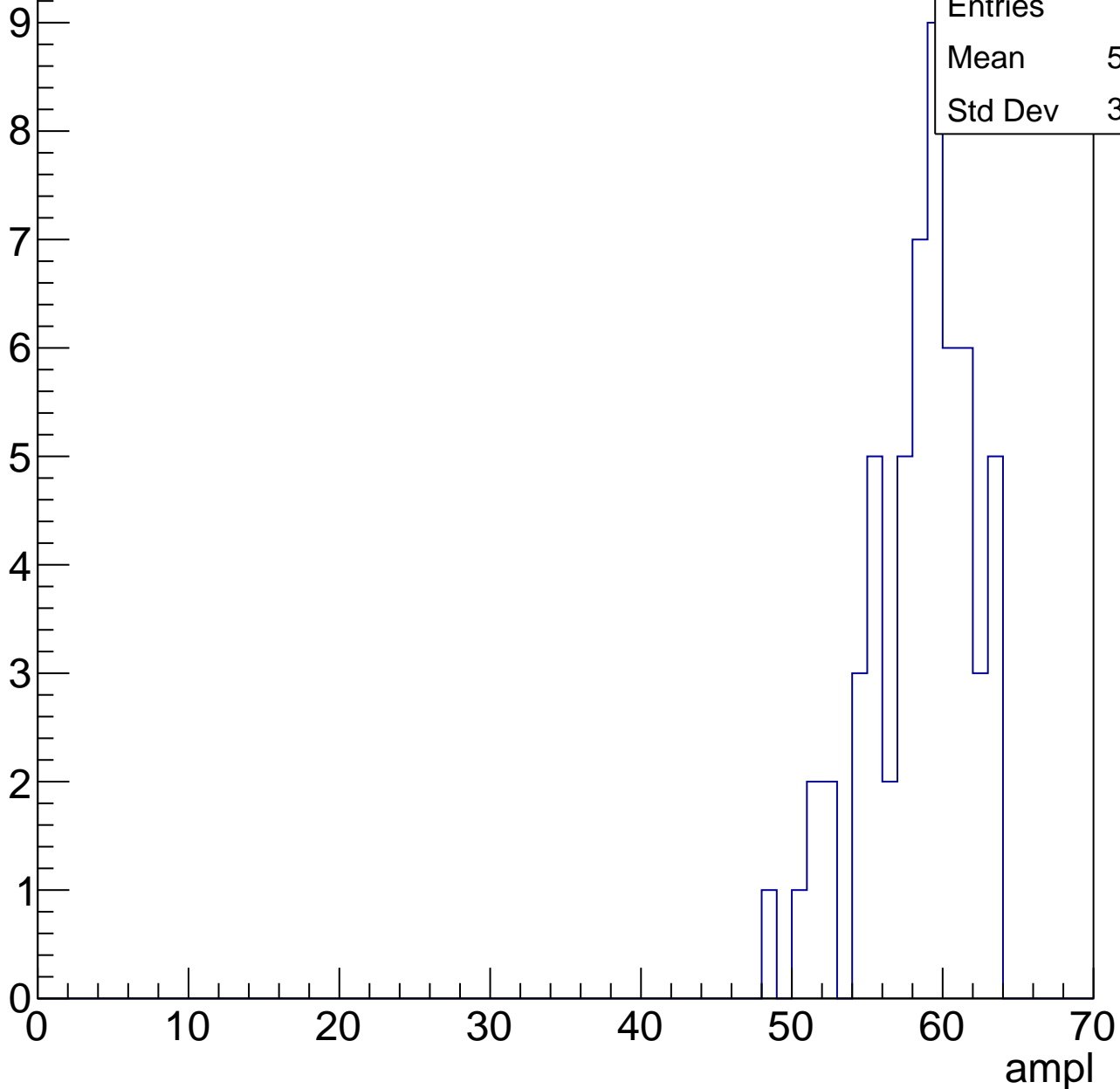
Entries	66
Mean	52.59
Std Dev	3.92

B0L002S, U18-ch83, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	57.93
Std Dev	3.504

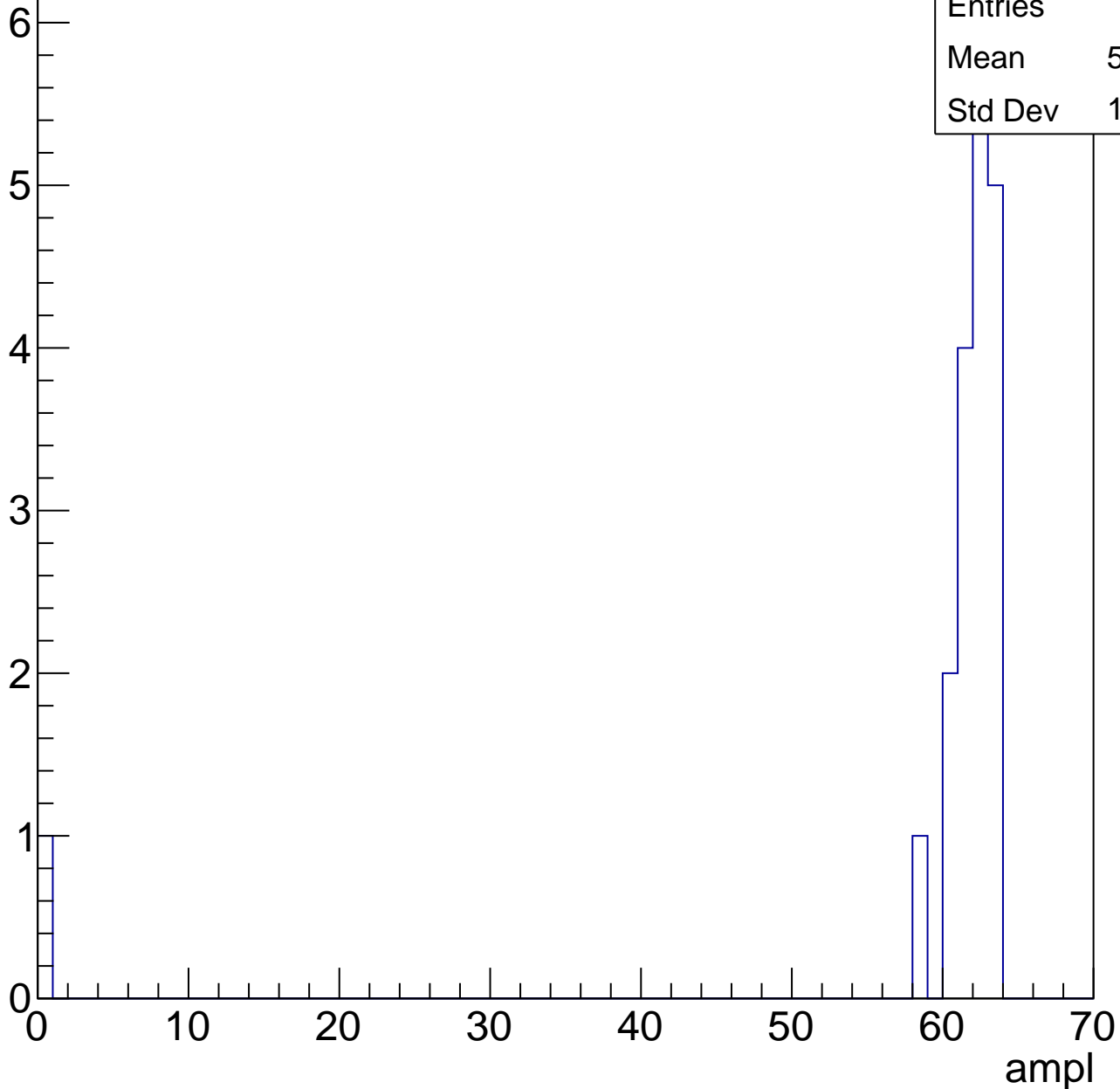


B0L002S, U18-ch83, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	19
Mean	58.37
Std Dev	13.82



B0L002S, U18-ch83, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch83, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch84, adc0

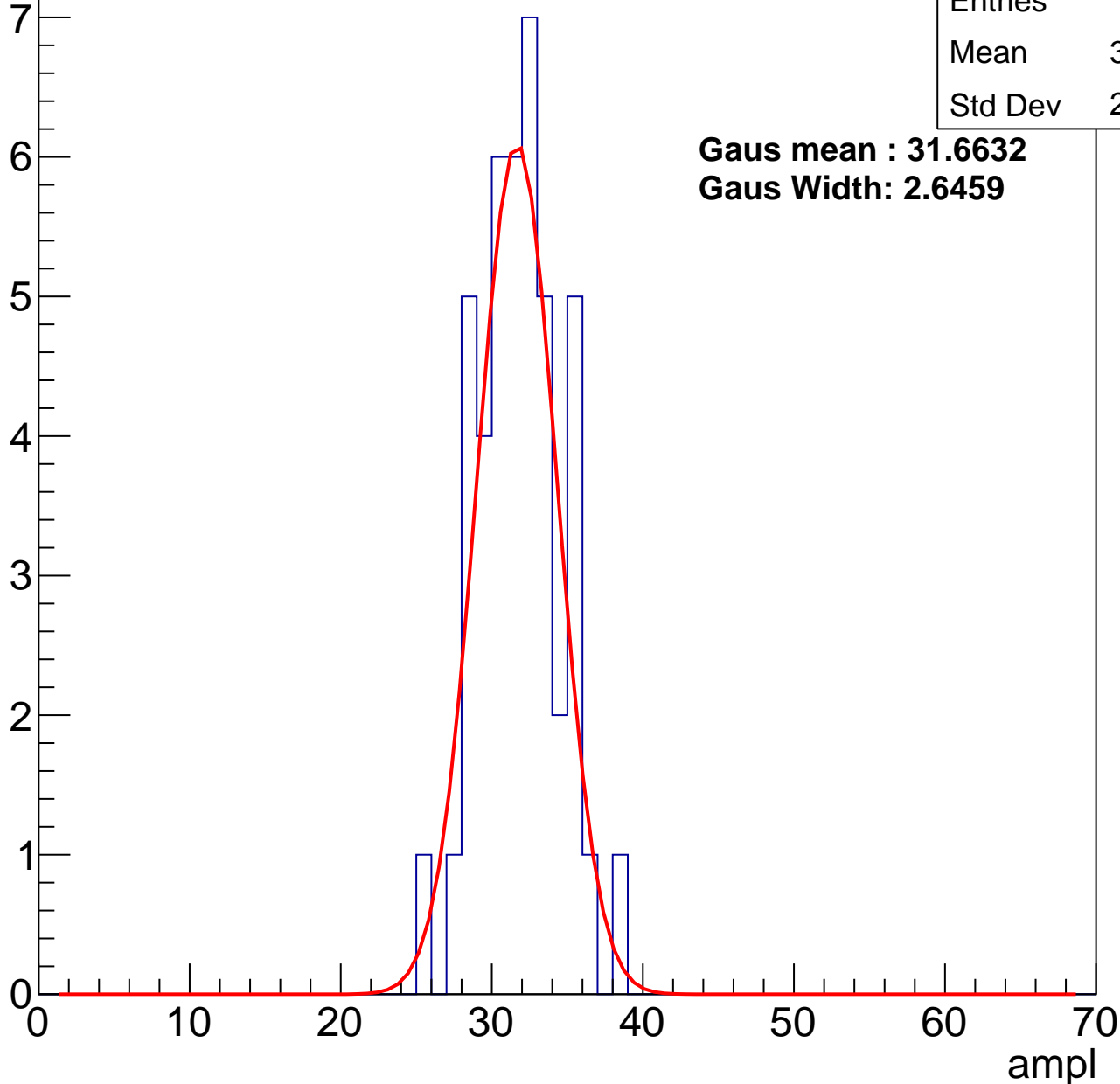
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	31.36
Std Dev	2.664

Gaus mean : 31.6632

Gaus Width: 2.6459



B0L002S, U18-ch84, adc1

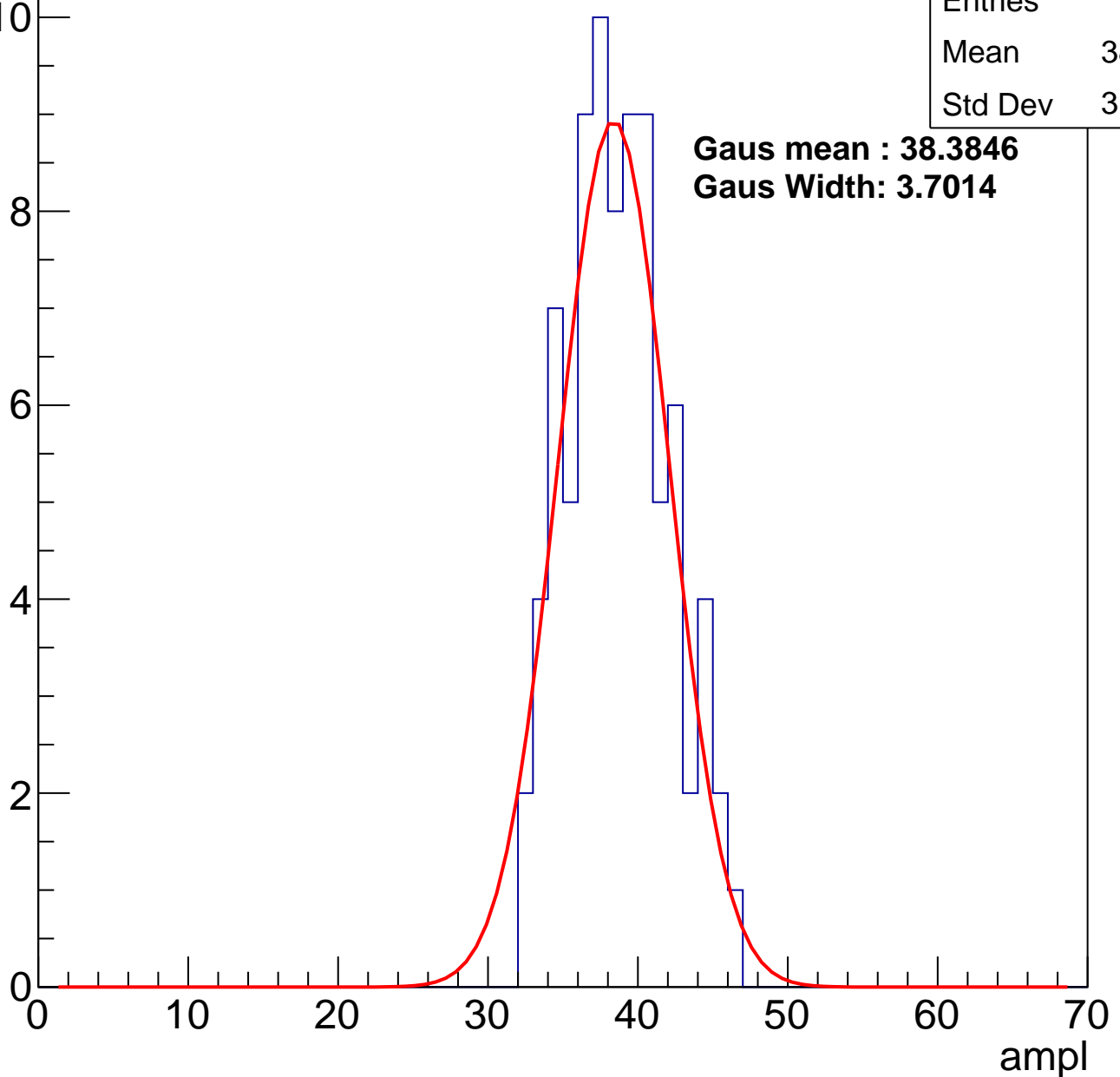
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	38.23
Std Dev	3.316

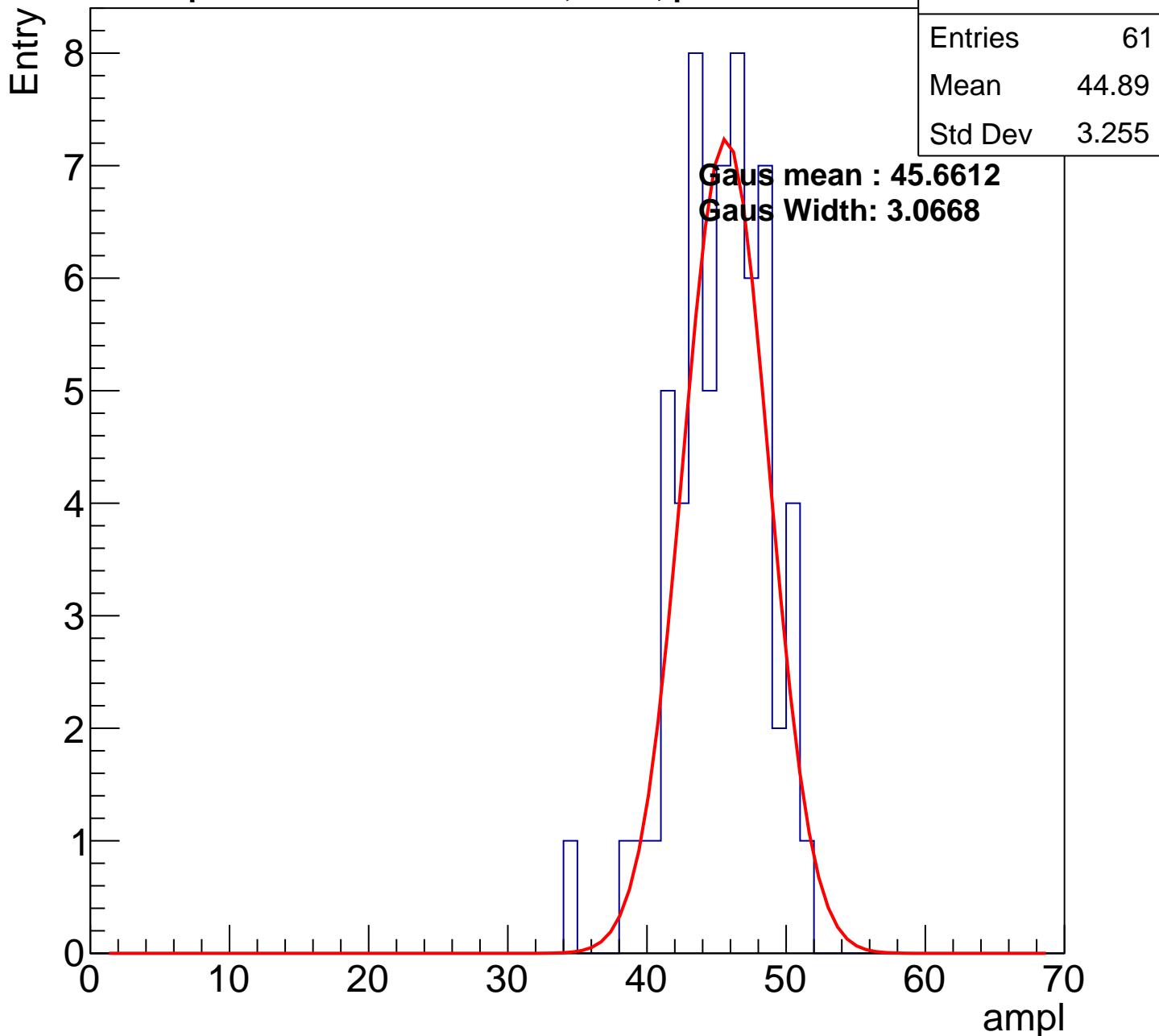
Gaus mean : 38.3846

Gaus Width: 3.7014



B0L002S, U18-ch84, adc2

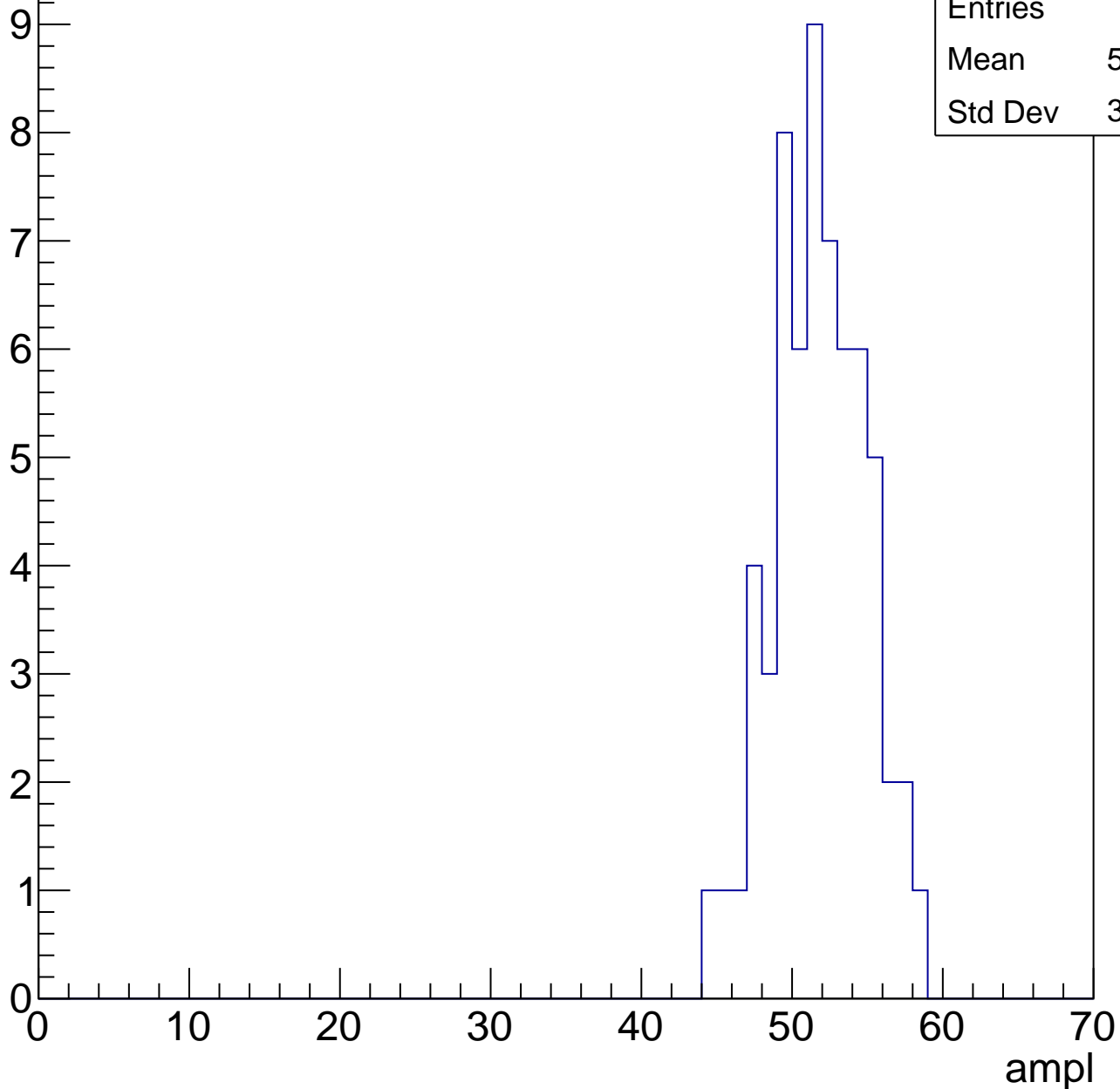
calib_packv5_042523_0143.root, FC#8, port C1



B0L002S, U18-ch84, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



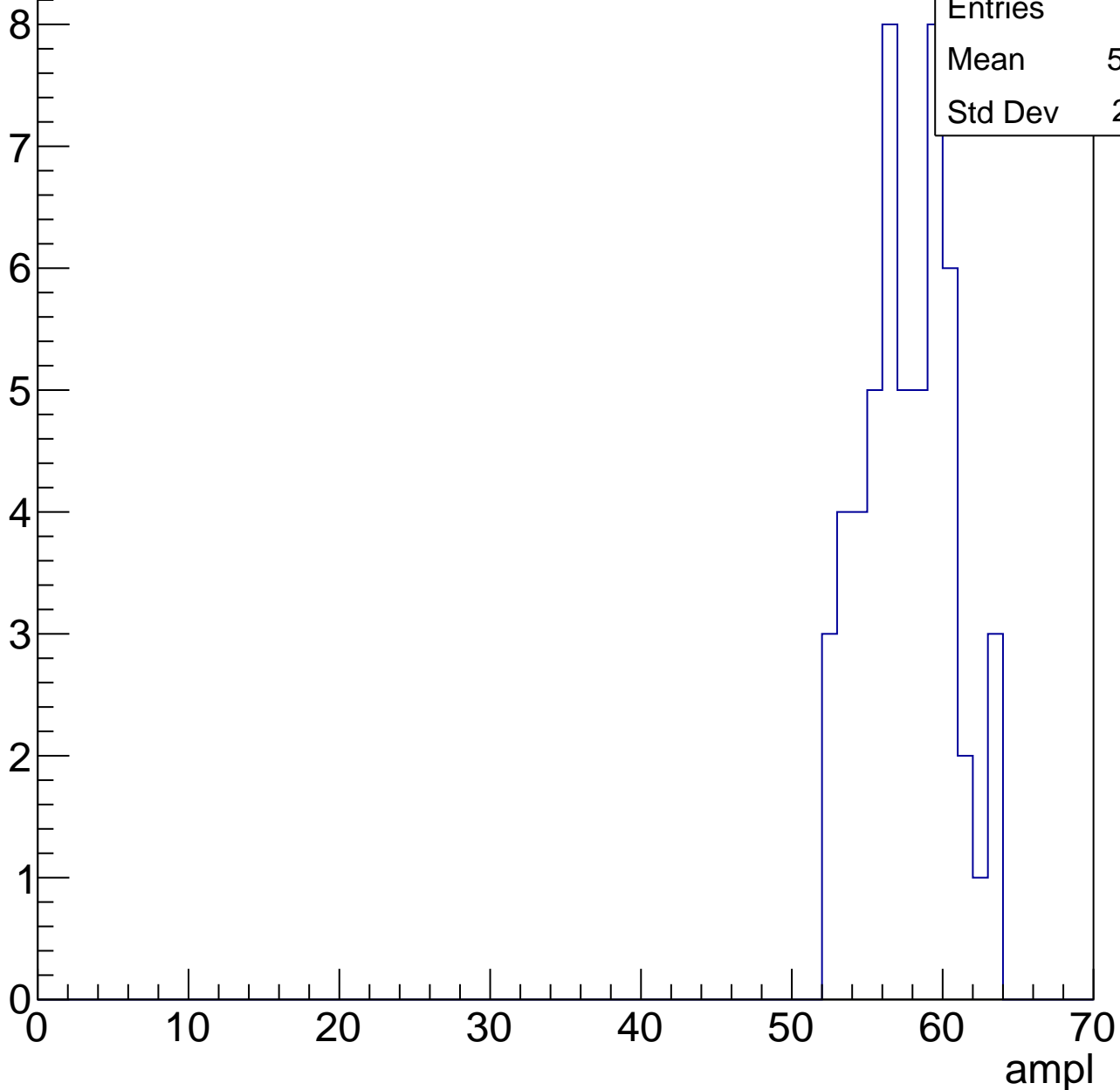
Entries	62
Mean	51.34
Std Dev	3.026

B0L002S, U18-ch84, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57.17
Std Dev	2.911

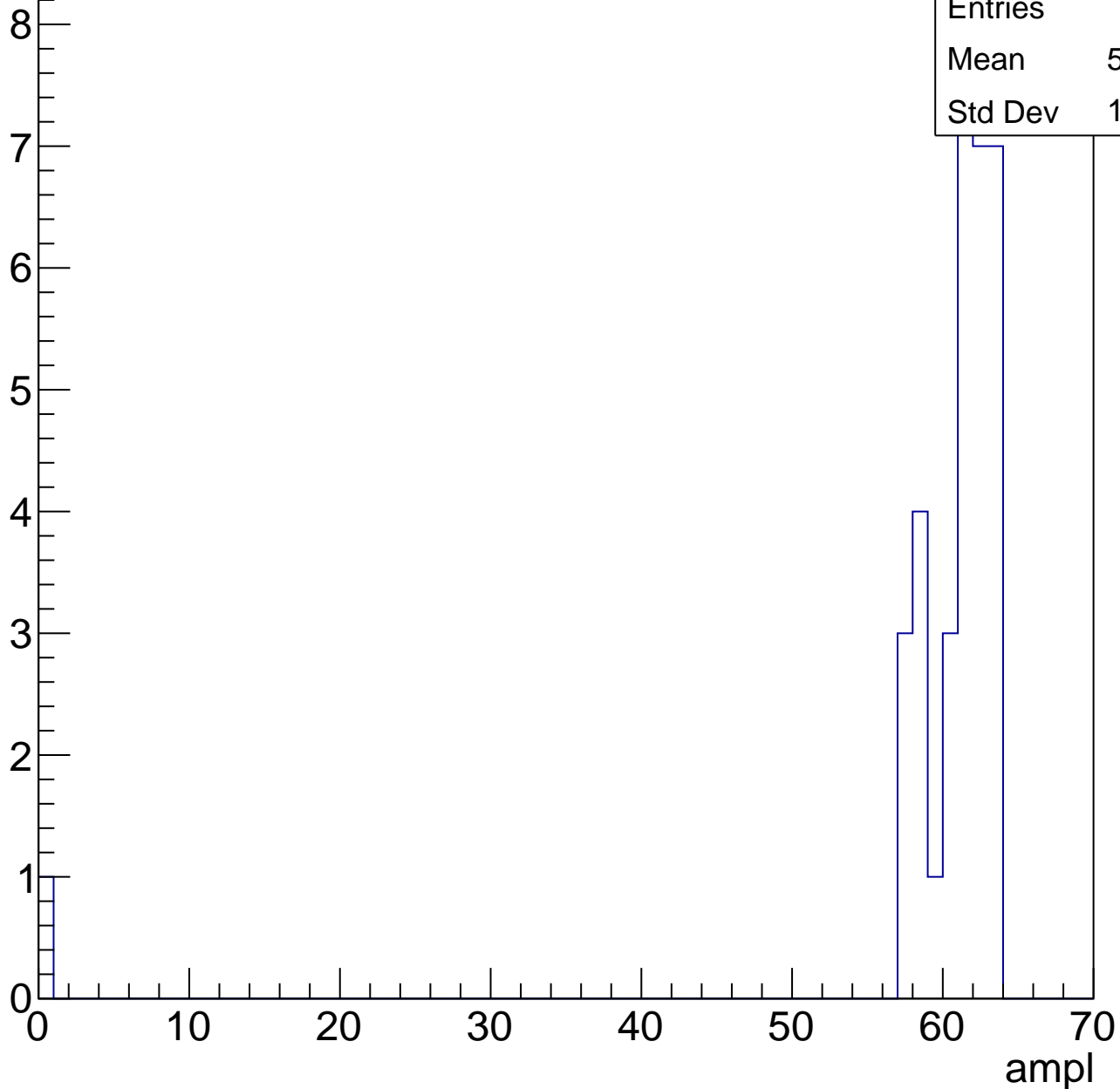


B0L002S, U18-ch84, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

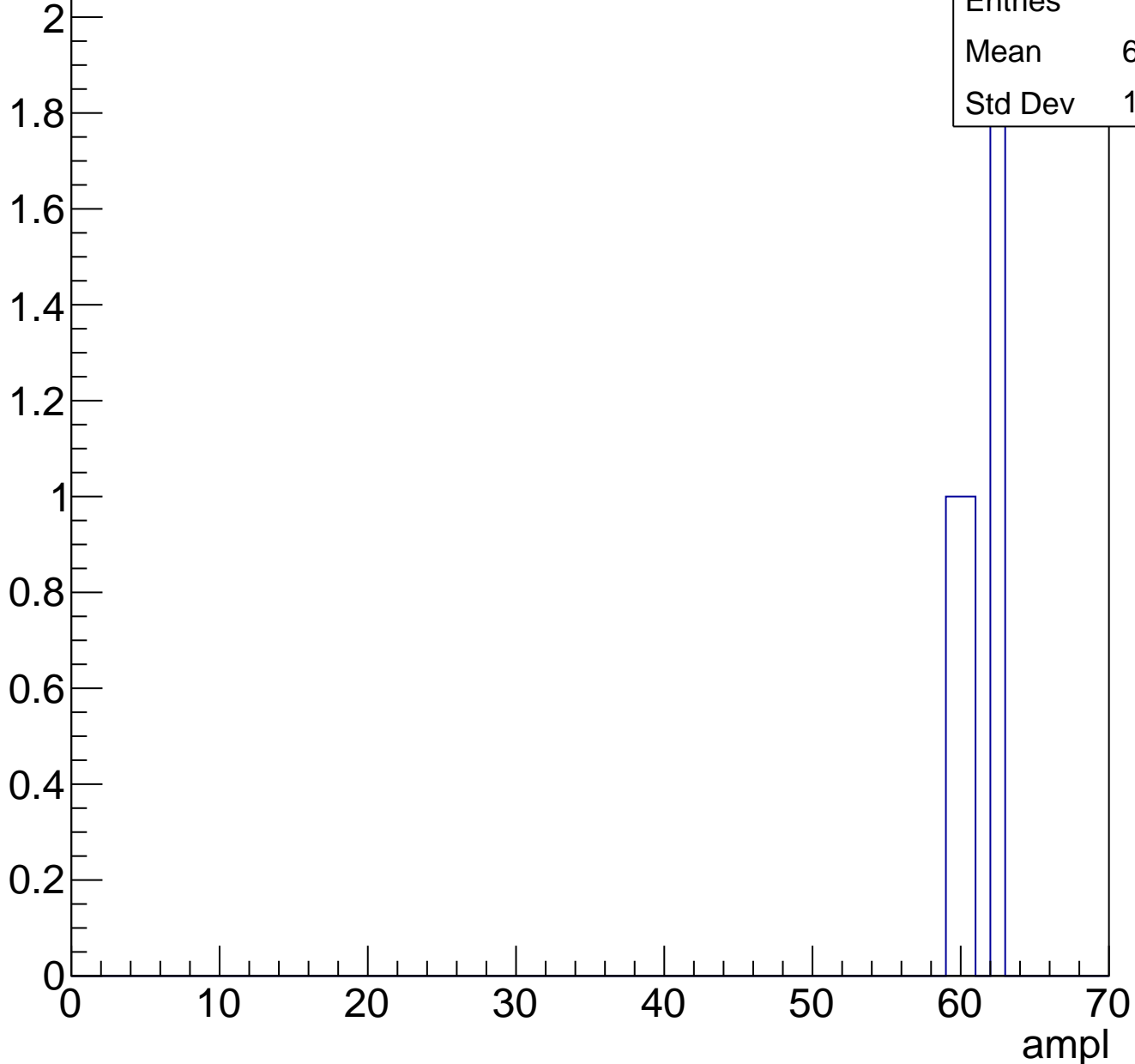
Entries	34
Mean	58.97
Std Dev	10.44



B0L002S, U18-ch84, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch84, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch85, adc0

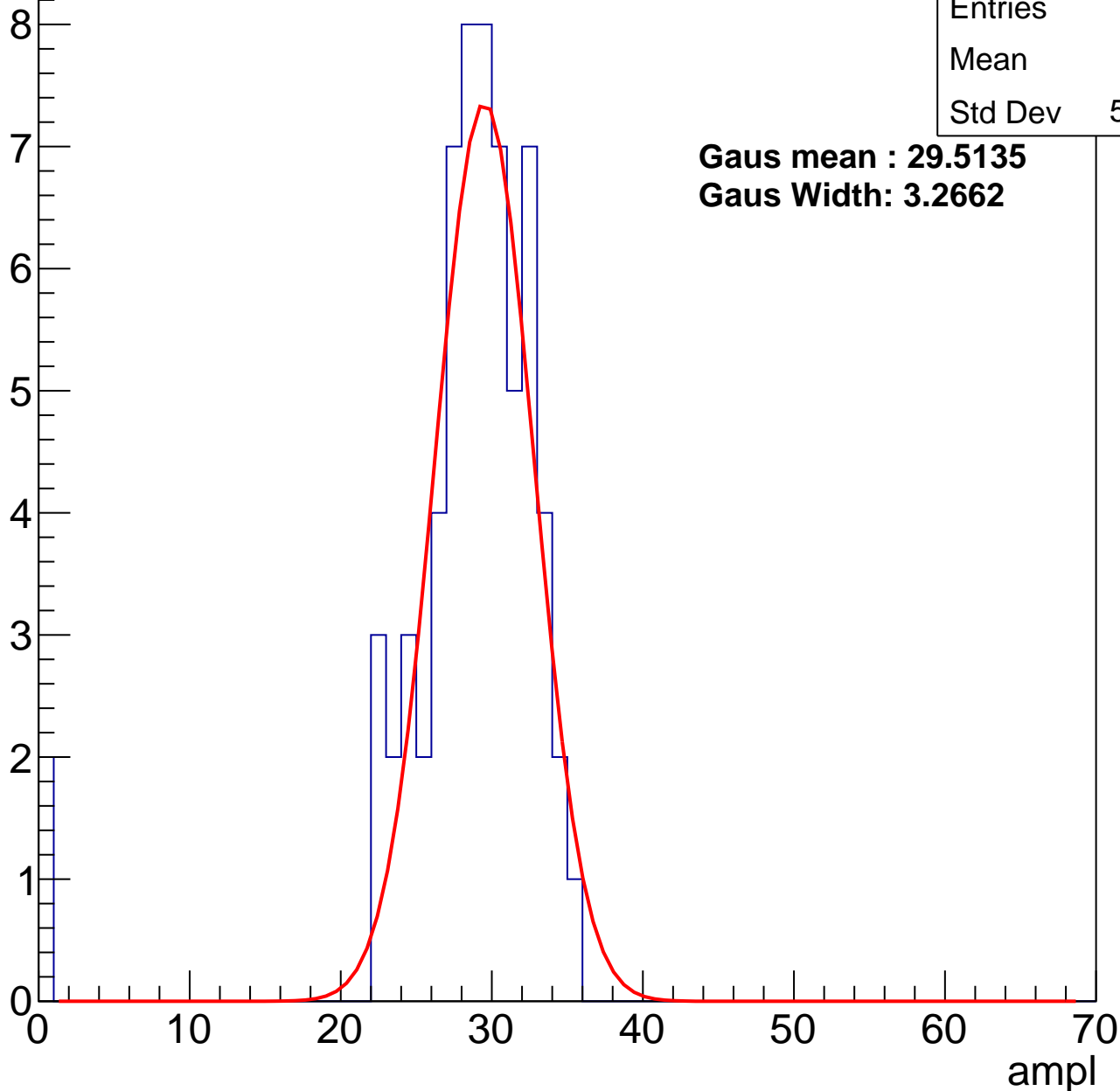
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	27.8
Std Dev	5.853

Gaus mean : 29.5135

Gaus Width: 3.2662



B0L002S, U18-ch85, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	89
Mean	36.57
Std Dev	4.165

Gaus mean : 36.7489

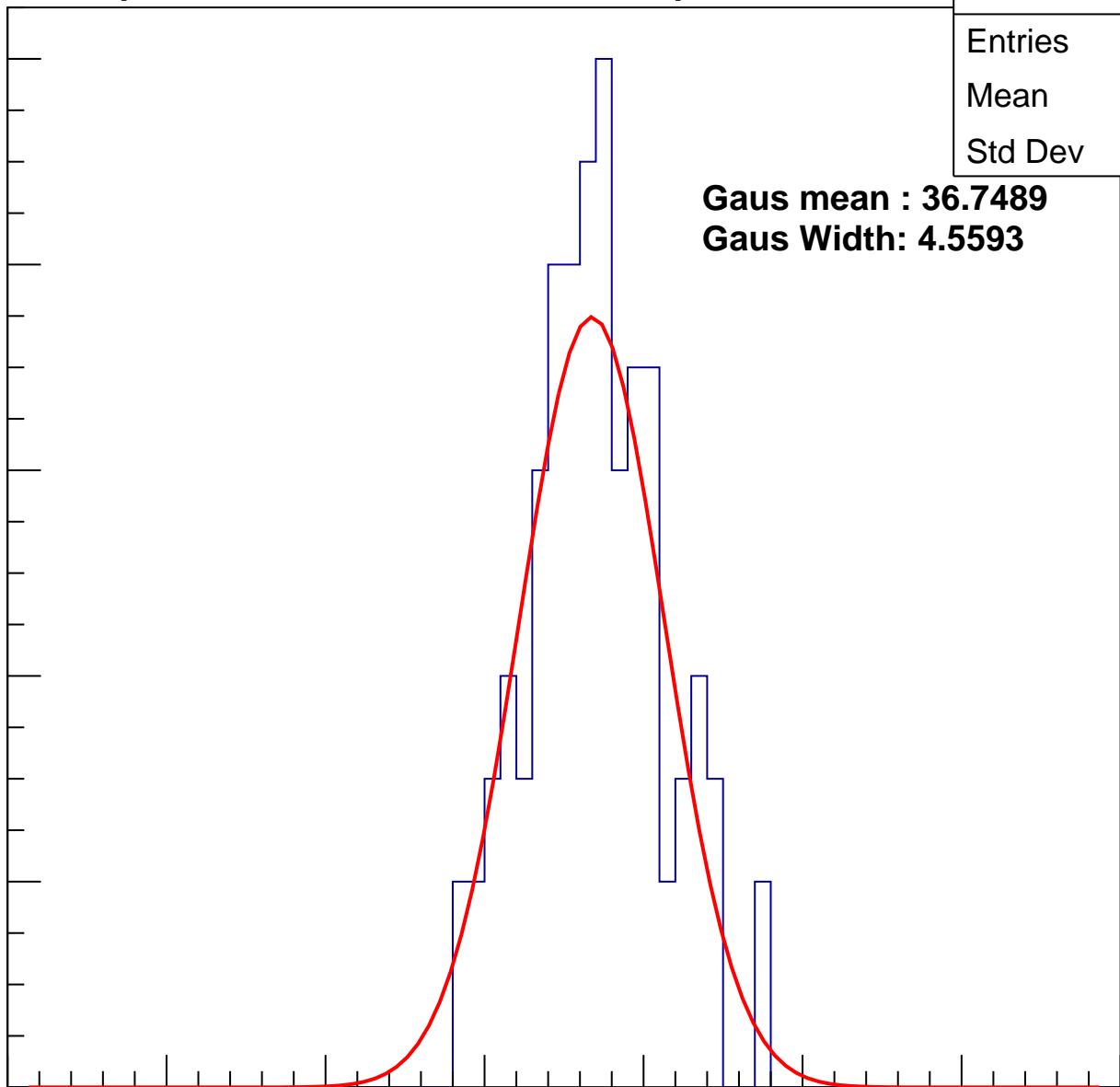
Gaus Width: 4.5593

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch85, adc2

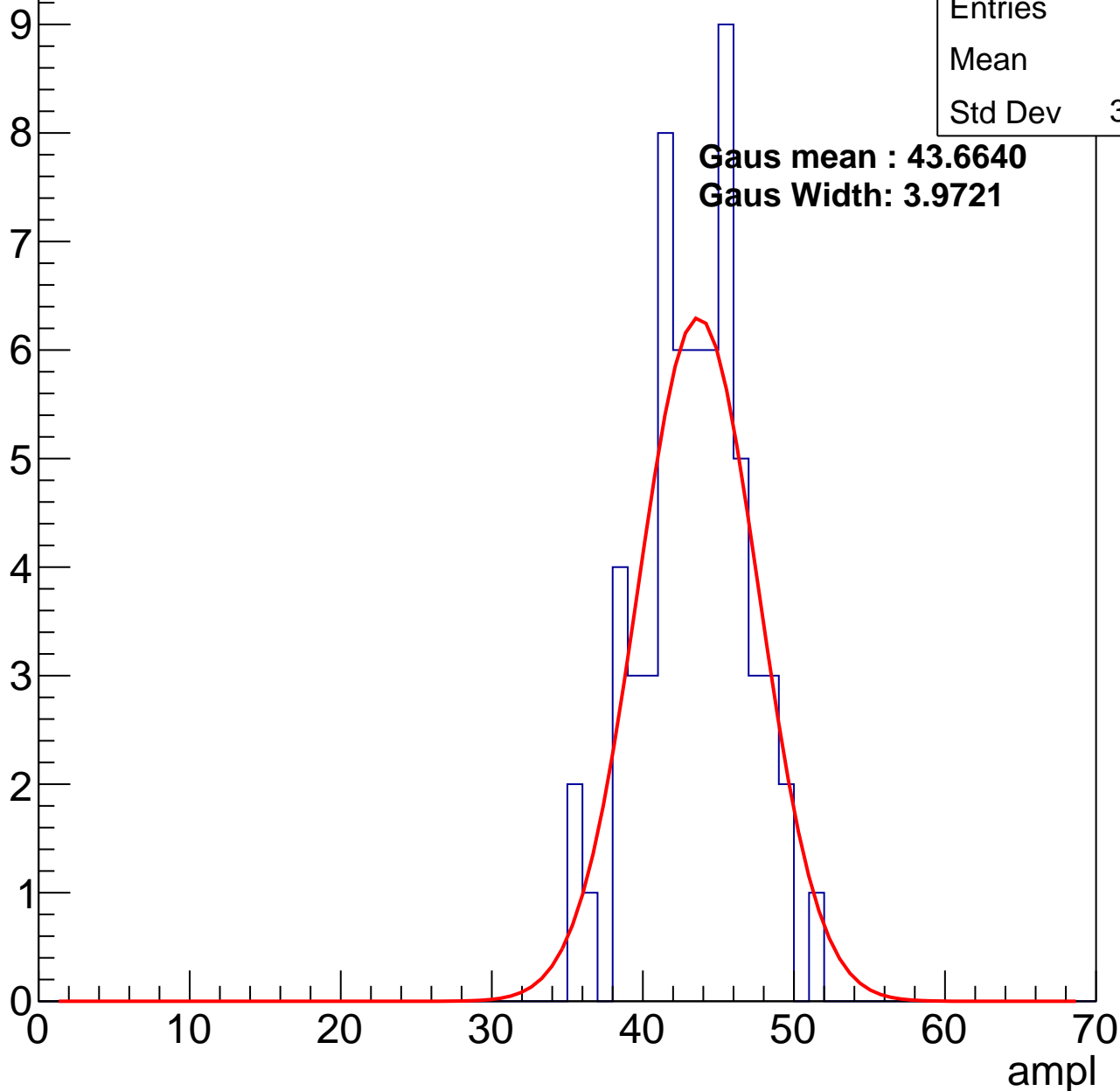
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43
Std Dev	3.445

Gaus mean : 43.6640

Gaus Width: 3.9721

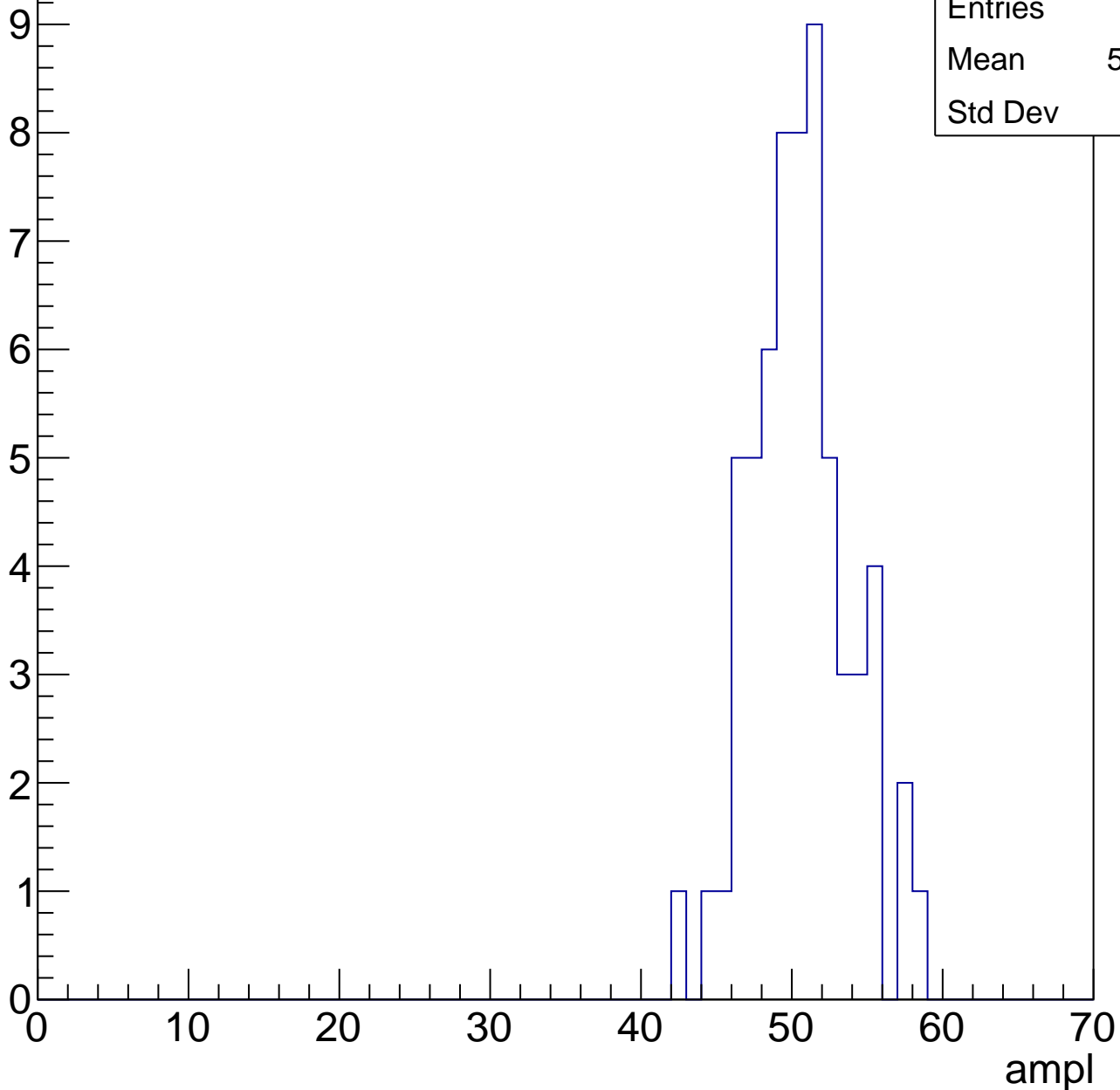


B0L002S, U18-ch85, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	50.13
Std Dev	3.23

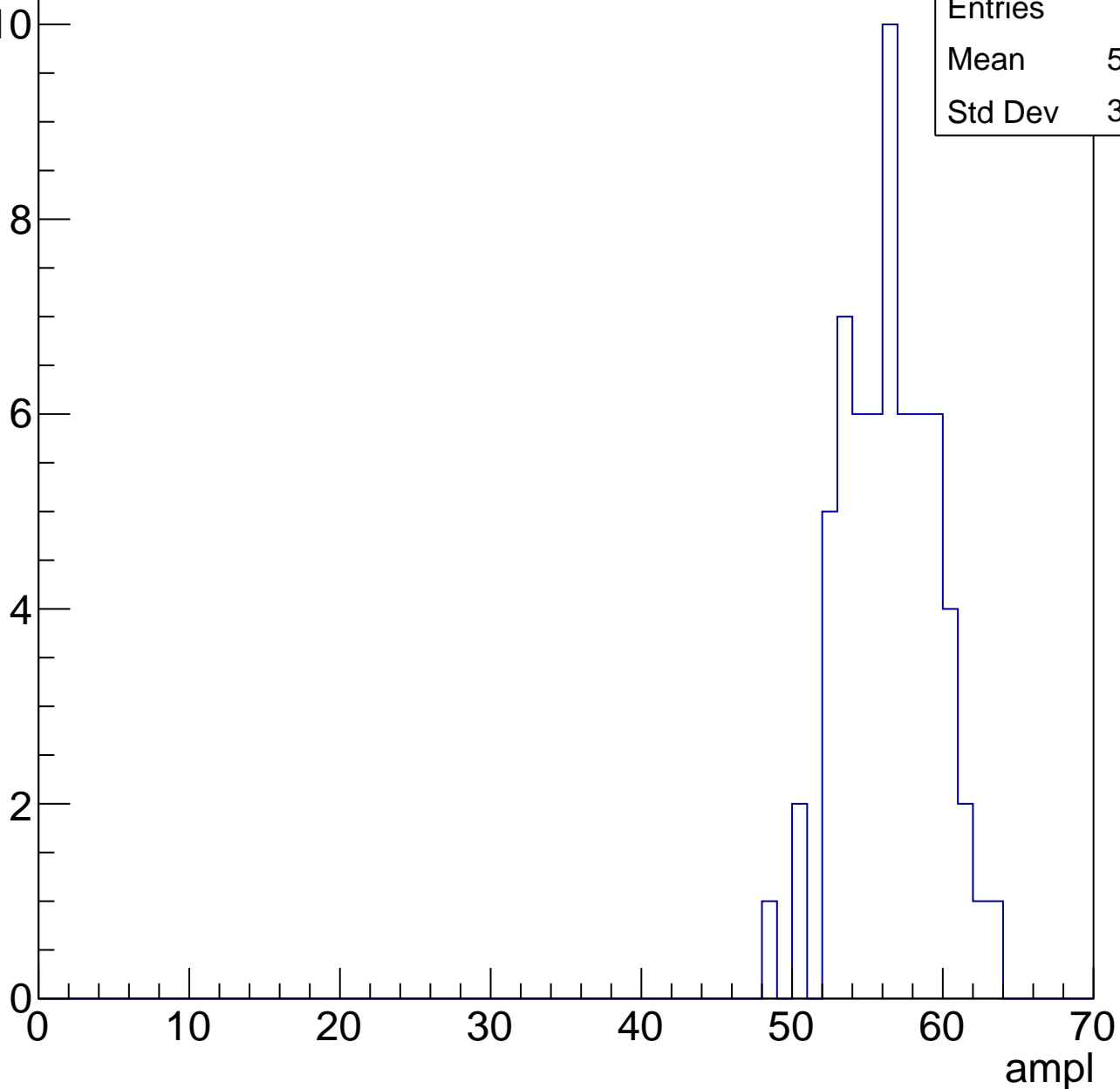


B0L002S, U18-ch85, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	55.94
Std Dev	3.065

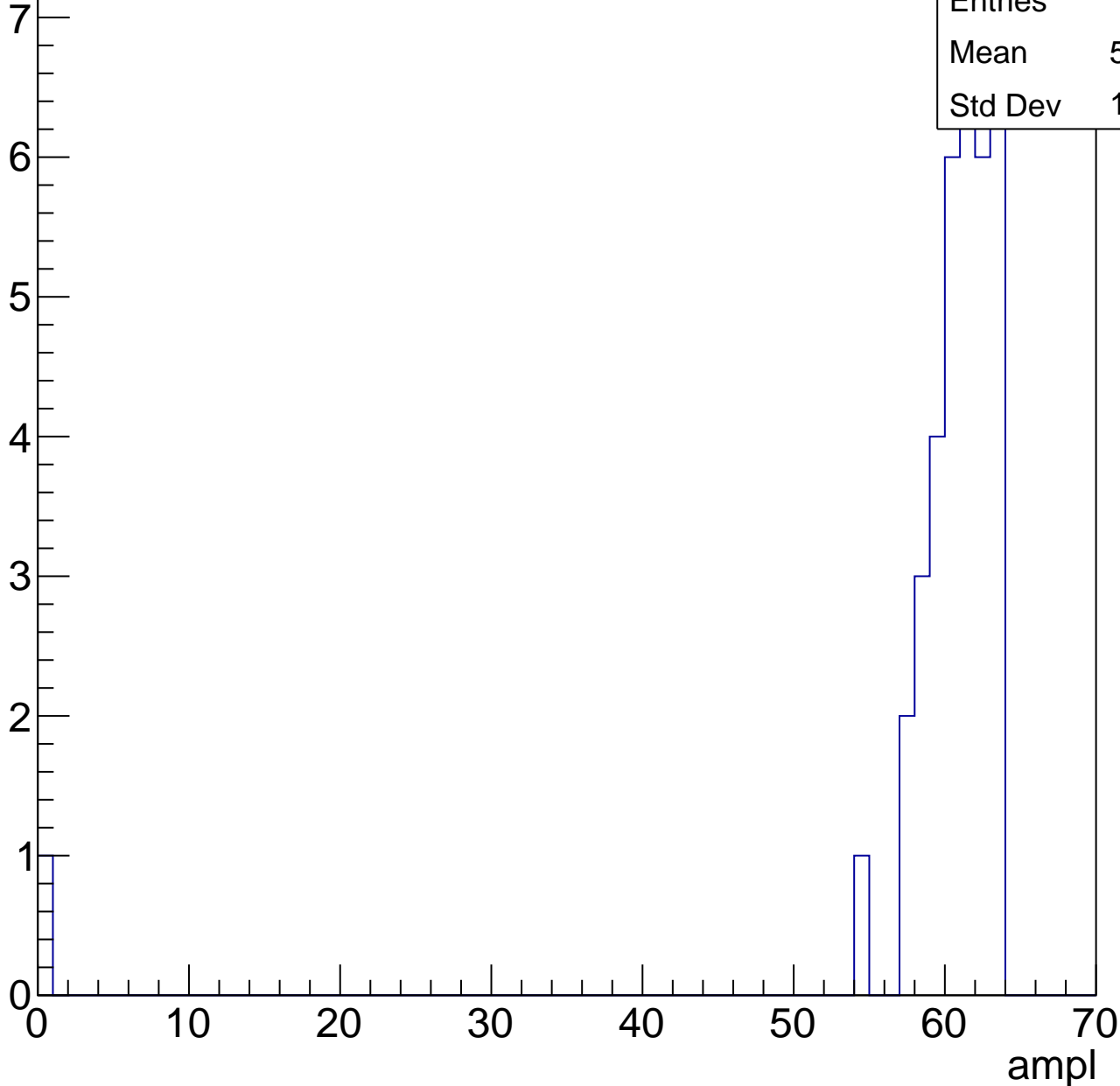


B0L002S, U18-ch85, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.86
Std Dev	10.02



B0L002S, U18-ch85, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch85, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch86, adc0

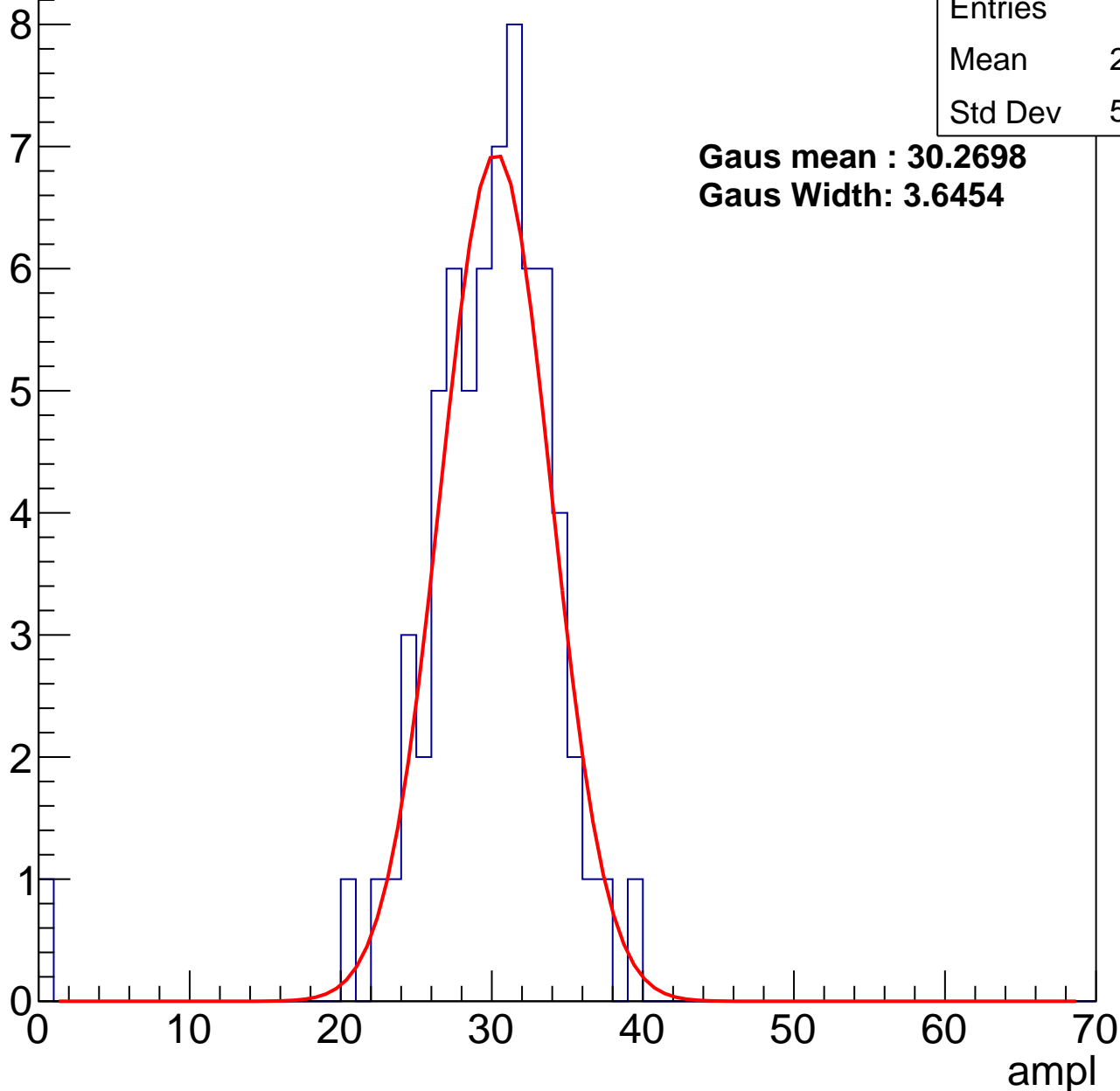
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.24
Std Dev	5.123

Gaus mean : 30.2698

Gaus Width: 3.6454



B0L002S, U18-ch86, adc1

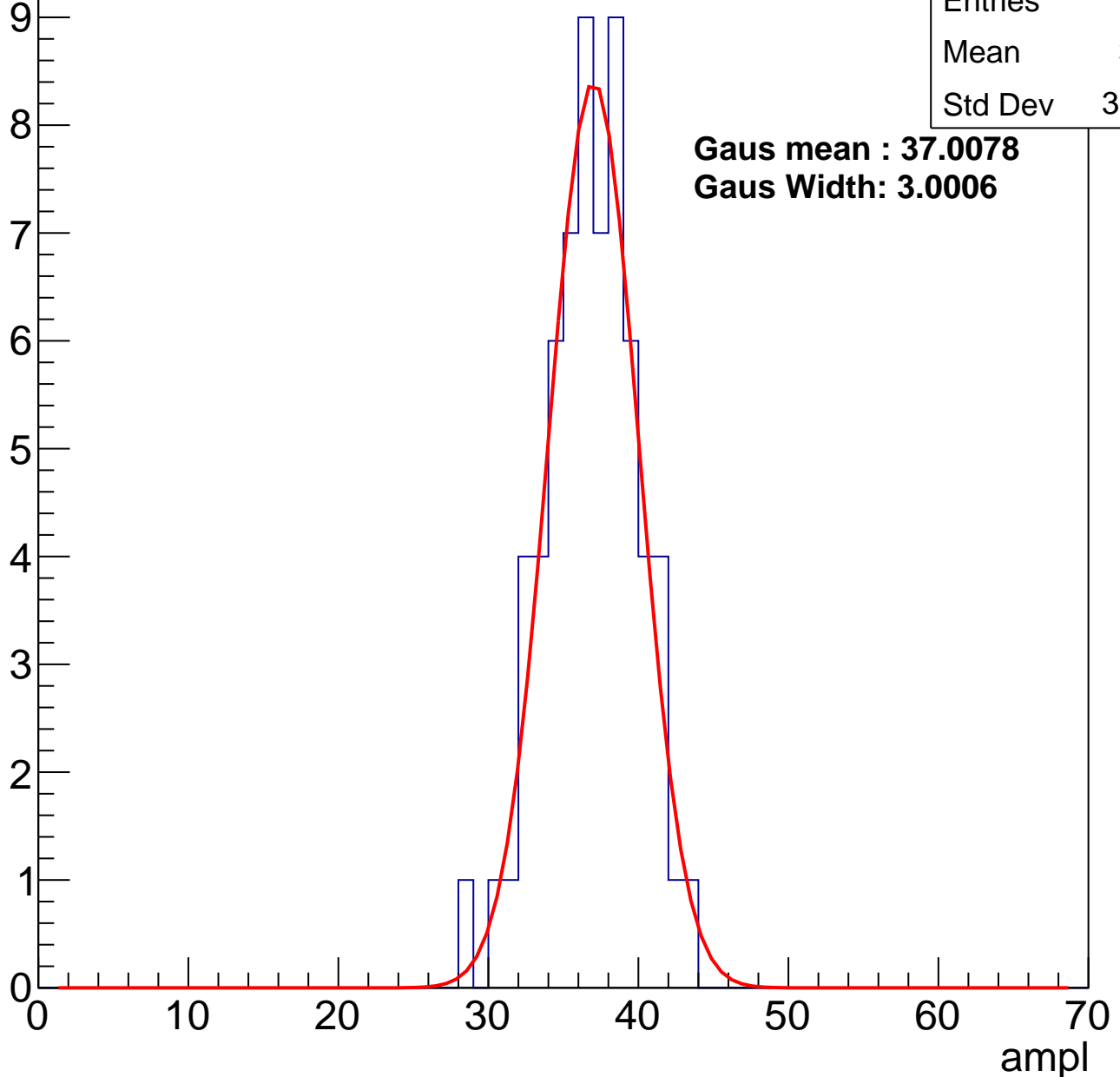
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.4
Std Dev	3.017

Gaus mean : 37.0078

Gaus Width: 3.0006



B0L002S, U18-ch86, adc2

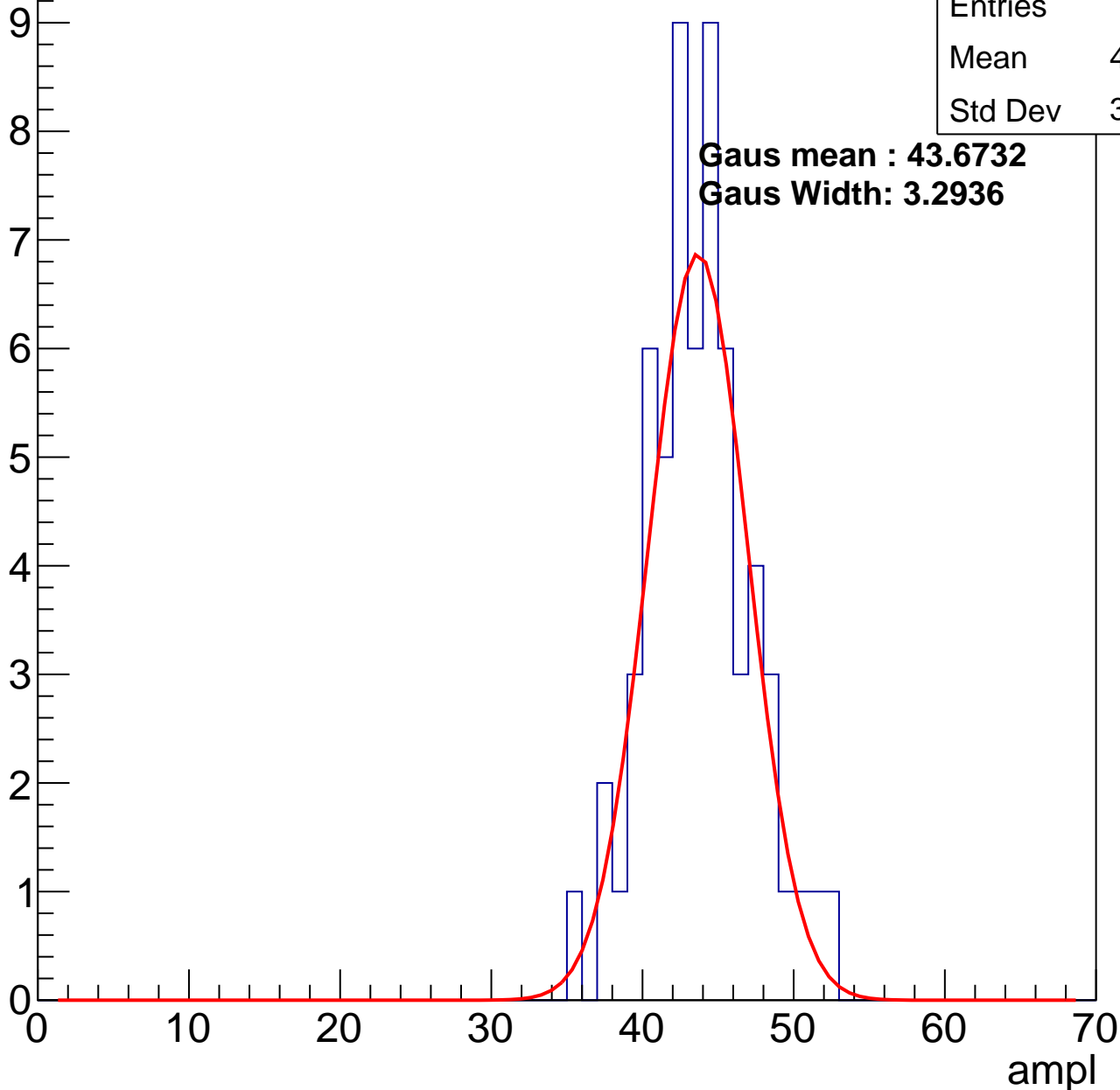
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	43.27
Std Dev	3.399

Gaus mean : 43.6732

Gaus Width: 3.2936

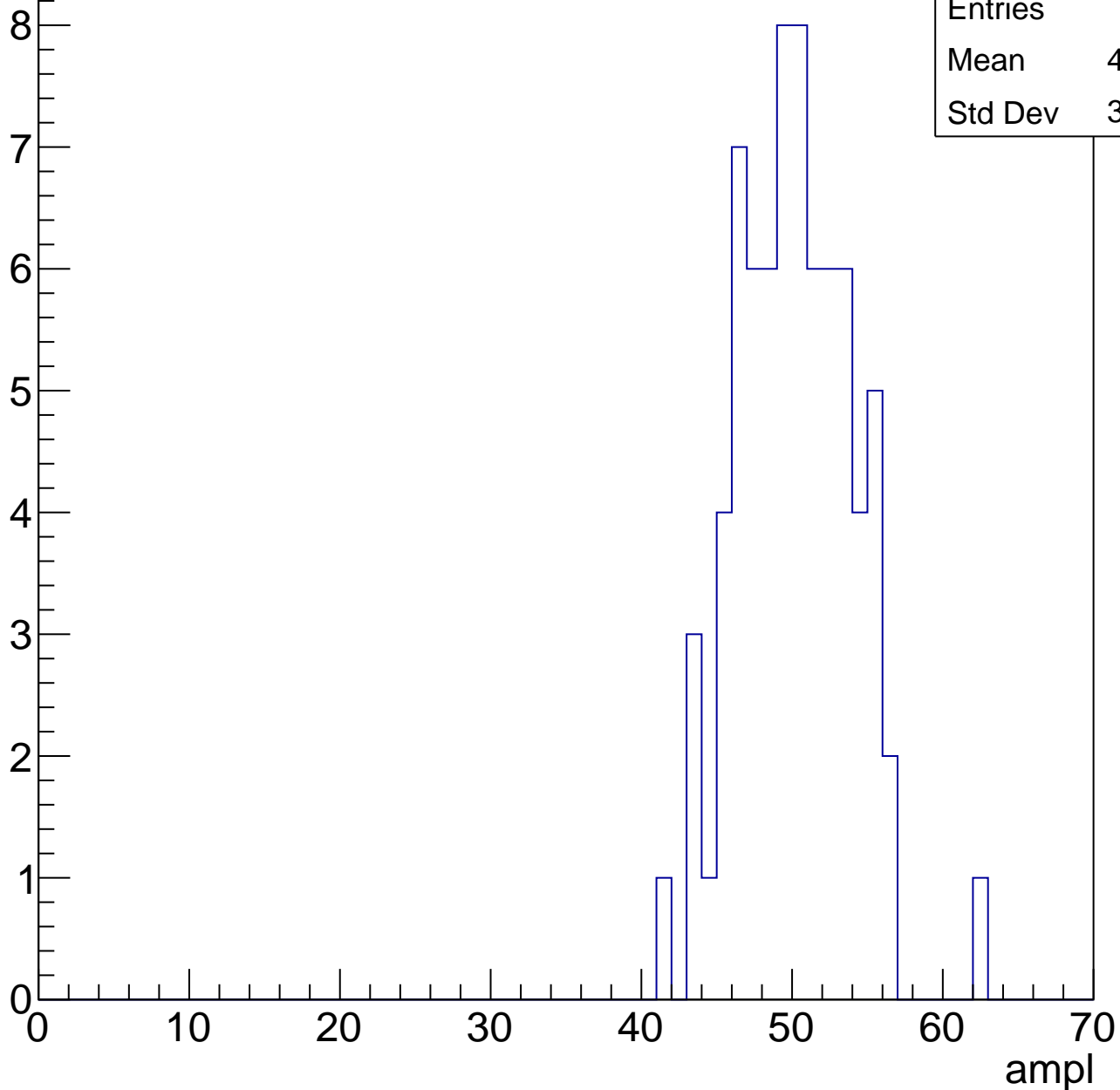


B0L002S, U18-ch86, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

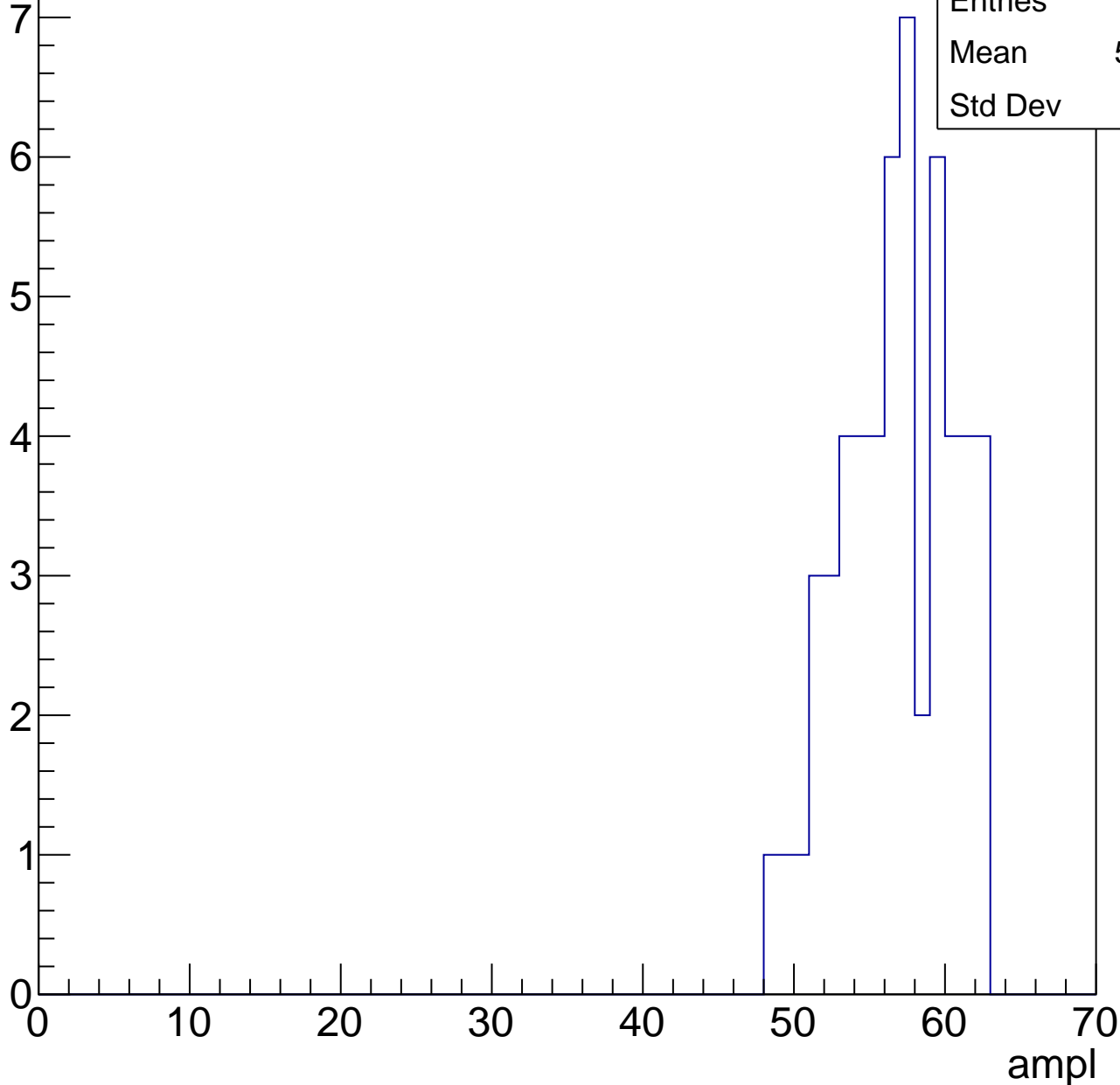
Entries	74
Mean	49.72
Std Dev	3.758



B0L002S, U18-ch86, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

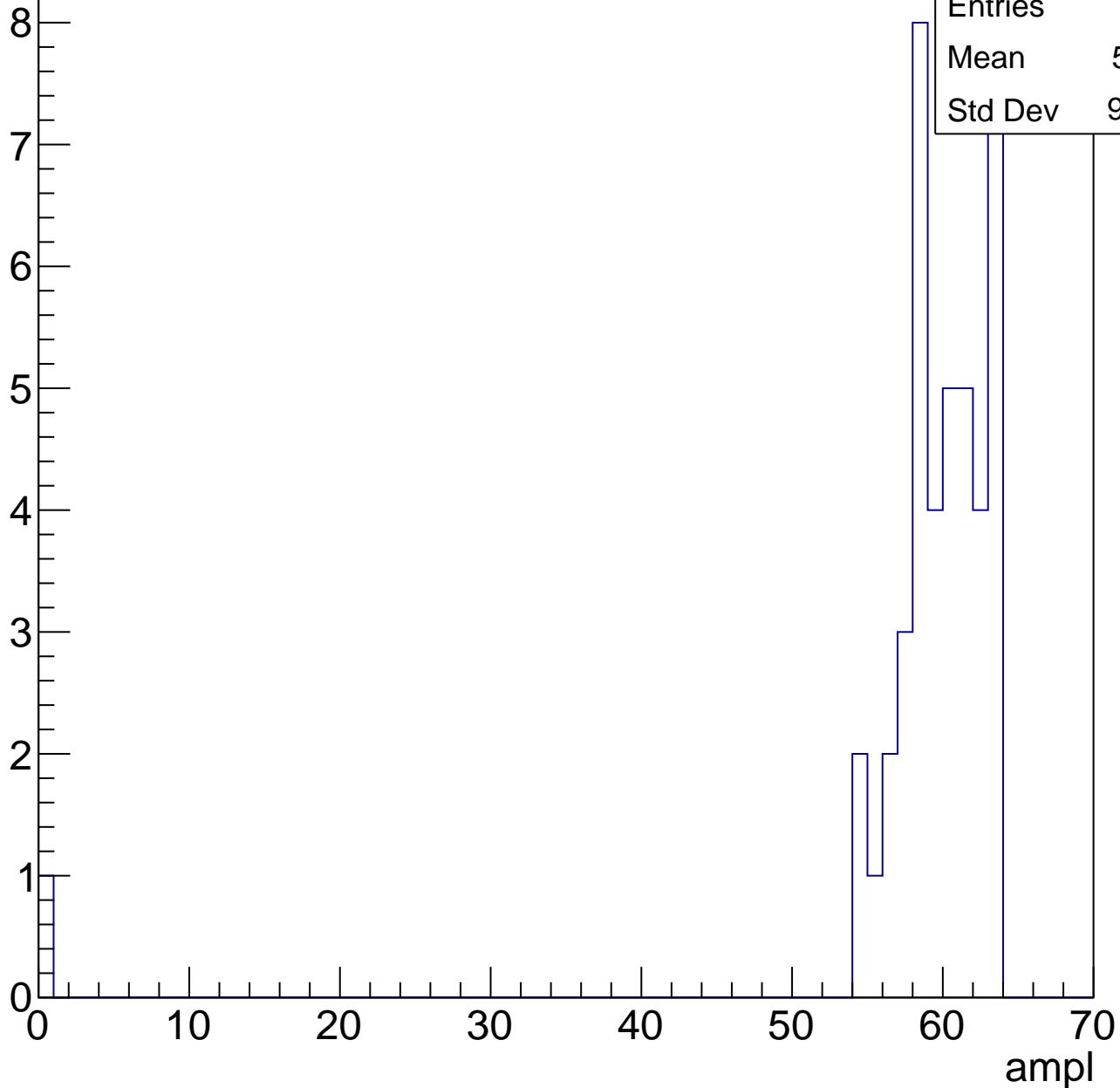


Entries	54
Mean	56.31
Std Dev	3.6

B0L002S, U18-ch86, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch86, adc6

calib_packv5_042523_0143.root, FC#8, port C1

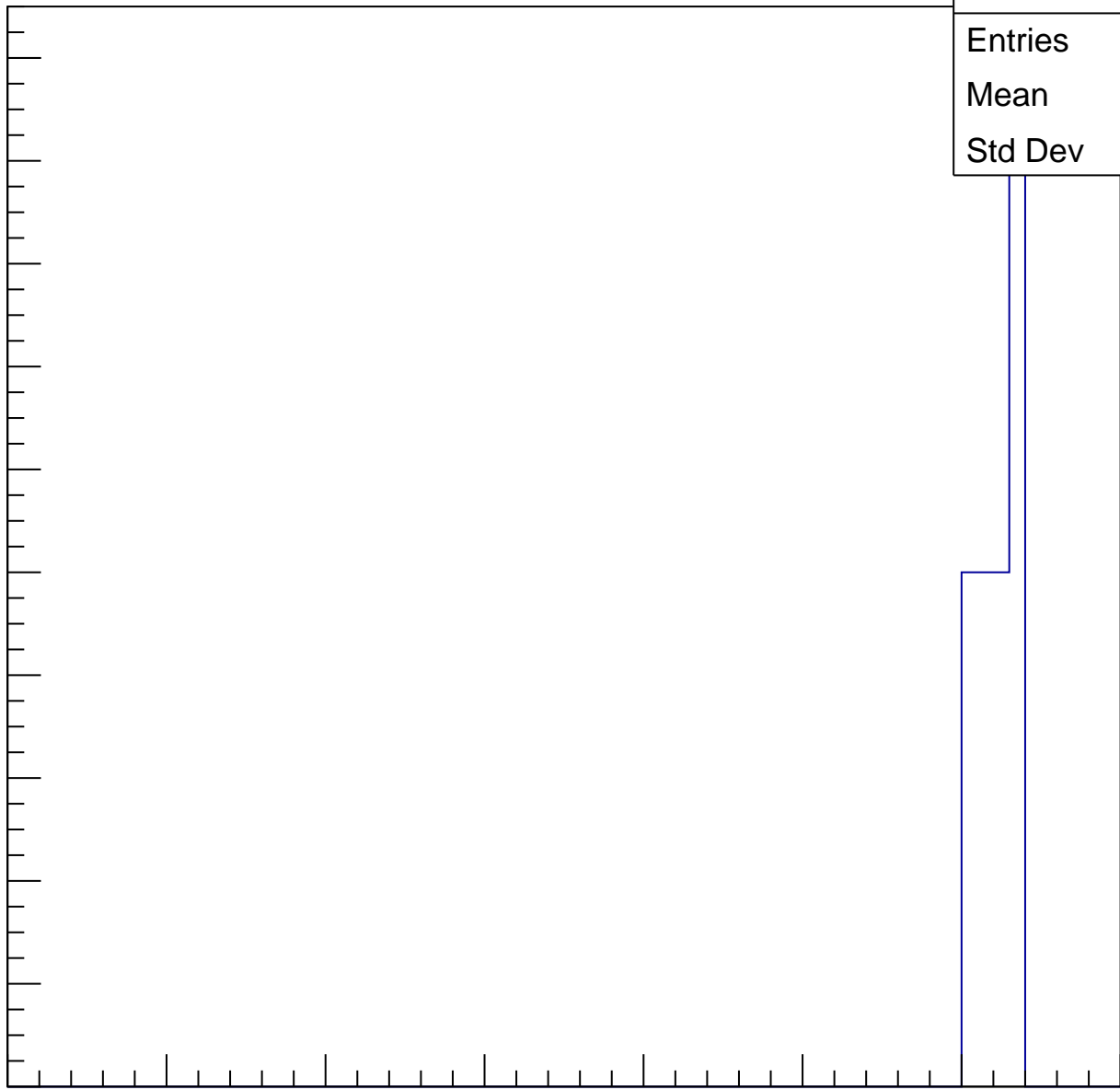
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	61.8
Std Dev	1.166

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch86, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch87, adc0

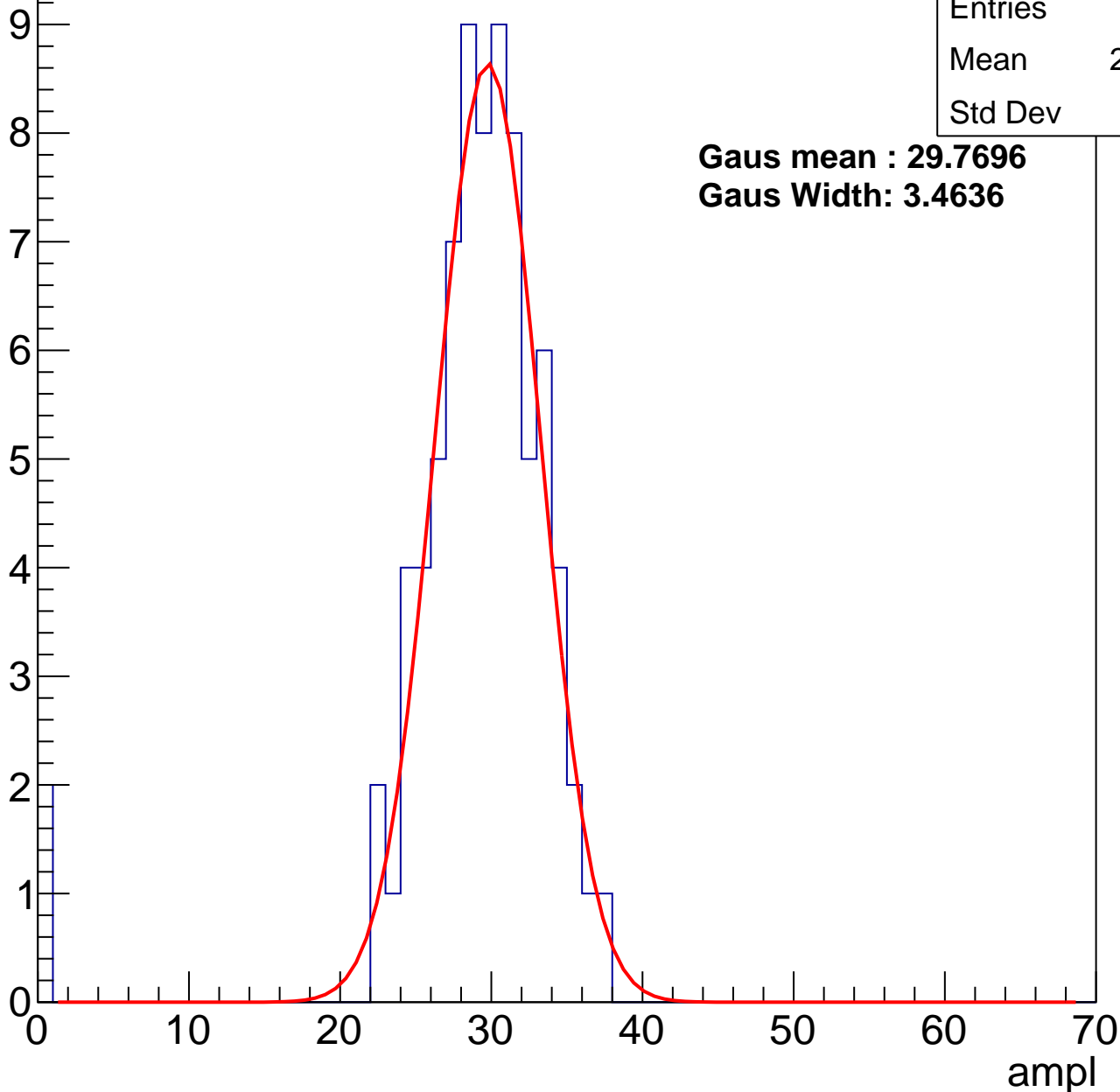
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	28.47
Std Dev	5.67

Gaus mean : 29.7696

Gaus Width: 3.4636



B0L002S, U18-ch87, adc1

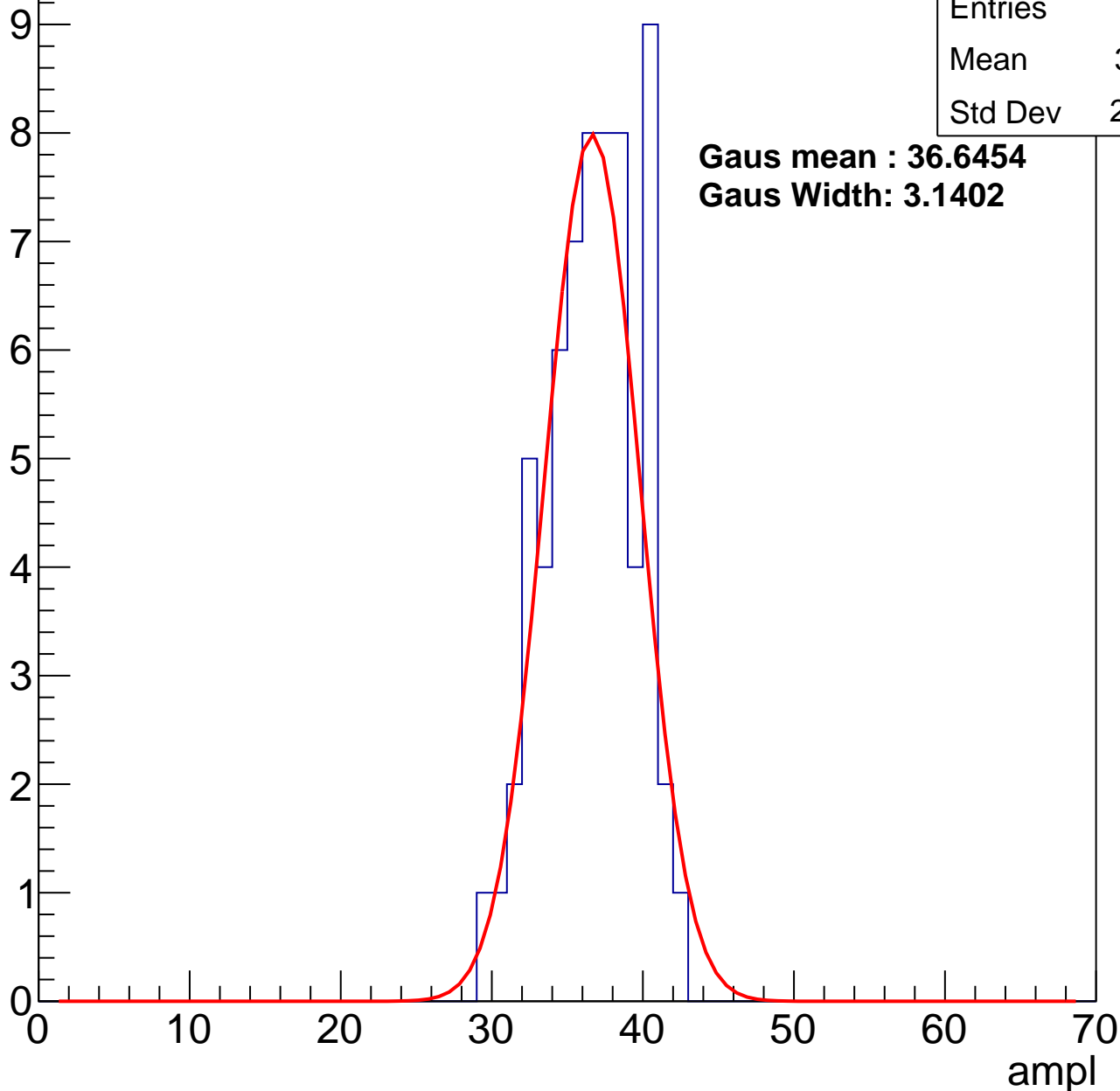
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.21
Std Dev	2.977

Gaus mean : 36.6454

Gaus Width: 3.1402



B0L002S, U18-ch87, adc2

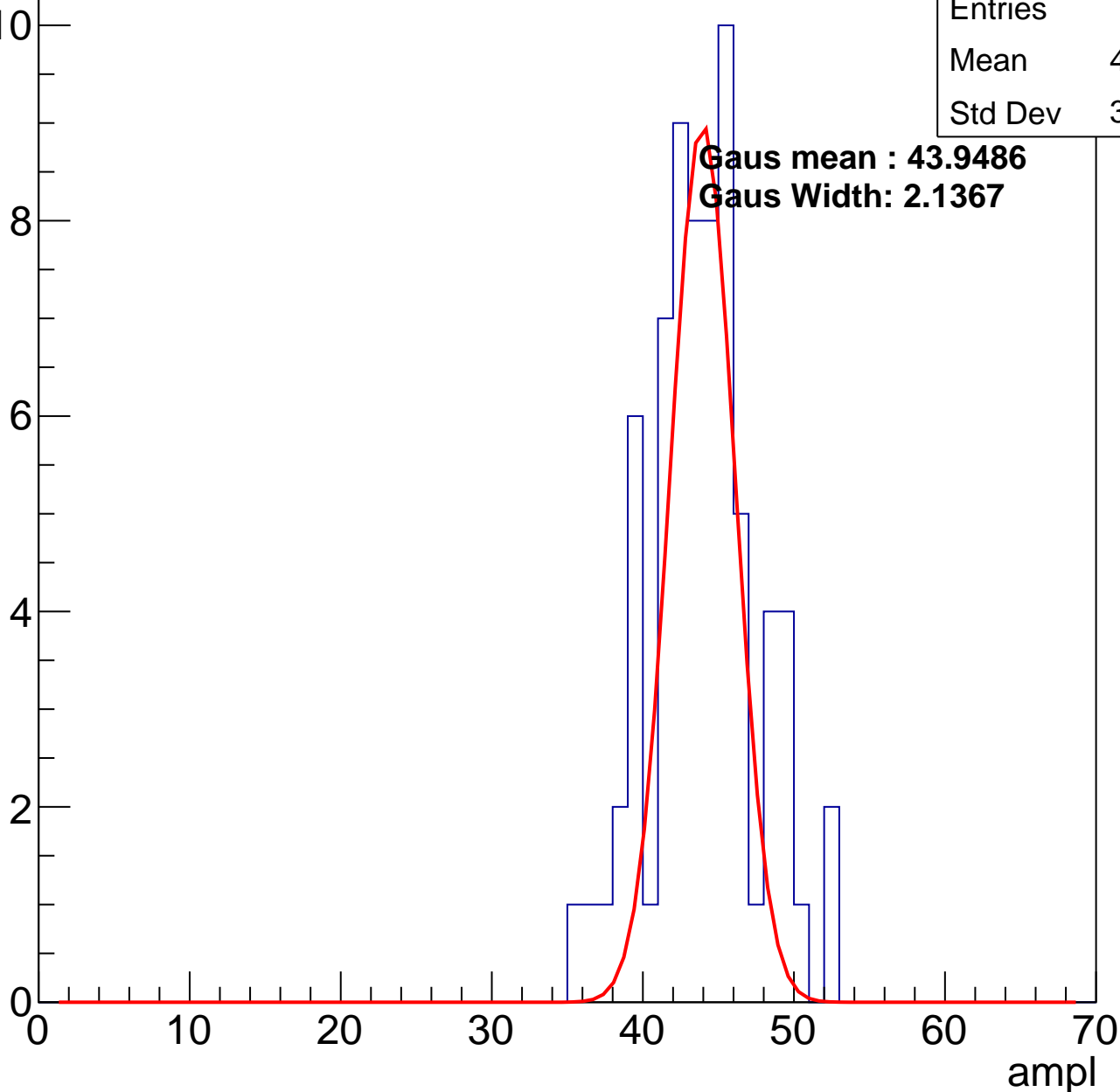
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	43.49
Std Dev	3.536

Gaus mean : 43.9486

Gaus Width: 2.1367

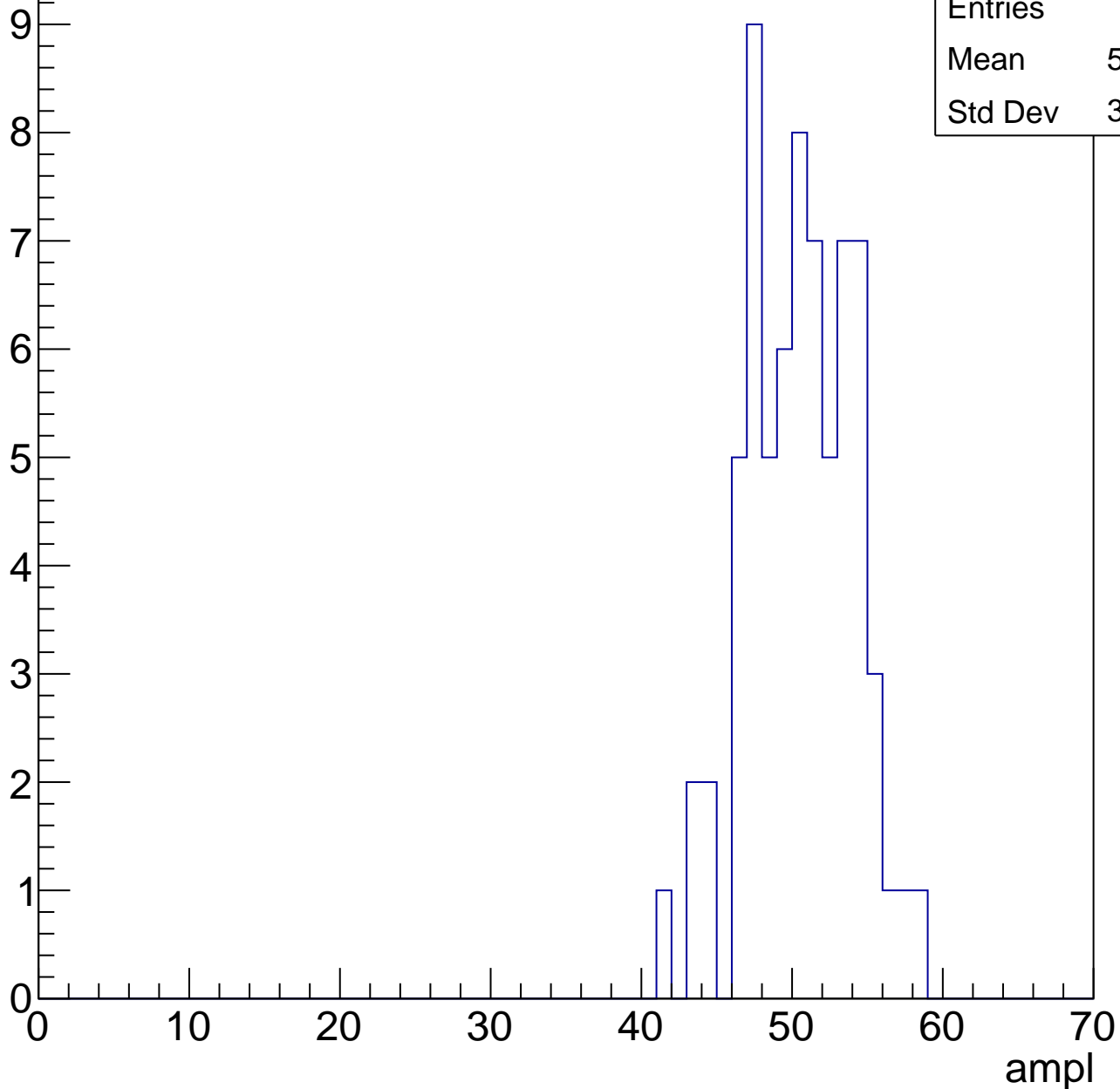


B0L002S, U18-ch87, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

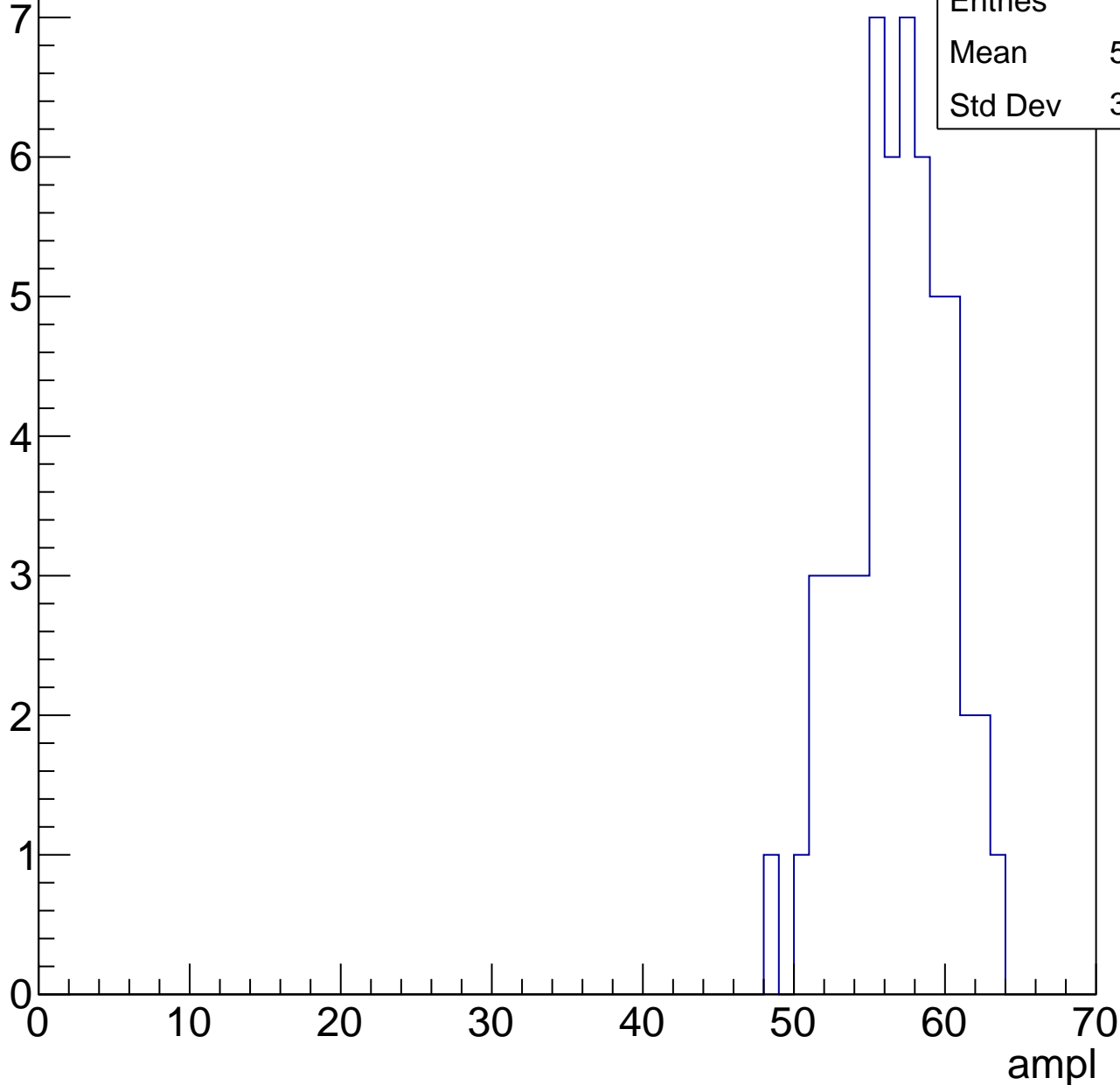
Entries	70
Mean	50.06
Std Dev	3.513



B0L002S, U18-ch87, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



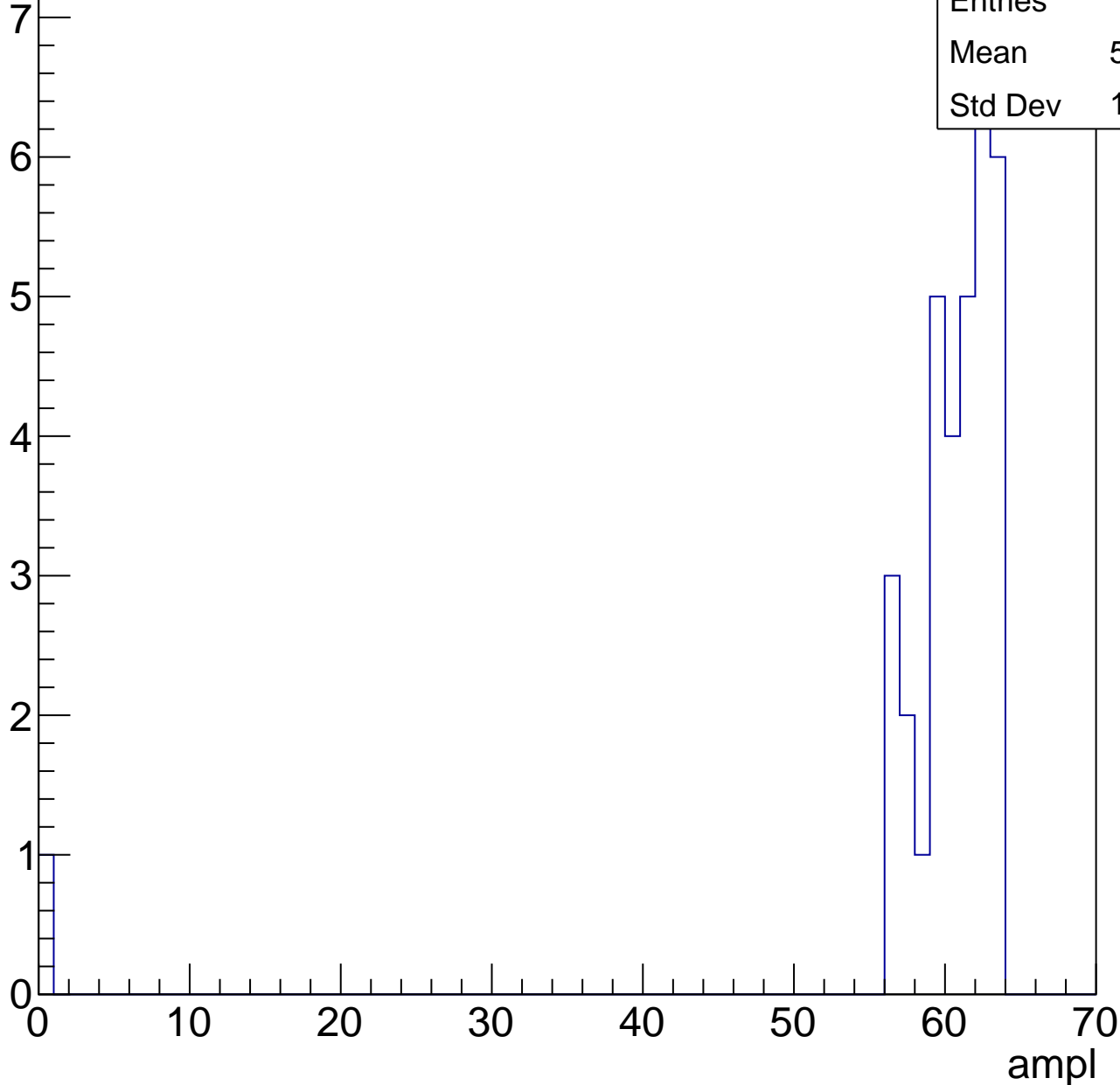
Entries	55
Mean	56.36
Std Dev	3.272

B0L002S, U18-ch87, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	34
Mean	58.59
Std Dev	10.42



B0L002S, U18-ch87, adc6

calib_packv5_042523_0143.root, FC#8, port C1

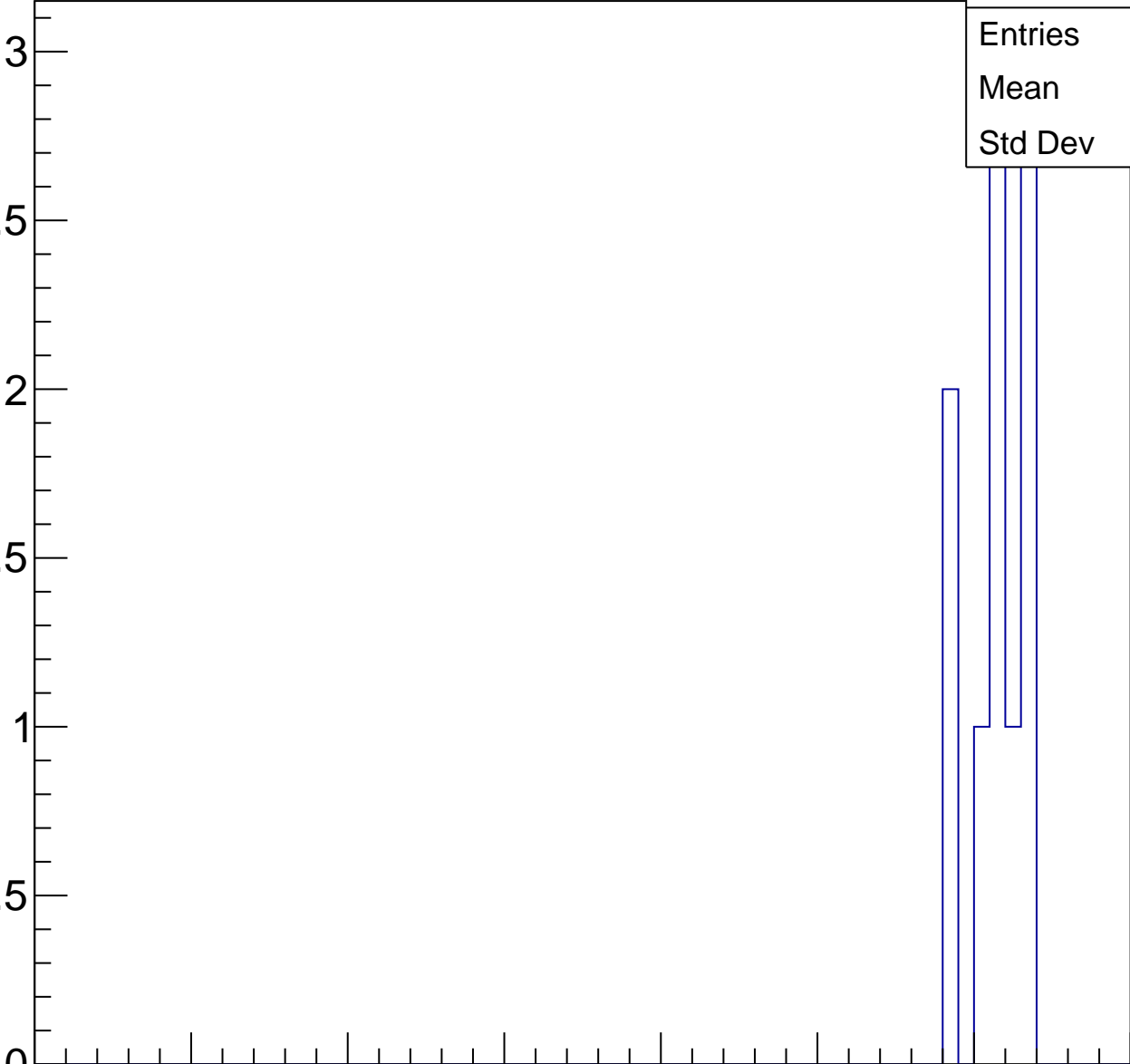
Entry

3
2.5
2
1.5
1
0.5
0

Entries	10
Mean	61
Std Dev	1.789

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch87, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch88, adc0

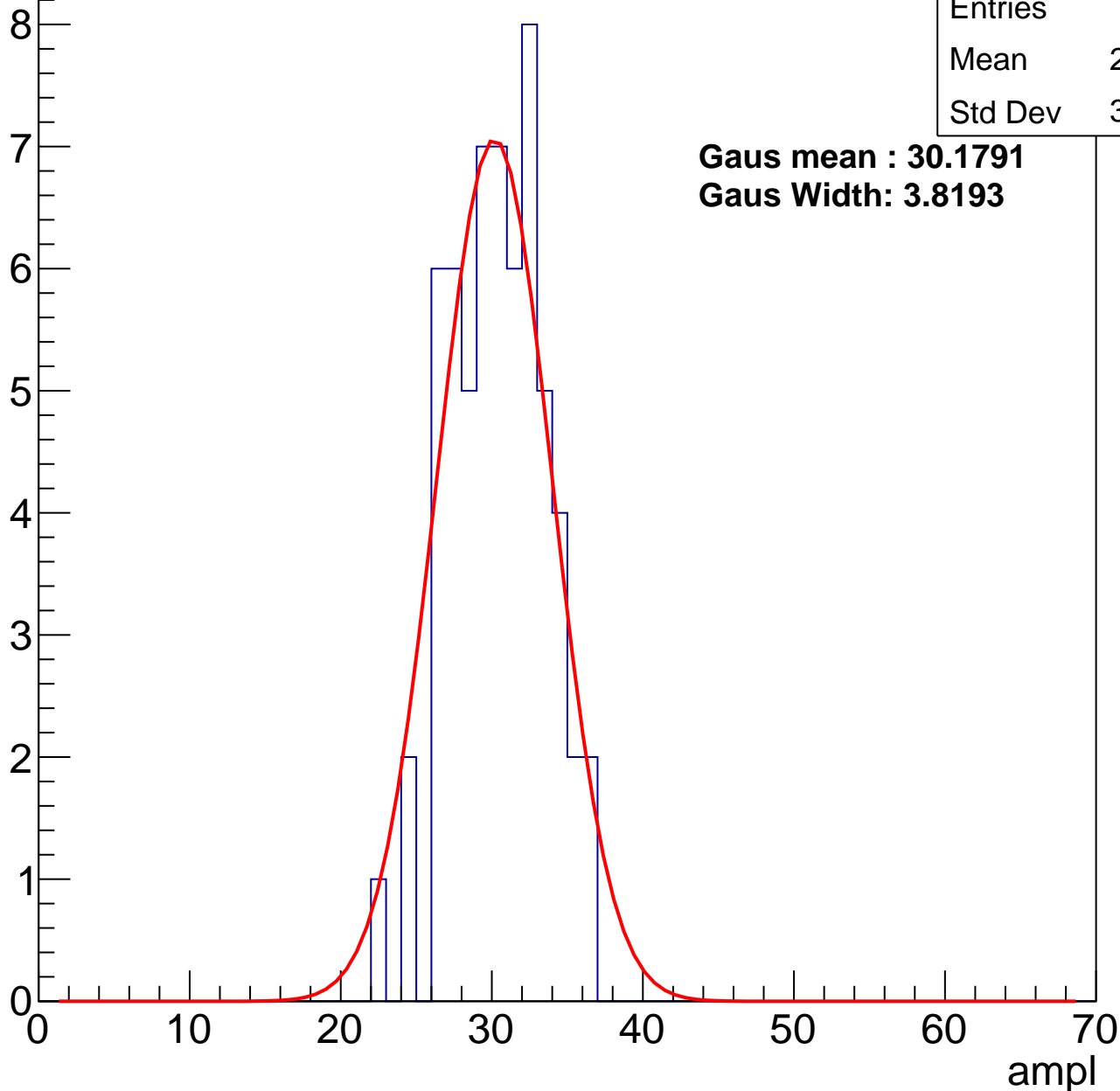
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.93
Std Dev	3.088

Gaus mean : 30.1791

Gaus Width: 3.8193



B0L002S, U18-ch88, adc1

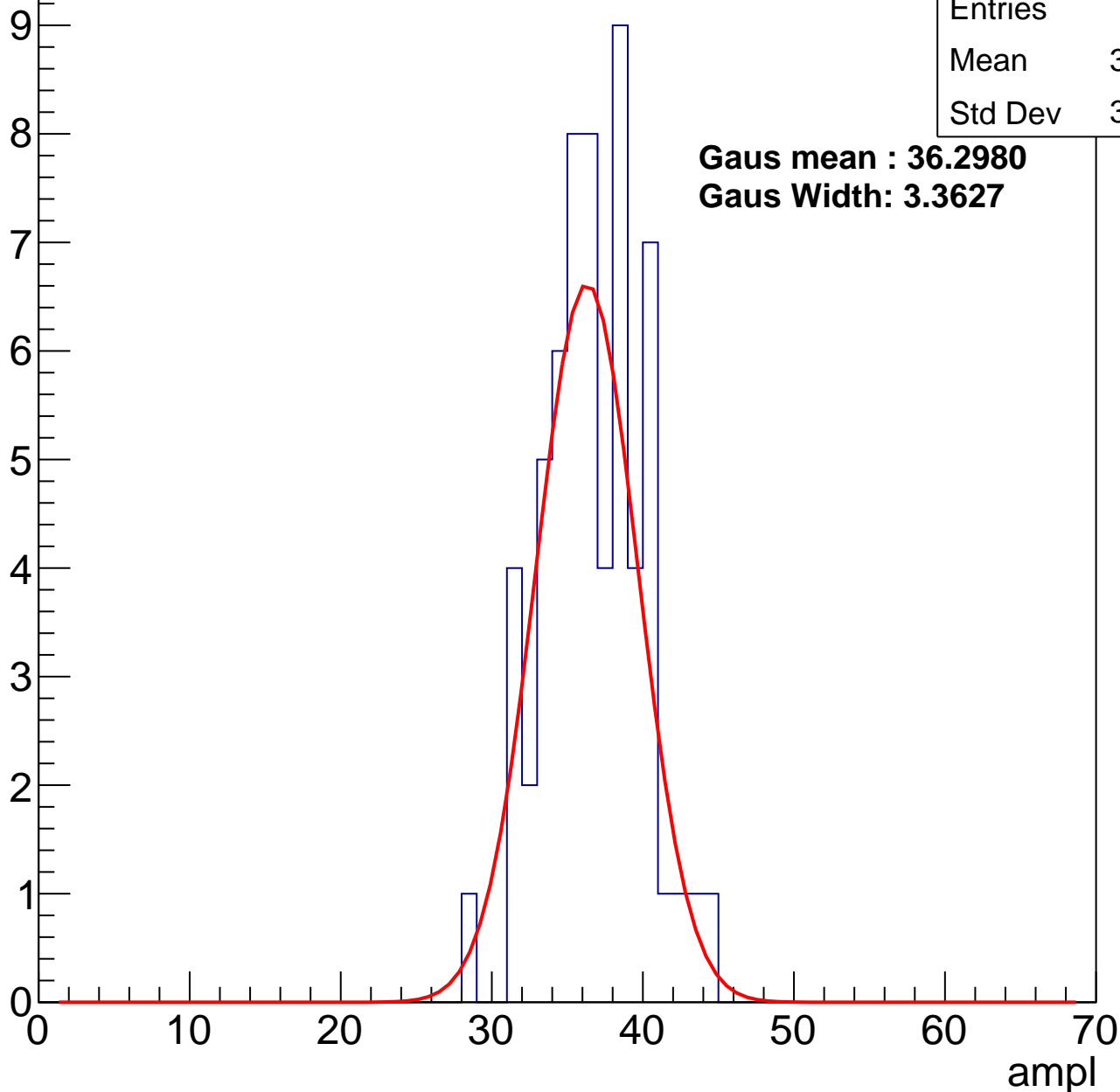
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	36.27
Std Dev	3.189

Gaus mean : 36.2980

Gaus Width: 3.3627



B0L002S, U18-ch88, adc2

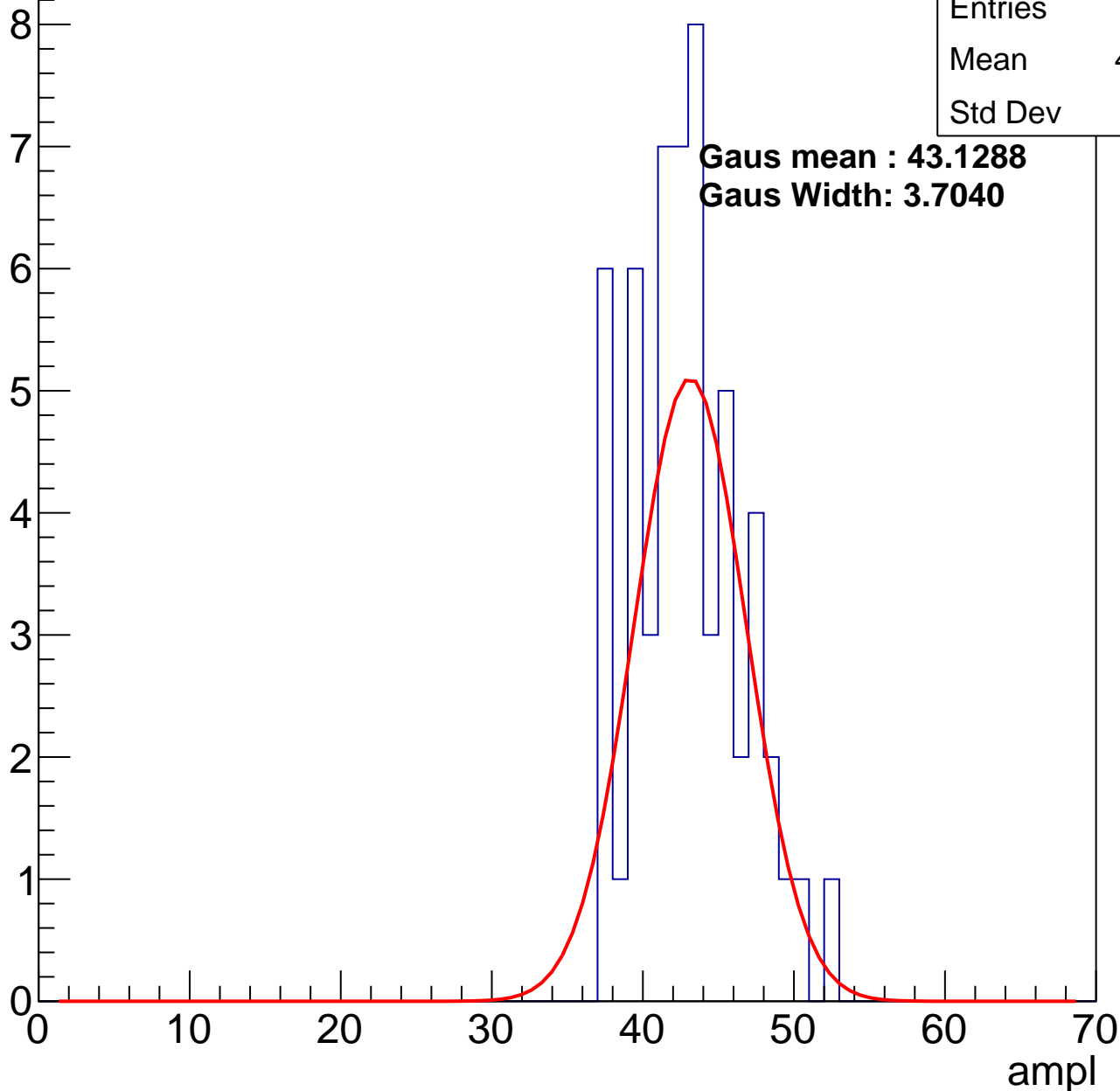
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	42.51
Std Dev	3.52

Gaus mean : 43.1288

Gaus Width: 3.7040

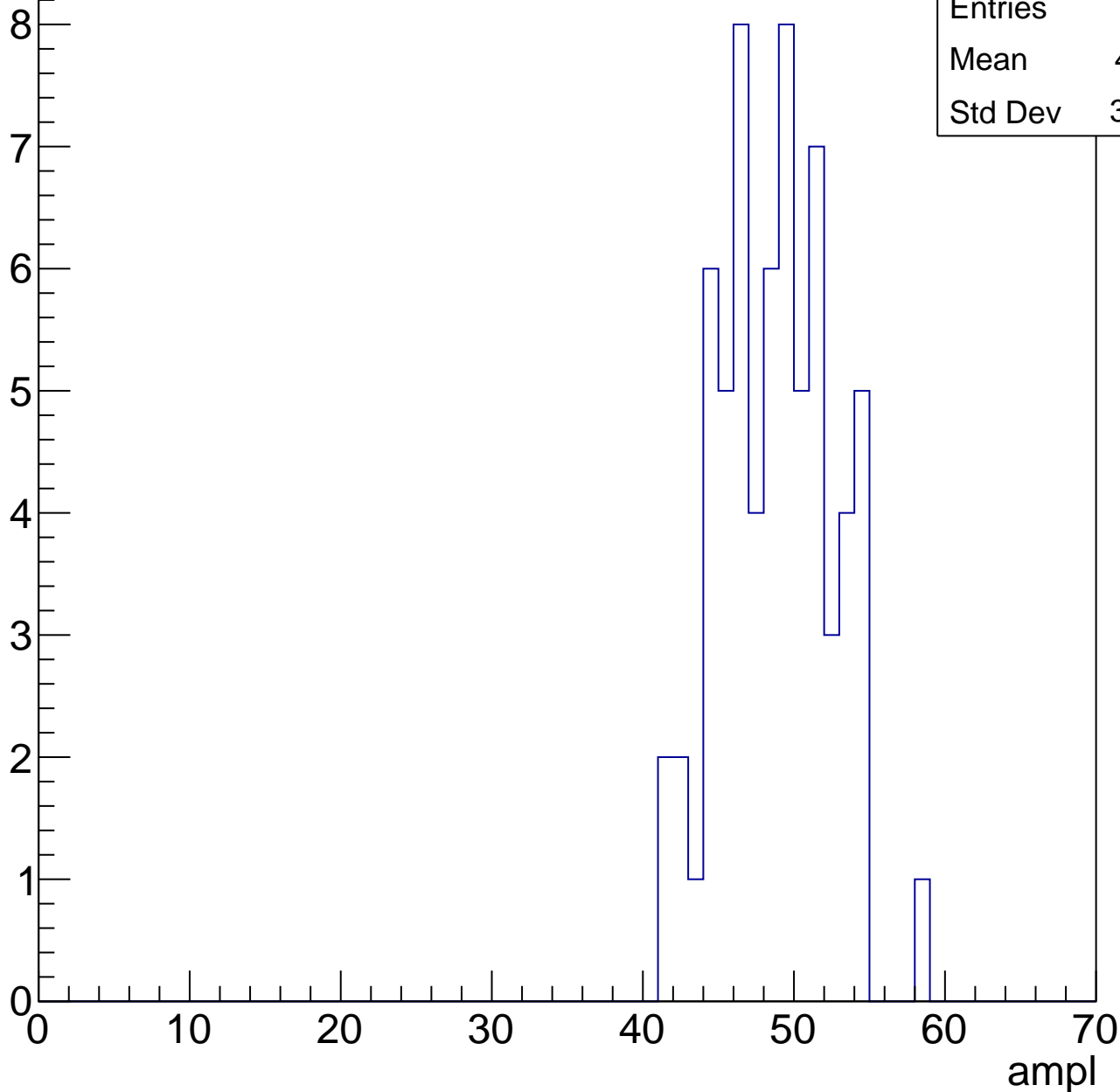


B0L002S, U18-ch88, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

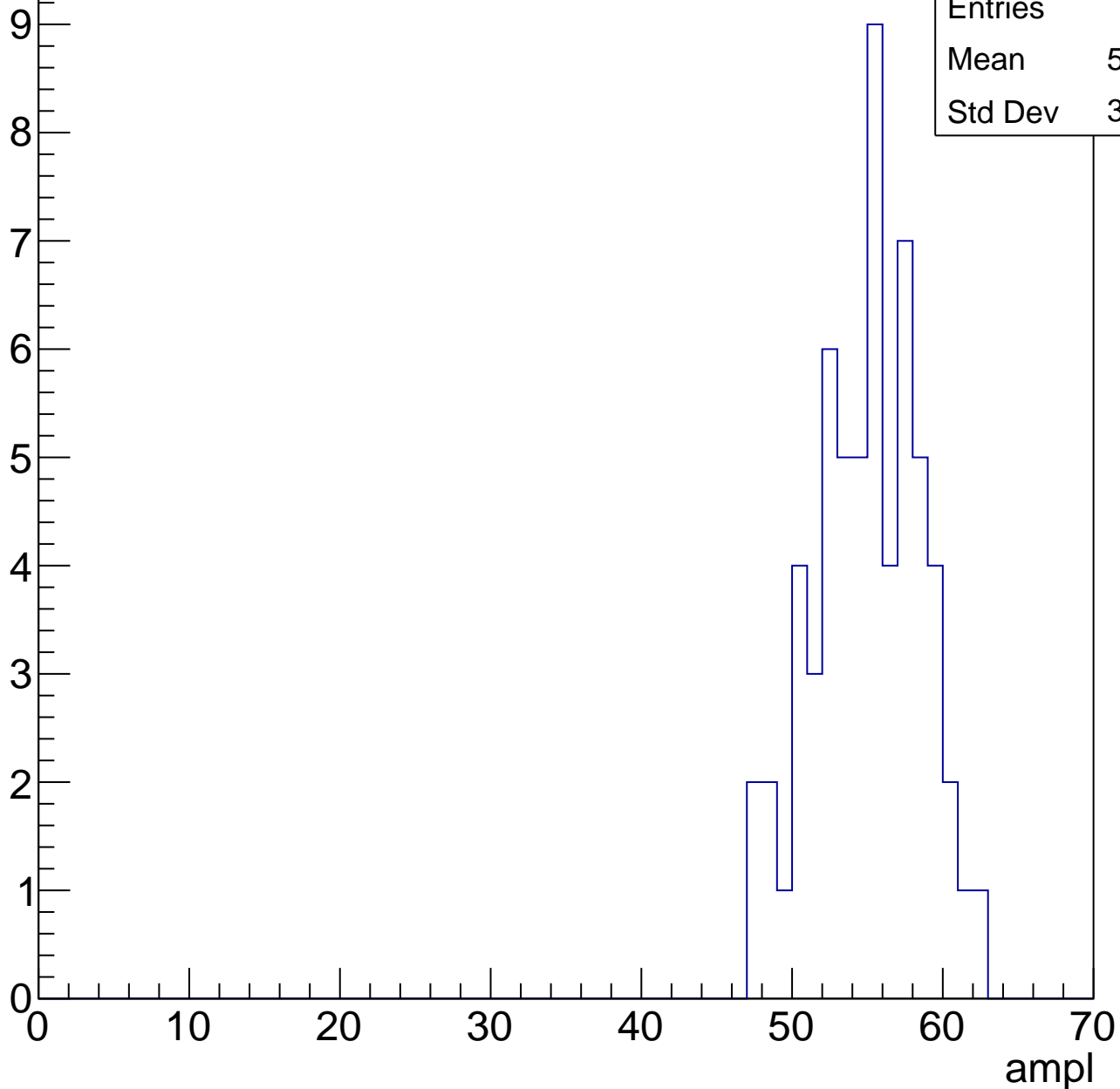
Entries	67
Mean	48.31
Std Dev	3.633



B0L002S, U18-ch88, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

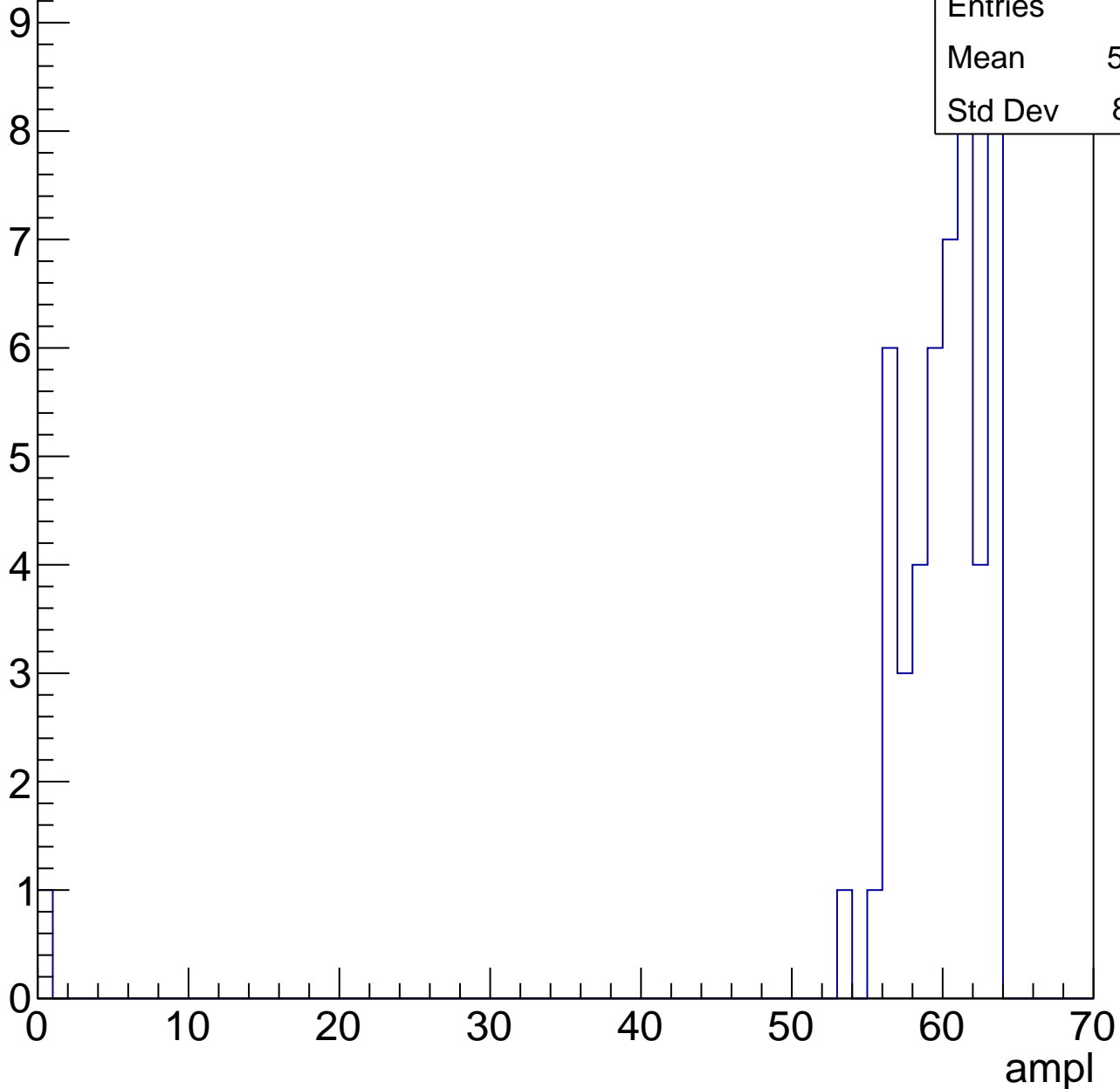


B0L002S, U18-ch88, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	58.44
Std Dev	8.711

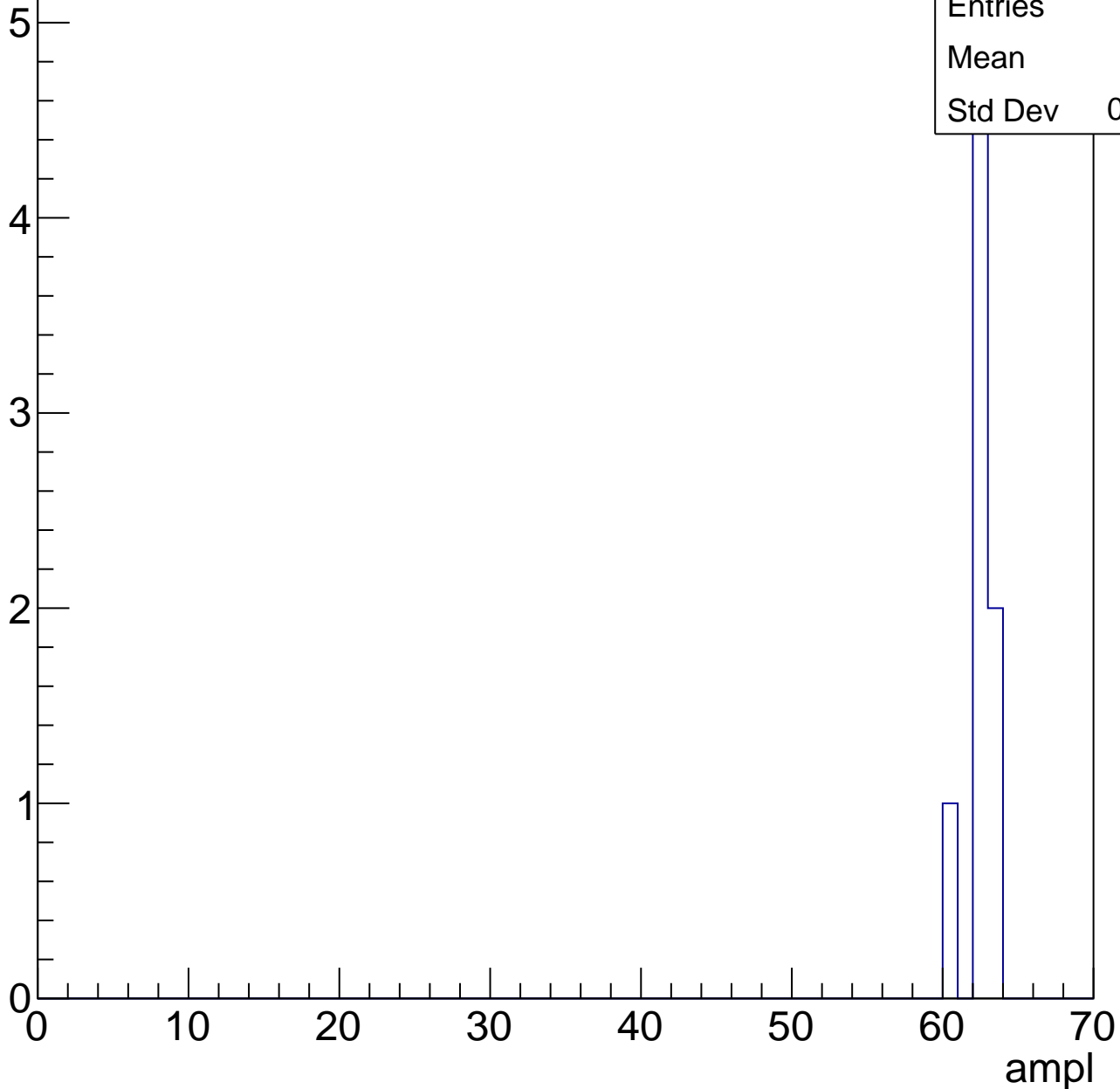


B0L002S, U18-ch88, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	62
Std Dev	0.866



B0L002S, U18-ch88, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	7.333
Std Dev	10.37

B0L002S, U18-ch89, adc0

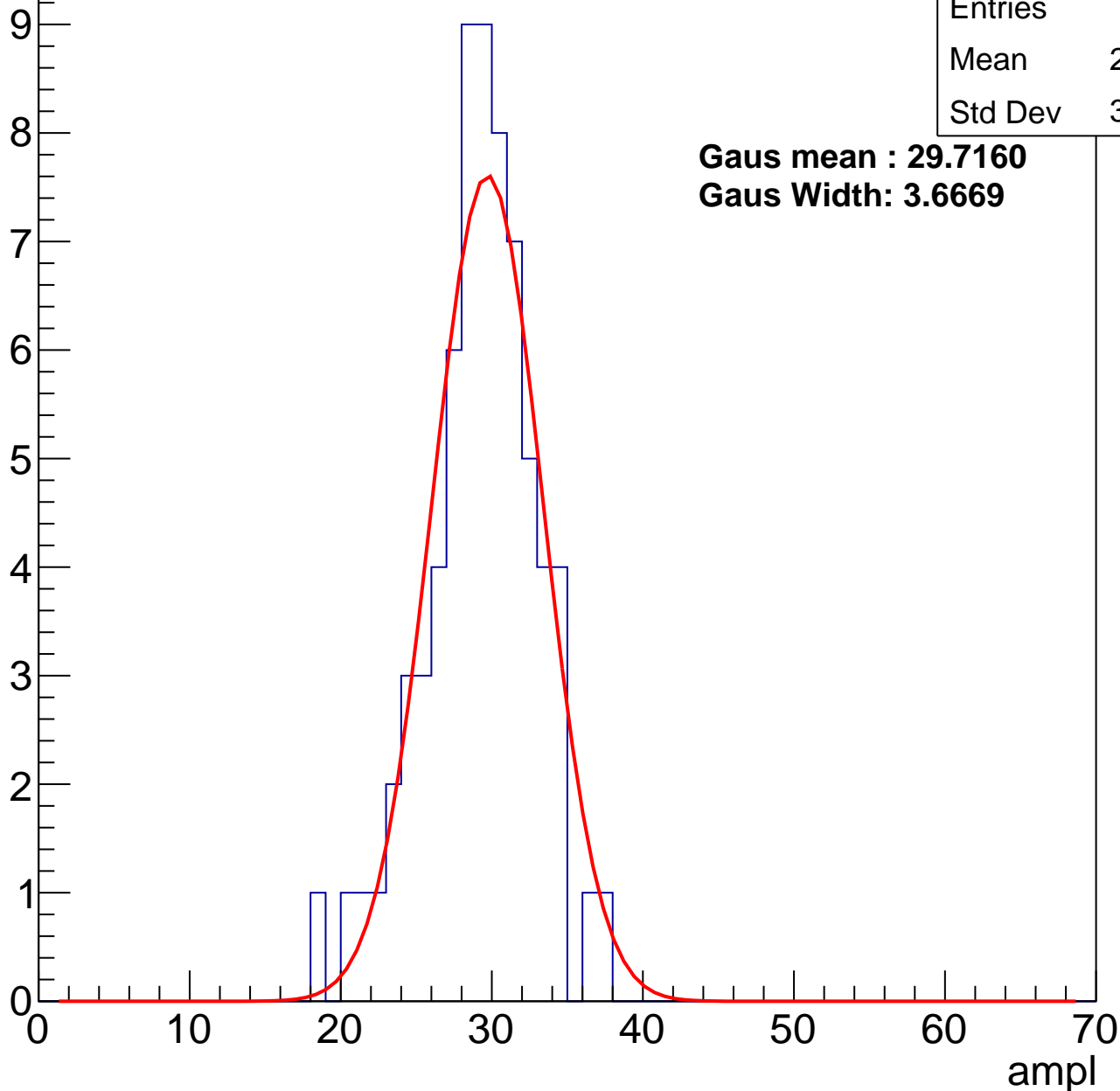
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.73
Std Dev	3.649

Gaus mean : 29.7160

Gaus Width: 3.6669



B0L002S, U18-ch89, adc1

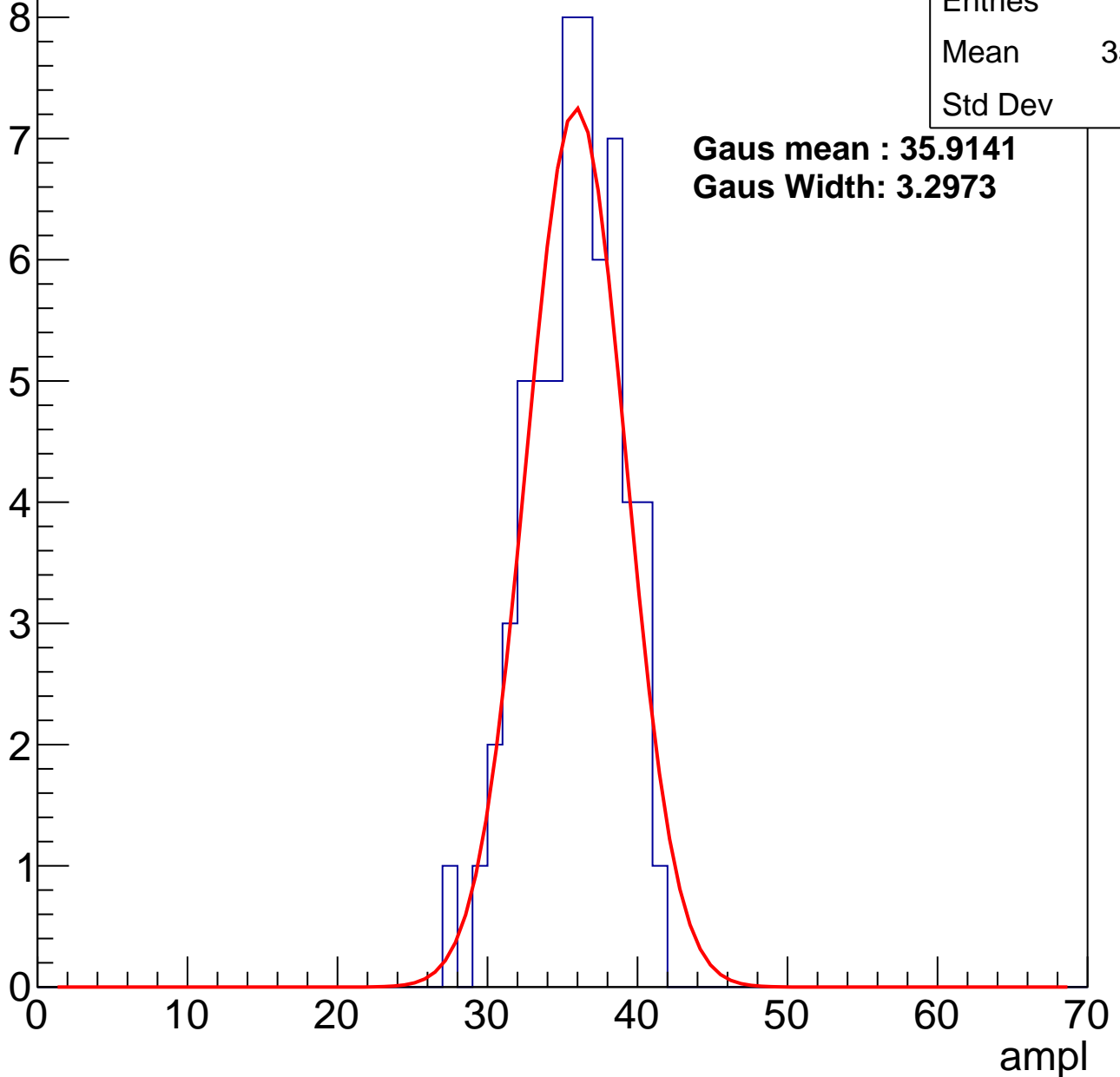
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	35.28
Std Dev	3.05

Gaus mean : 35.9141

Gaus Width: 3.2973



B0L002S, U18-ch89, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

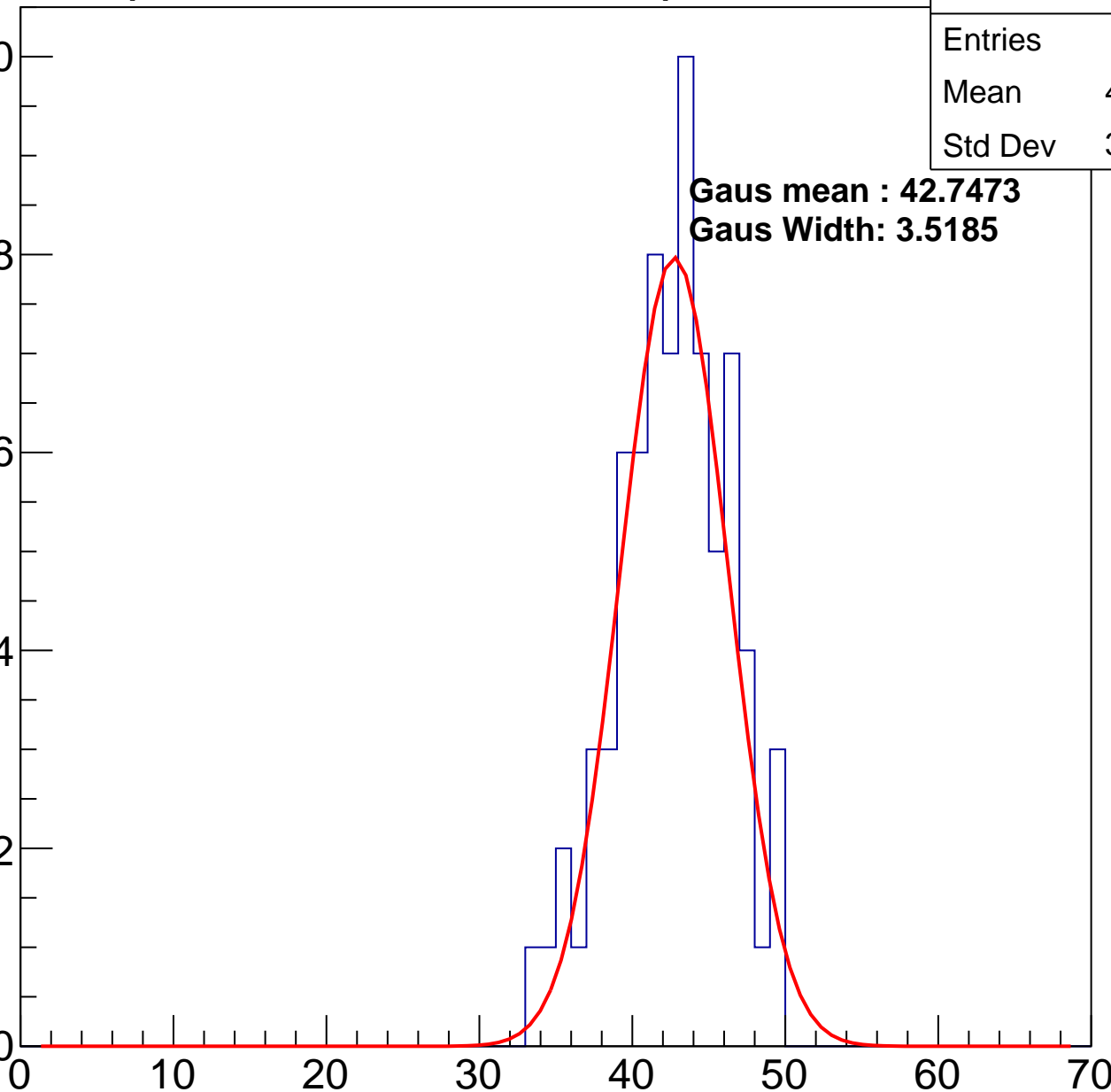
Entries	75
Mean	42.16
Std Dev	3.585

Gaus mean : 42.7473

Gaus Width: 3.5185

10
8
6
4
2
0

ampl

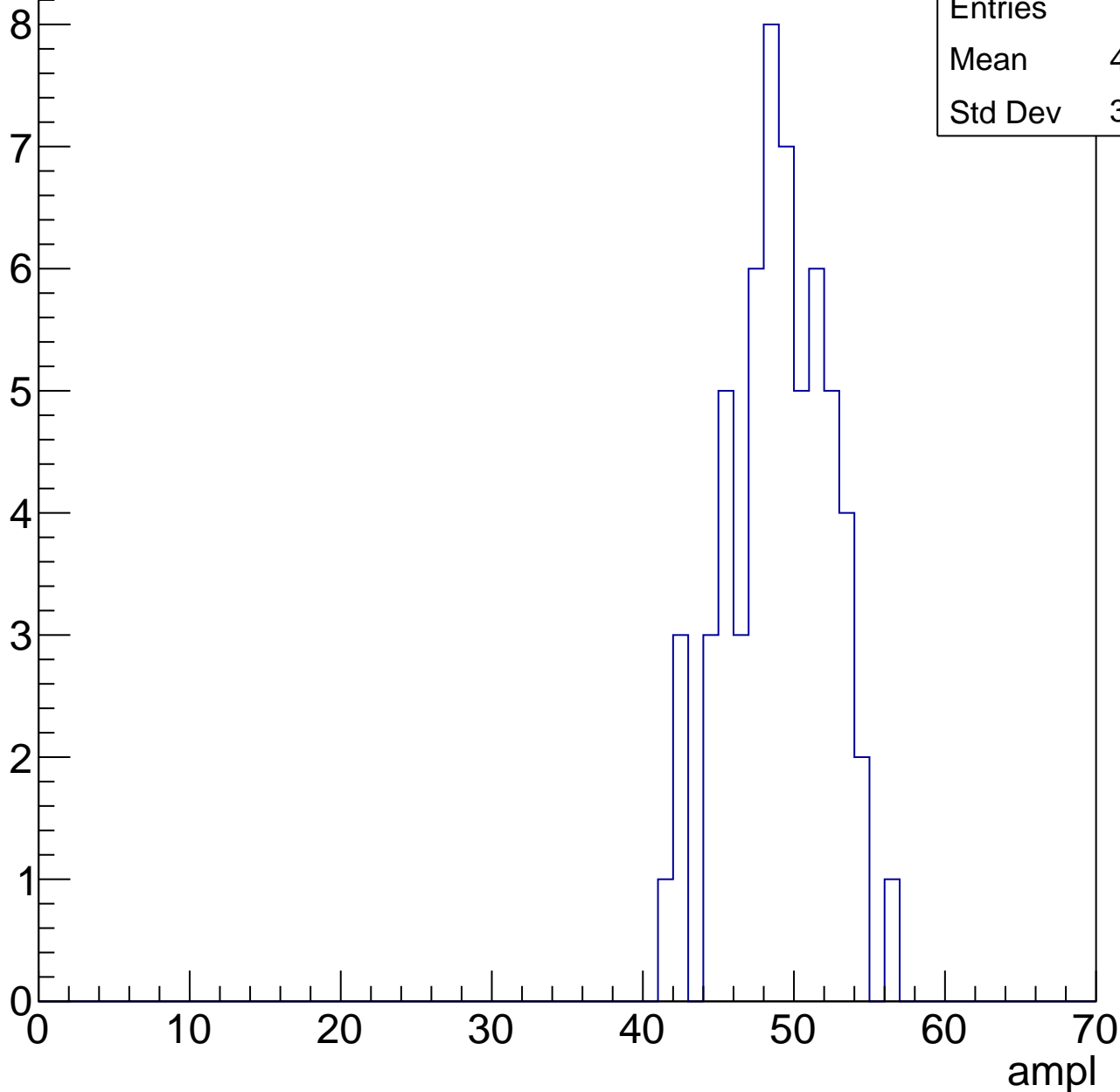


B0L002S, U18-ch89, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	48.53
Std Dev	3.316

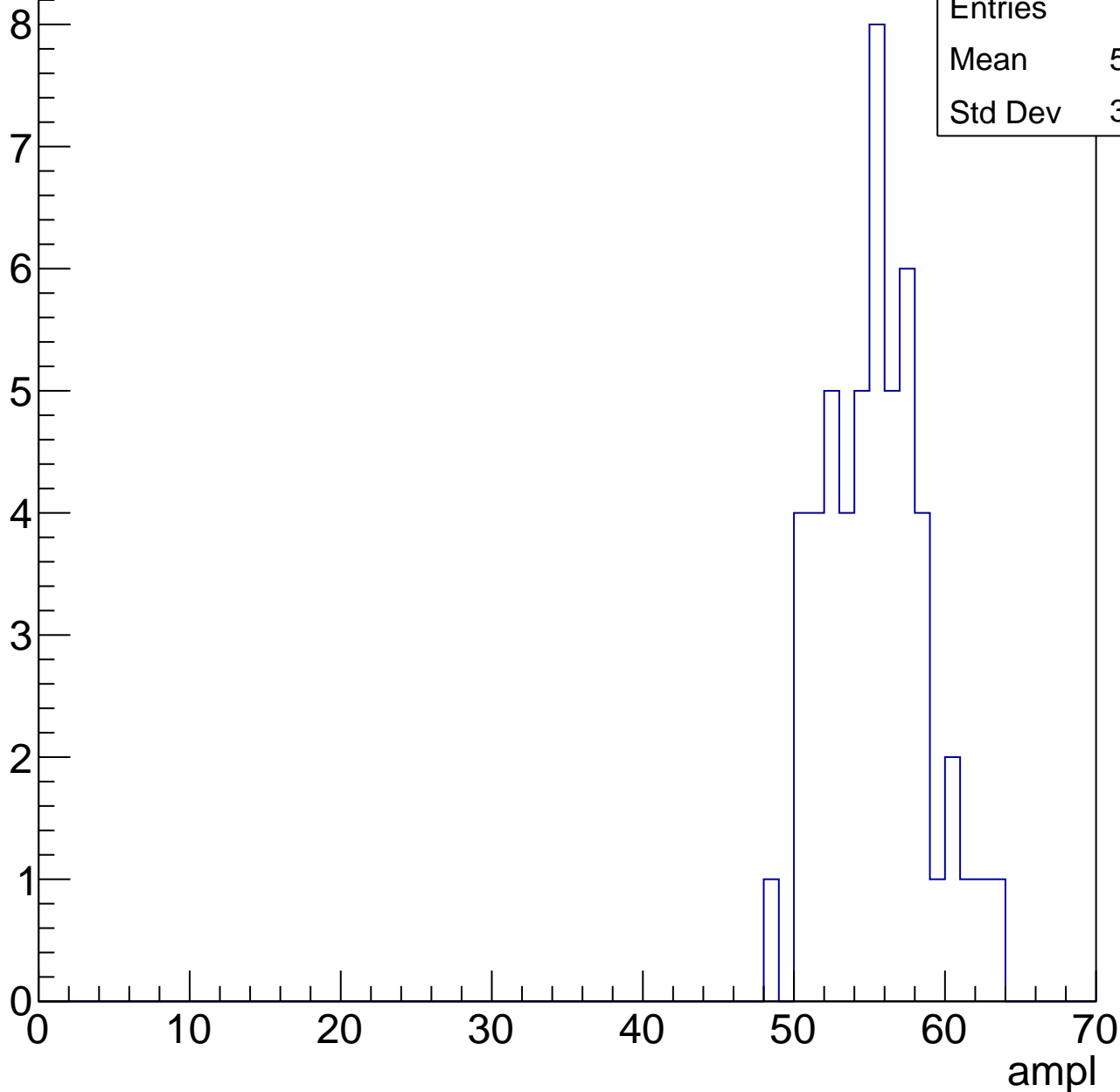


B0L002S, U18-ch89, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	54.87
Std Dev	3.276



B0L002S, U18-ch89, adc5

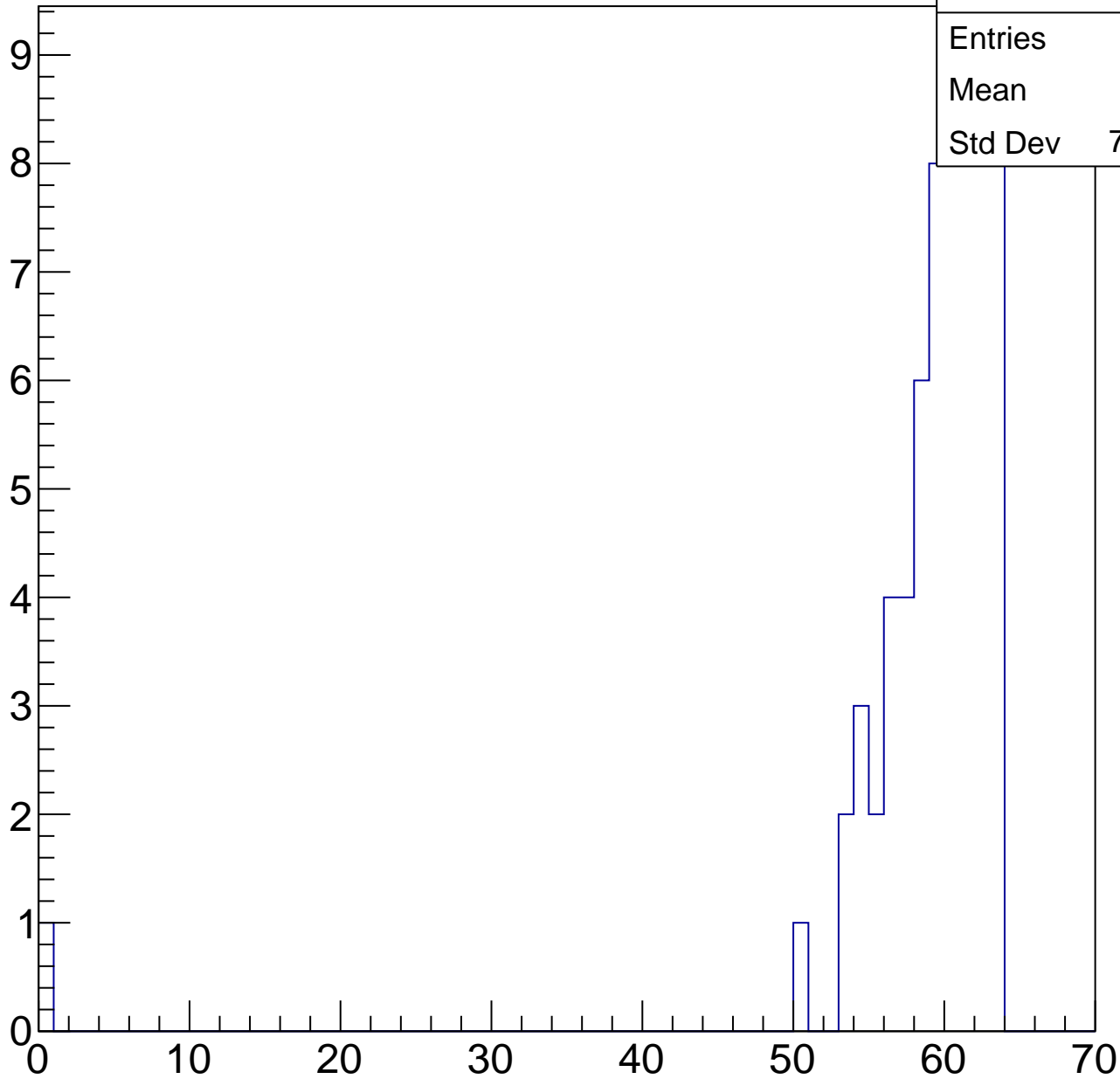
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	64
Mean	58.3
Std Dev	7.929

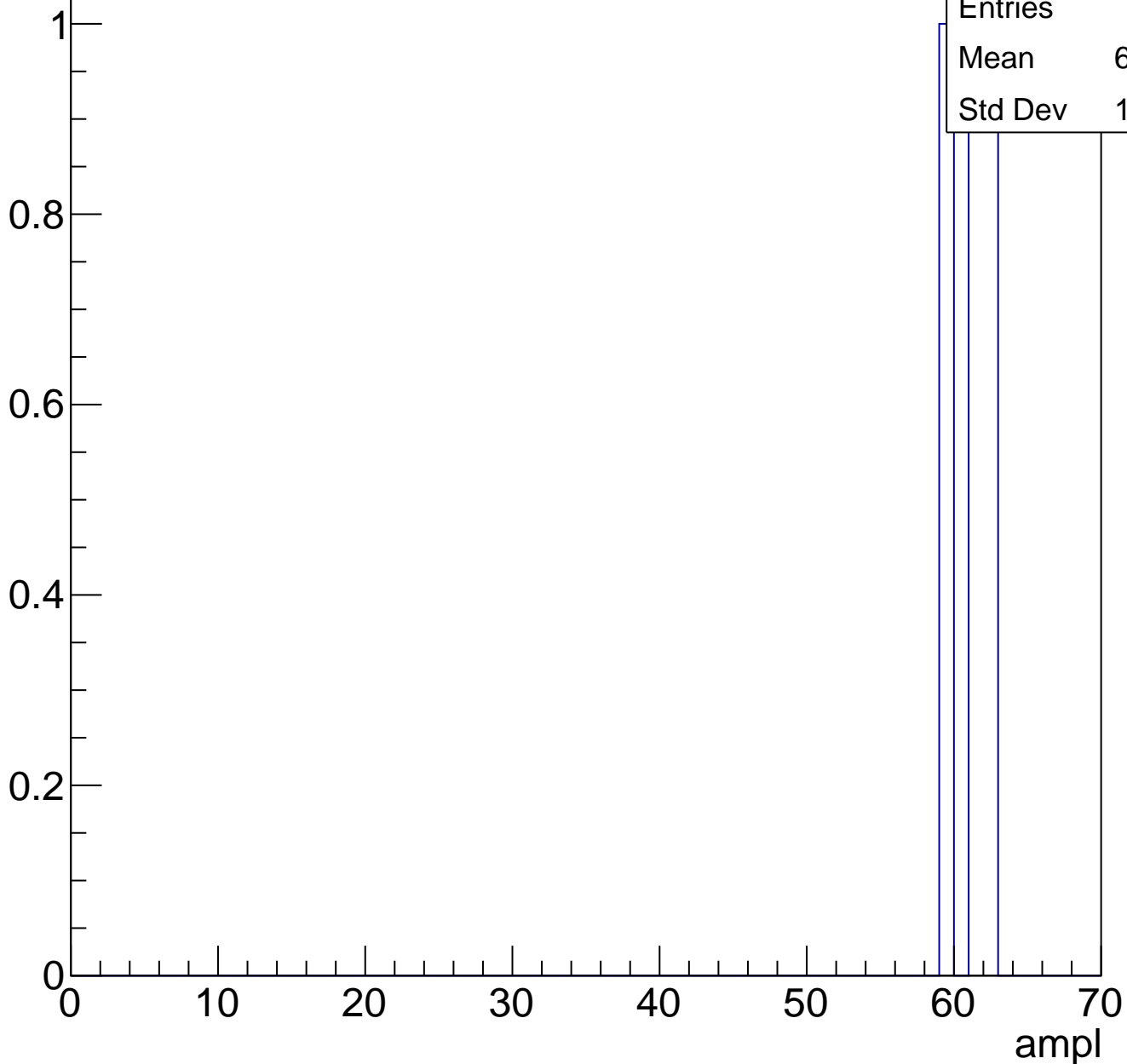
ampl



B0L002S, U18-ch89, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch89, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch90, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	27.45
Std Dev	5.673

Gaus mean : 28.5739

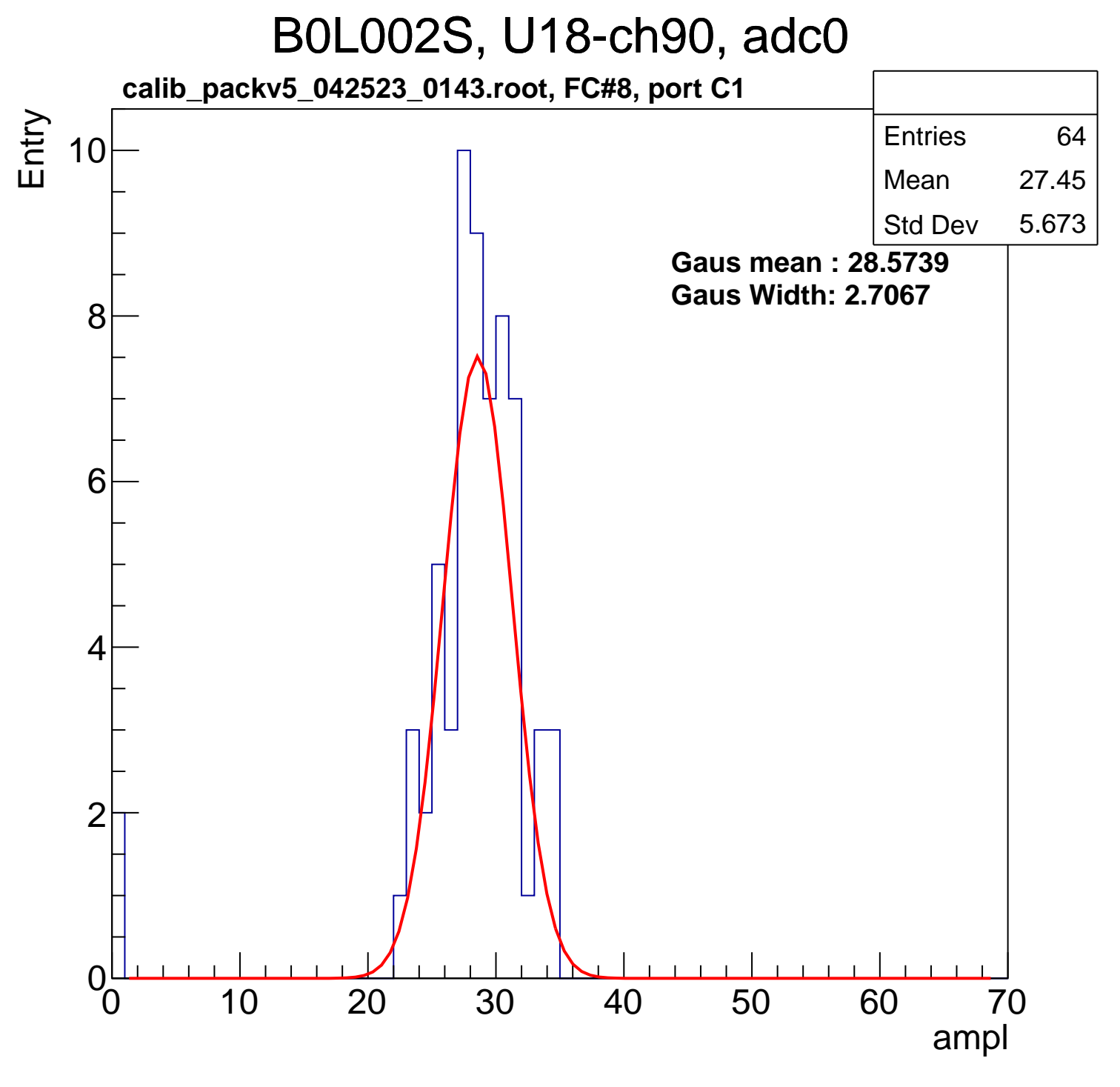
Gaus Width: 2.7067

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch90, adc1

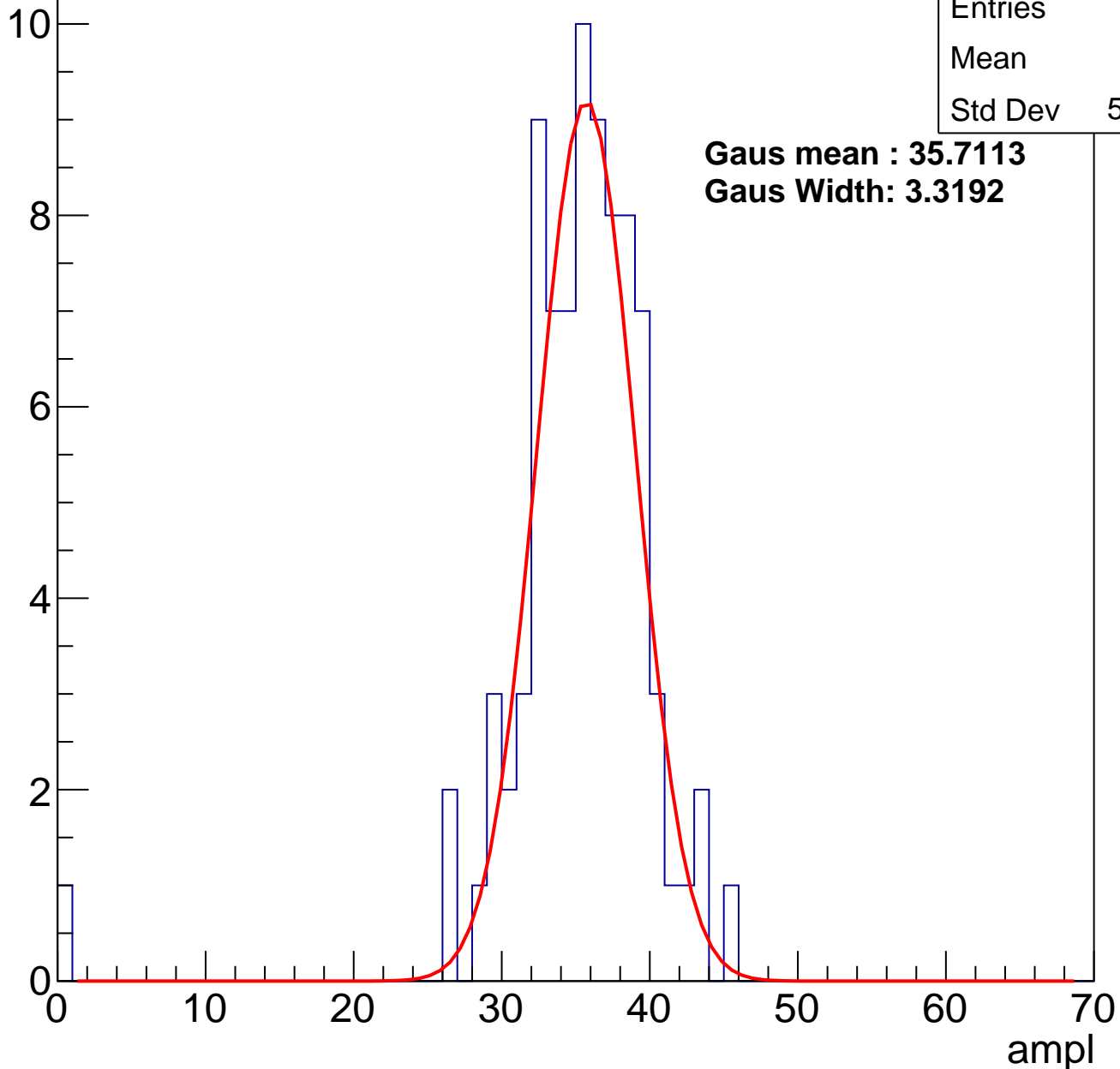
calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	34.8
Std Dev	5.278

Gaus mean : 35.7113

Gaus Width: 3.3192

Entry



B0L002S, U18-ch90, adc2

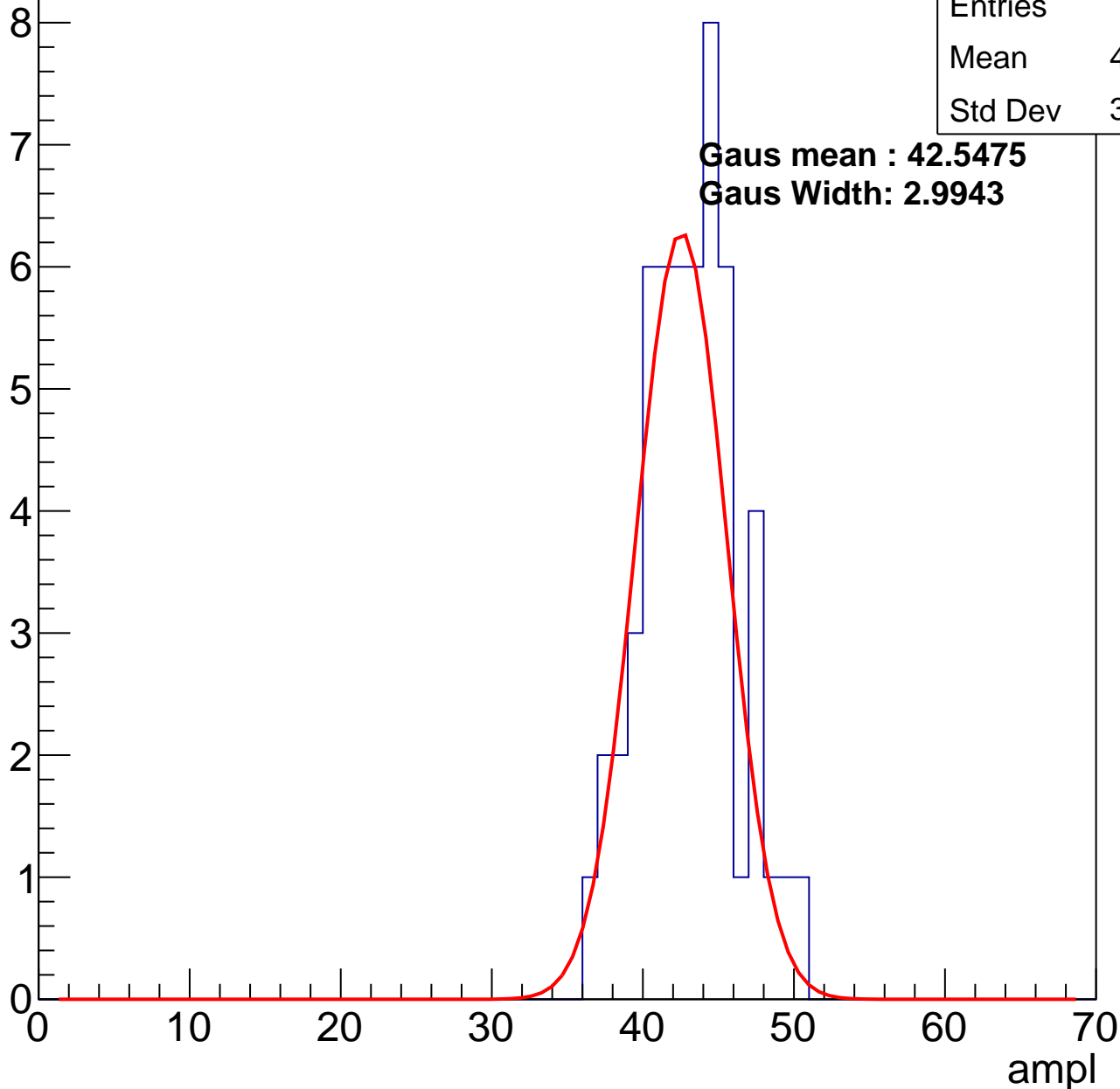
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	42.63
Std Dev	3.075

Gaus mean : 42.5475

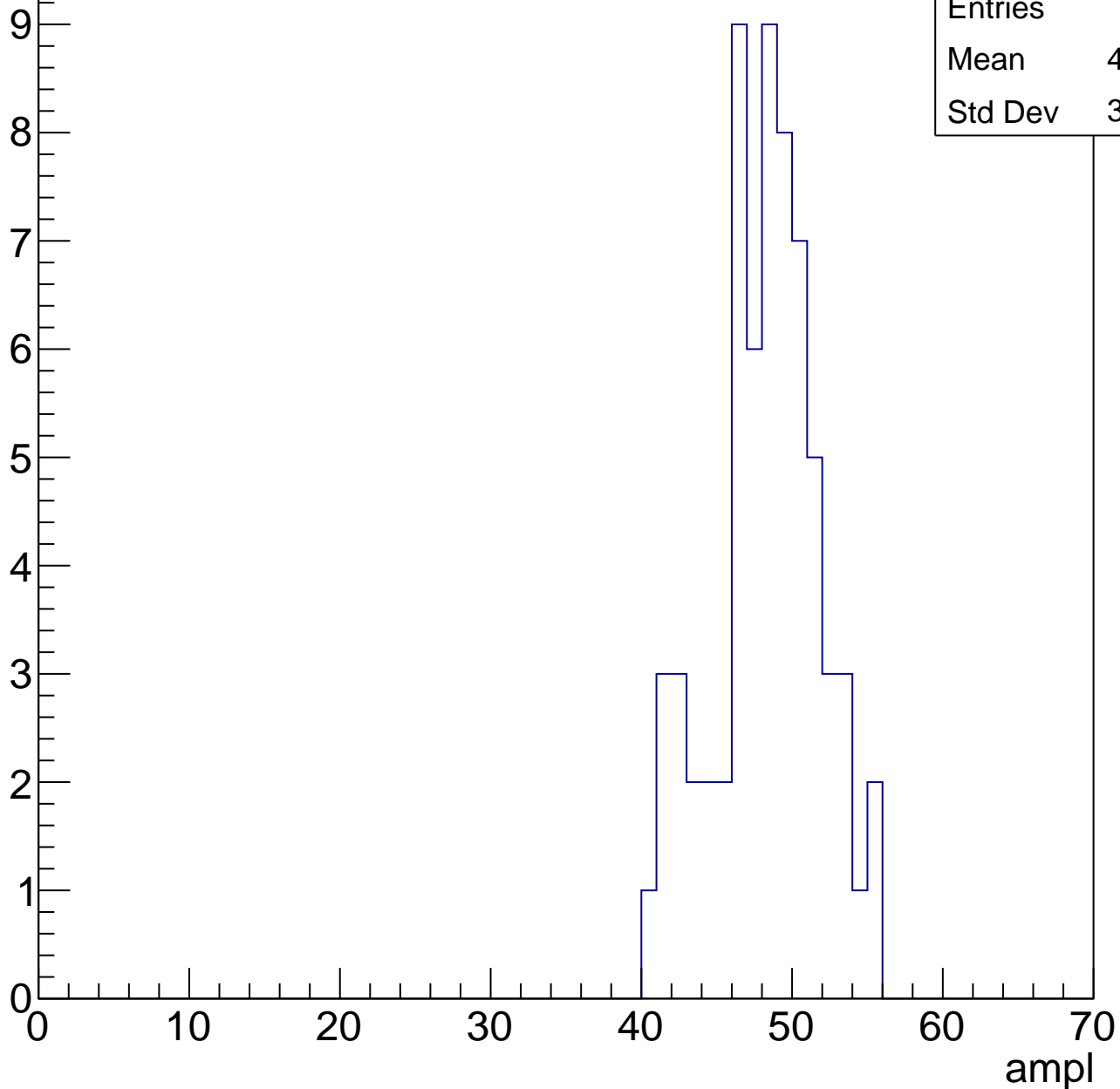
Gaus Width: 2.9943



B0L002S, U18-ch90, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

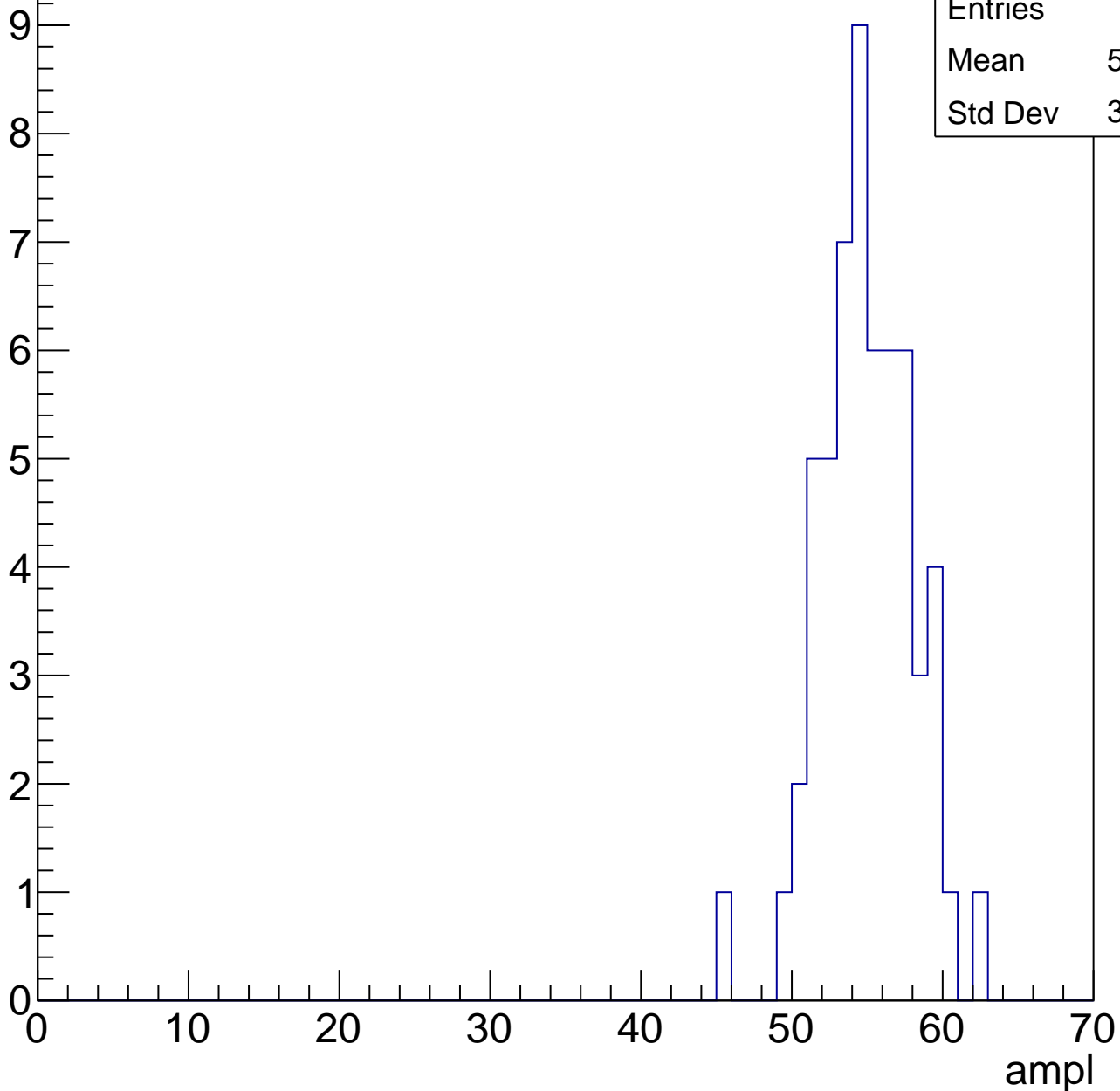


B0L002S, U18-ch90, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	54.49
Std Dev	3.044

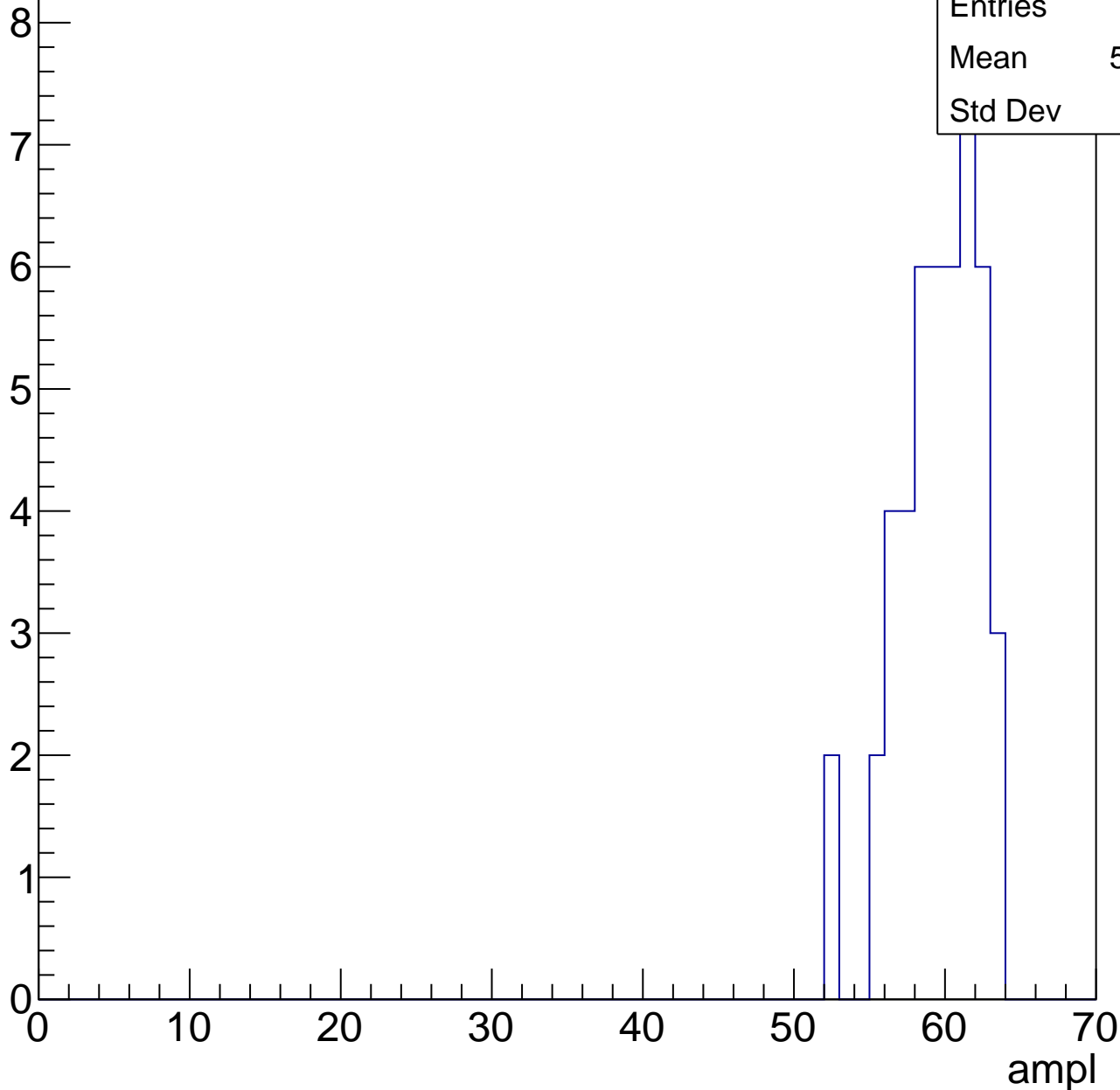


B0L002S, U18-ch90, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	59.09
Std Dev	2.64

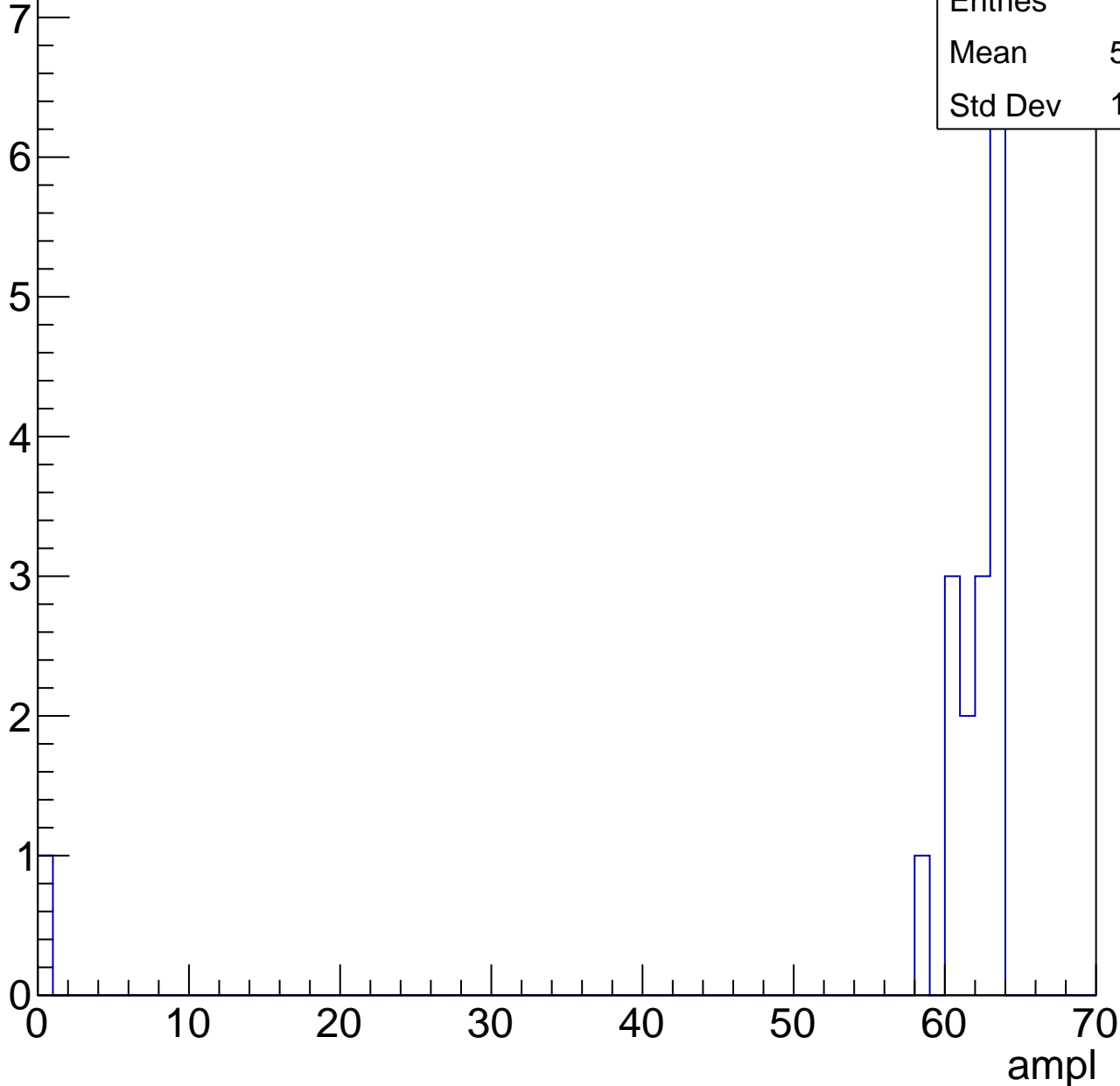


B0L002S, U18-ch90, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	58.06
Std Dev	14.59



B0L002S, U18-ch90, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch91, adc0

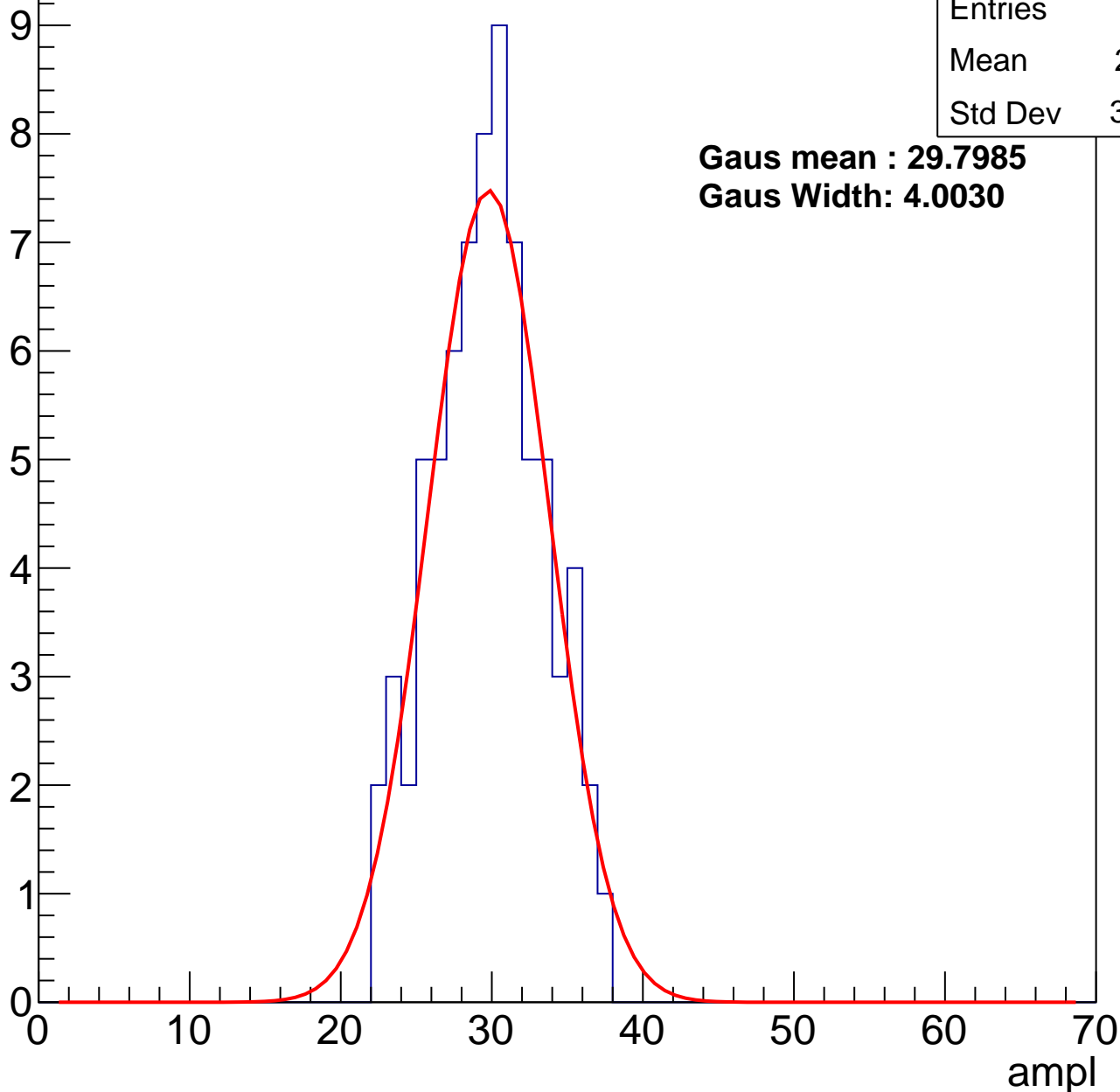
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	29.31
Std Dev	3.579

Gaus mean : 29.7985

Gaus Width: 4.0030



B0L002S, U18-ch91, adc1

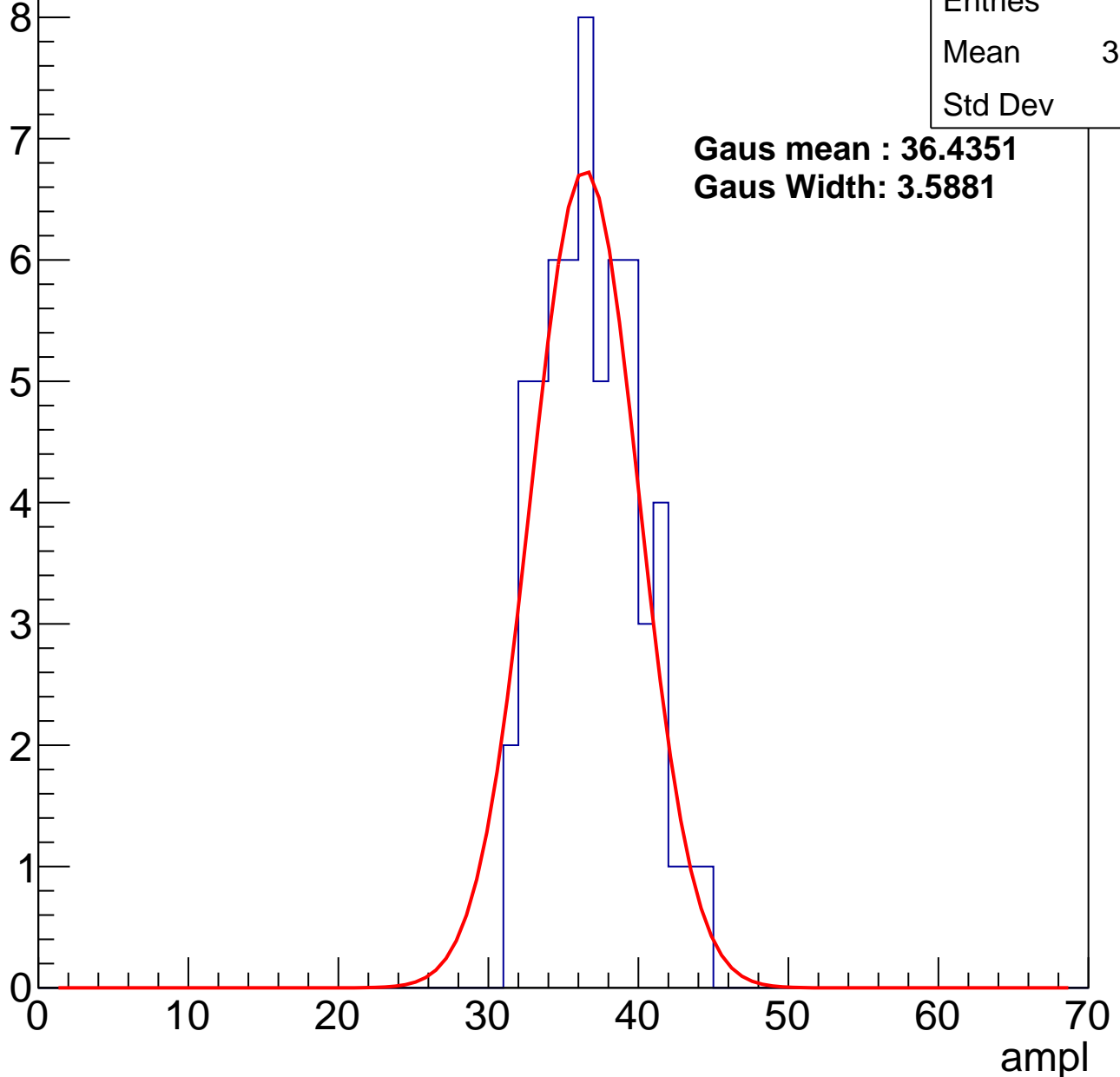
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.42
Std Dev	3.12

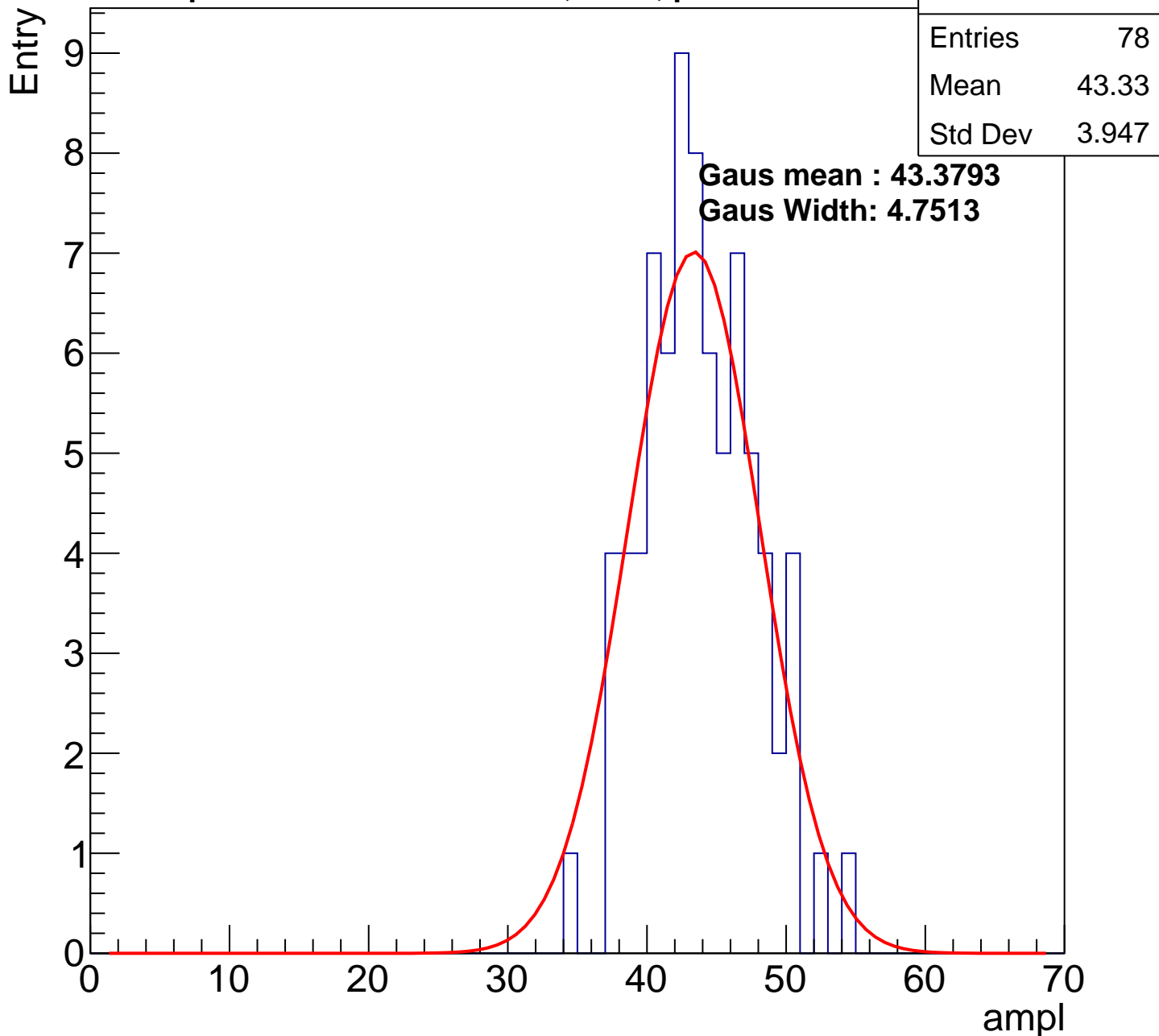
Gaus mean : 36.4351

Gaus Width: 3.5881



B0L002S, U18-ch91, adc2

calib_packv5_042523_0143.root, FC#8, port C1

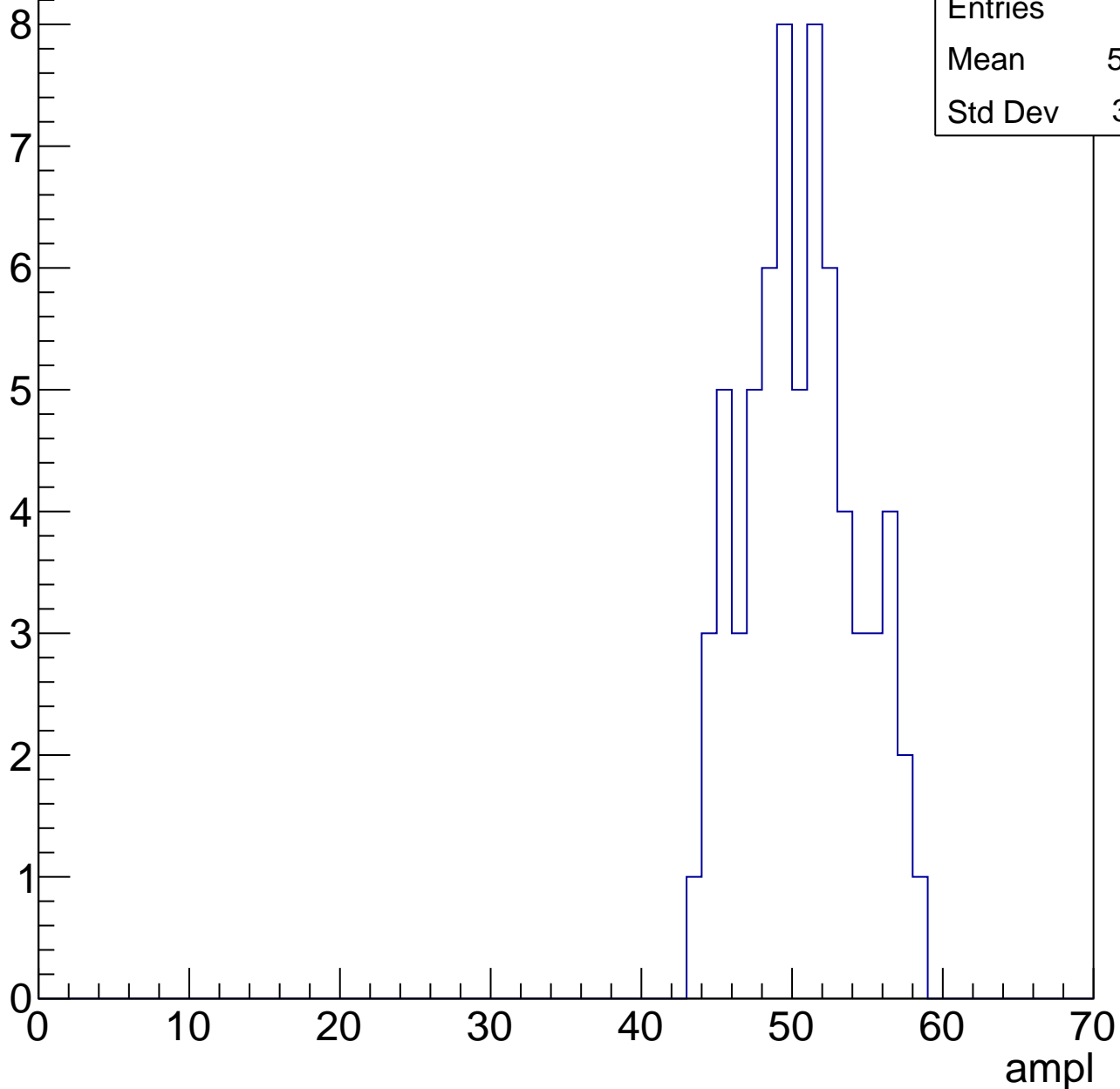


B0L002S, U18-ch91, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

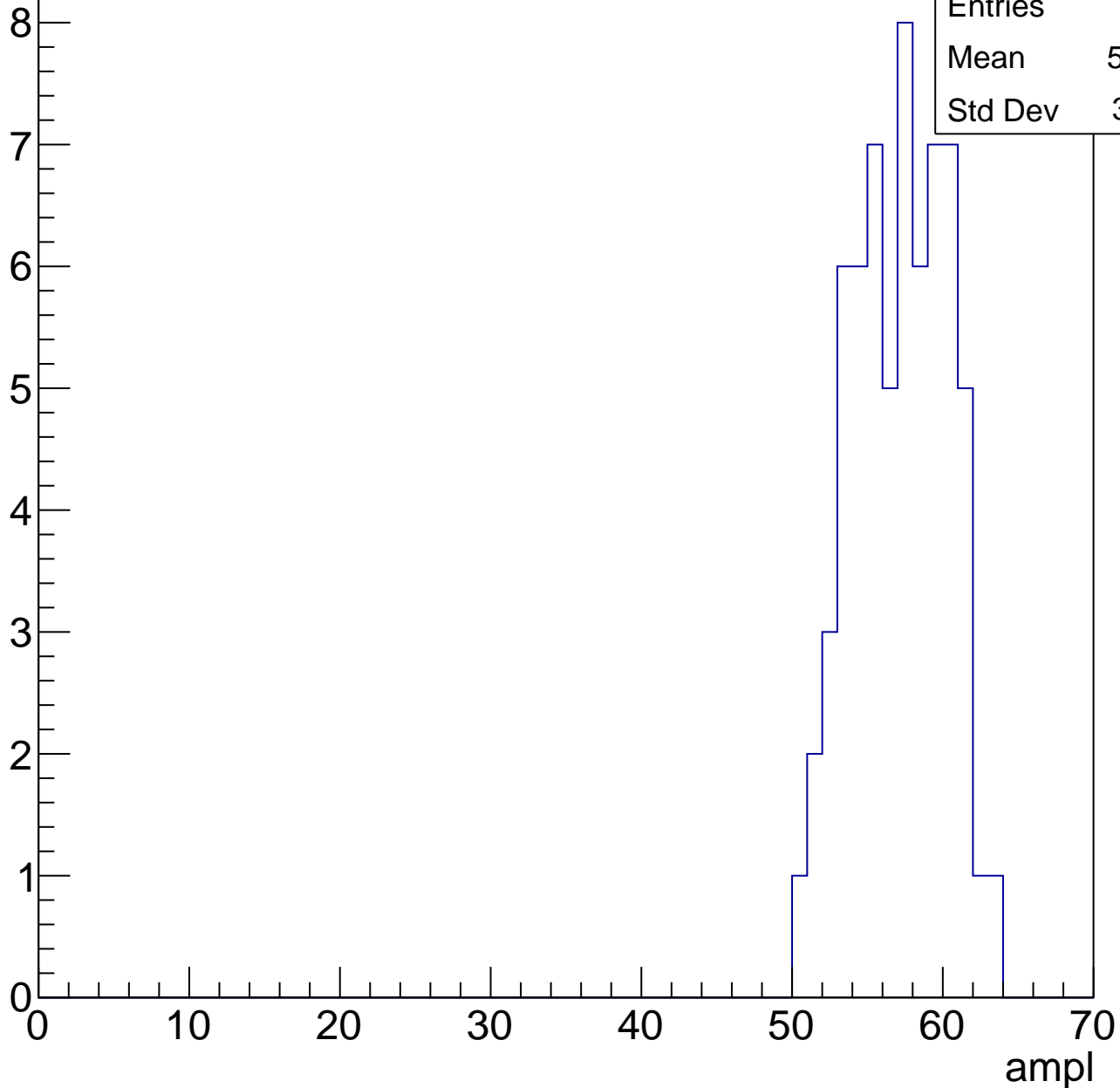
Entries	67
Mean	50.12
Std Dev	3.671



B0L002S, U18-ch91, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



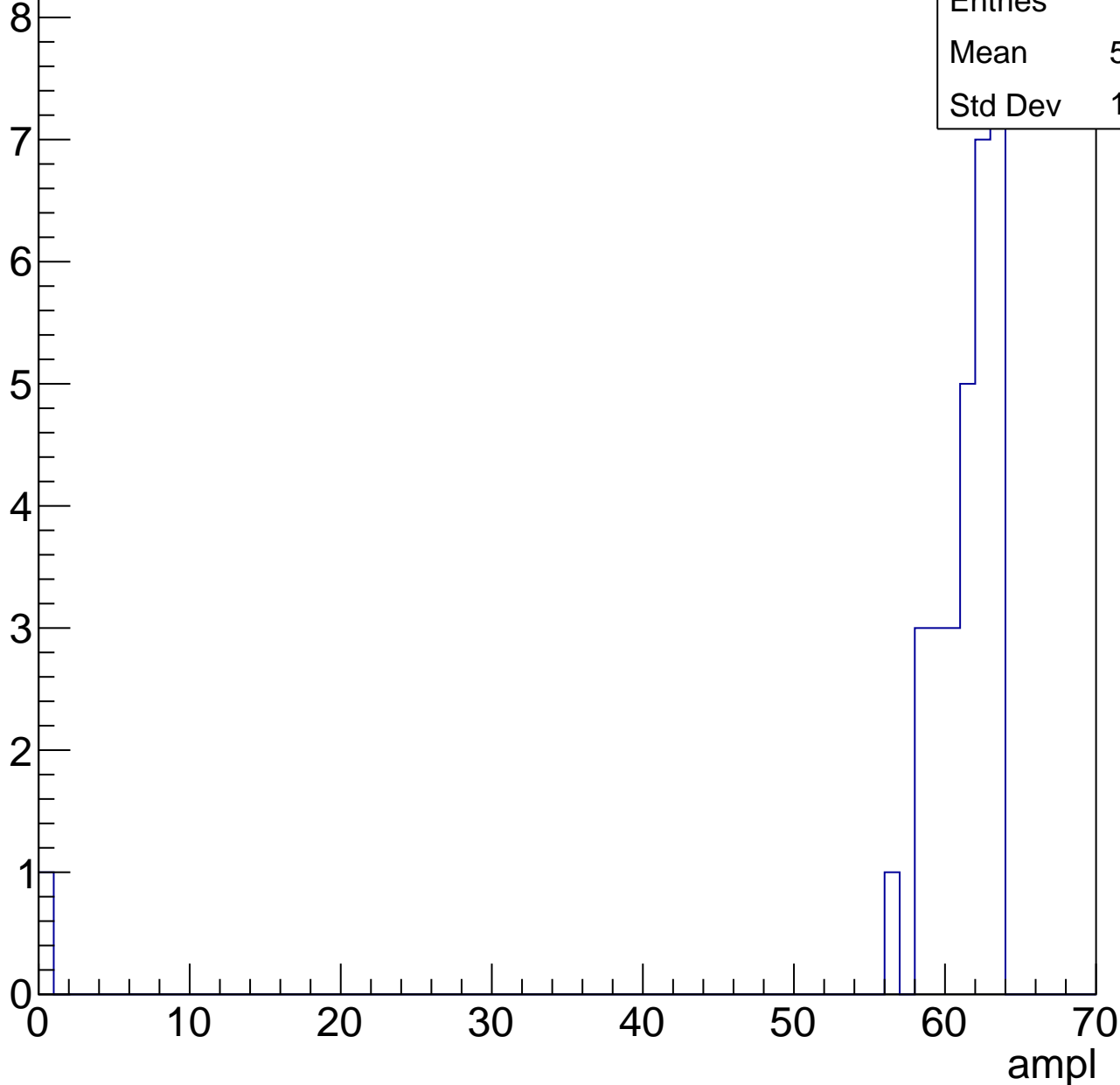
Entries	65
Mean	56.65
Std Dev	3.061

B0L002S, U18-ch91, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	31
Mean	59.03
Std Dev	10.94



B0L002S, U18-ch91, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch91, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch92, adc0

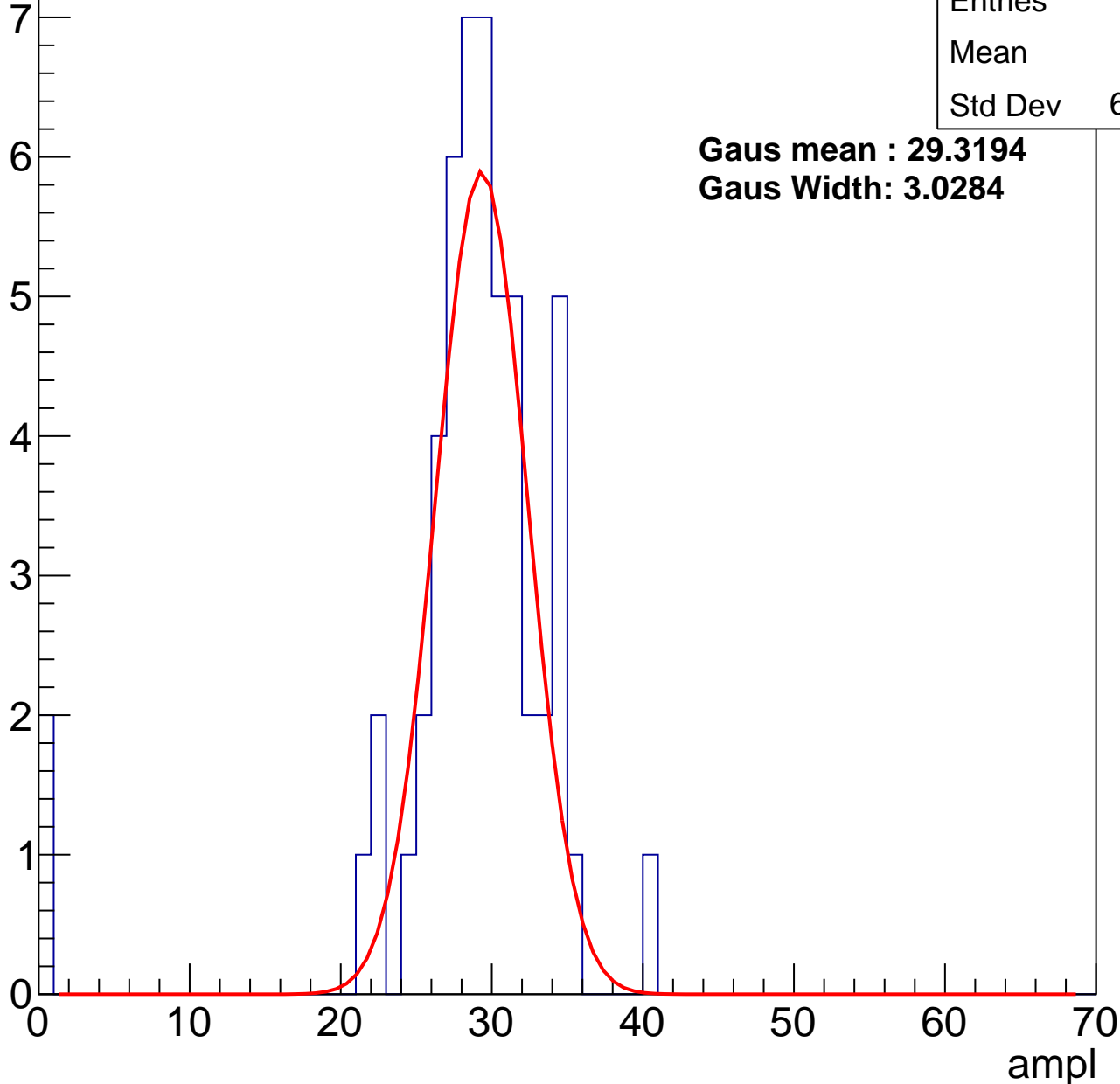
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	28
Std Dev	6.547

Gaus mean : 29.3194

Gaus Width: 3.0284



B0L002S, U18-ch92, adc1

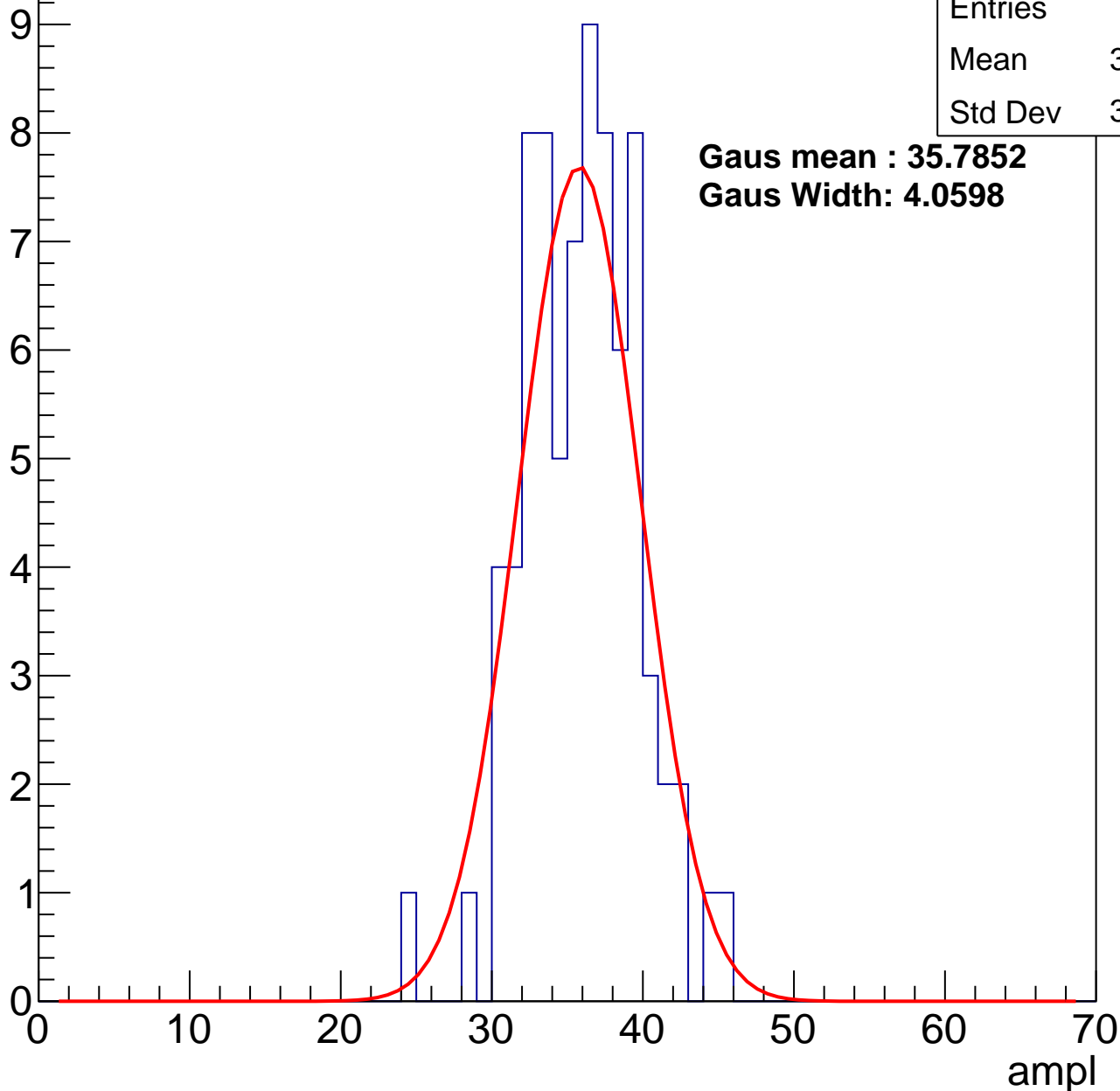
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.46
Std Dev	3.713

Gaus mean : 35.7852

Gaus Width: 4.0598



B0L002S, U18-ch92, adc2

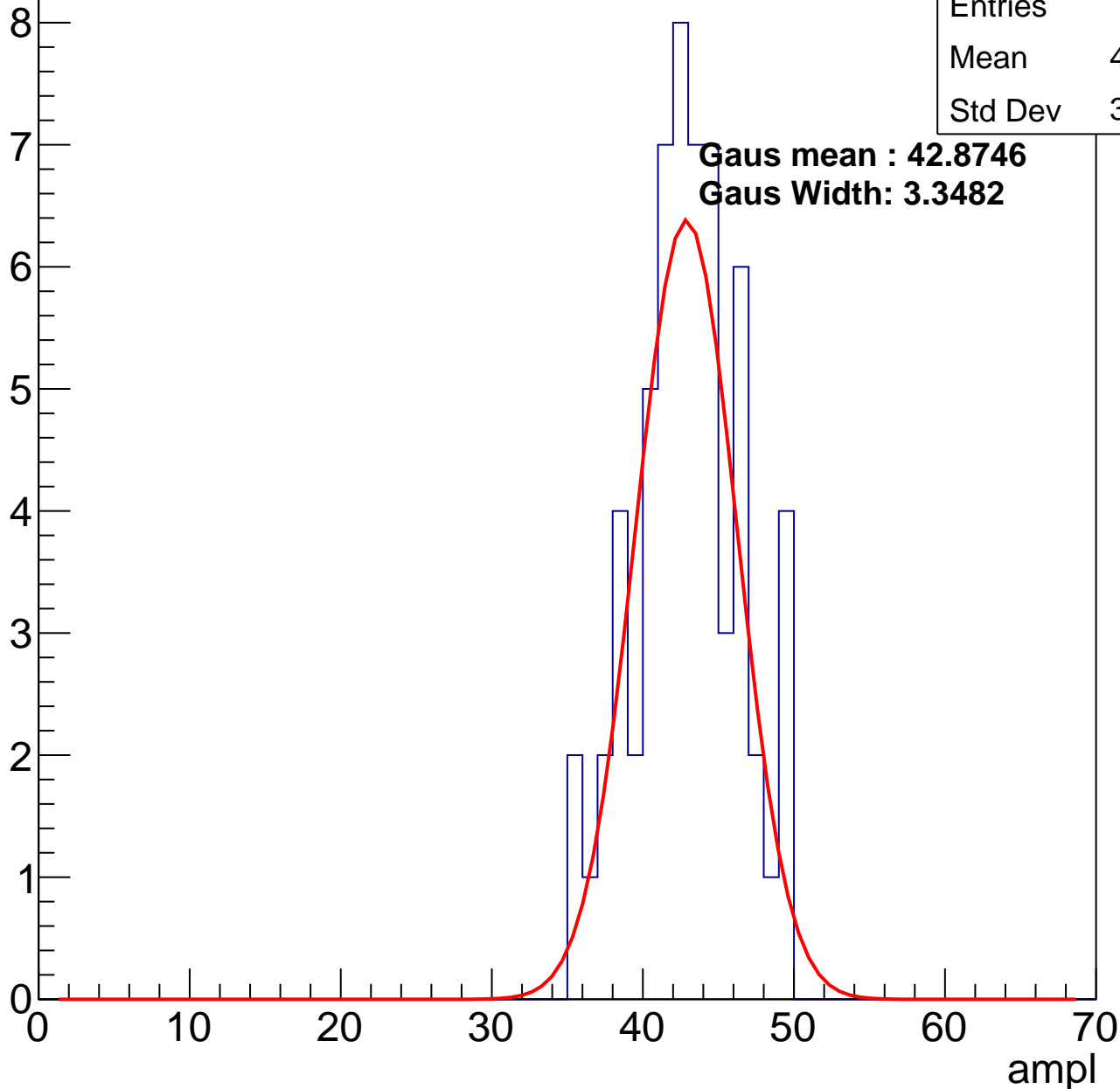
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.48
Std Dev	3.434

Gaus mean : 42.8746

Gaus Width: 3.3482

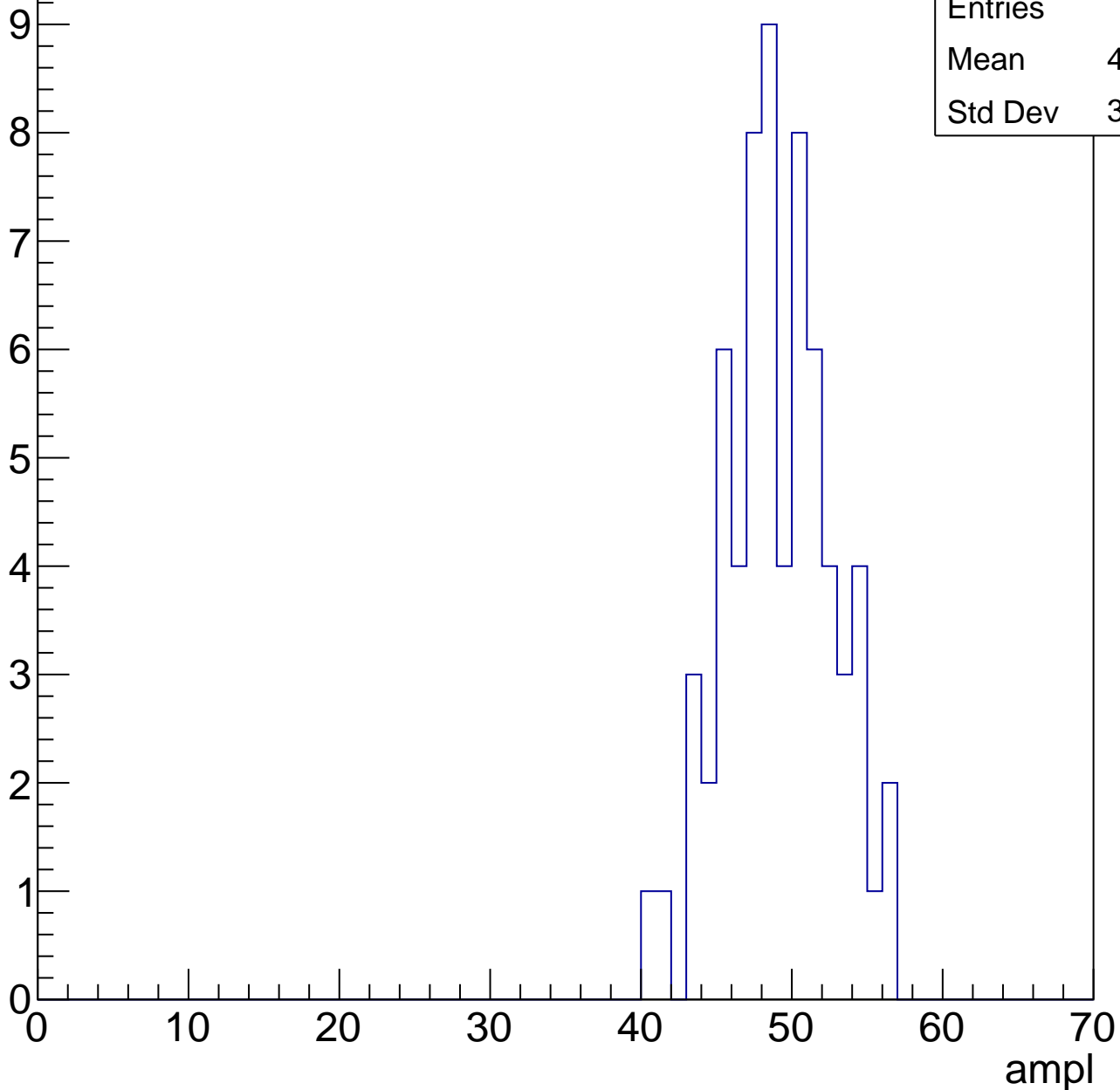


B0L002S, U18-ch92, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	48.67
Std Dev	3.522

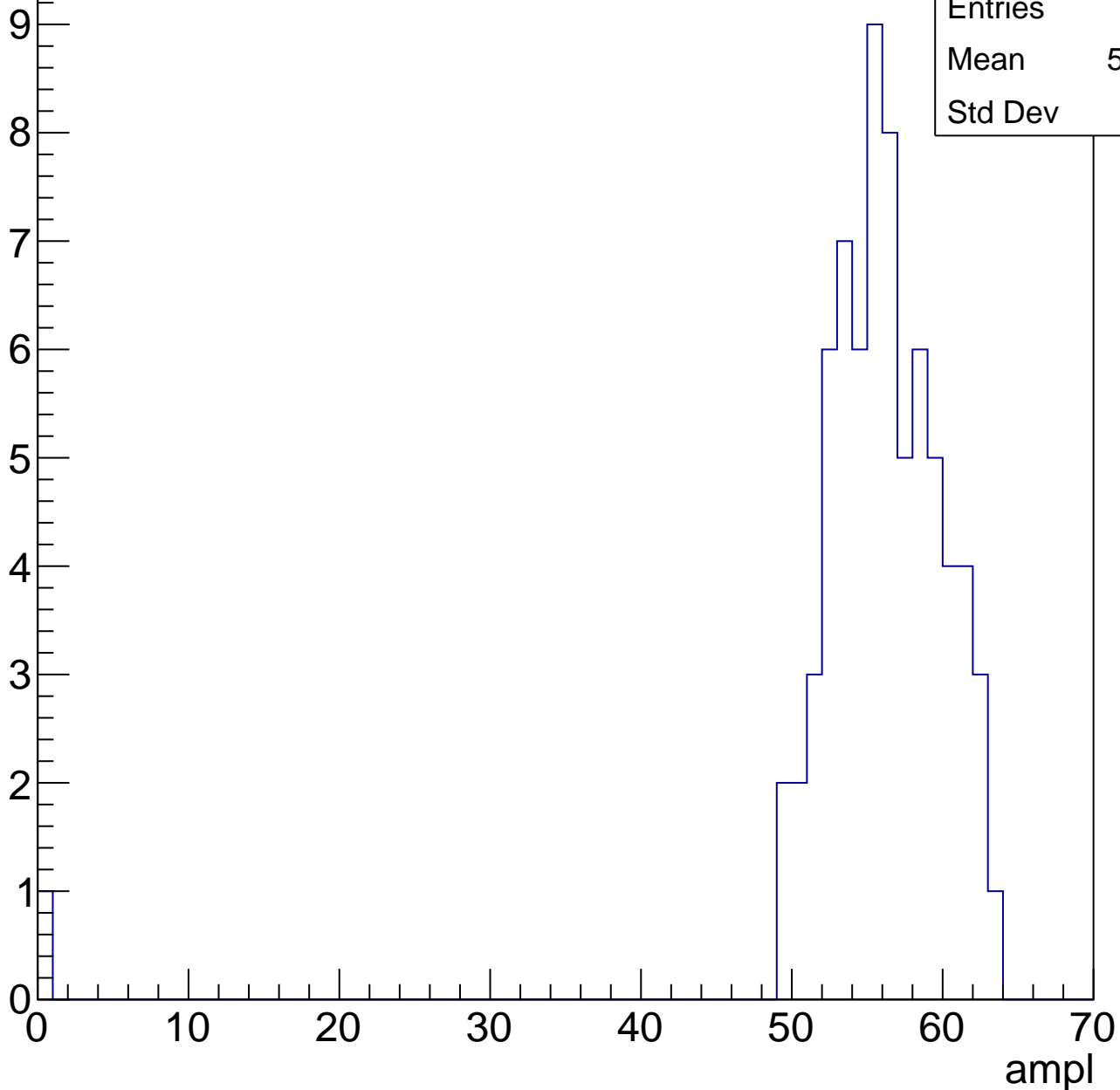


B0L002S, U18-ch92, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

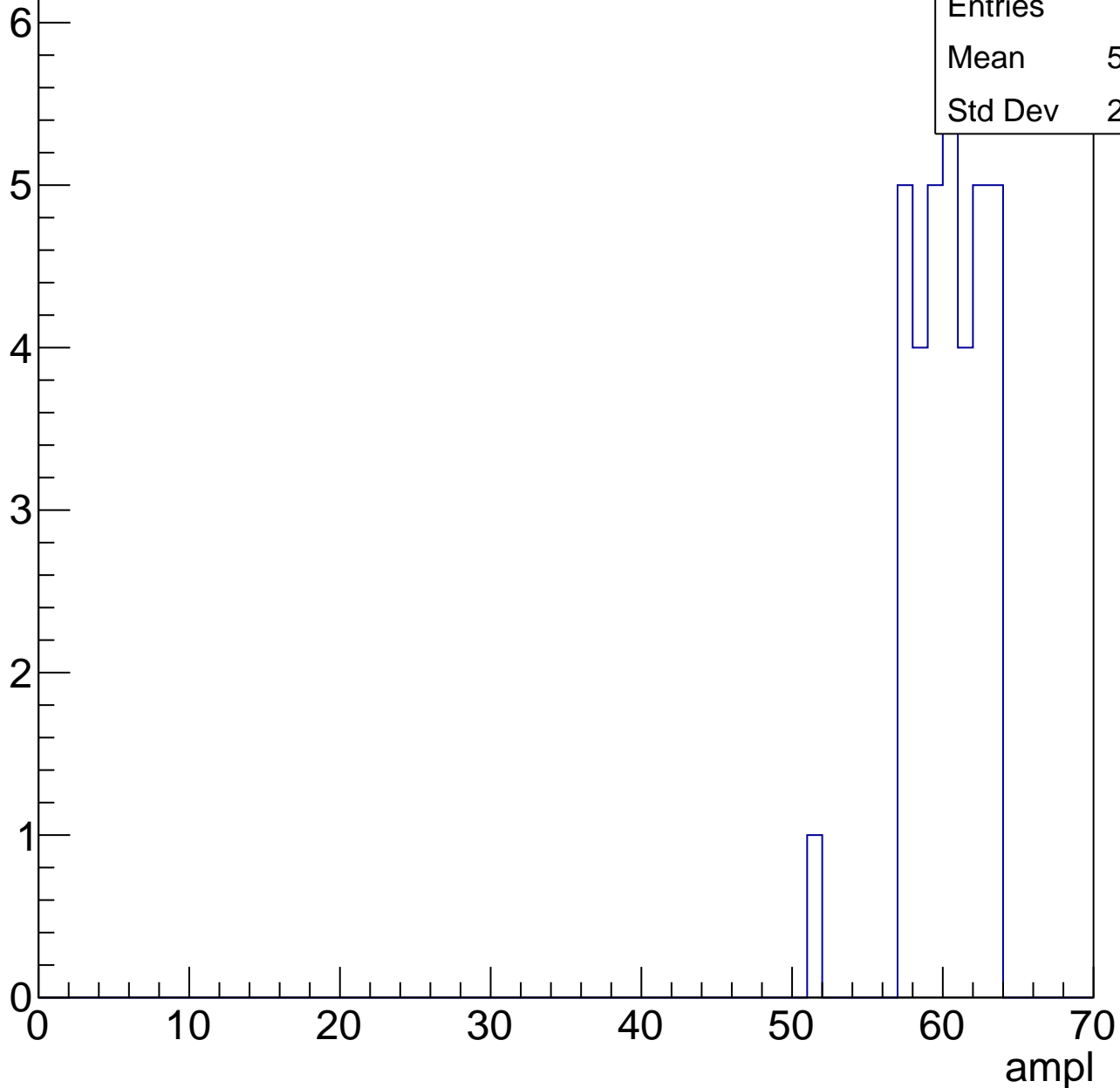
Entries	72
Mean	55.03
Std Dev	7.36



B0L002S, U18-ch92, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch92, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch92, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch93, adc0

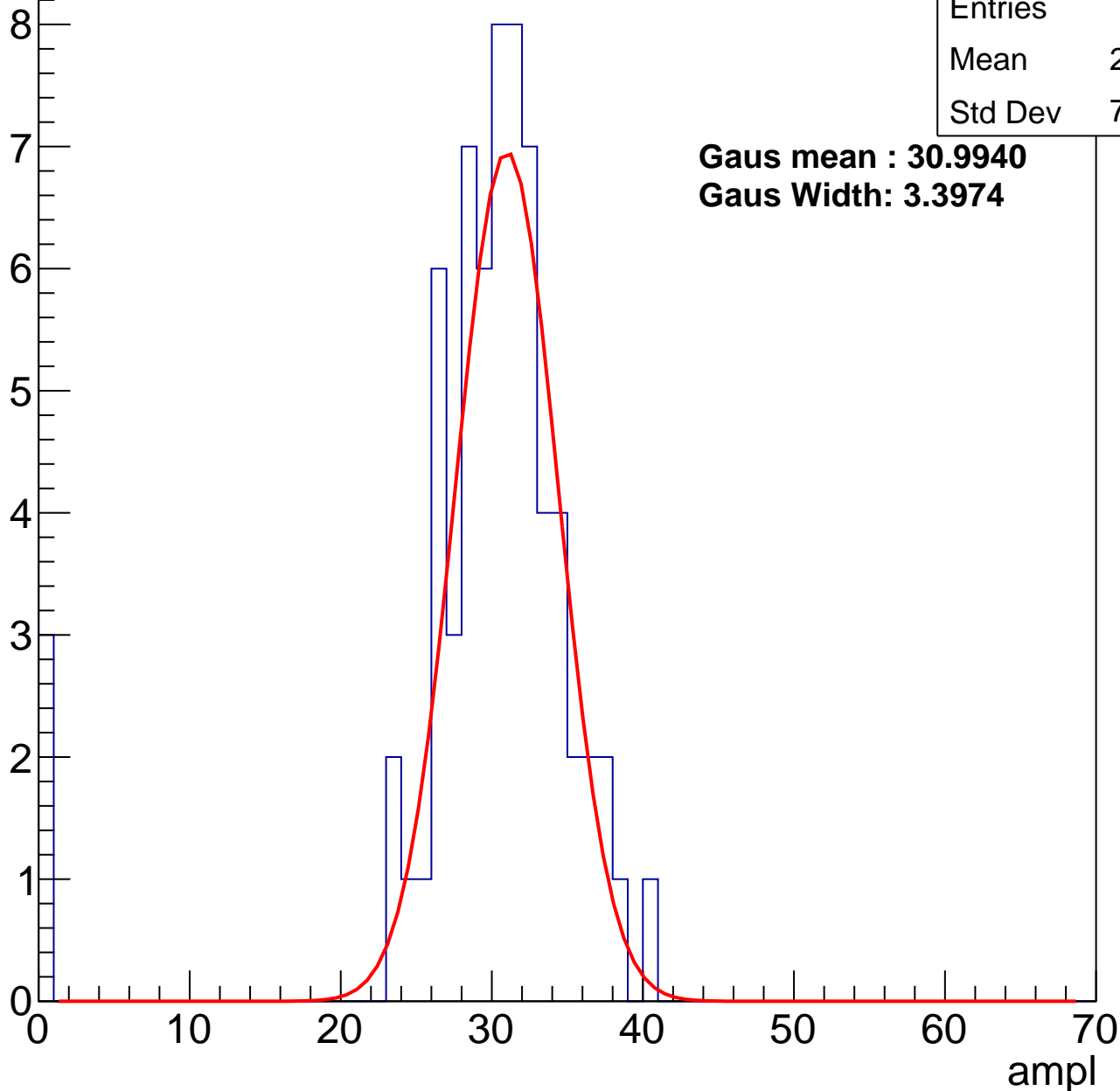
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.06
Std Dev	7.149

Gaus mean : 30.9940

Gaus Width: 3.3974



B0L002S, U18-ch93, adc1

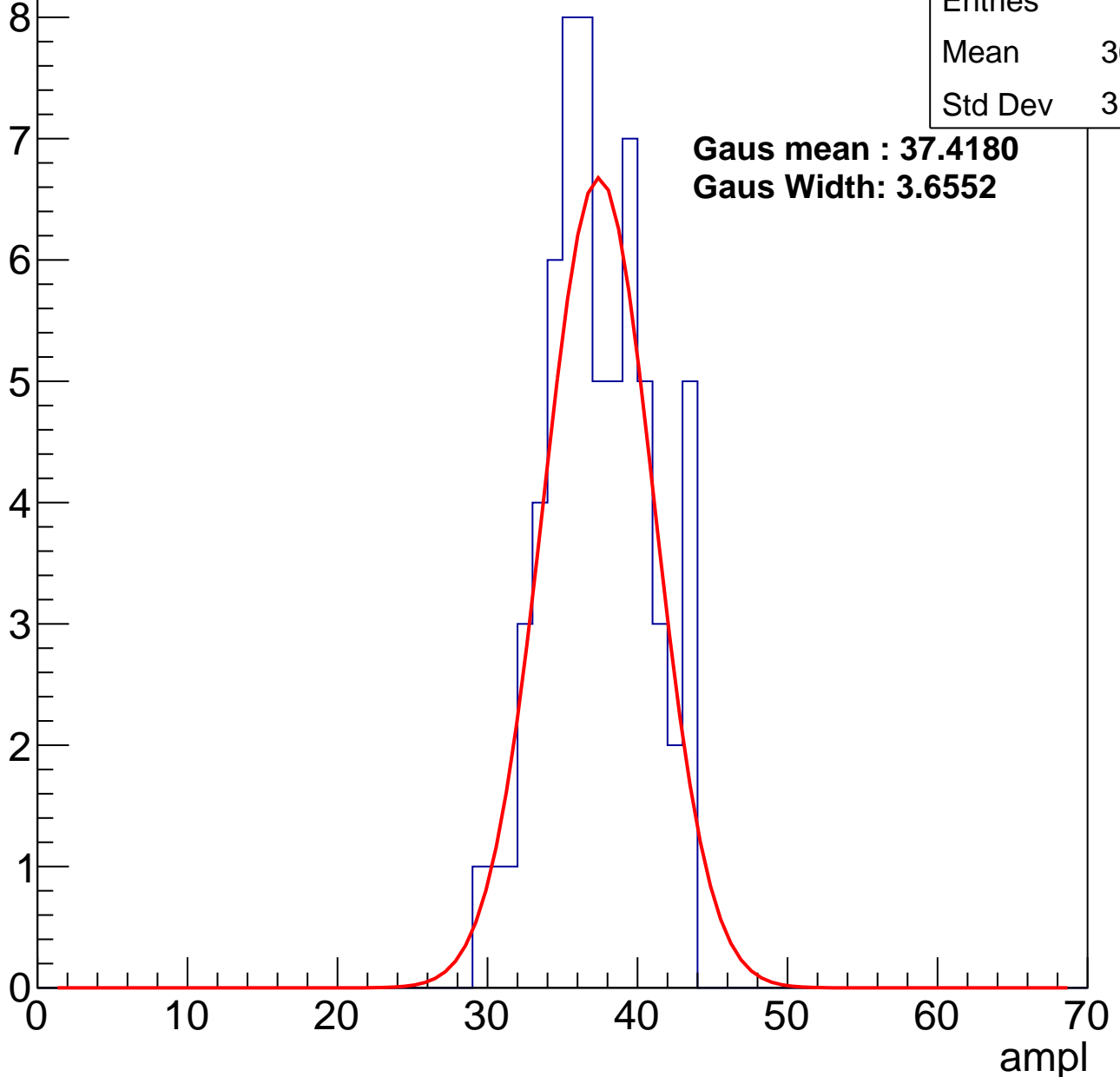
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.88
Std Dev	3.393

Gaus mean : 37.4180

Gaus Width: 3.6552



B0L002S, U18-ch93, adc2

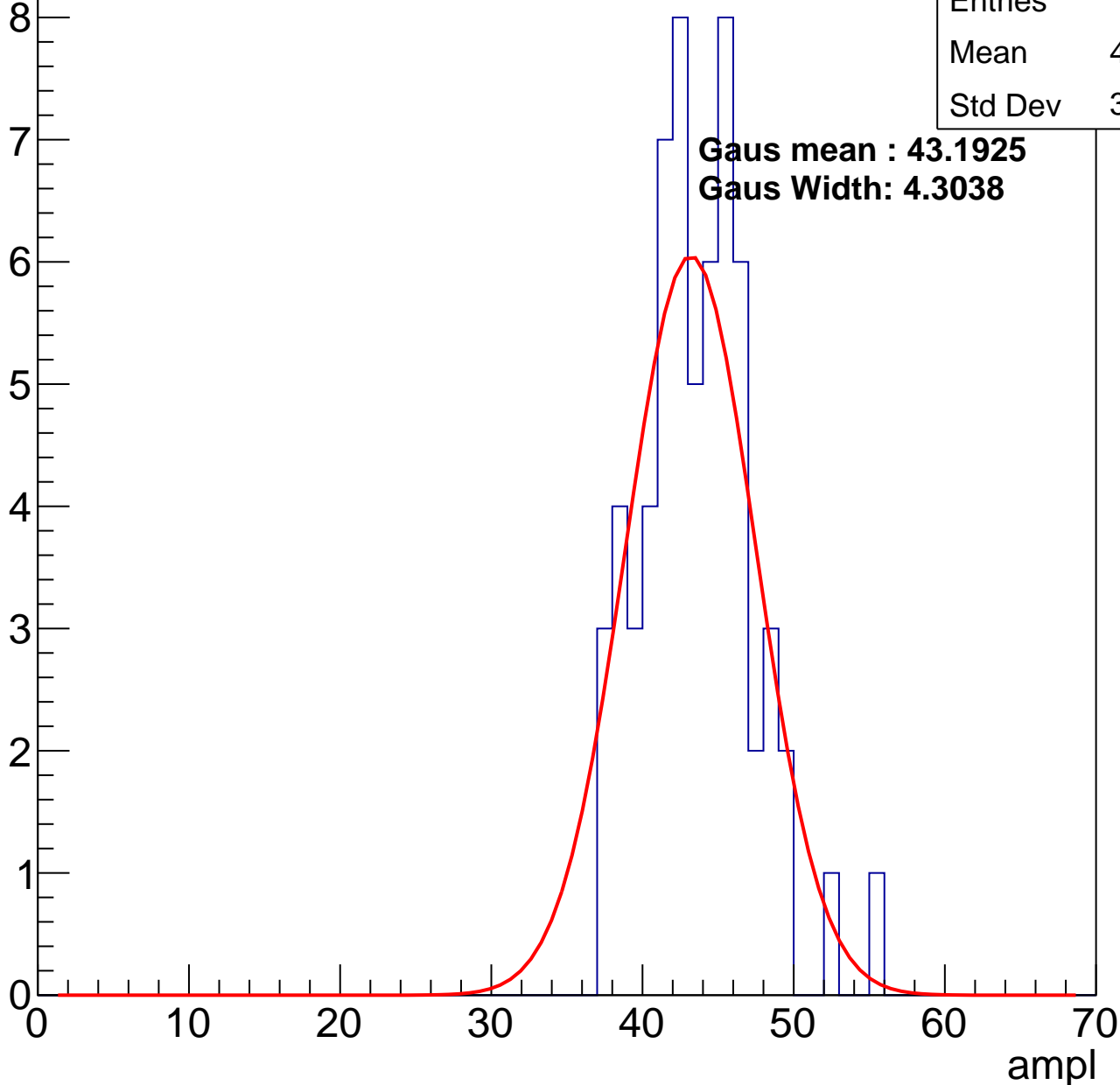
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	43.19
Std Dev	3.612

Gaus mean : 43.1925

Gaus Width: 4.3038



B0L002S, U18-ch93, adc3

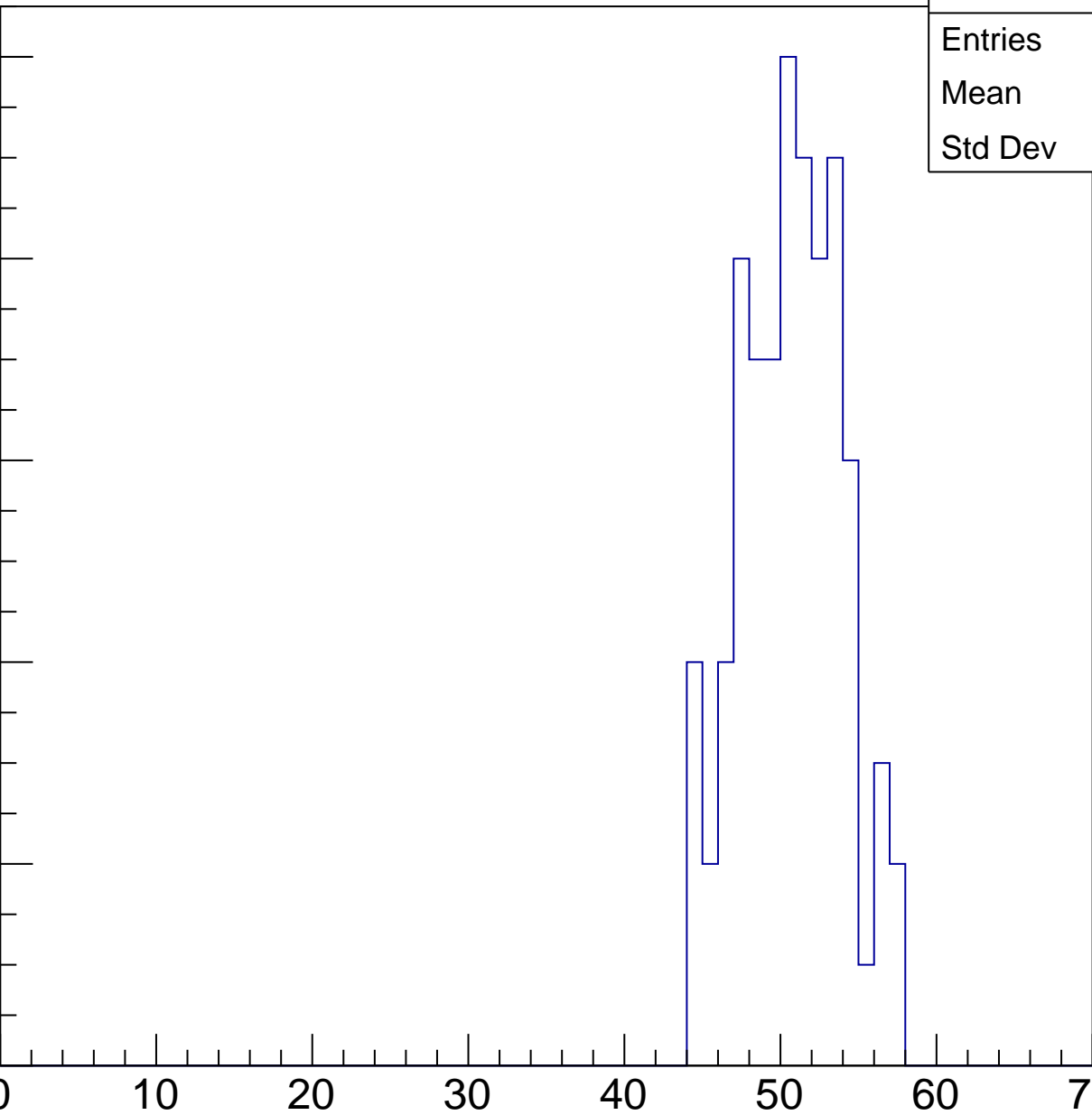
calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	50.23
Std Dev	3.182

Entry

10
8
6
4
2
0

ampl



B0L002S, U18-ch93, adc4

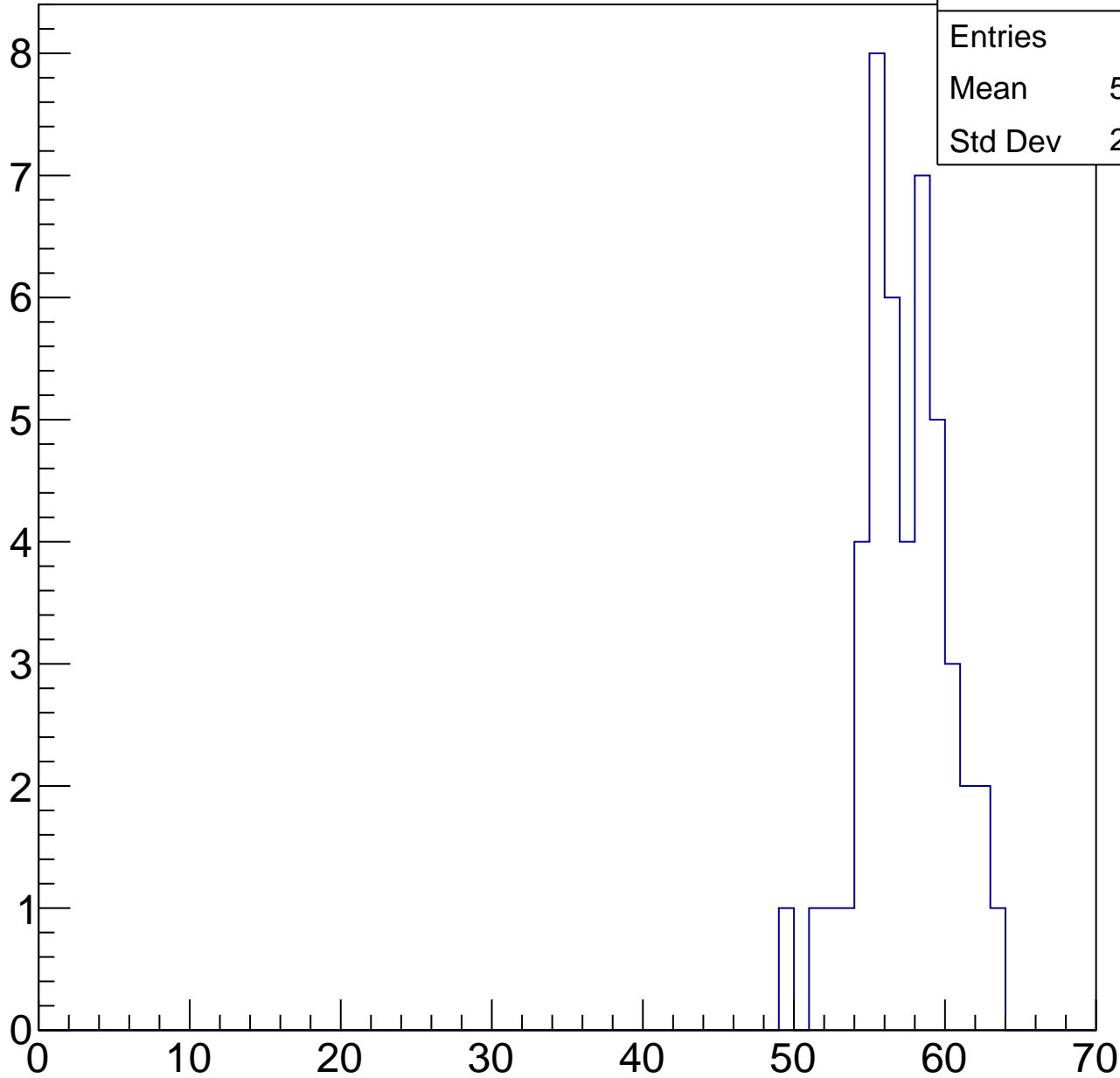
calib_packv5_042523_0143.root, FC#8, port C1

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	56.85
Std Dev	2.897

ampl



B0L002S, U18-ch93, adc5

calib_packv5_042523_0143.root, FC#8, port C1

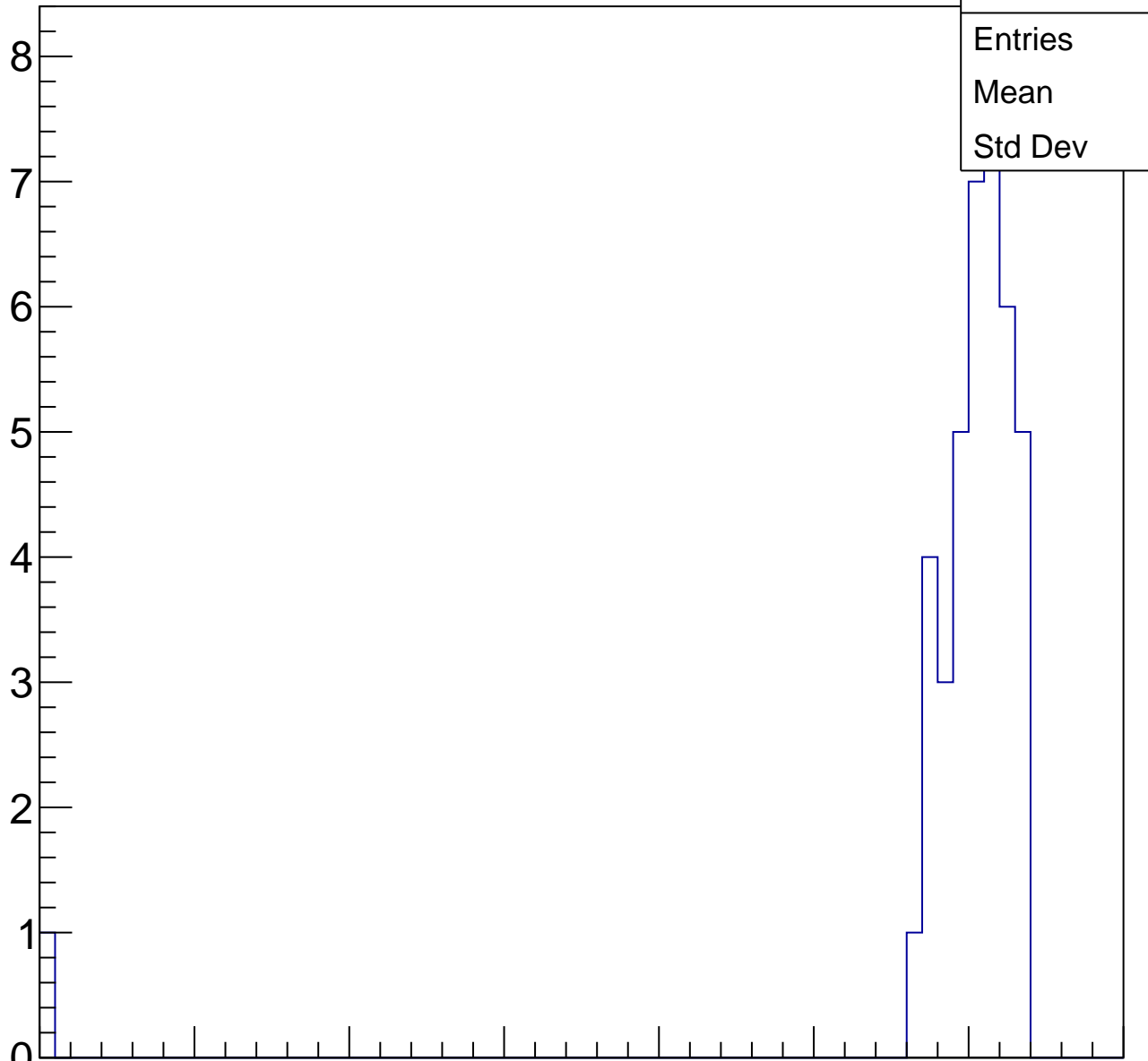
Entry

8
7
6
5
4
3
2
1
0

Entries	40
Mean	58.7
Std Dev	9.59

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch93, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch93, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch94, adc0

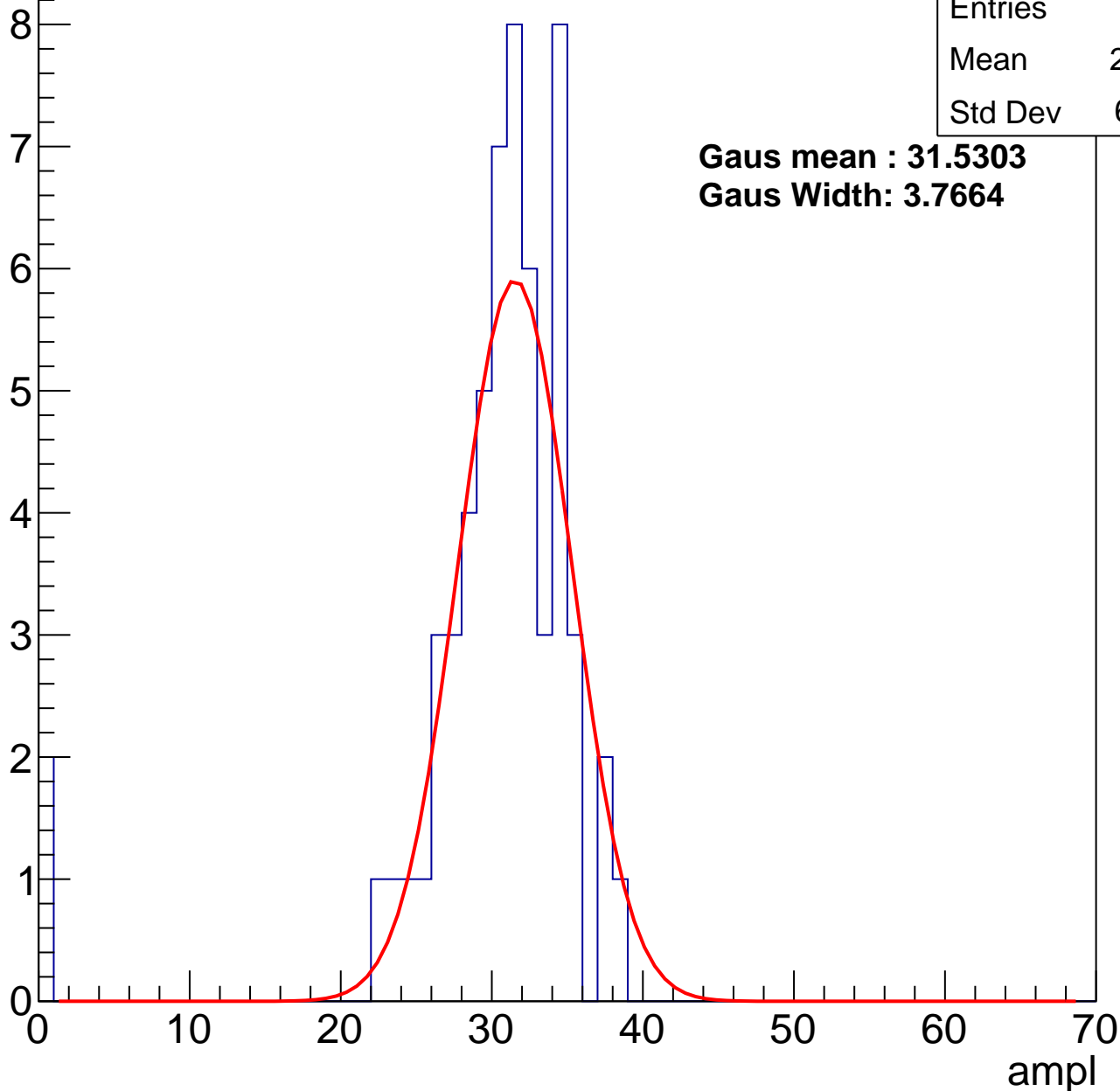
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	29.63
Std Dev	6.491

Gaus mean : 31.5303

Gaus Width: 3.7664



B0L002S, U18-ch94, adc1

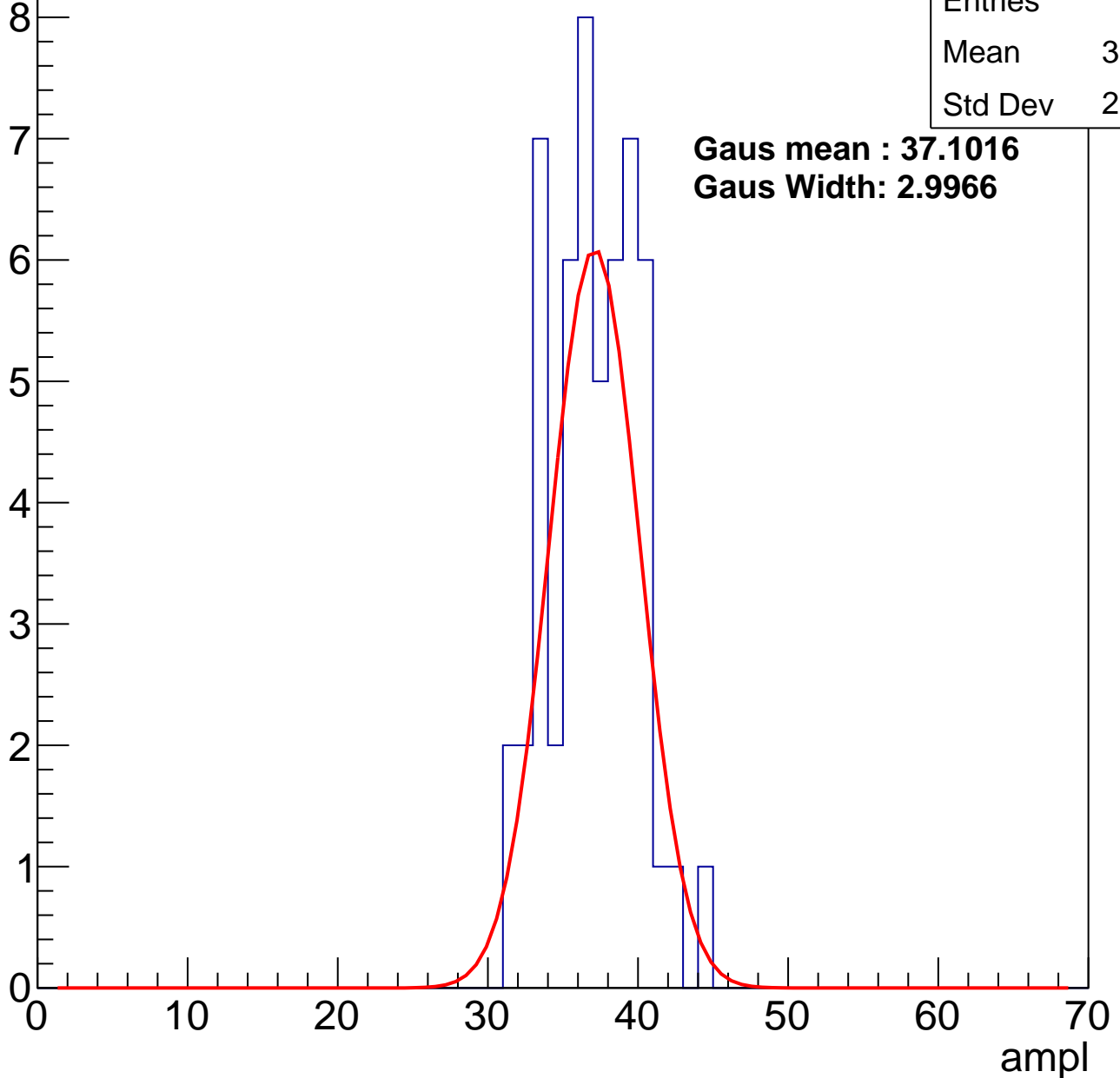
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	36.59
Std Dev	2.896

Gaus mean : 37.1016

Gaus Width: 2.9966



B0L002S, U18-ch94, adc2

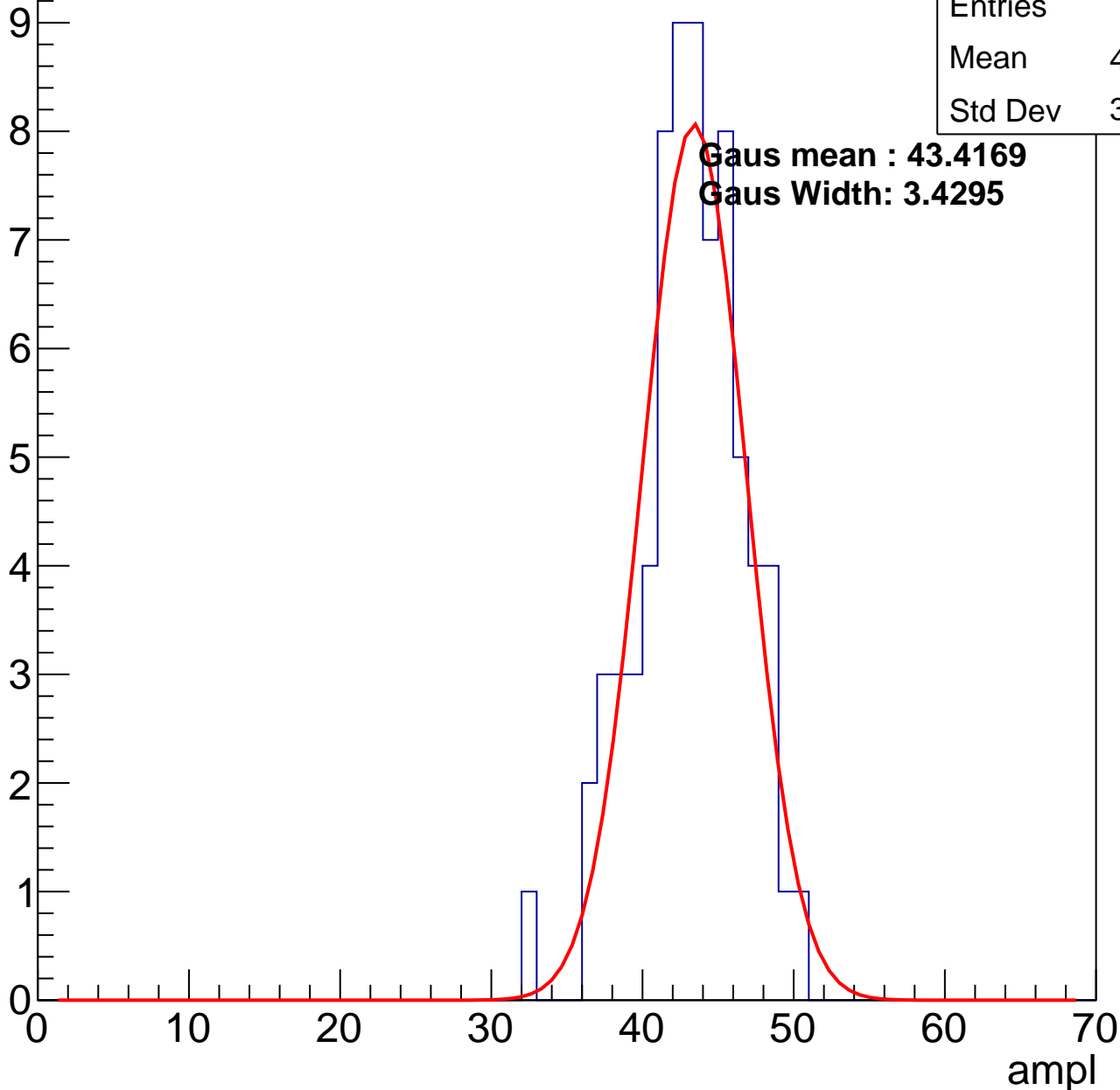
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	42.72
Std Dev	3.445

Gaus mean : 43.4169

Gaus Width: 3.4295

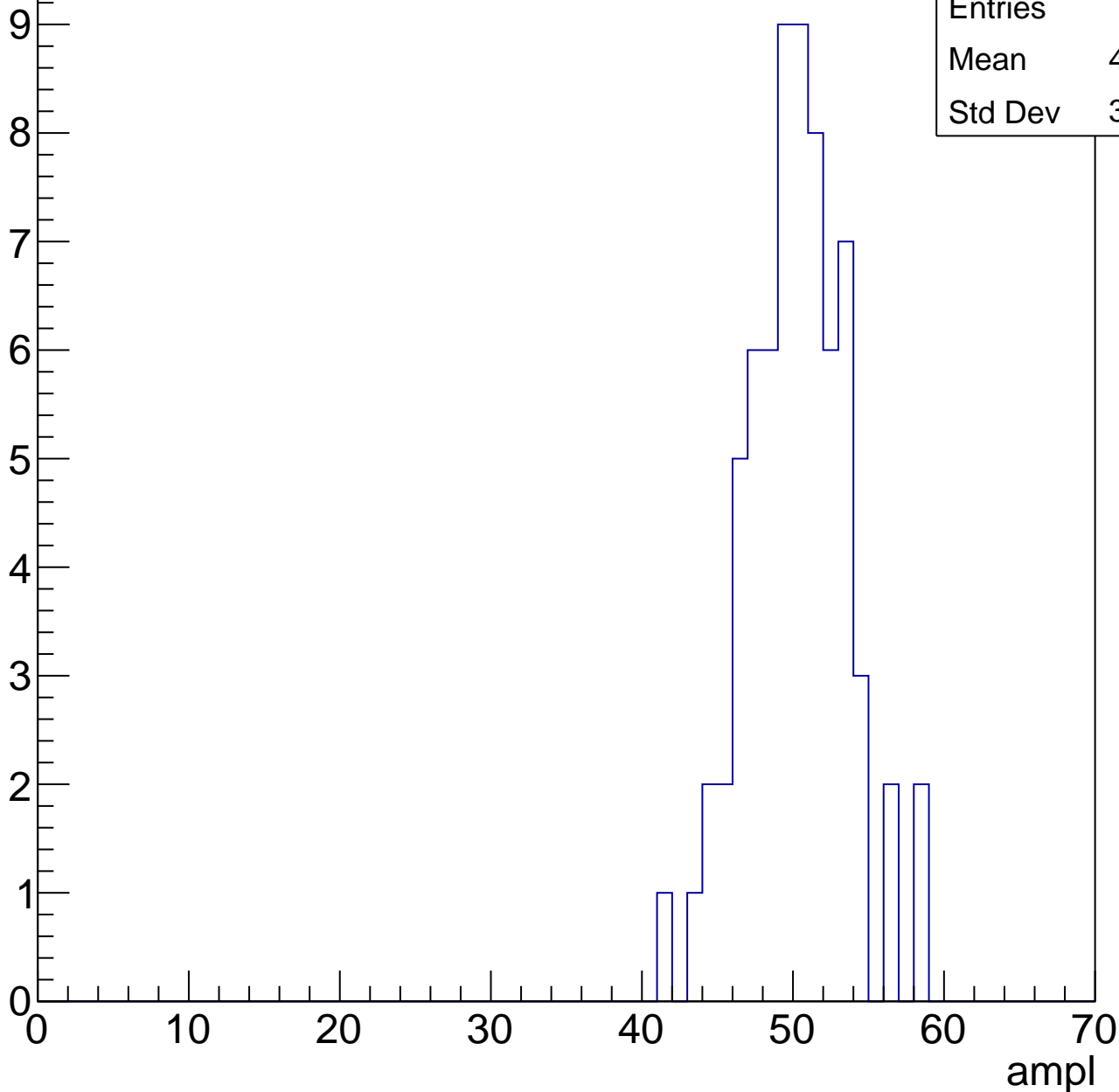


B0L002S, U18-ch94, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	49.77
Std Dev	3.315

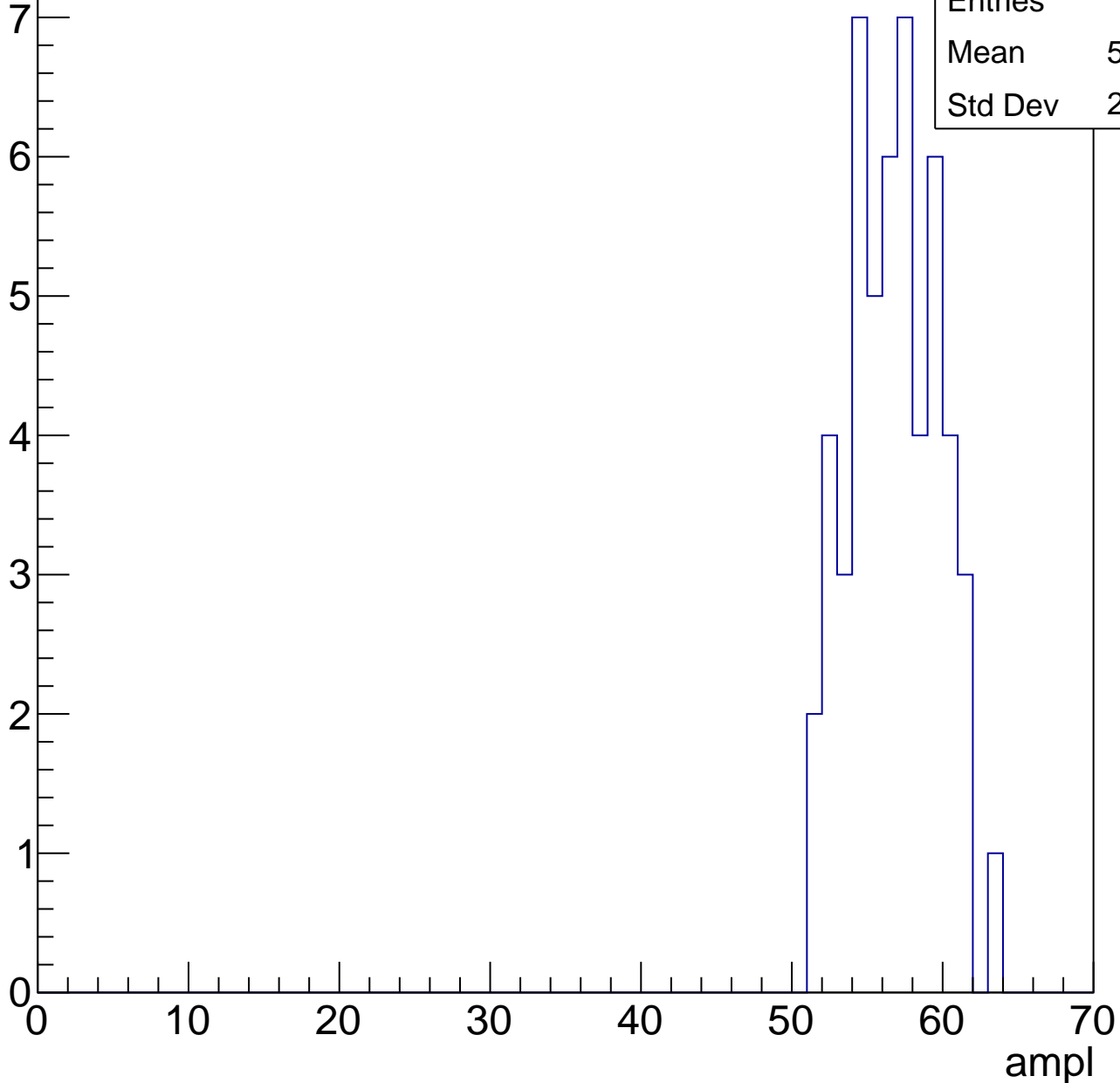


B0L002S, U18-ch94, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	56.33
Std Dev	2.887

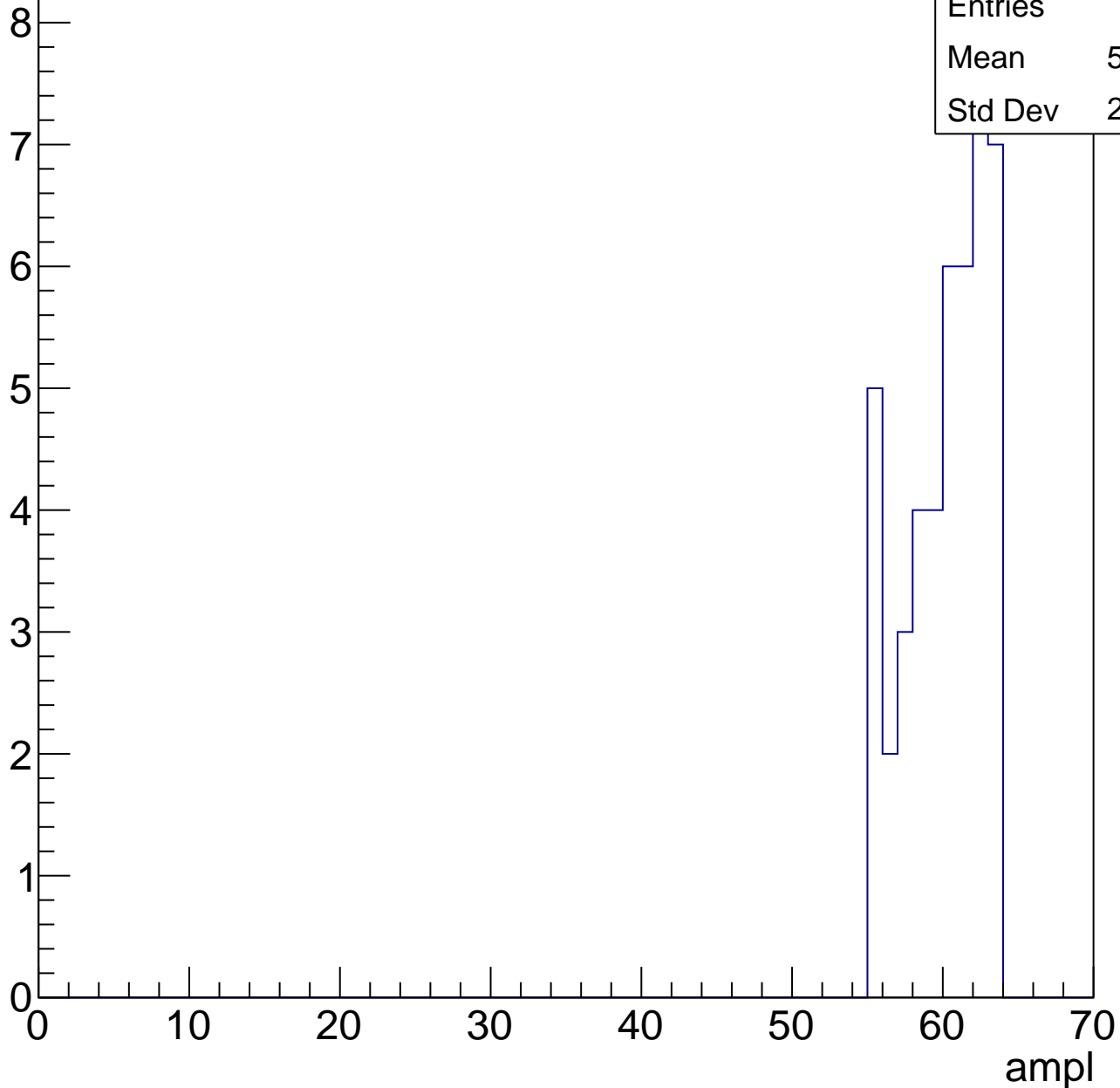


B0L002S, U18-ch94, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

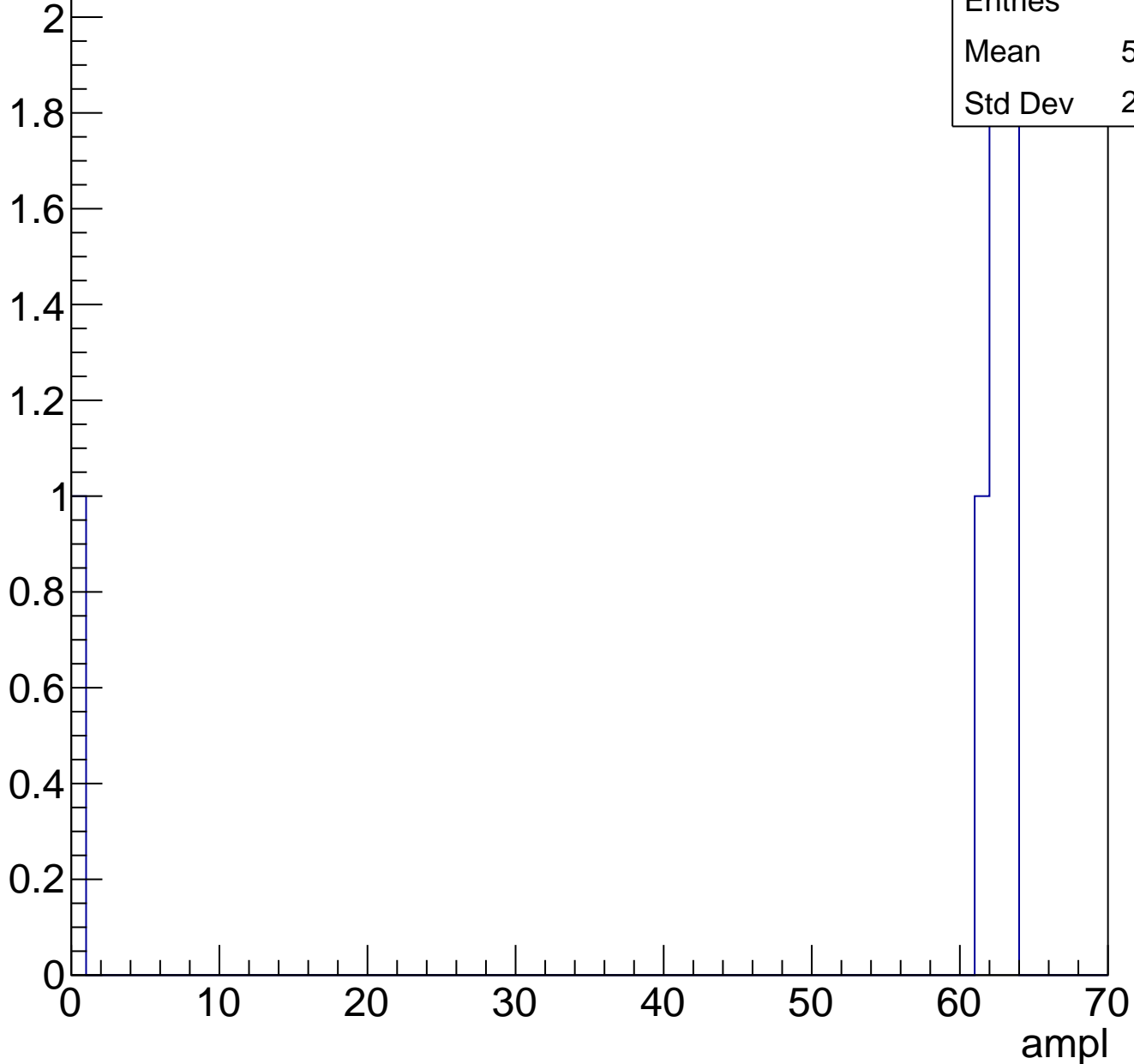
Entries	45
Mean	59.76
Std Dev	2.592



B0L002S, U18-ch94, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch94, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch95, adc0

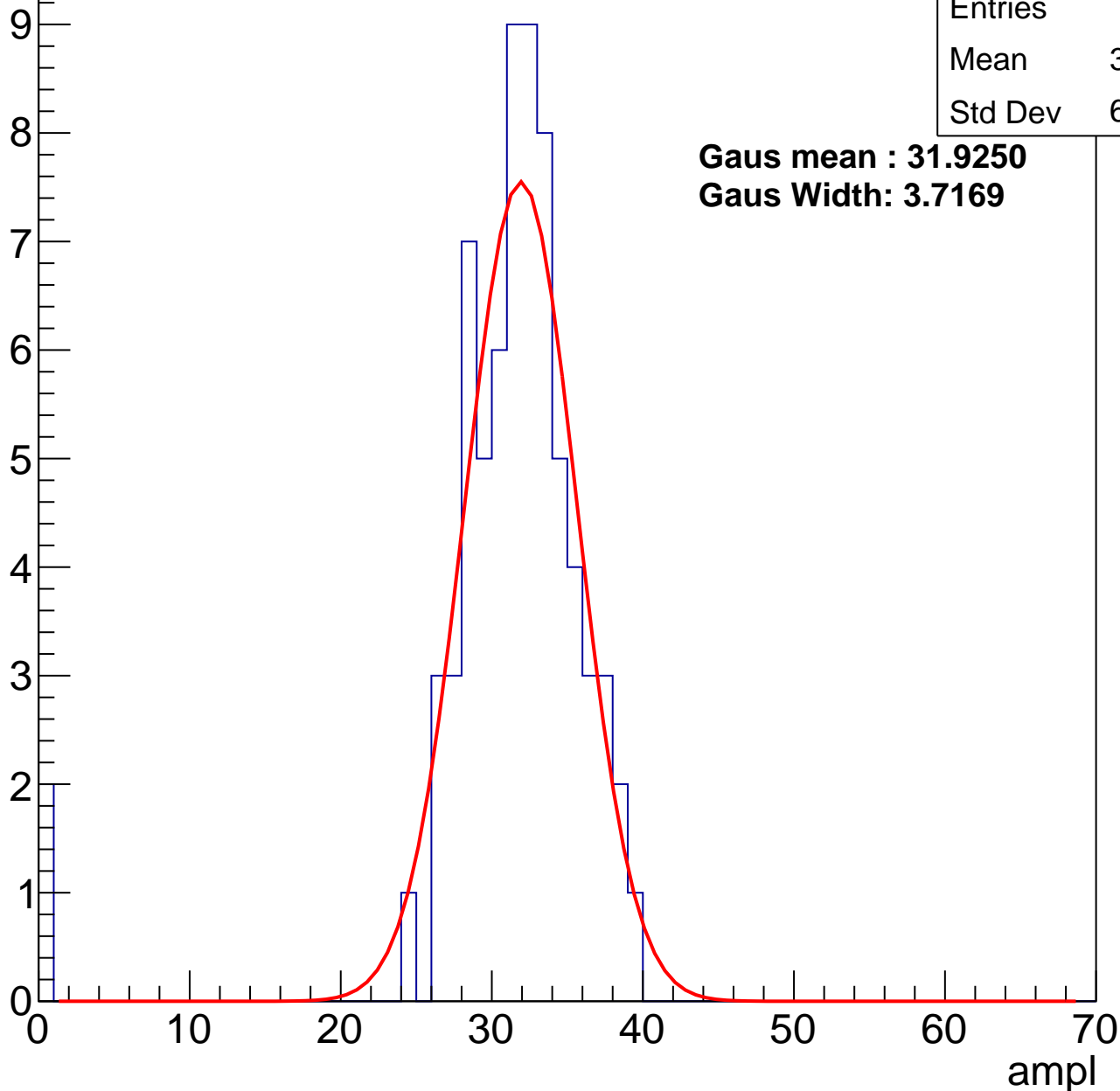
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	30.69
Std Dev	6.134

Gaus mean : 31.9250

Gaus Width: 3.7169



B0L002S, U18-ch95, adc1

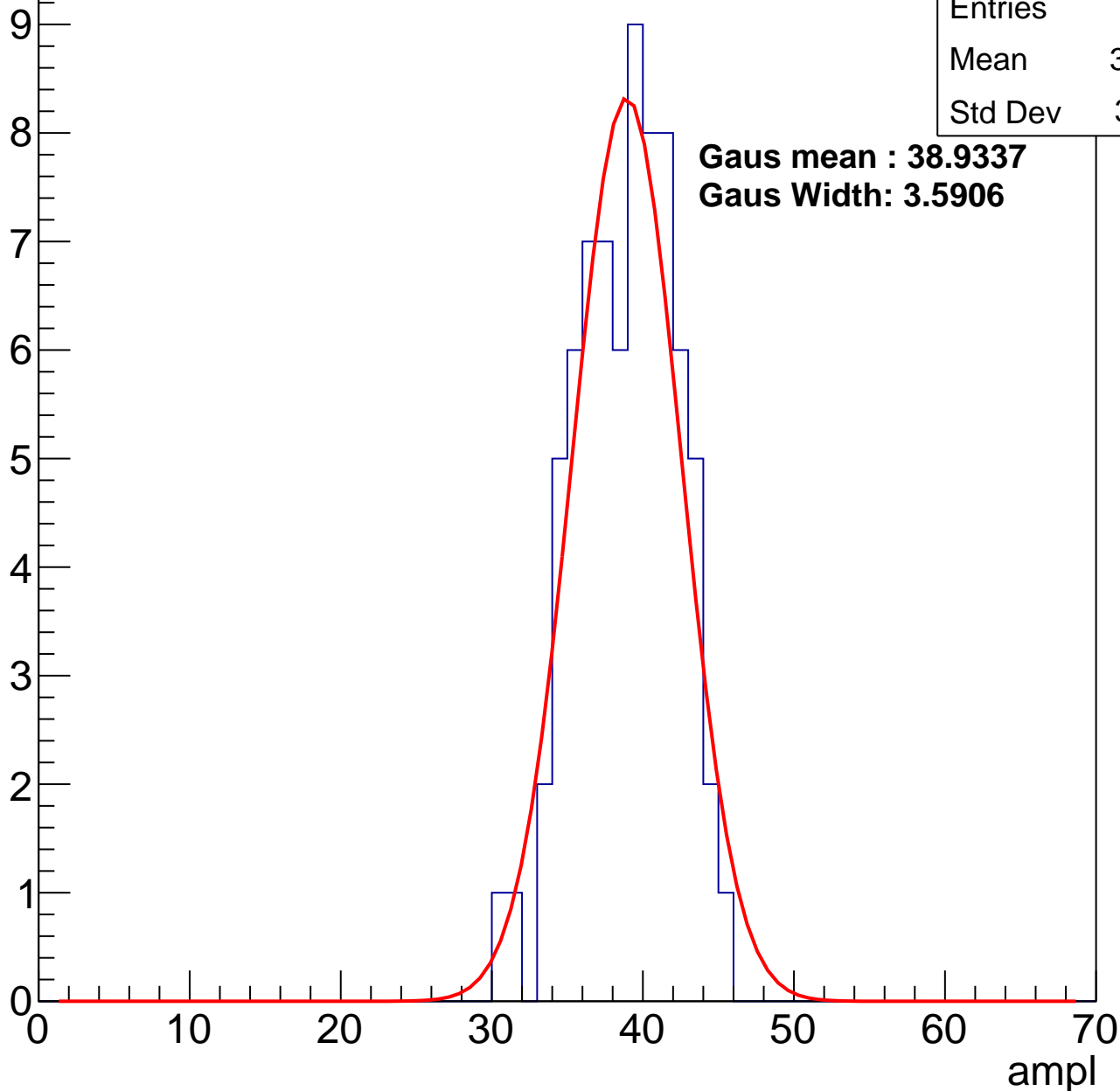
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	74
Mean	38.45
Std Dev	3.231

Gaus mean : 38.9337

Gaus Width: 3.5906



B0L002S, U18-ch95, adc2

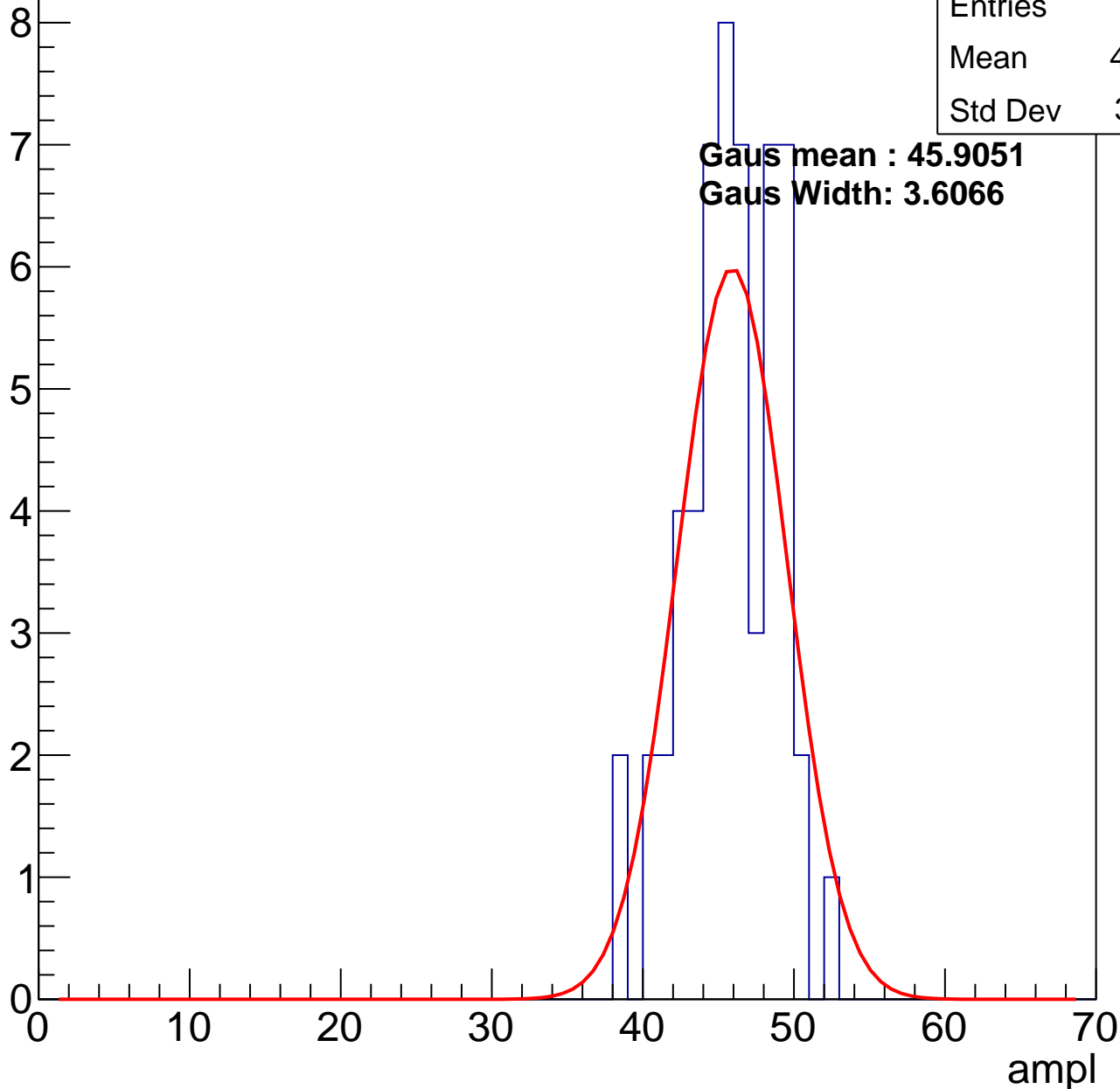
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	45.36
Std Dev	3.061

Gaus mean : 45.9051

Gaus Width: 3.6066



B0L002S, U18-ch95, adc3

calib_packv5_042523_0143.root, FC#8, port C1

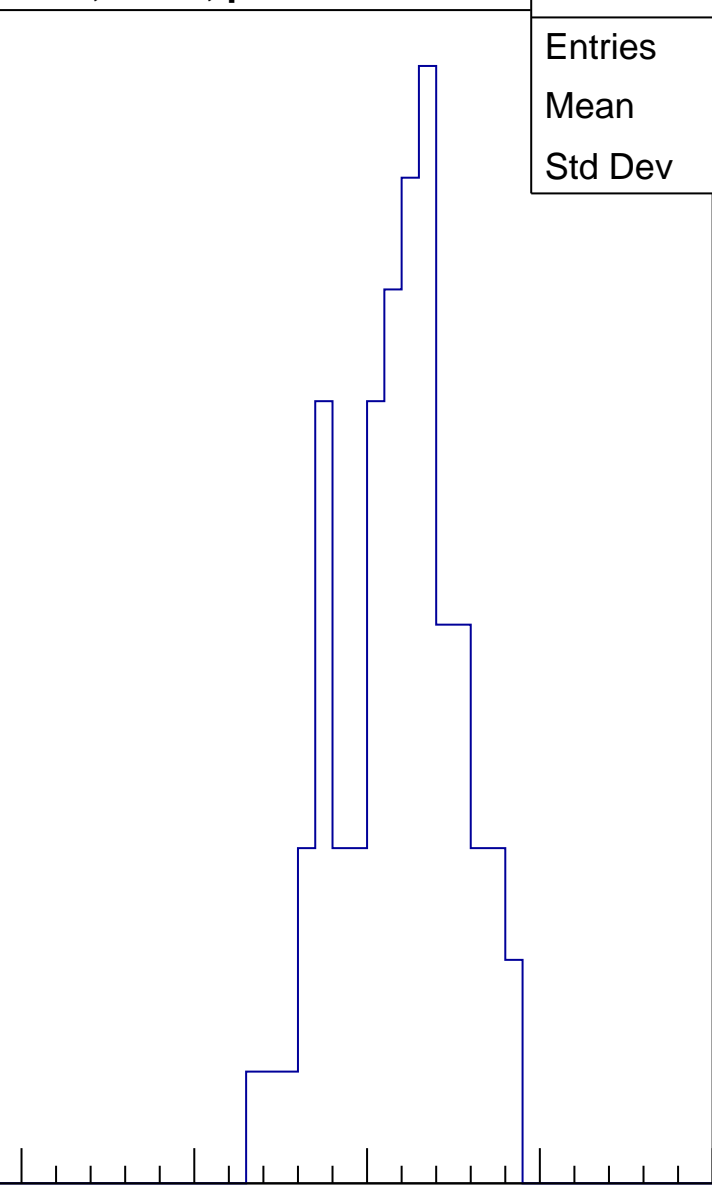
Entries	71
Mean	51.35
Std Dev	3.415

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

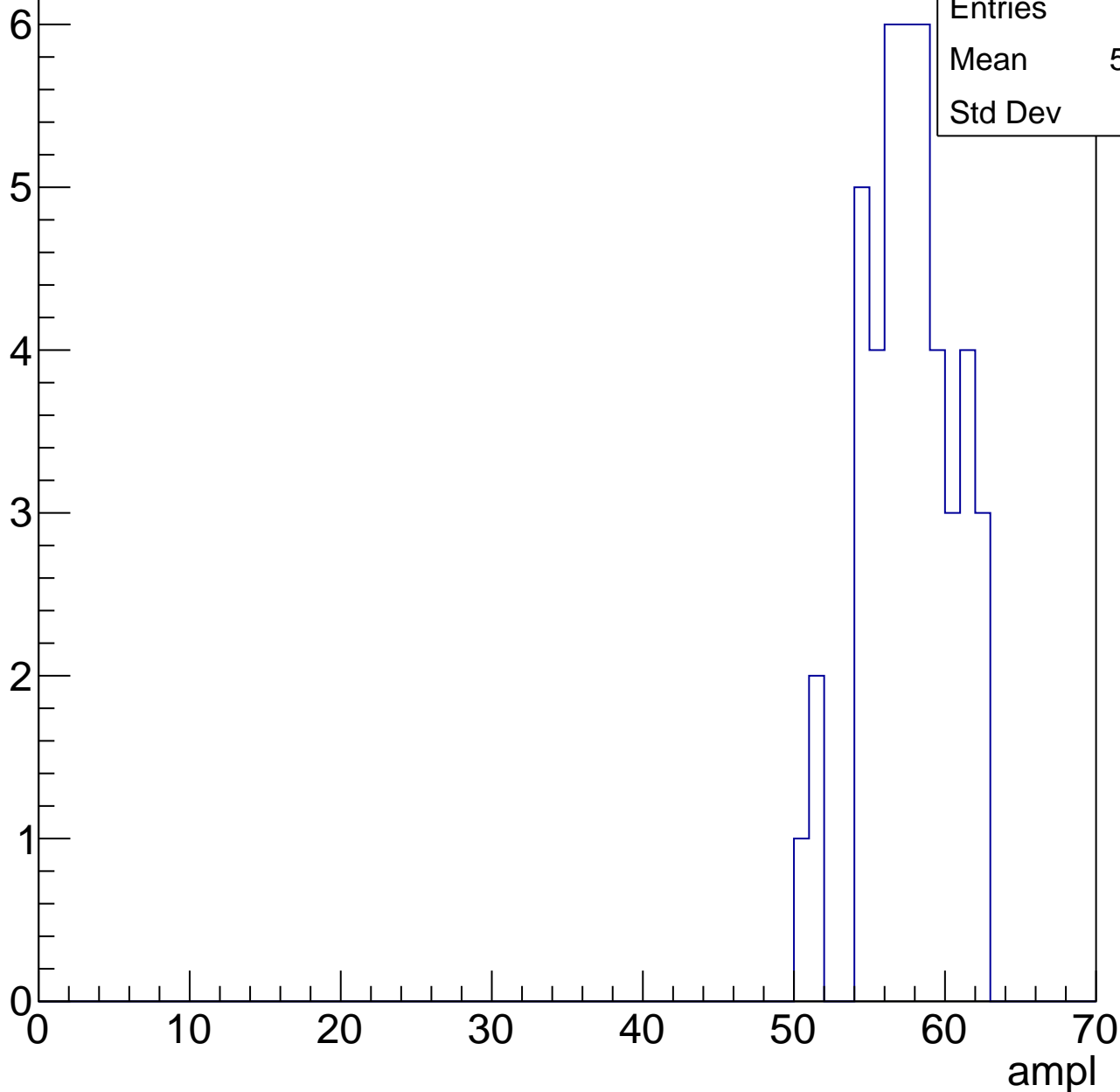


B0L002S, U18-ch95, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	57.14
Std Dev	2.92

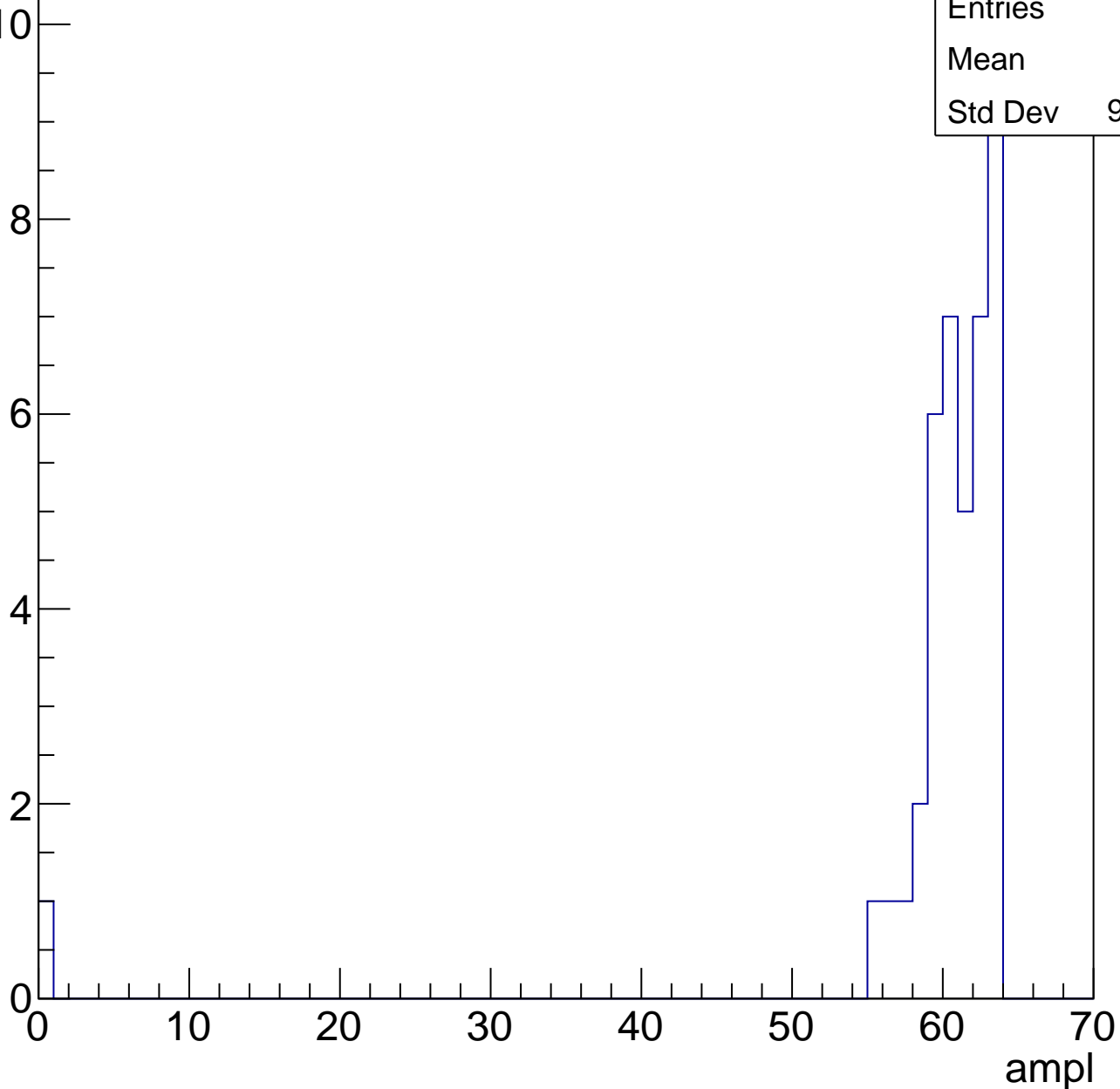


B0L002S, U18-ch95, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	59.2
Std Dev	9.577



B0L002S, U18-ch95, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch95, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	11.5
Std Dev	11.5

B0L002S, U18-ch96, adc0

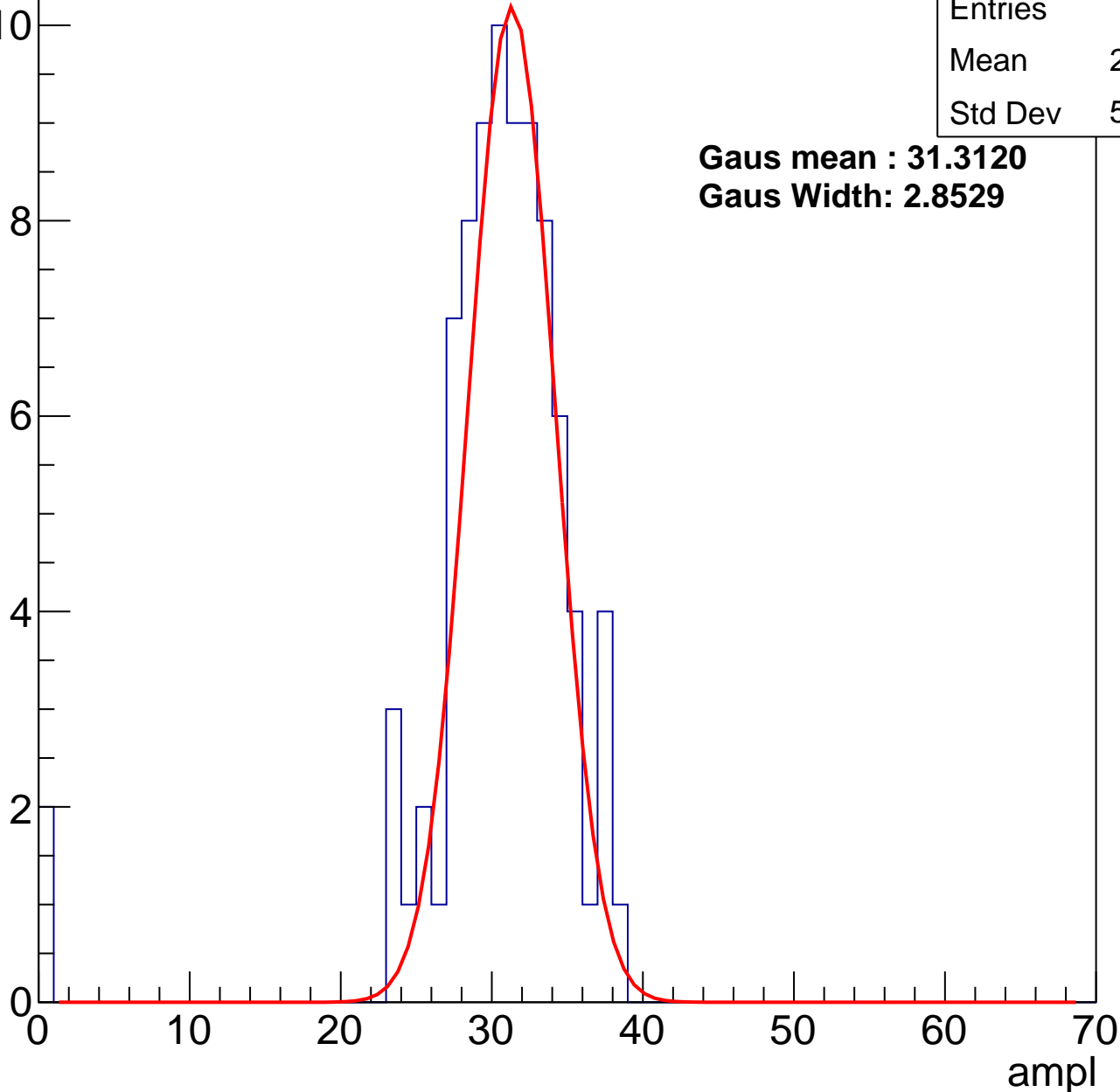
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	29.88
Std Dev	5.699

Gaus mean : 31.3120

Gaus Width: 2.8529



B0L002S, U18-ch96, adc1

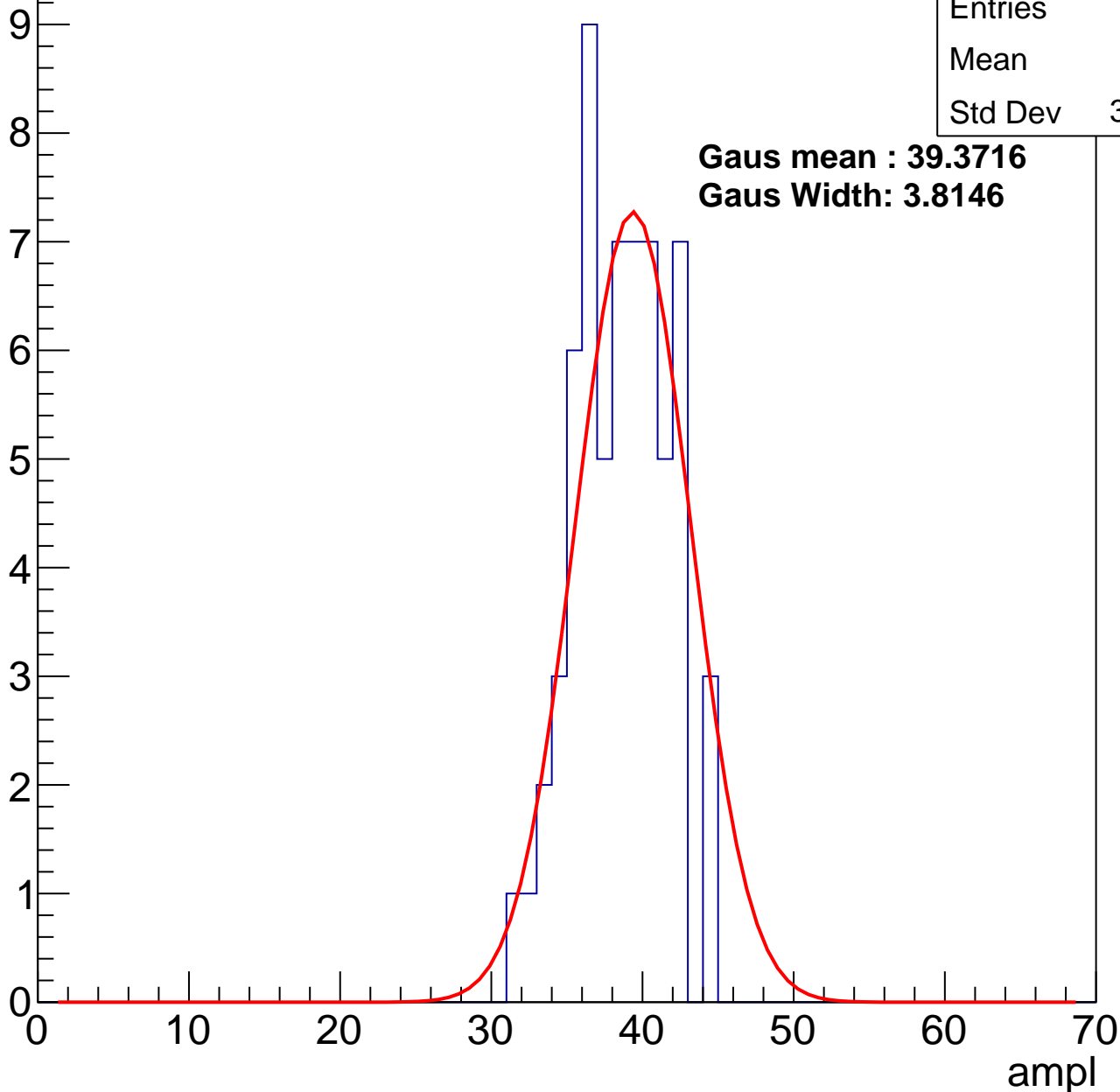
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	38.1
Std Dev	3.027

Gaus mean : 39.3716

Gaus Width: 3.8146



B0L002S, U18-ch96, adc2

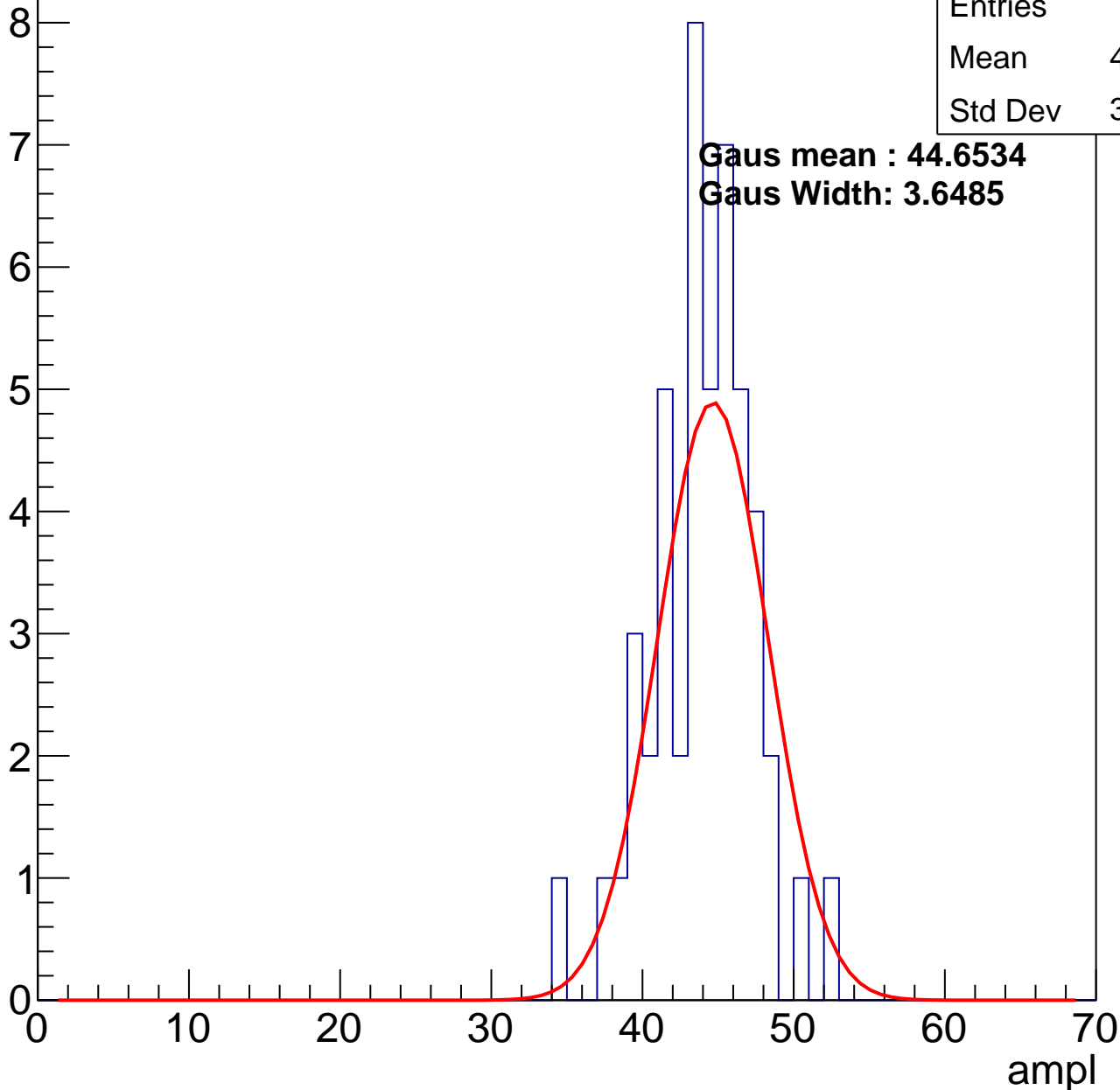
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	43.54
Std Dev	3.348

Gaus mean : 44.6534

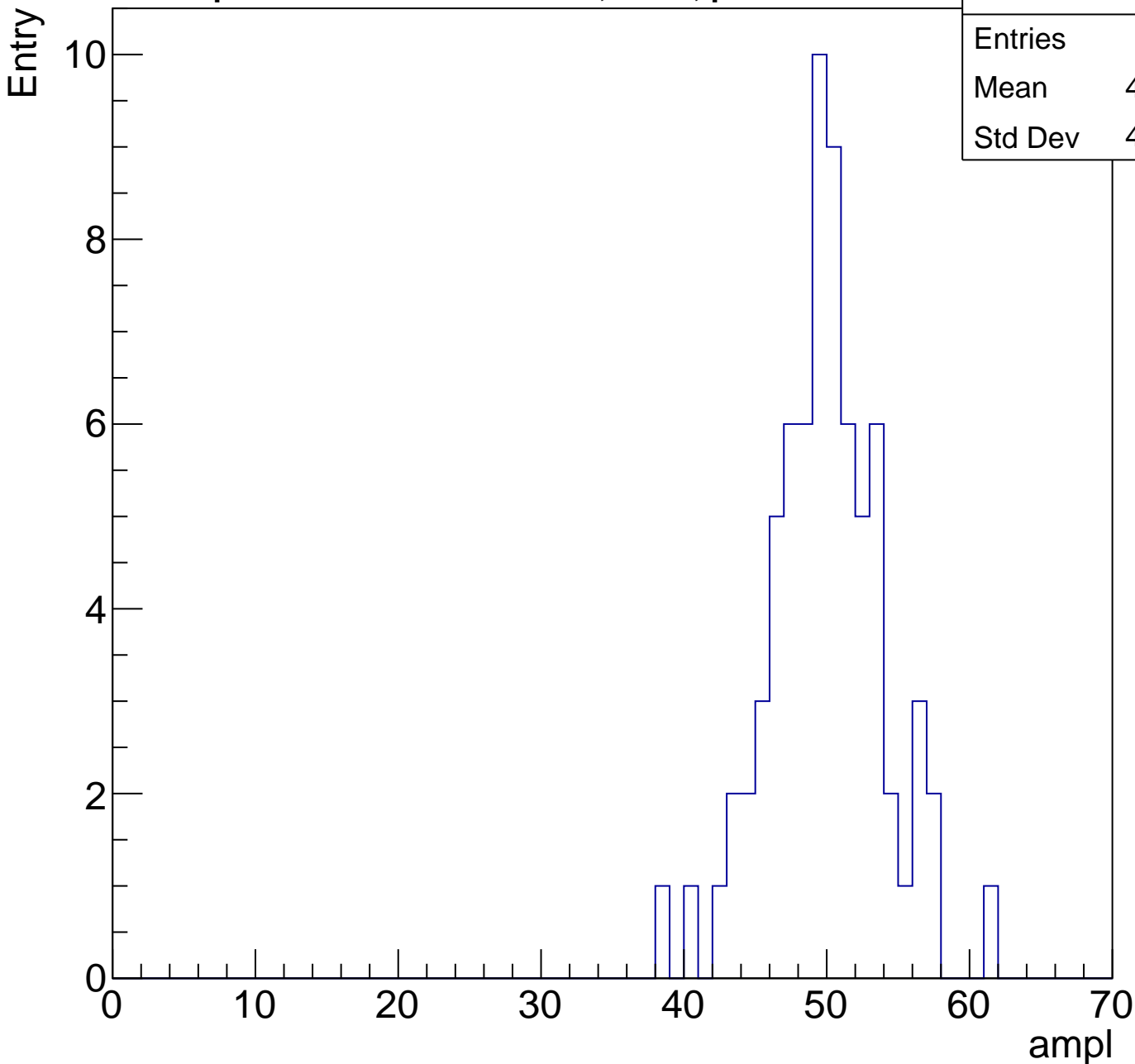
Gaus Width: 3.6485



B0L002S, U18-ch96, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	72
Mean	49.43
Std Dev	4.017

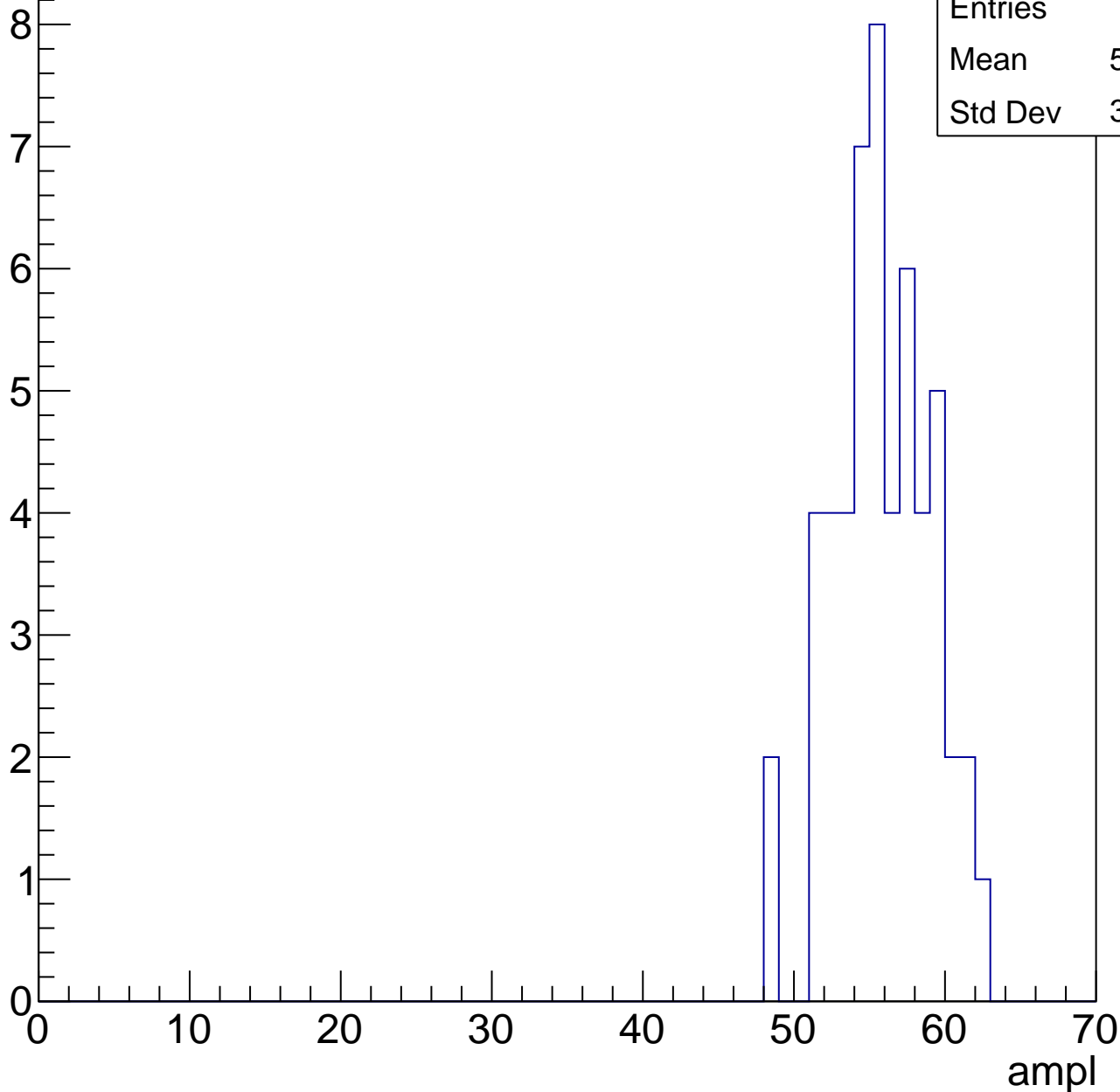


B0L002S, U18-ch96, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

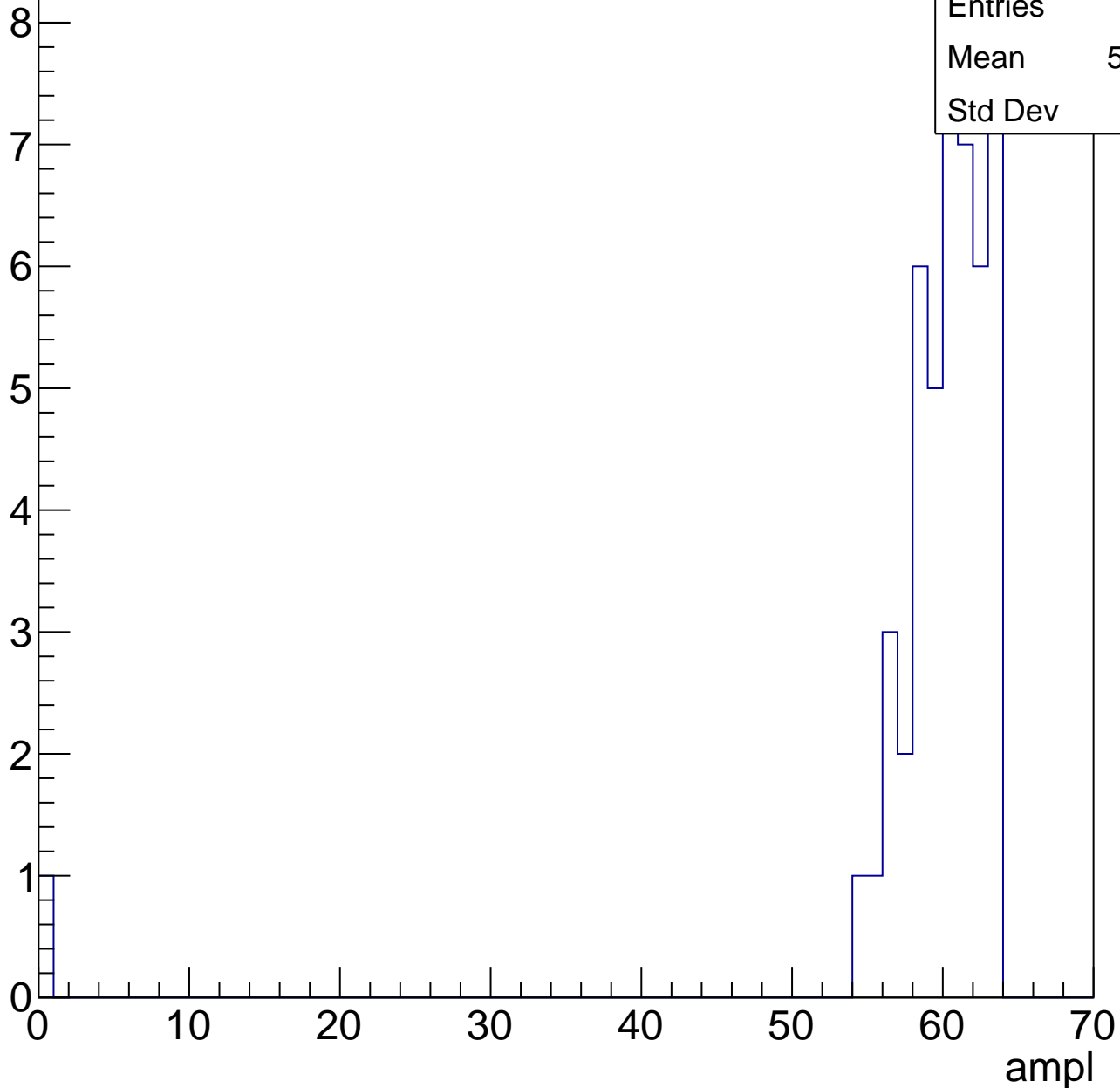
Entries	53
Mean	55.38
Std Dev	3.158



B0L002S, U18-ch96, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch96, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

3

2.5

2

1.5

1

0.5

0

0

10

20

30

40

50

60

70

ampl

Entries

5

Mean

62.4

Std Dev

0.4899

B0L002S, U18-ch96, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch97, adc0

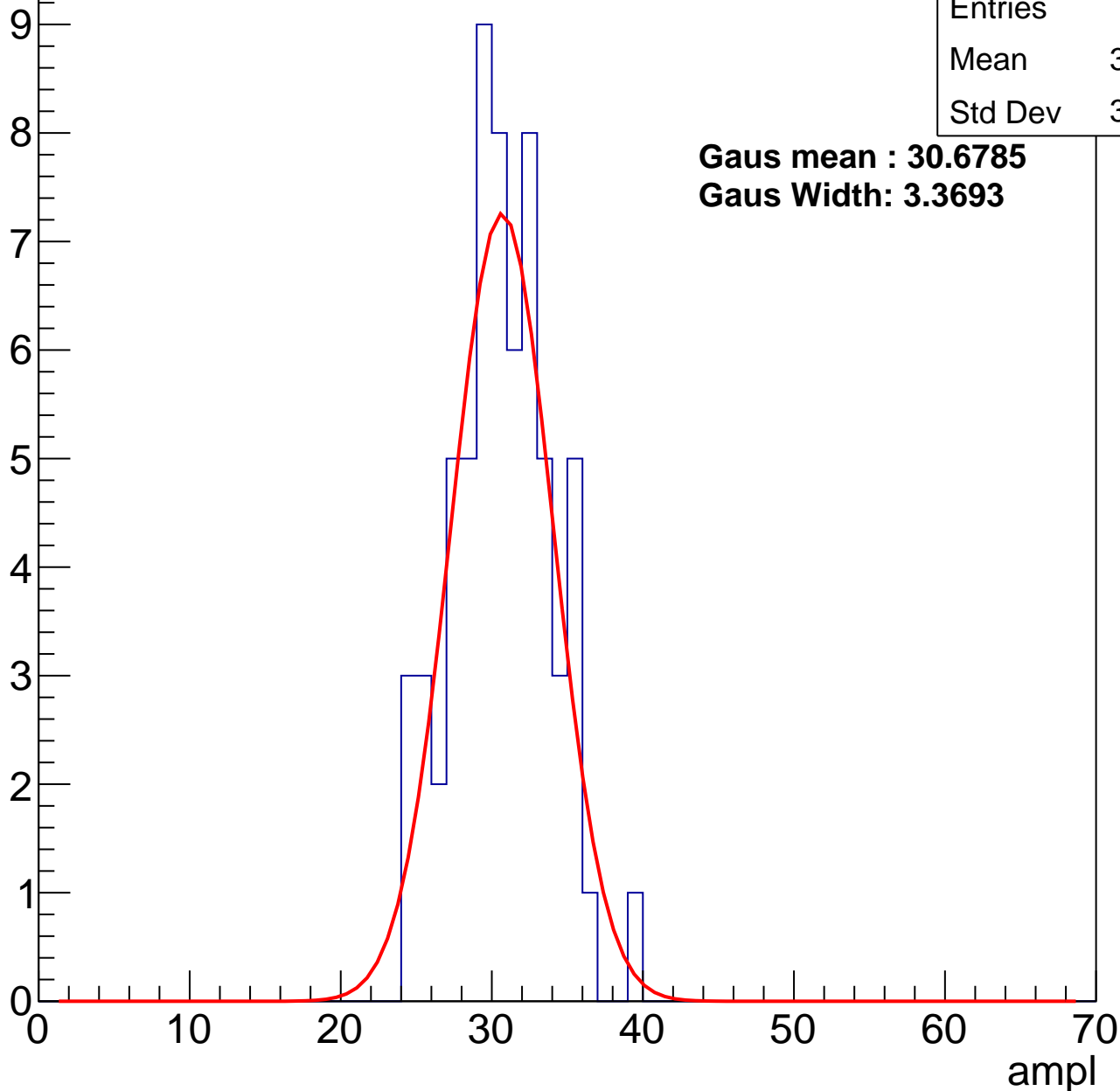
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	30.22
Std Dev	3.209

Gaus mean : 30.6785

Gaus Width: 3.3693



B0L002S, U18-ch97, adc1

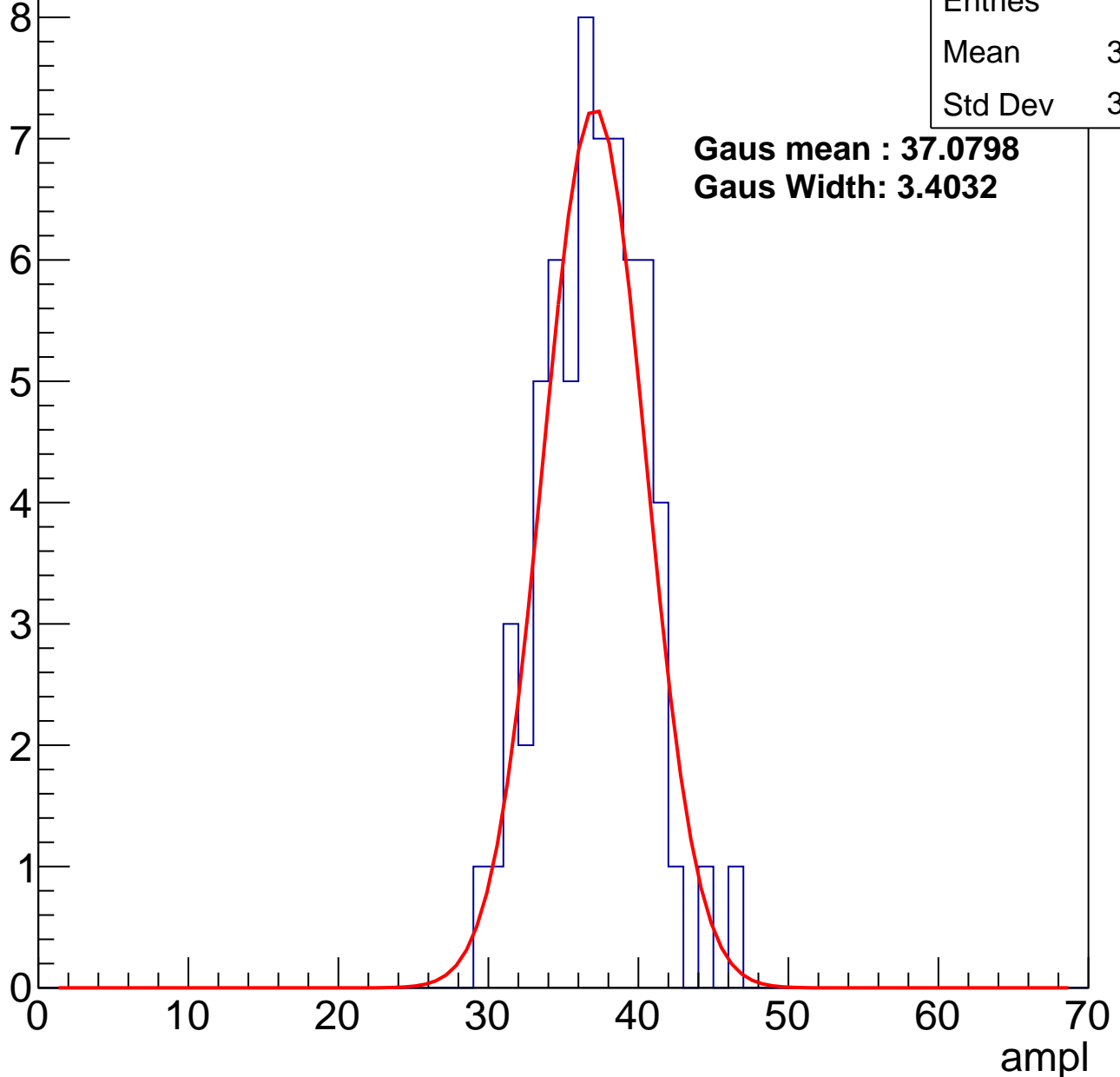
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	36.61
Std Dev	3.371

Gaus mean : 37.0798

Gaus Width: 3.4032



B0L002S, U18-ch97, adc2

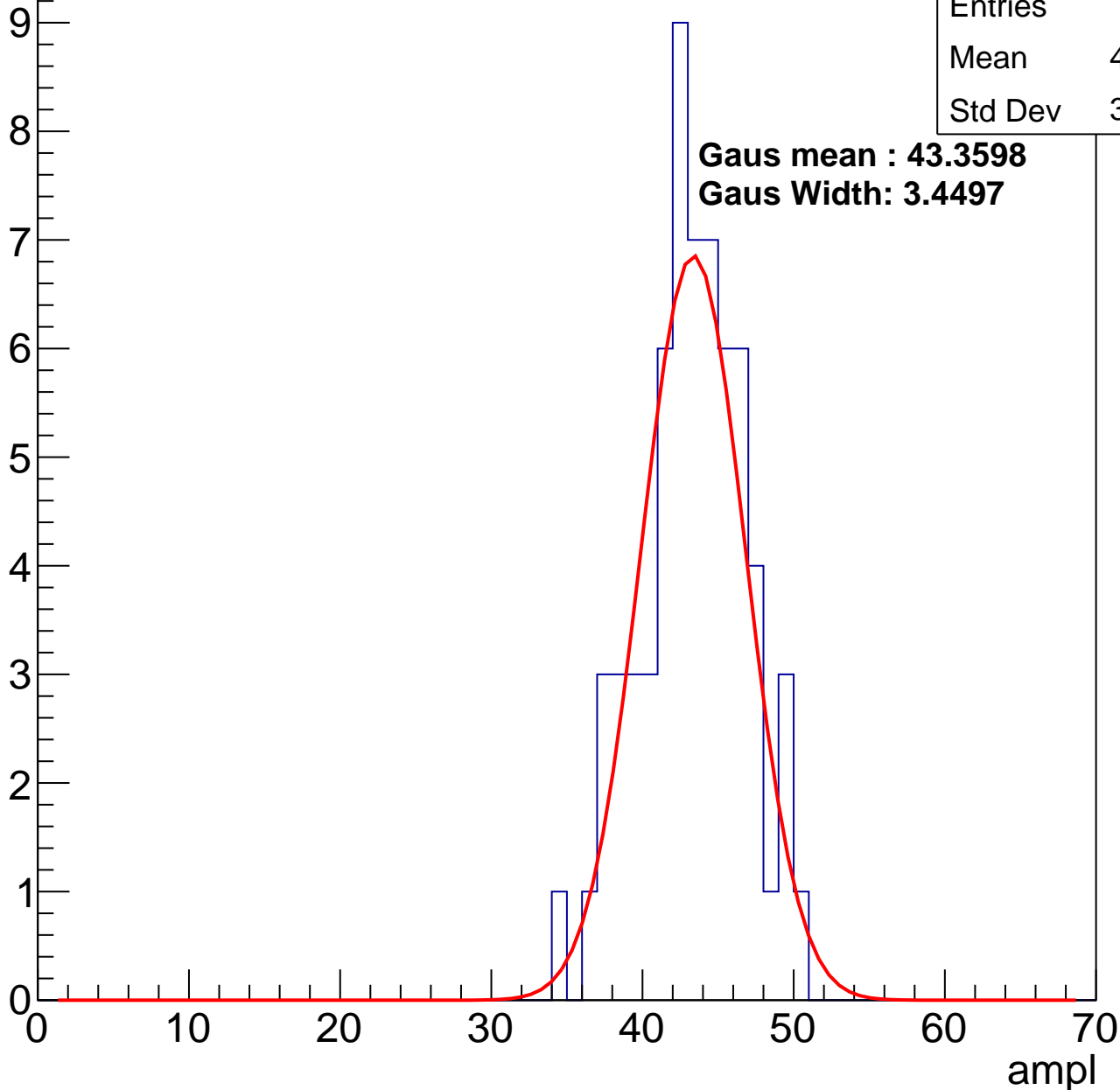
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	42.88
Std Dev	3.426

Gaus mean : 43.3598

Gaus Width: 3.4497

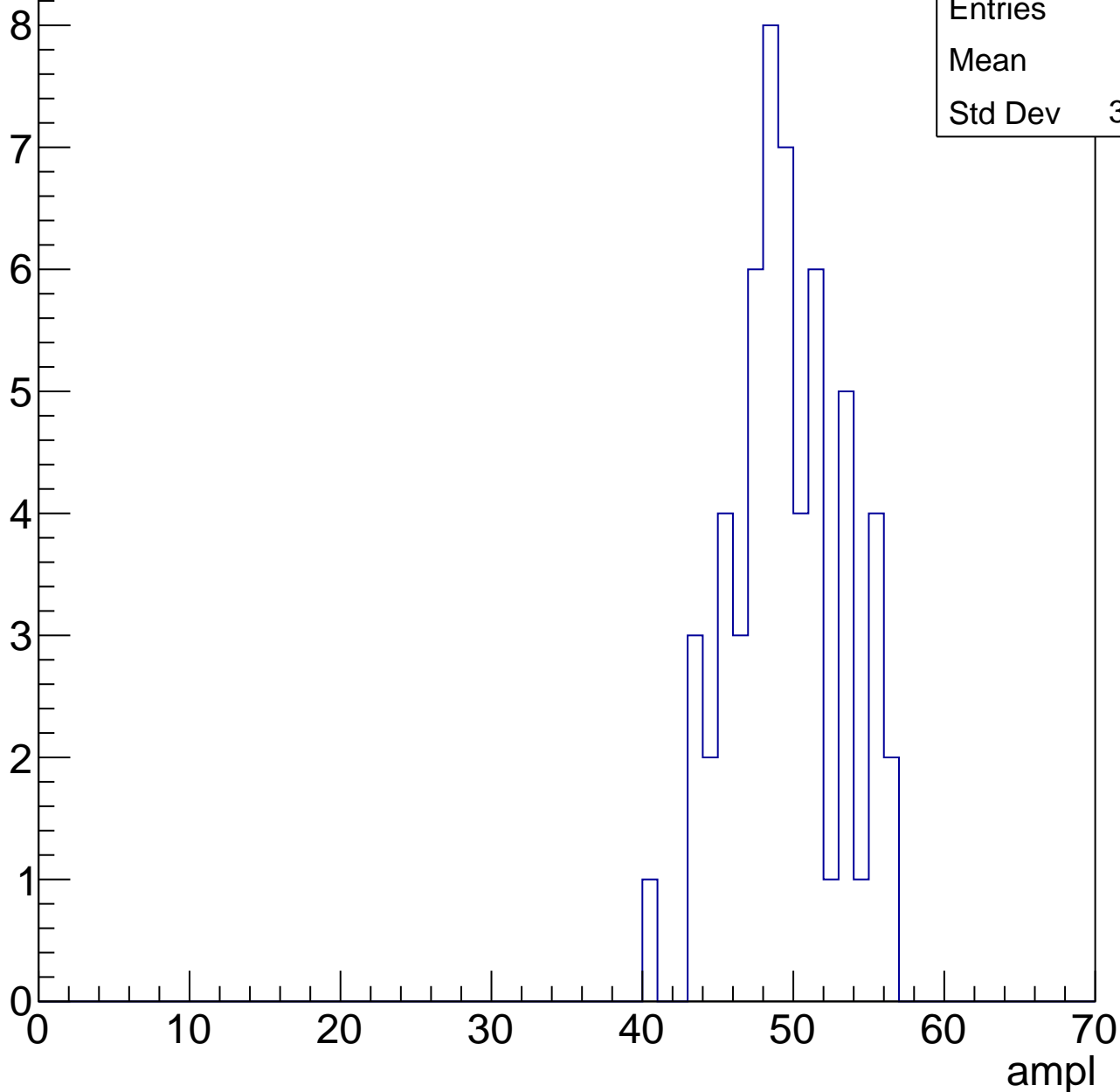


B0L002S, U18-ch97, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	49
Std Dev	3.618

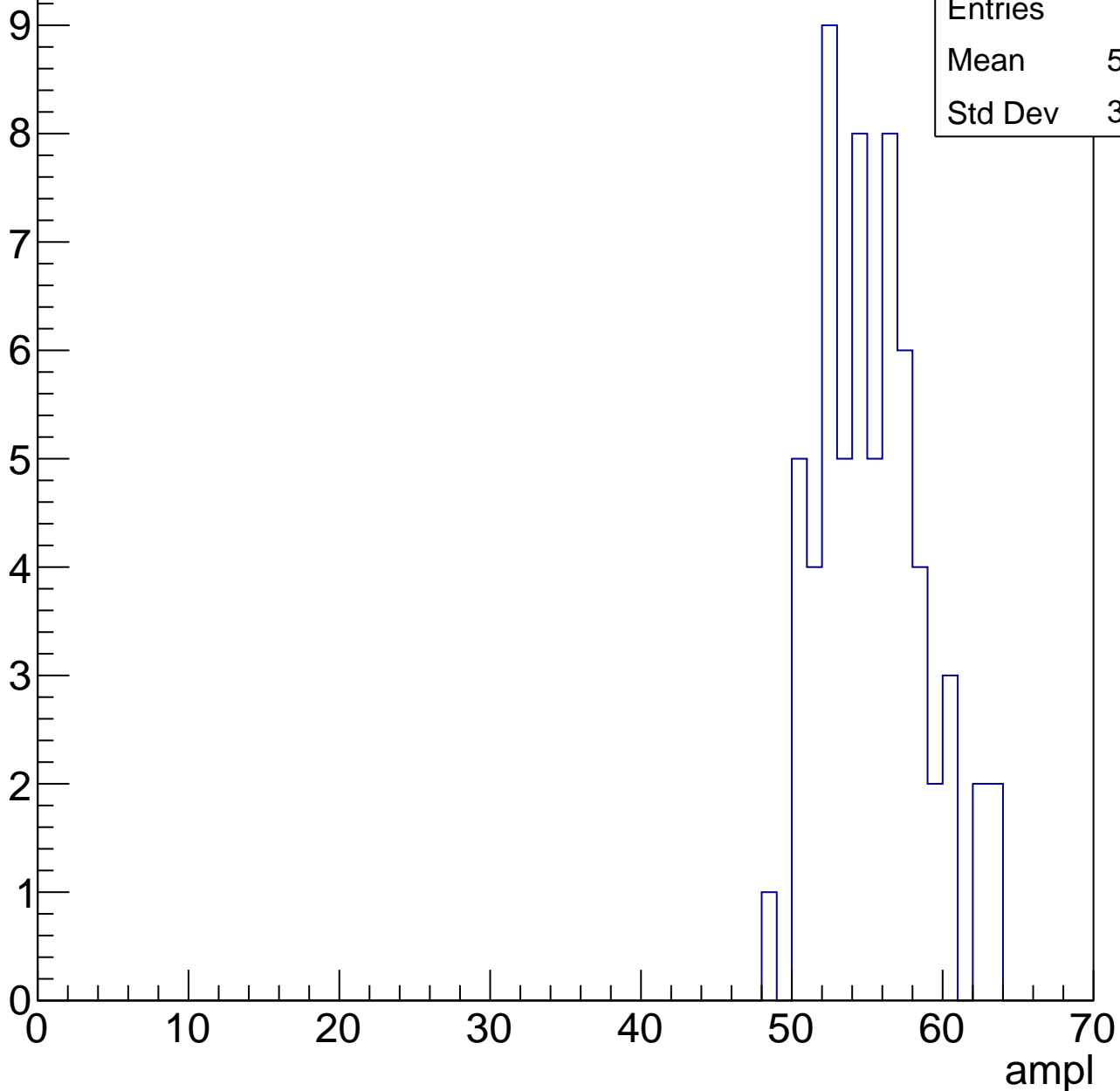


B0L002S, U18-ch97, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

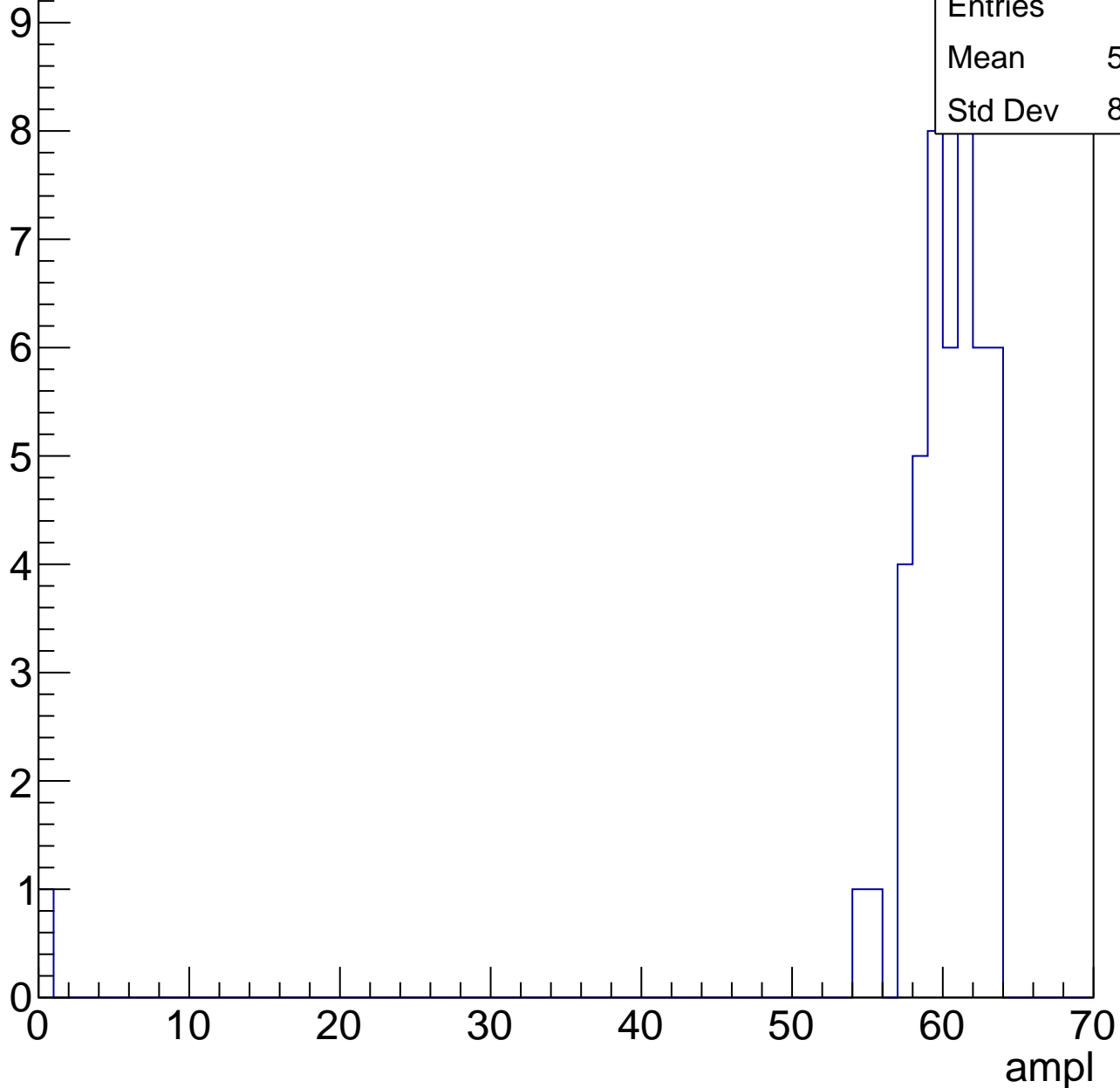
Entries	64
Mean	54.88
Std Dev	3.416



B0L002S, U18-ch97, adc5

calib_packv5_042523_0143.root, FC#8, port C1

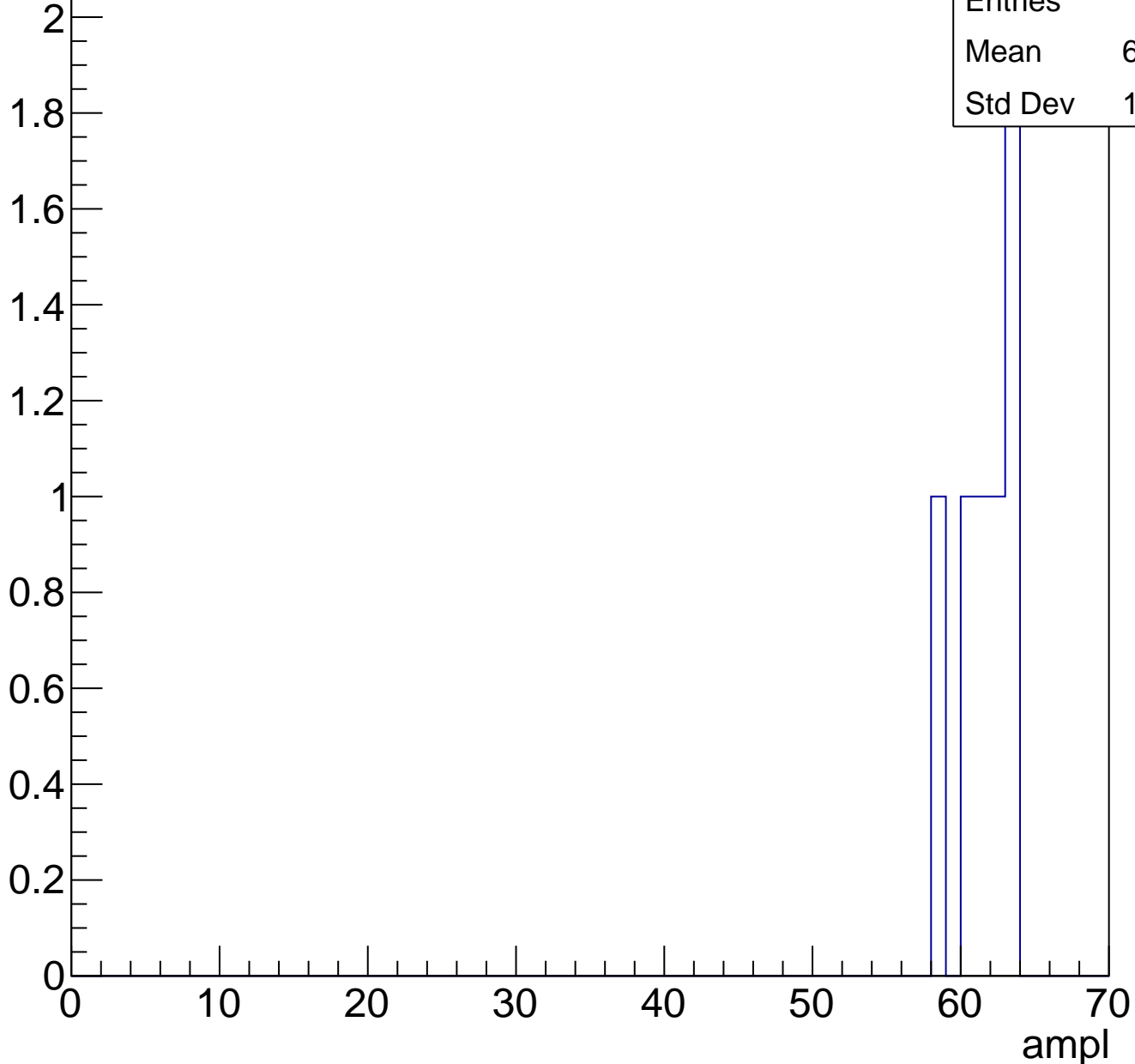
Entry



B0L002S, U18-ch97, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	6
Mean	61.17
Std Dev	1.772

B0L002S, U18-ch97, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch98, adc0

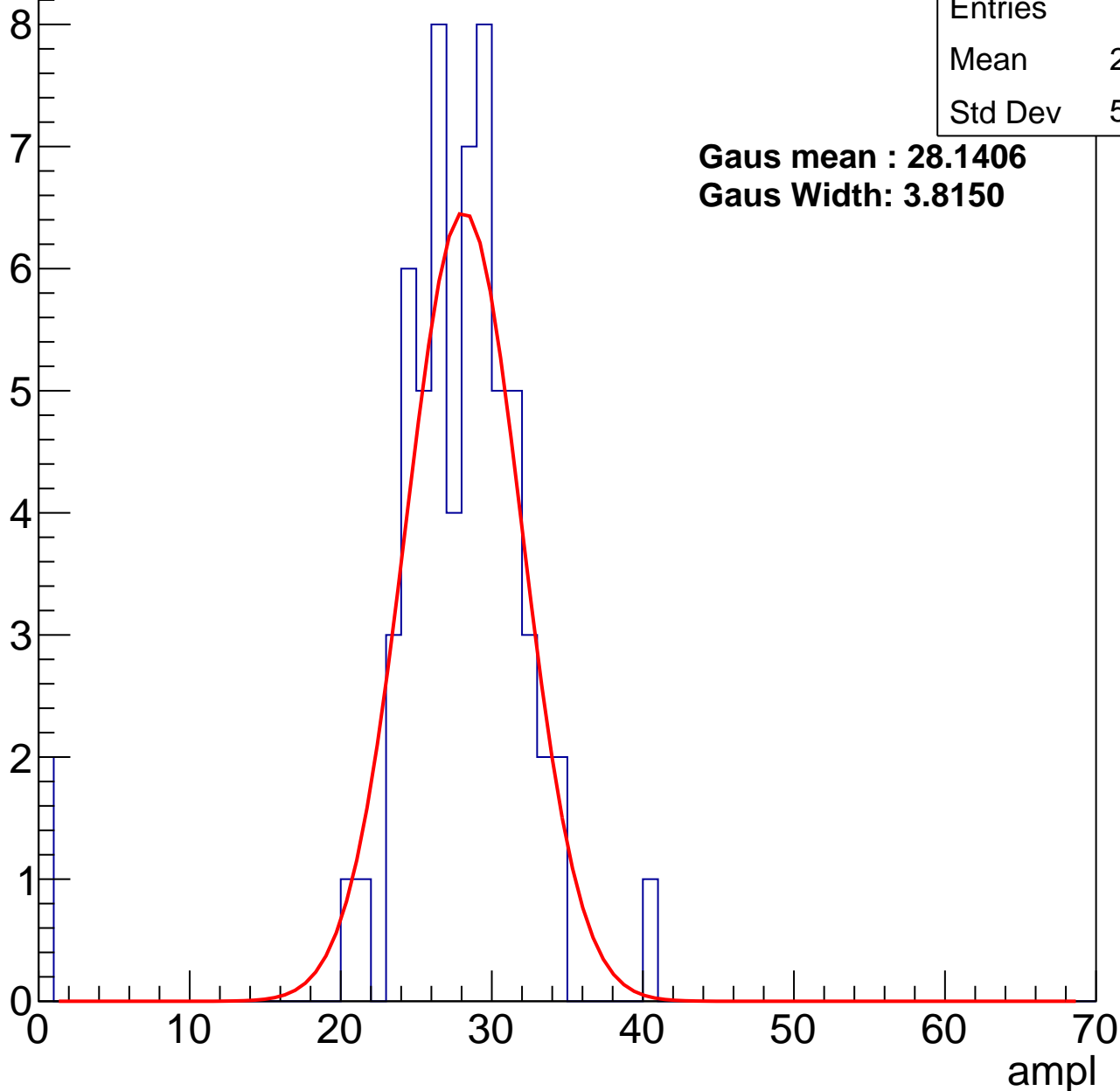
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	26.95
Std Dev	5.977

Gaus mean : 28.1406

Gaus Width: 3.8150



B0L002S, U18-ch98, adc1

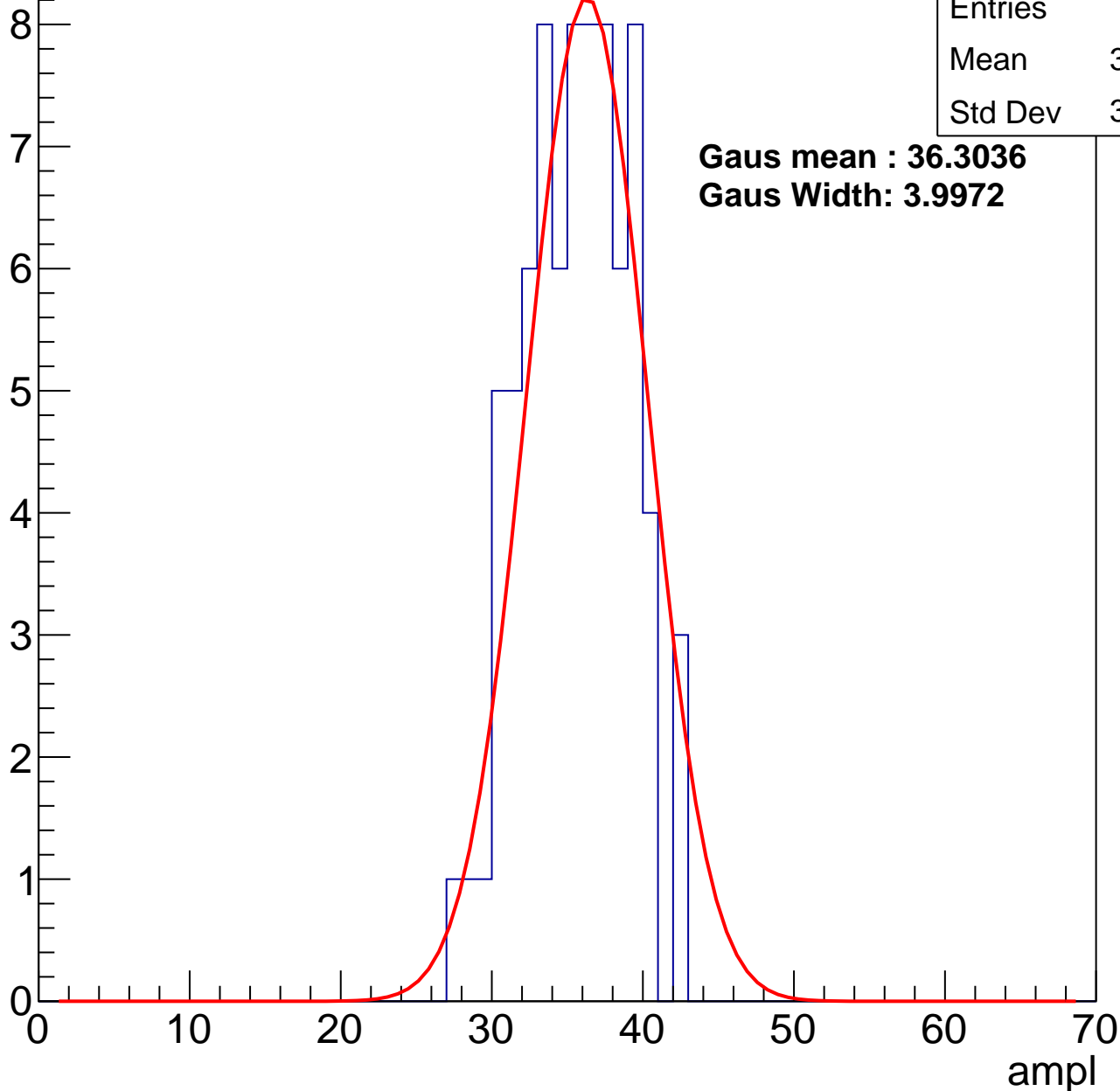
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	35.12
Std Dev	3.423

Gaus mean : 36.3036

Gaus Width: 3.9972



B0L002S, U18-ch98, adc2

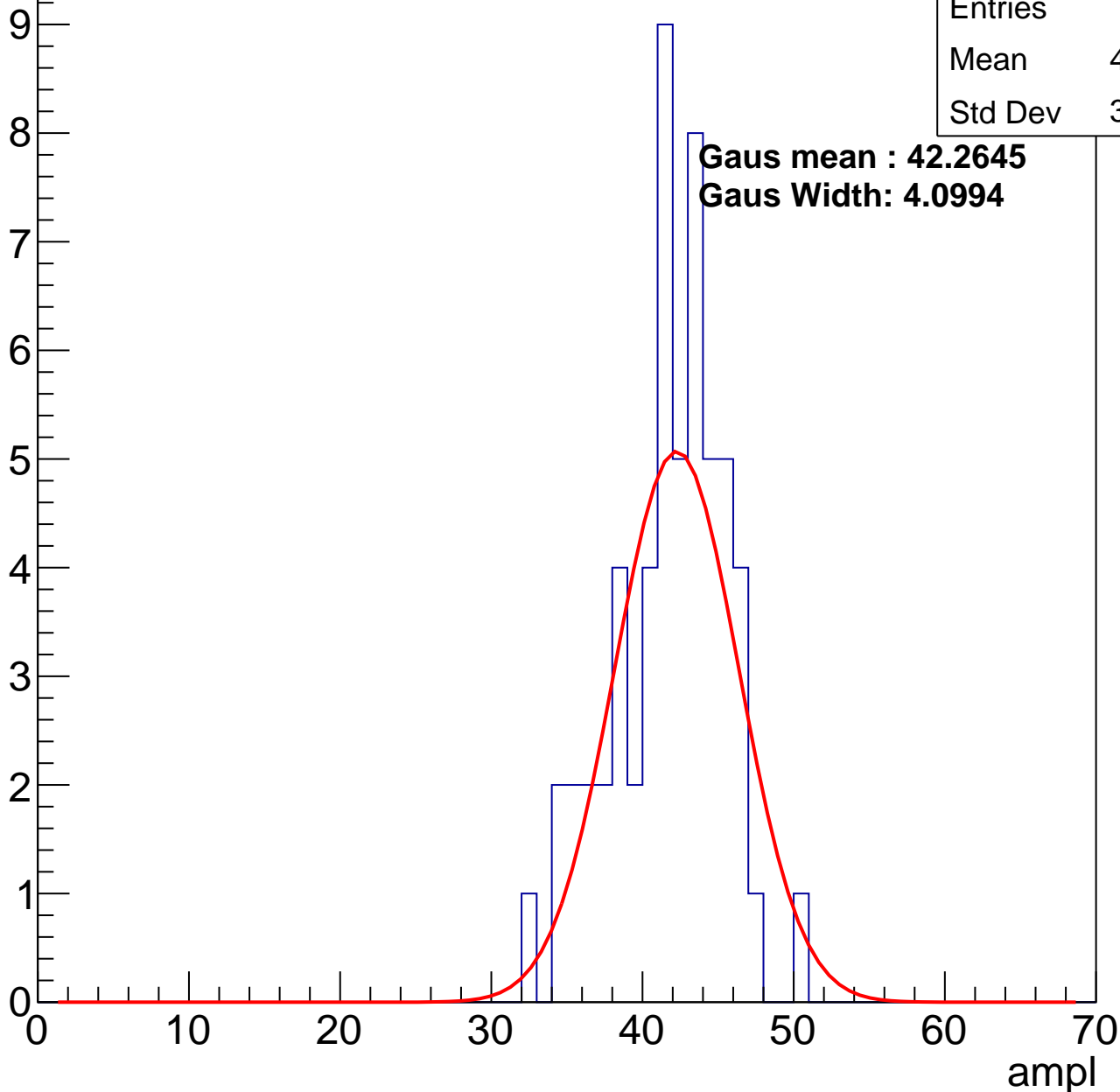
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	41.32
Std Dev	3.638

Gaus mean : 42.2645

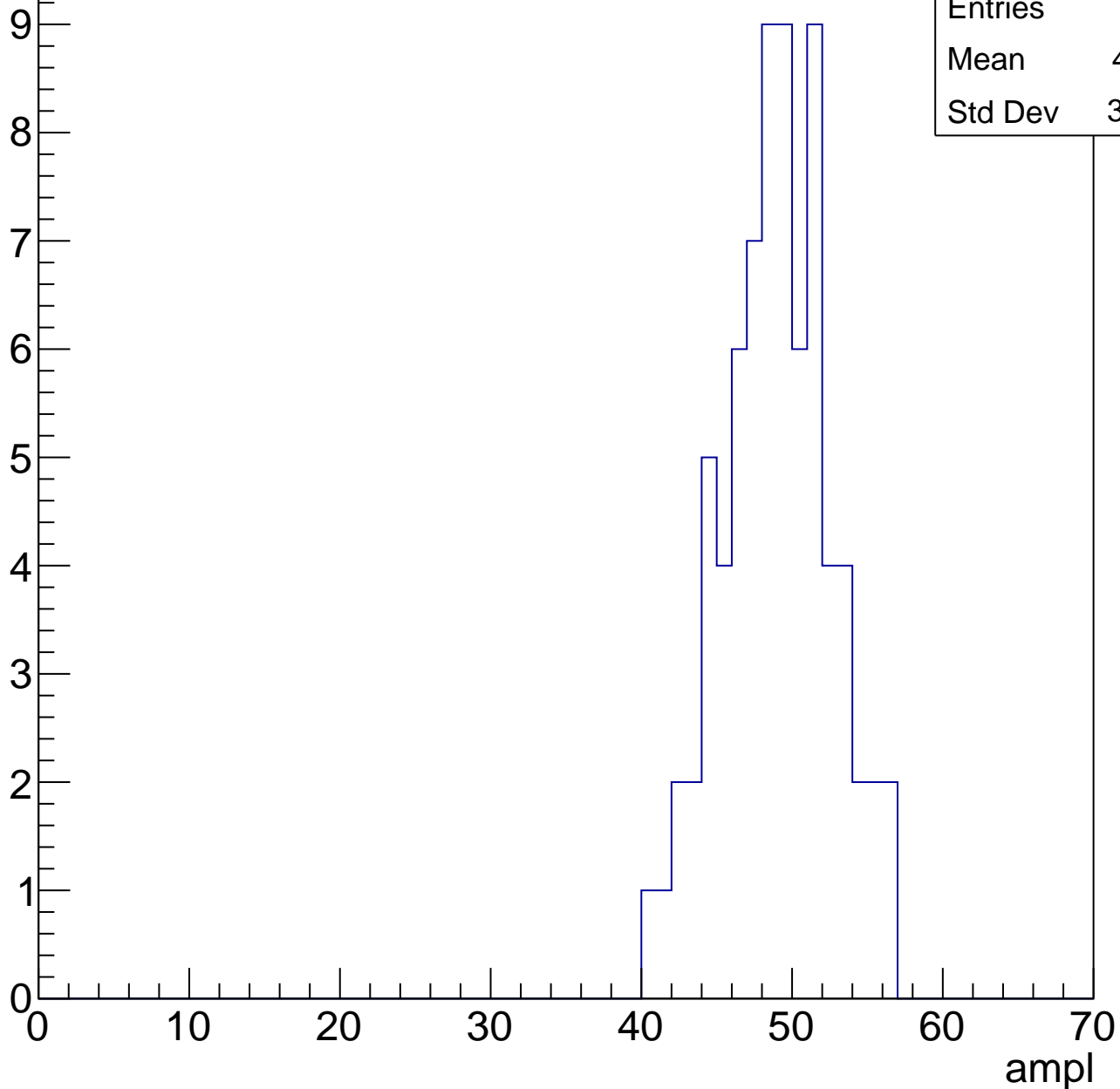
Gaus Width: 4.0994



B0L002S, U18-ch98, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry



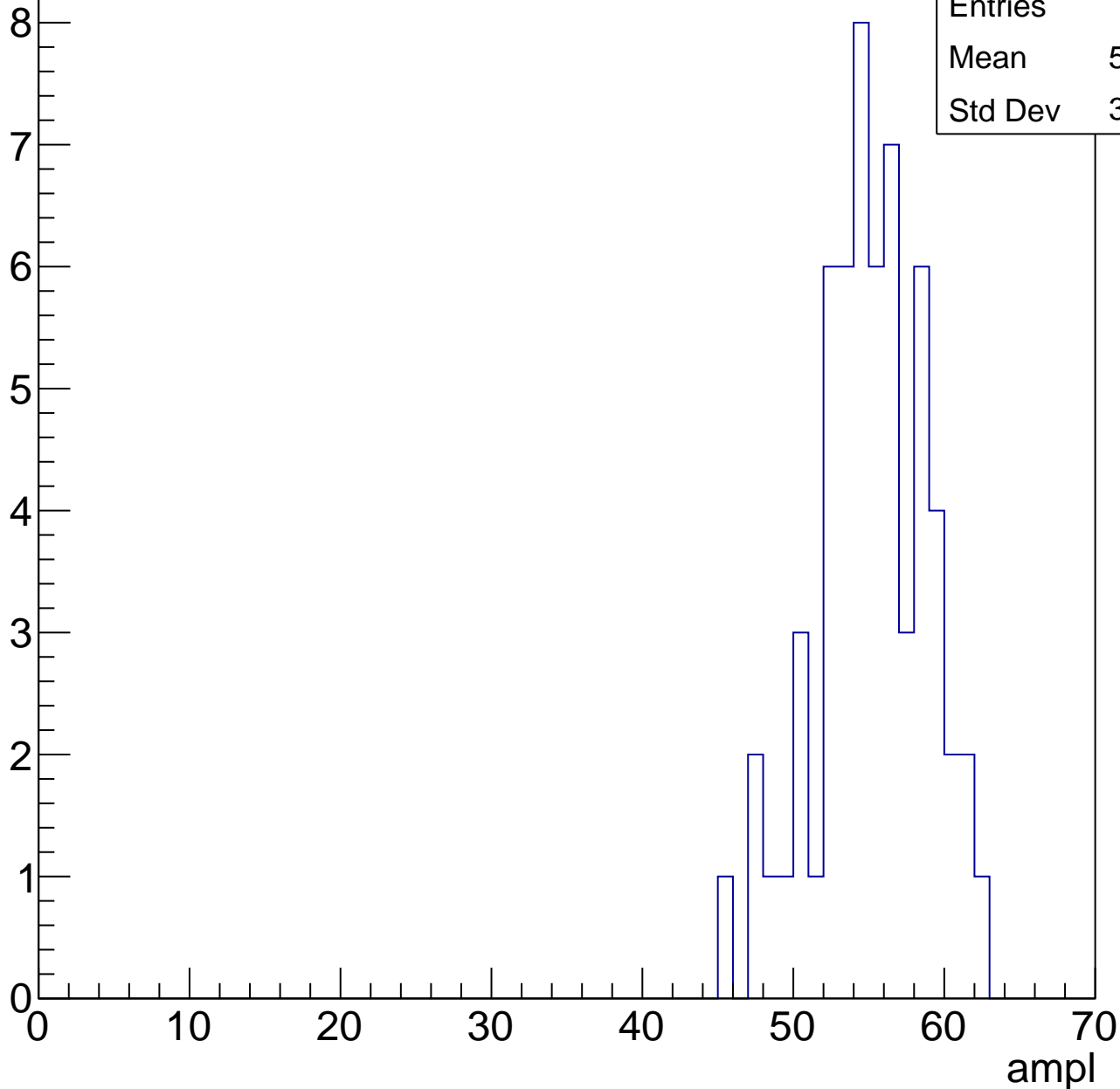
Entries	75
Mean	48.51
Std Dev	3.538

B0L002S, U18-ch98, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	54.67
Std Dev	3.632

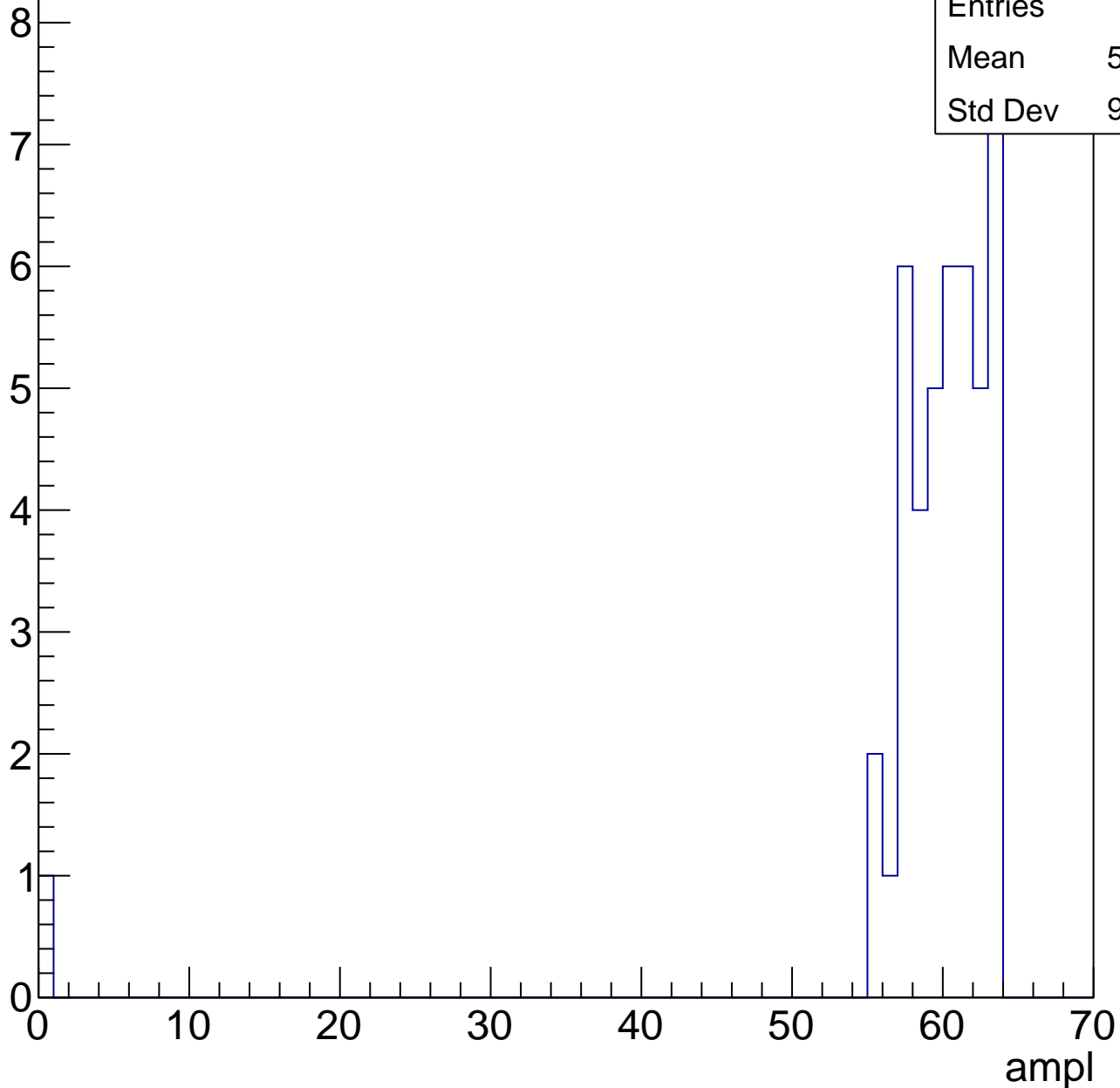


B0L002S, U18-ch98, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

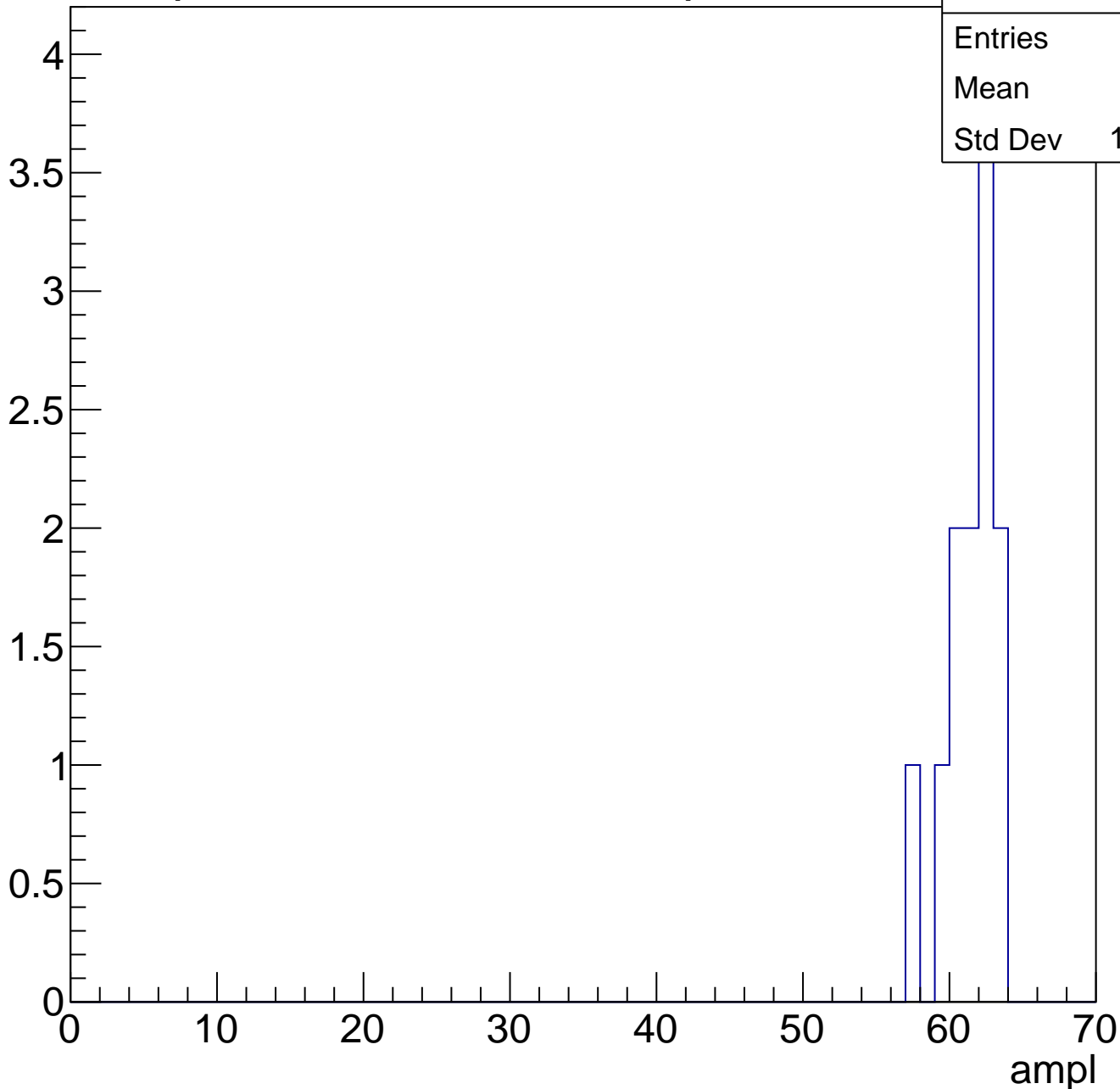
Entries	44
Mean	58.52
Std Dev	9.223



B0L002S, U18-ch98, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch98, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch99, adc0

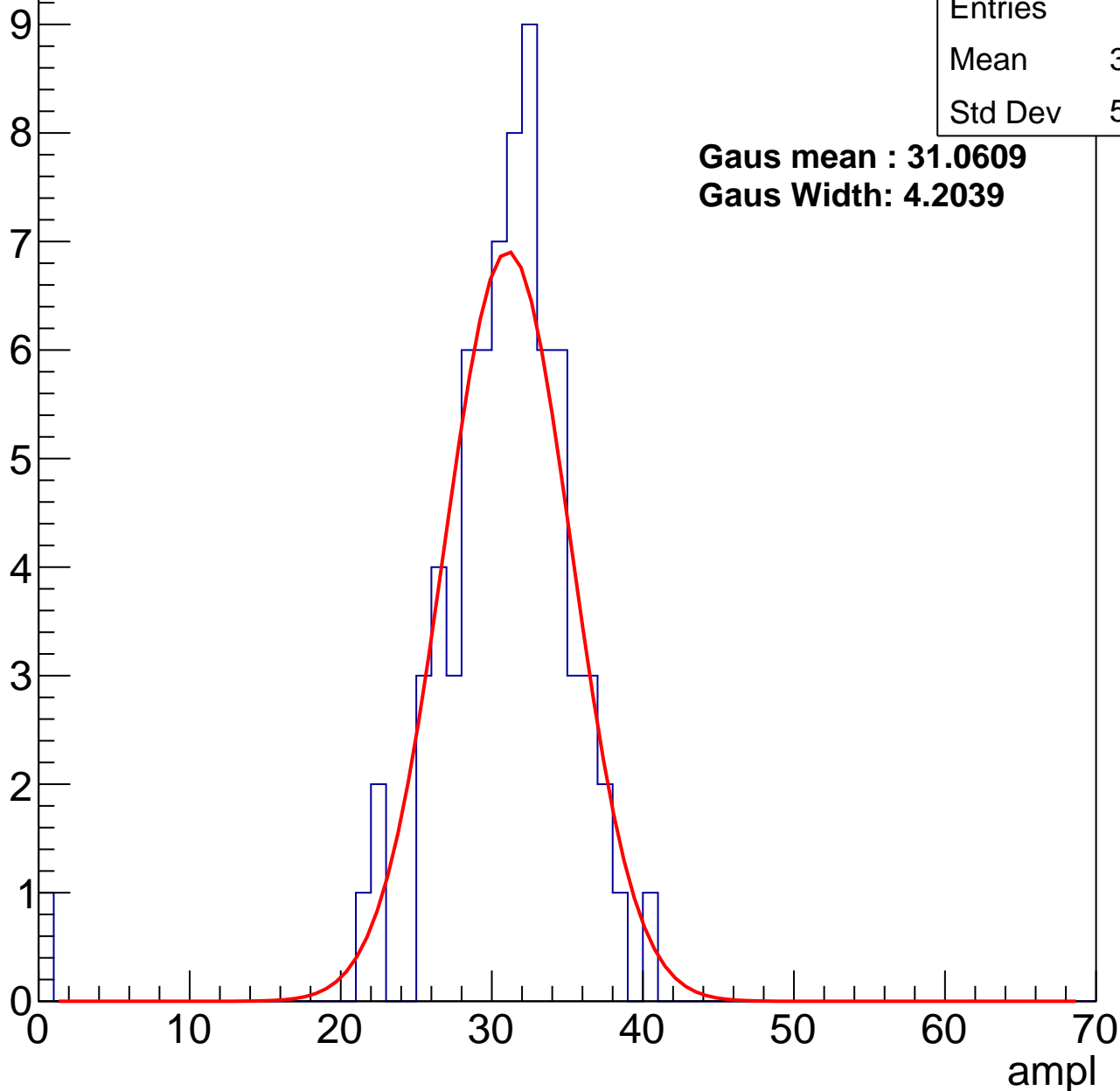
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	30.28
Std Dev	5.189

Gaus mean : 31.0609

Gaus Width: 4.2039



B0L002S, U18-ch99, adc1

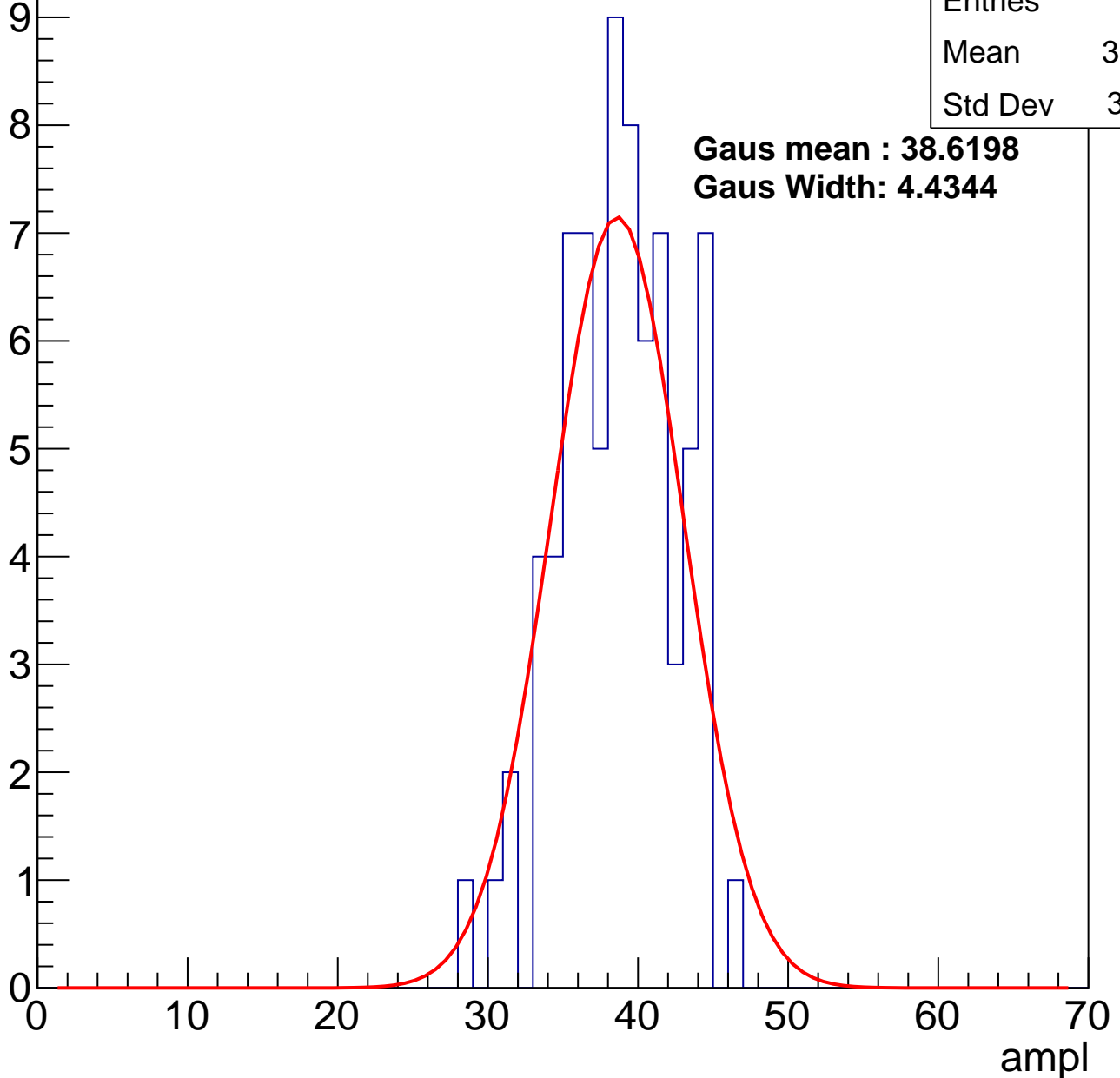
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	38.26
Std Dev	3.781

Gaus mean : 38.6198

Gaus Width: 4.4344



B0L002S, U18-ch99, adc2

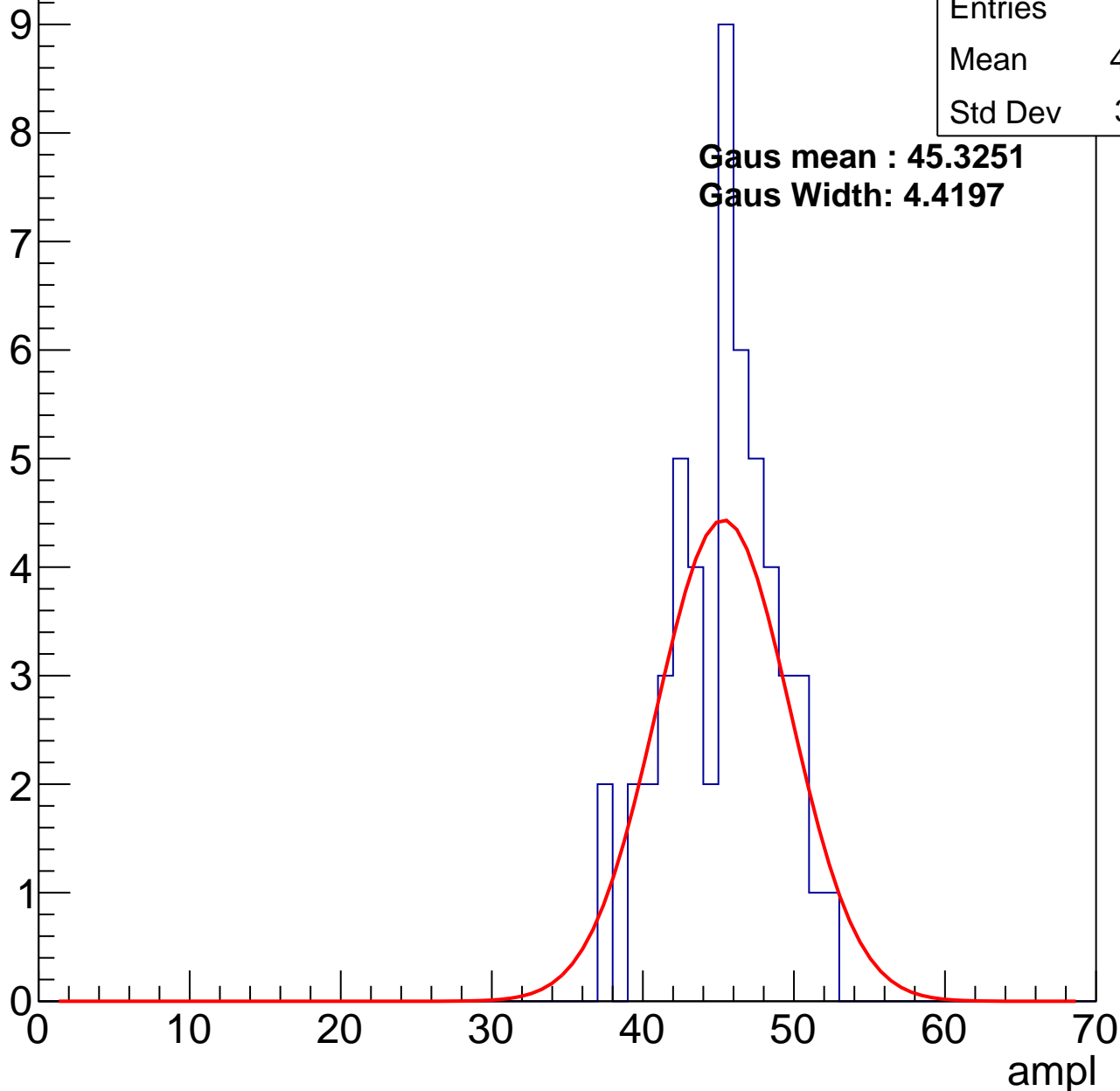
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	44.87
Std Dev	3.481

Gaus mean : 45.3251

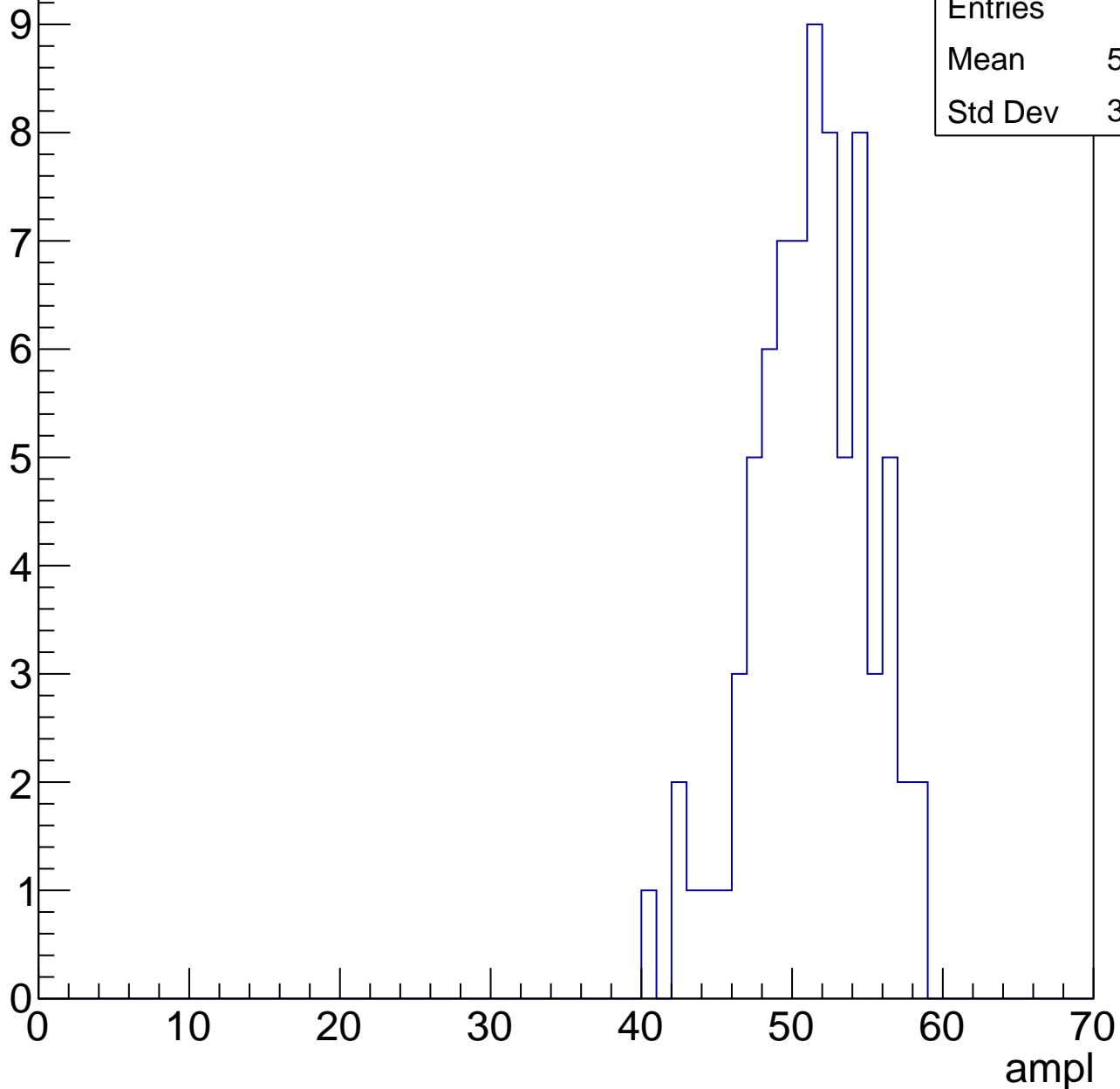
Gaus Width: 4.4197



B0L002S, U18-ch99, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

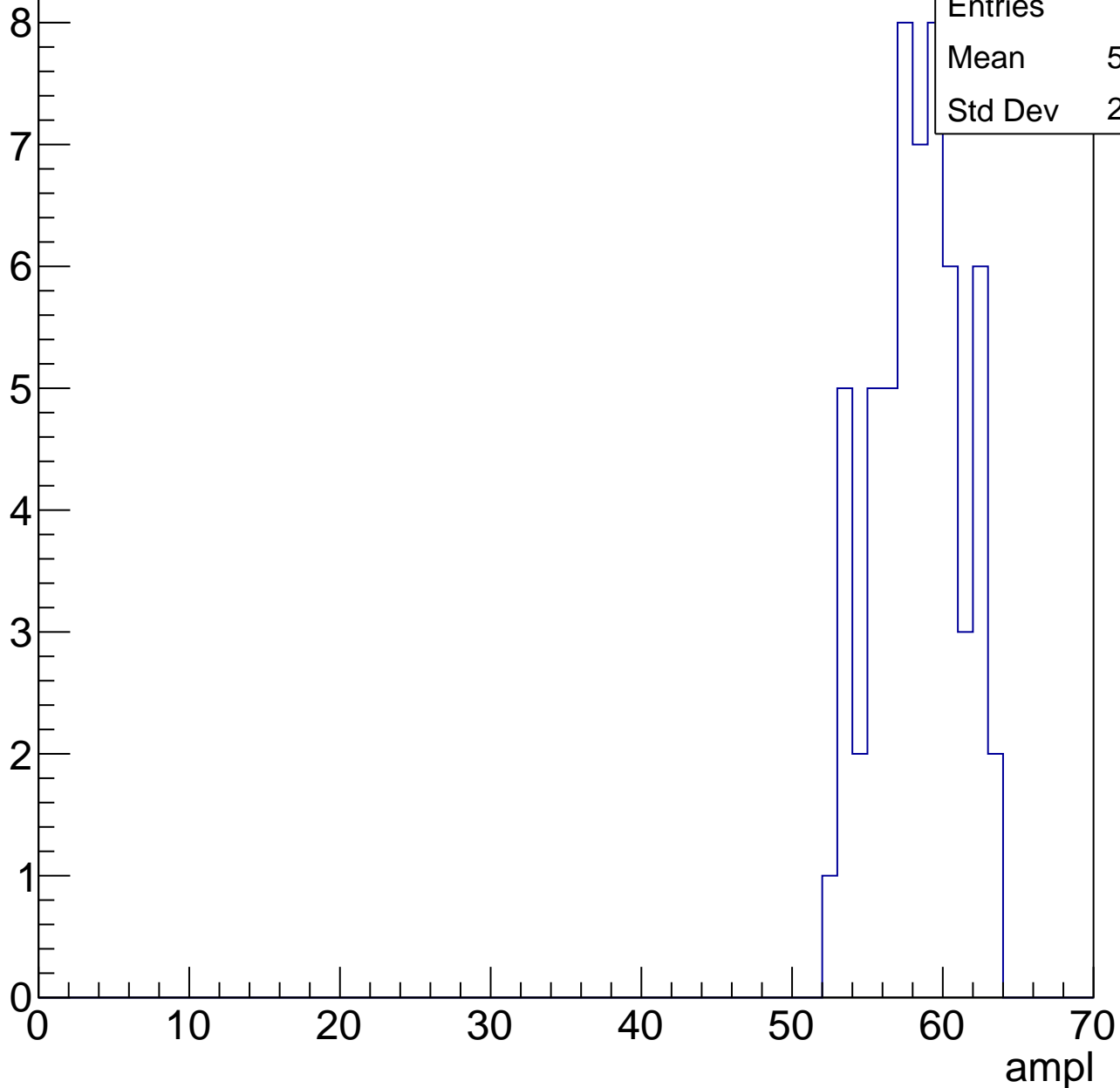


B0L002S, U18-ch99, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	57.84
Std Dev	2.845

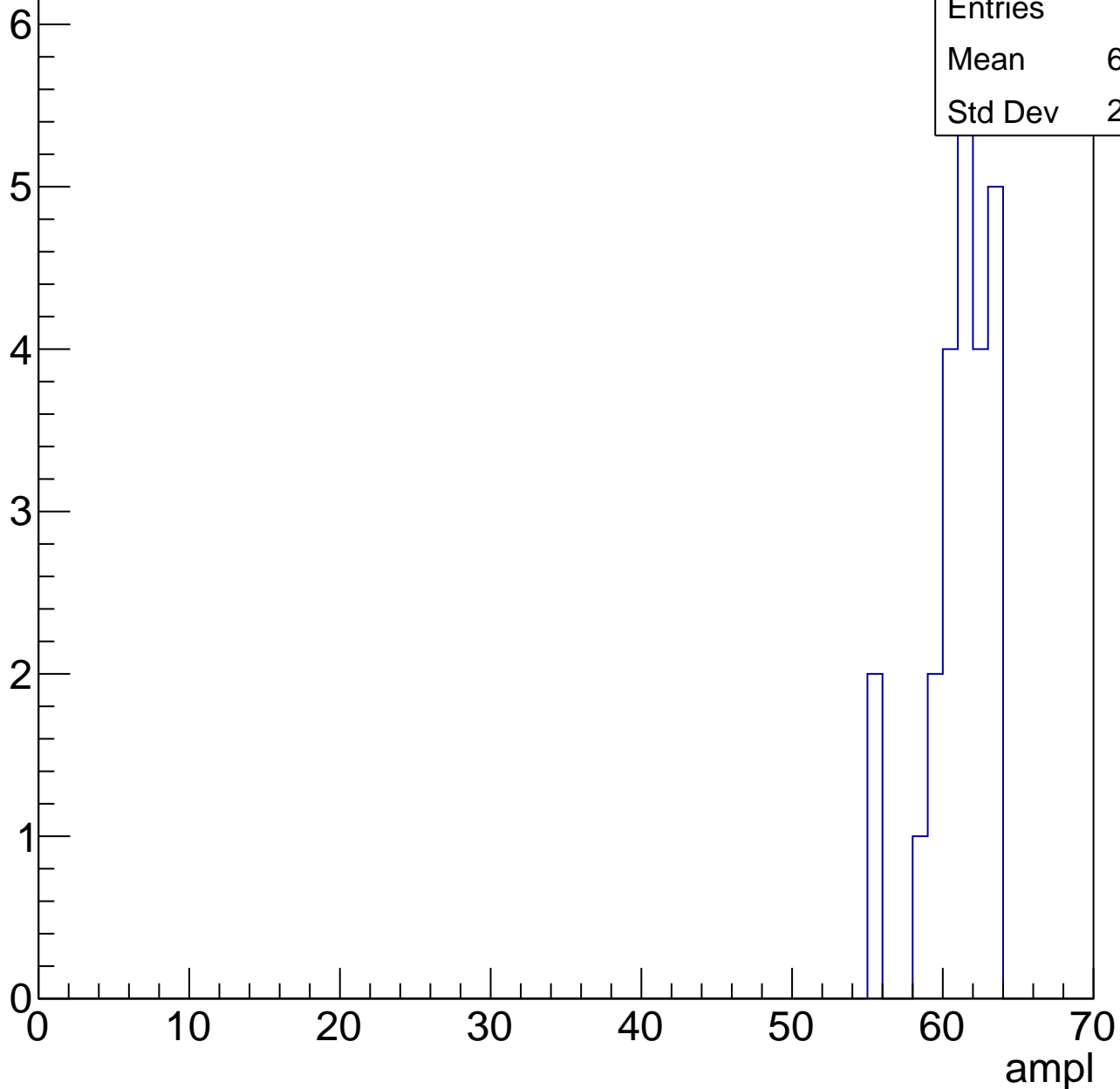


B0L002S, U18-ch99, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

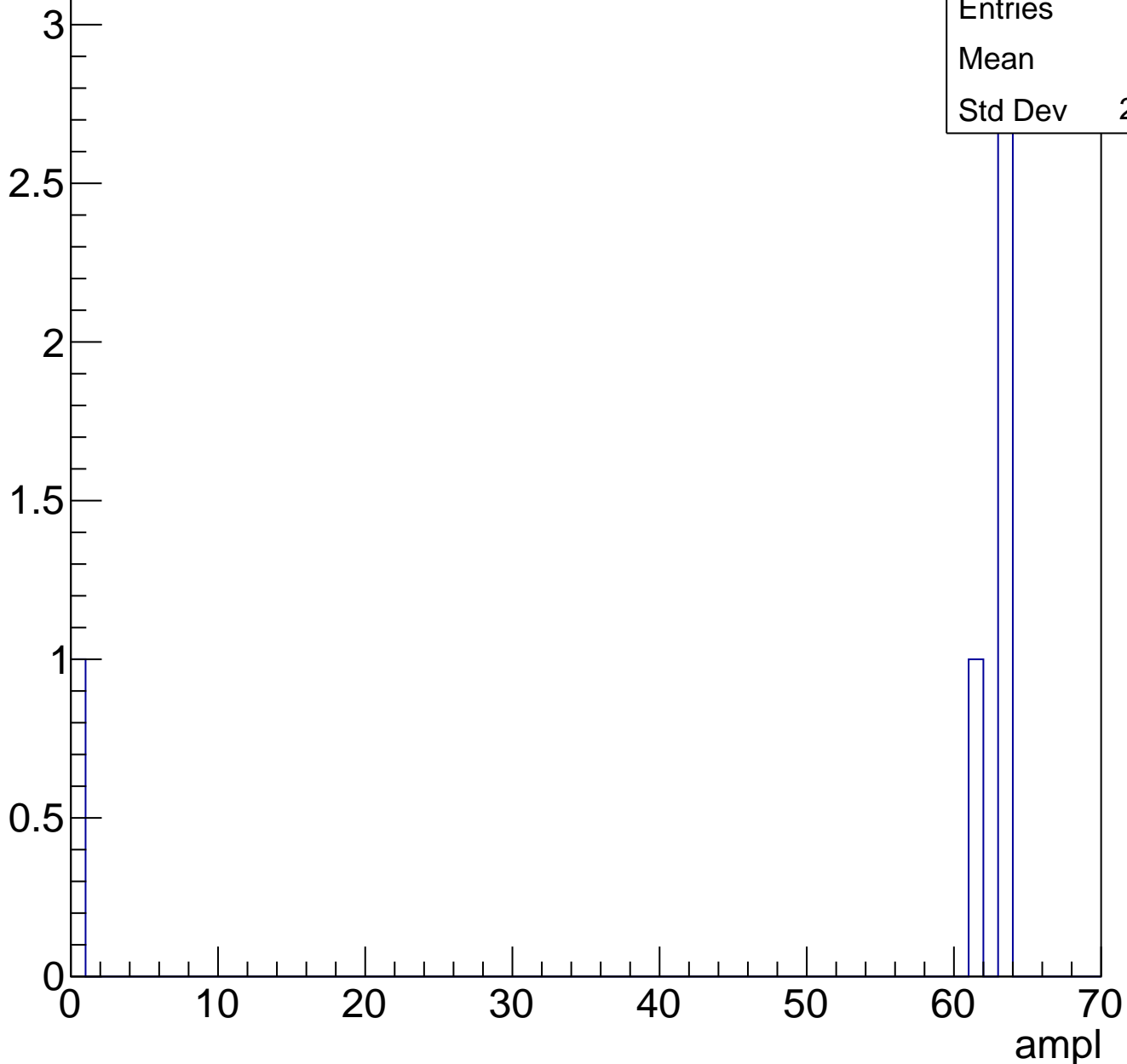
Entries	24
Mean	60.62
Std Dev	2.176



B0L002S, U18-ch99, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch99, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch100, adc0

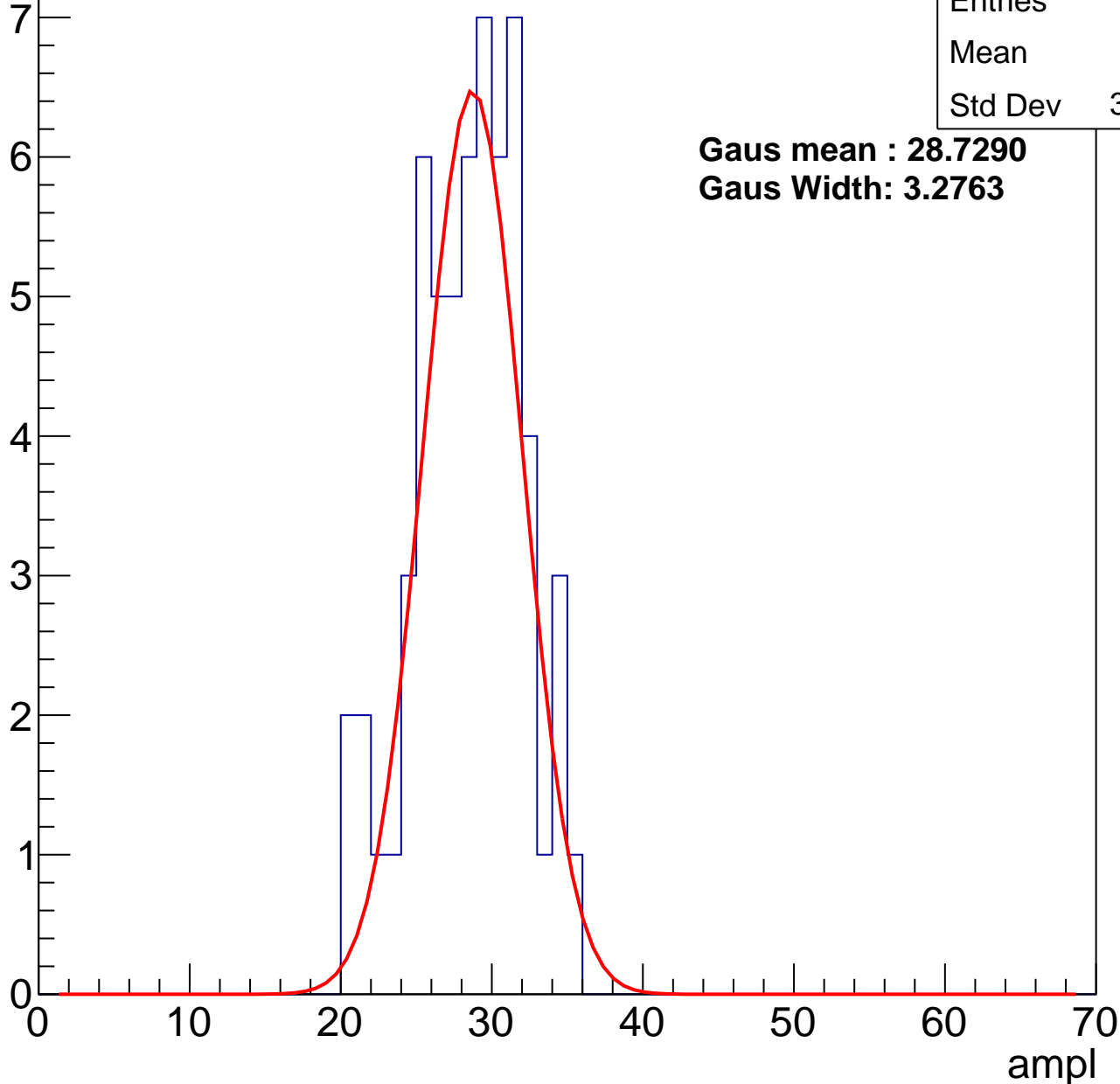
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	28
Std Dev	3.545

Gaus mean : 28.7290

Gaus Width: 3.2763



B0L002S, U18-ch100, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	34.44
Std Dev	3.73

Gaus mean : 35.7218

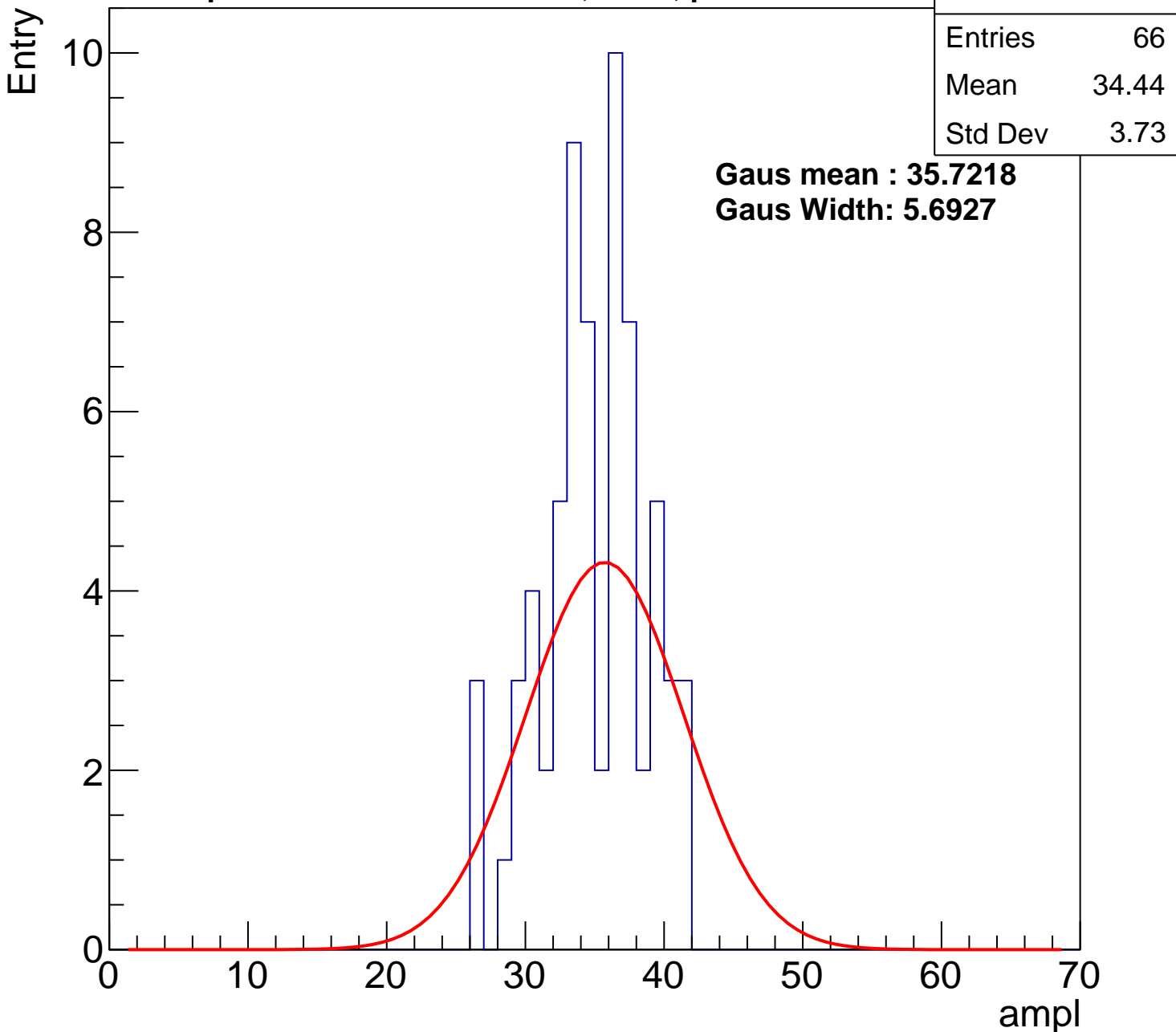
Gaus Width: 5.6927

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch100, adc2

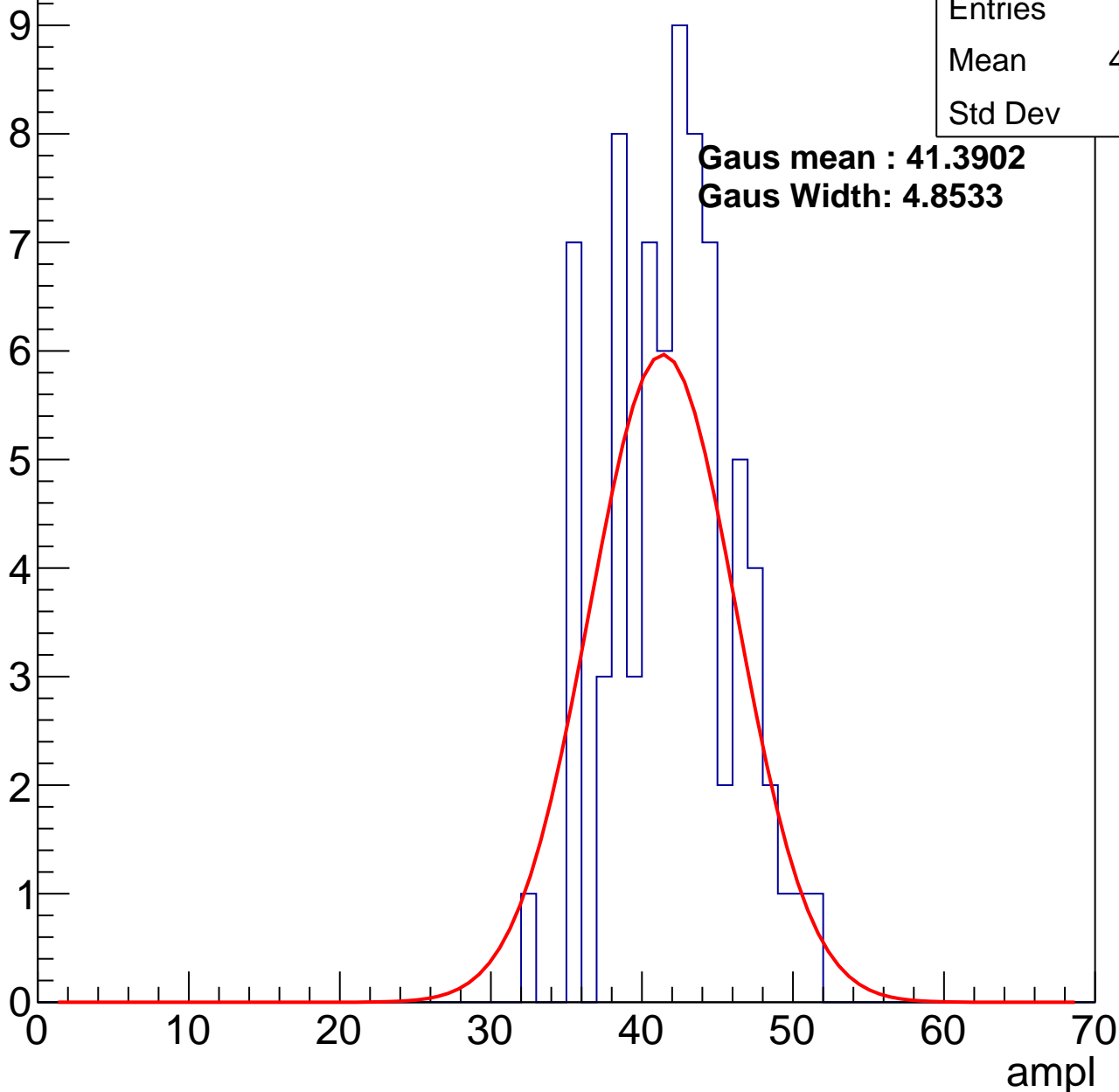
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	41.59
Std Dev	3.99

Gaus mean : 41.3902

Gaus Width: 4.8533

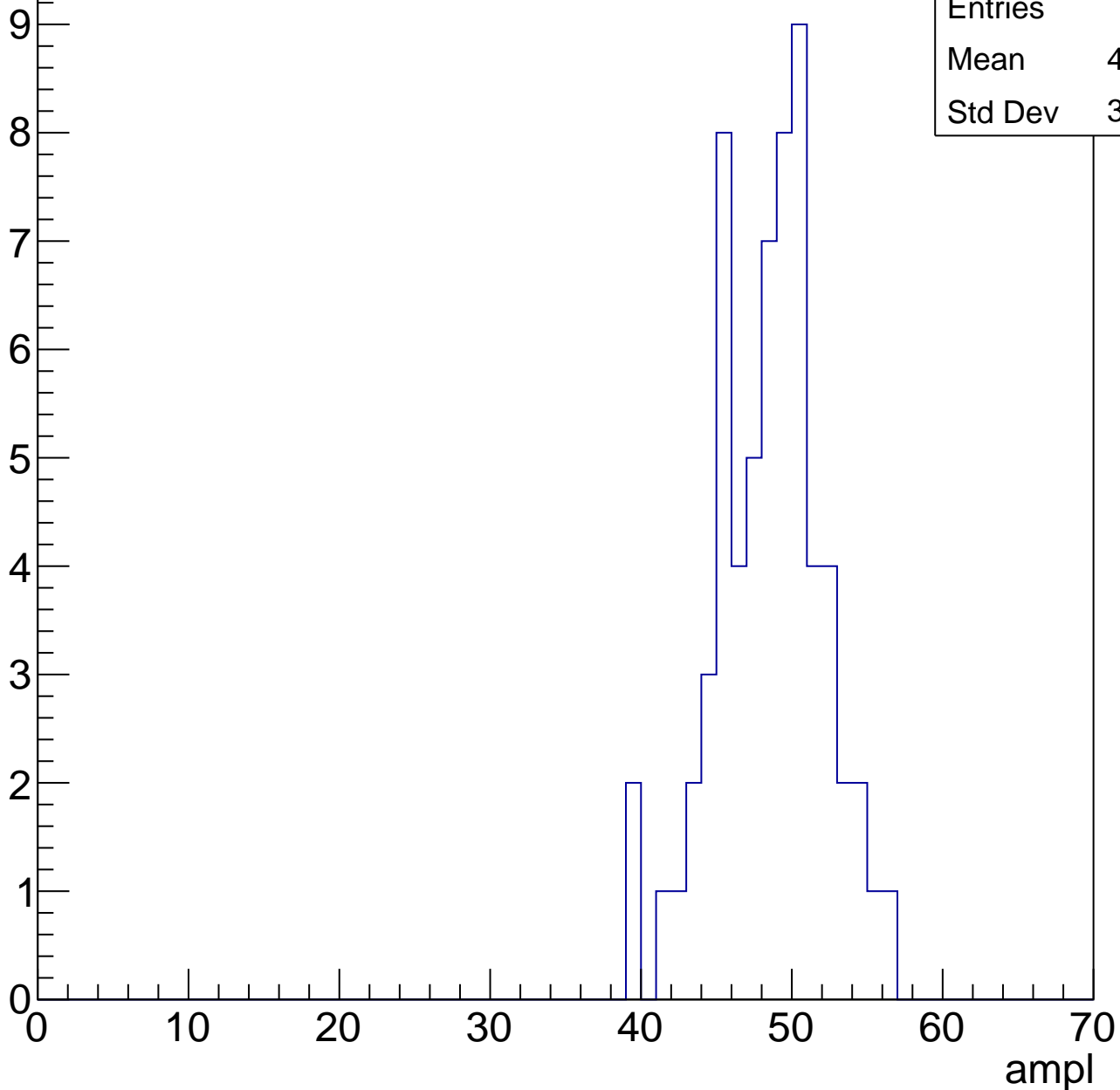


B0L002S, U18-ch100, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	48.02
Std Dev	3.573

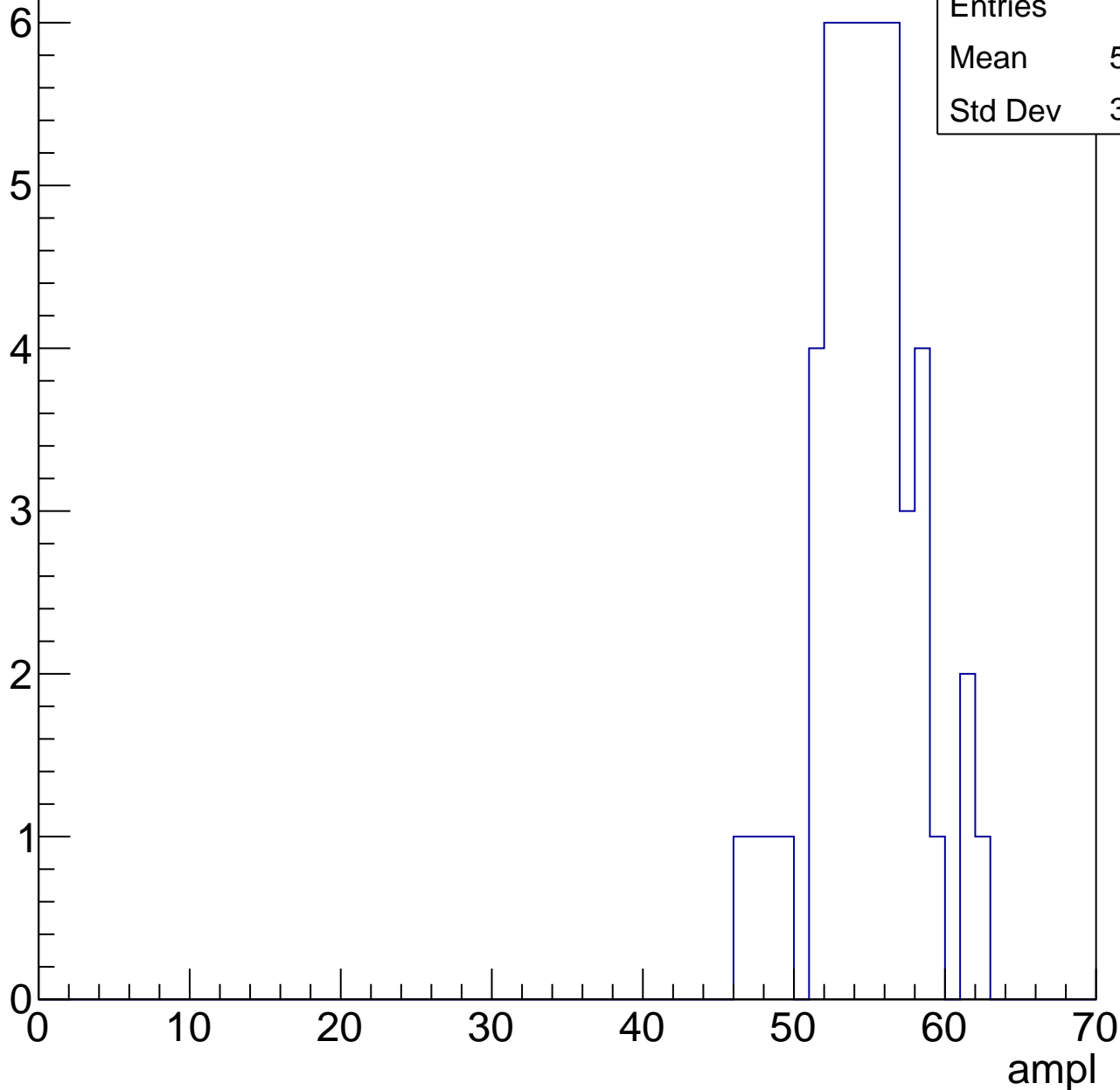


B0L002S, U18-ch100, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	49
Mean	54.29
Std Dev	3.332

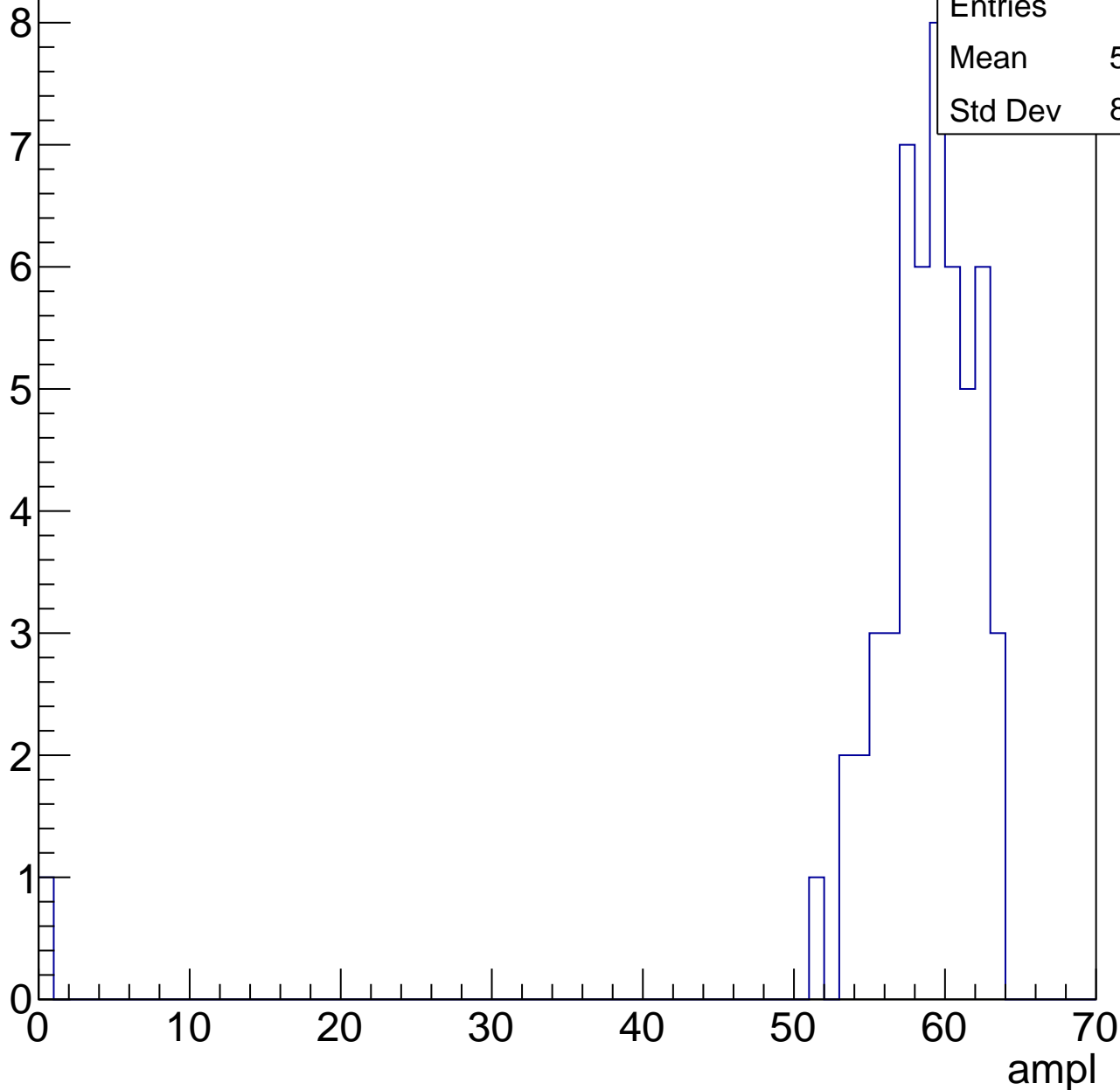


B0L002S, U18-ch100, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	57.42
Std Dev	8.435

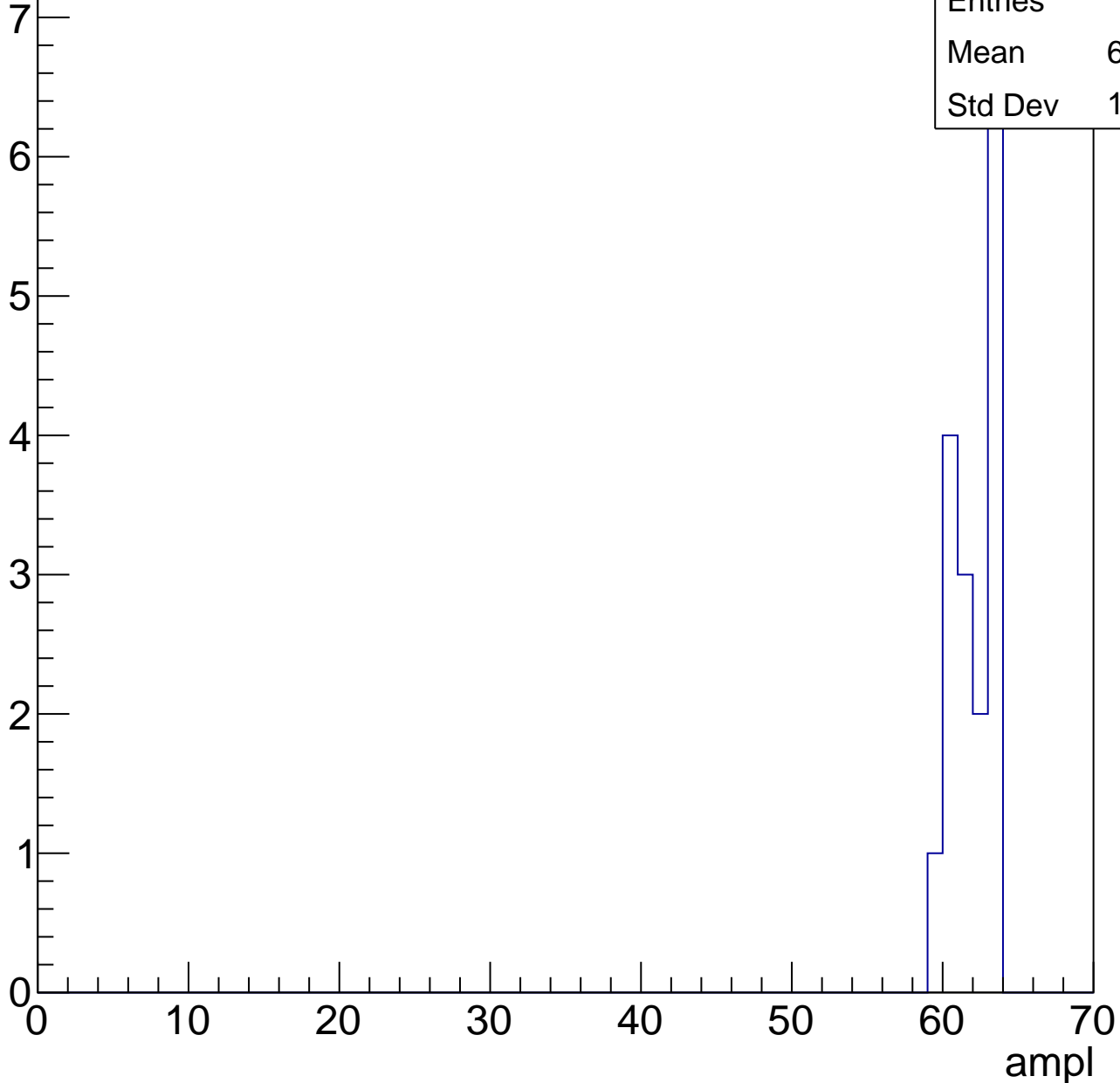


B0L002S, U18-ch100, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	17
Mean	61.59
Std Dev	1.375



B0L002S, U18-ch100, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L002S, U18-ch101, adc0

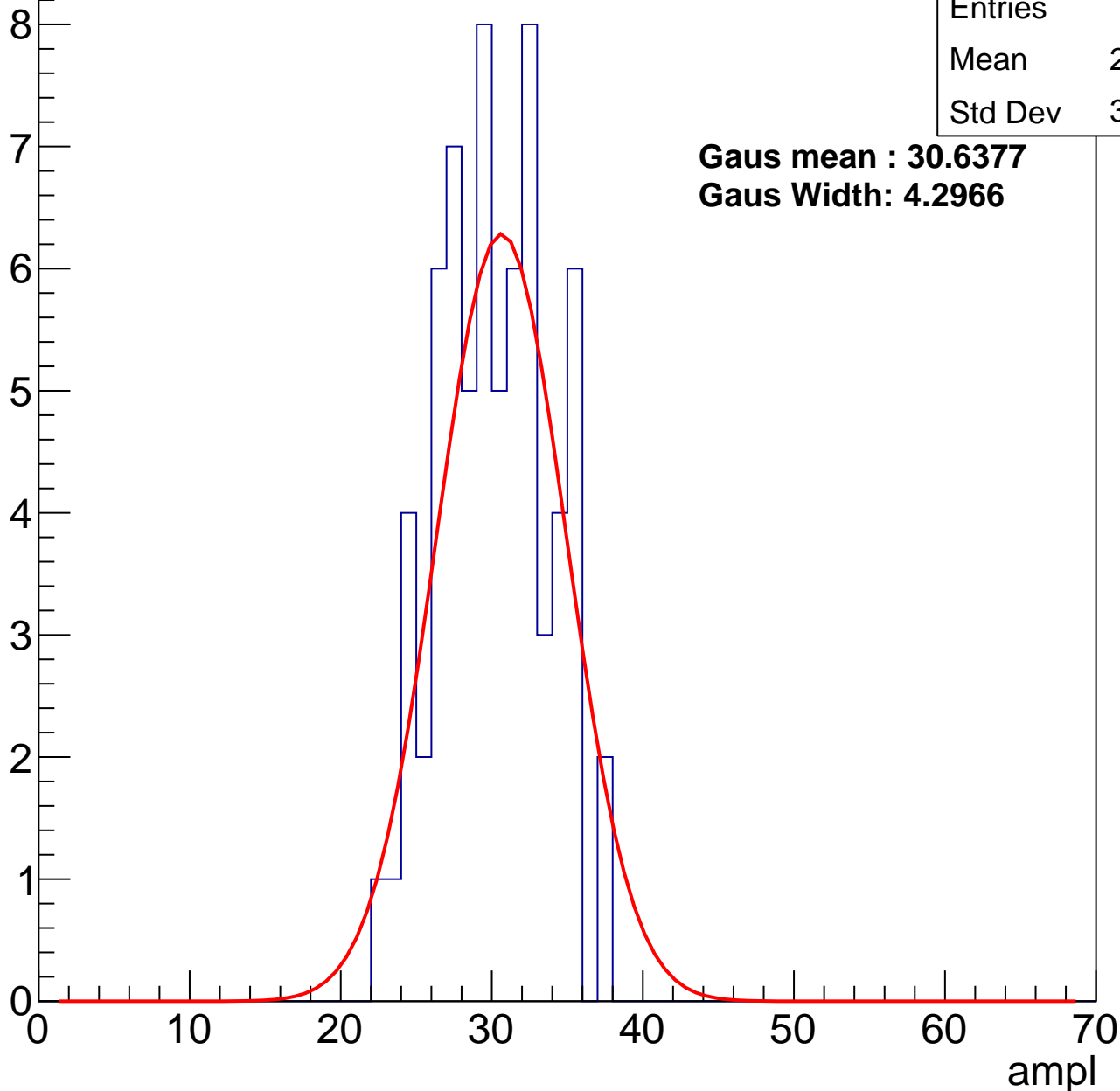
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	29.69
Std Dev	3.566

Gaus mean : 30.6377

Gaus Width: 4.2966



B0L002S, U18-ch101, adc1

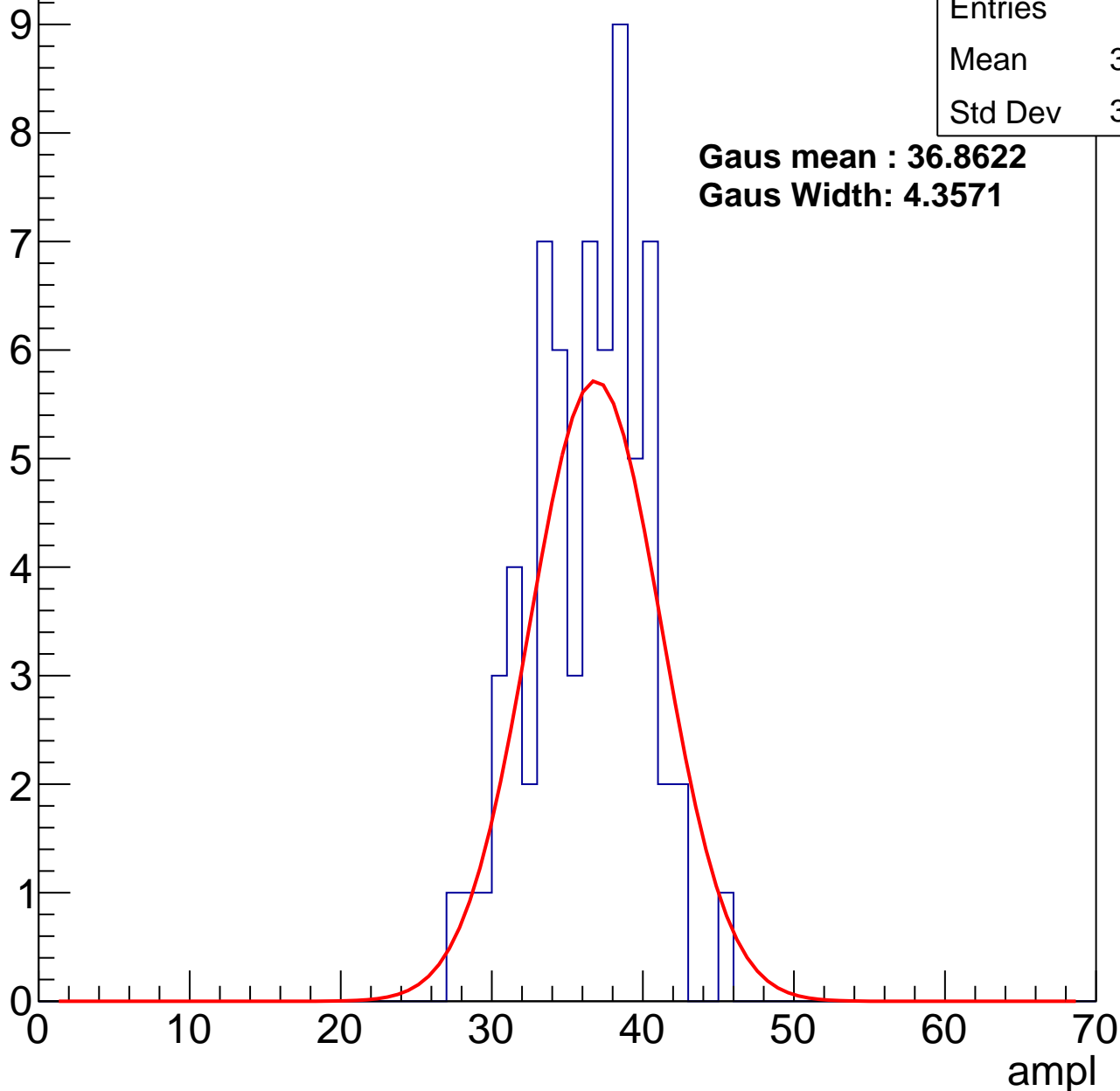
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	35.88
Std Dev	3.716

Gaus mean : 36.8622

Gaus Width: 4.3571



B0L002S, U18-ch101, adc2

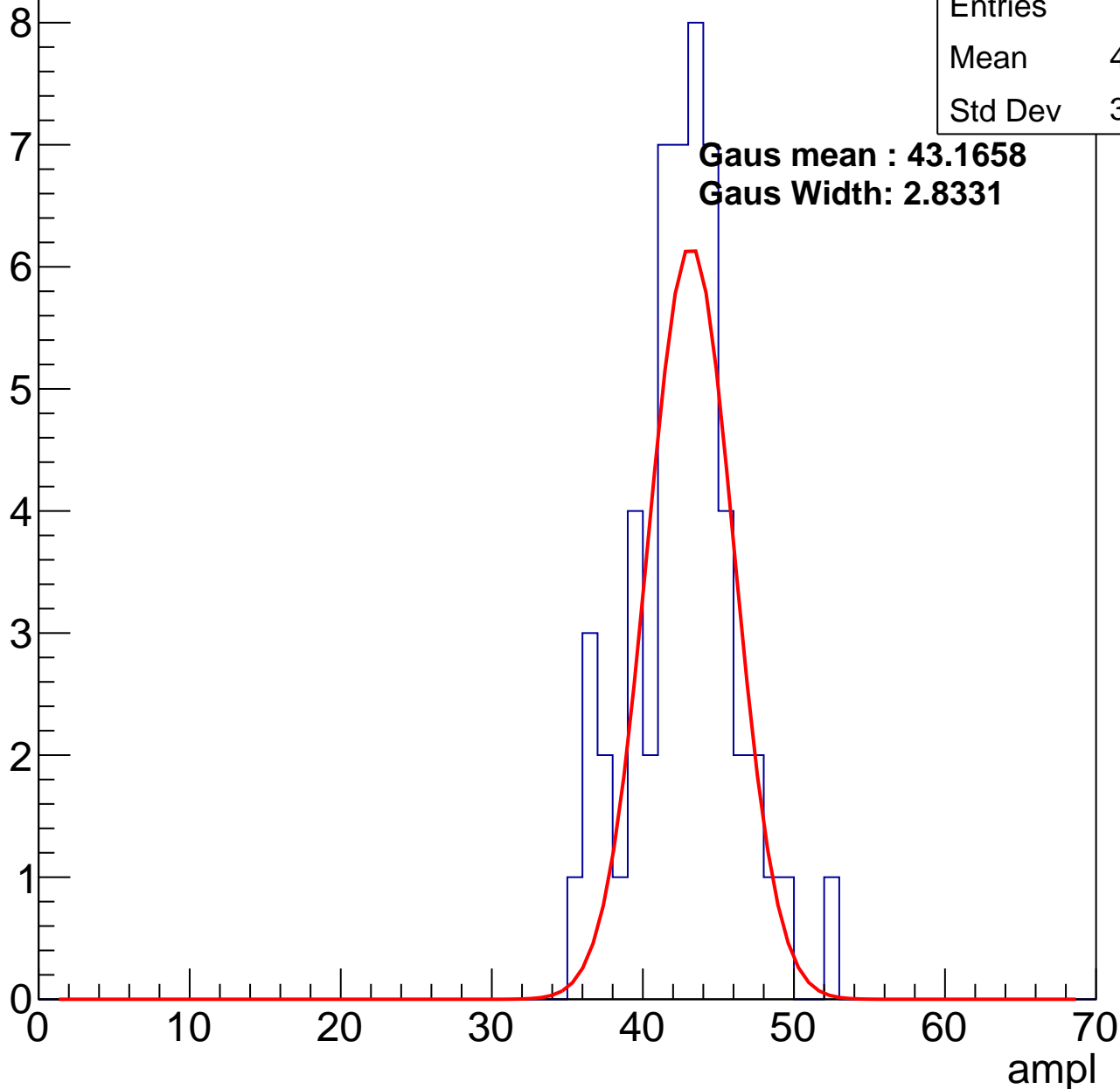
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	42.25
Std Dev	3.392

Gaus mean : 43.1658

Gaus Width: 2.8331



B0L002S, U18-ch101, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entries	83
Mean	48.83
Std Dev	3.728

Entry

10

8

6

4

2

0

0

10

20

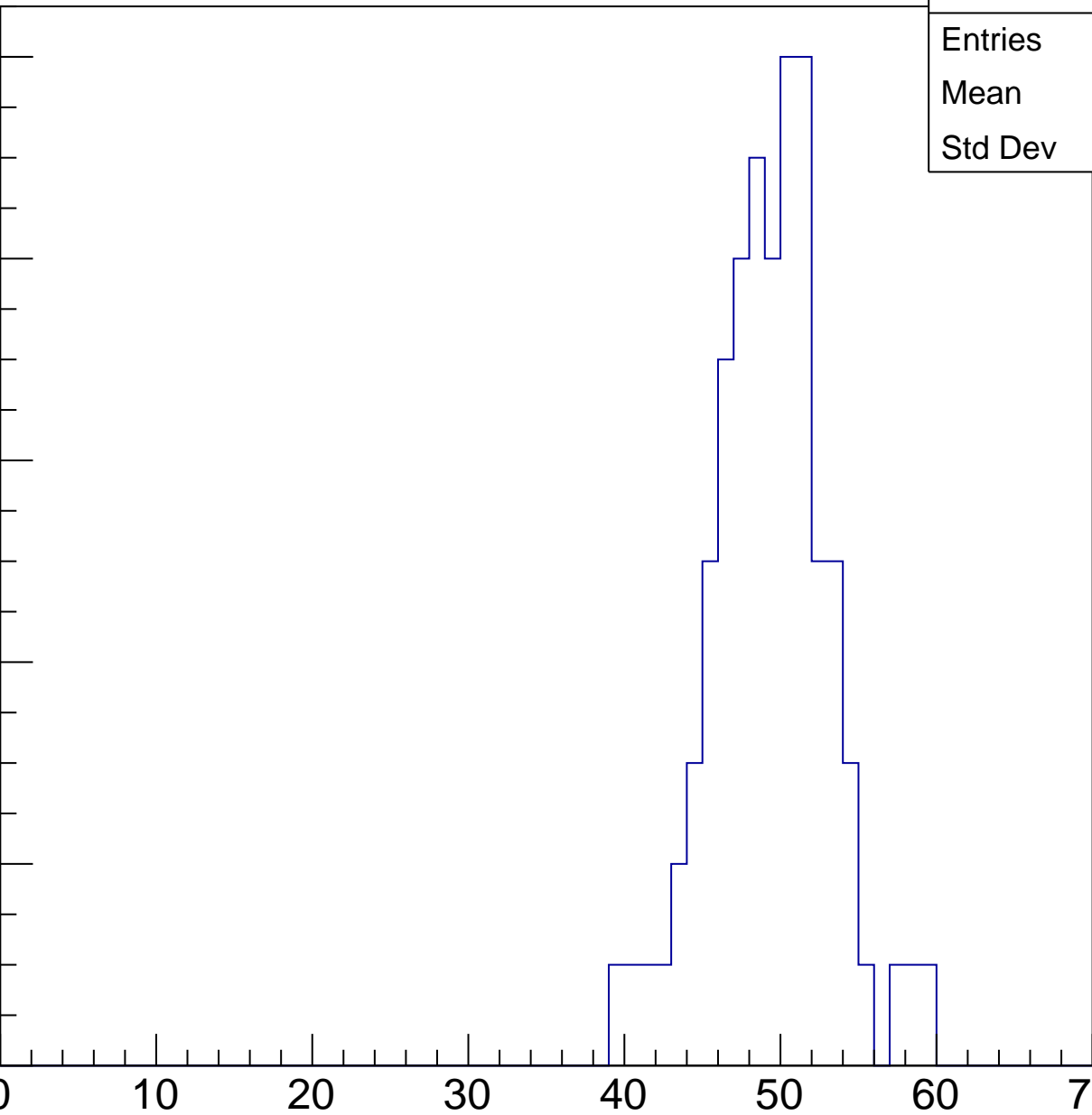
30

40

50

60

ampl

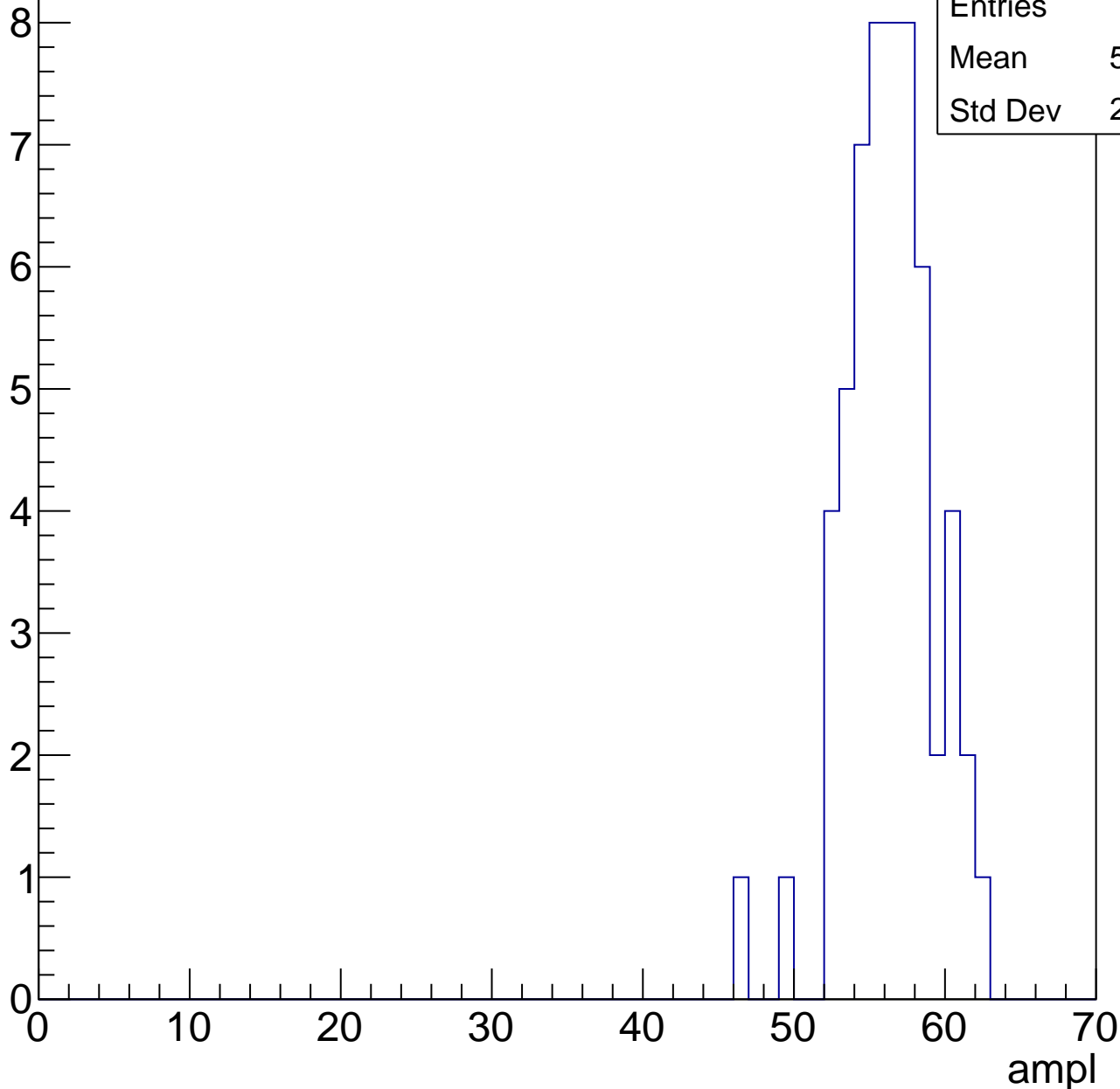


B0L002S, U18-ch101, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	55.79
Std Dev	2.936

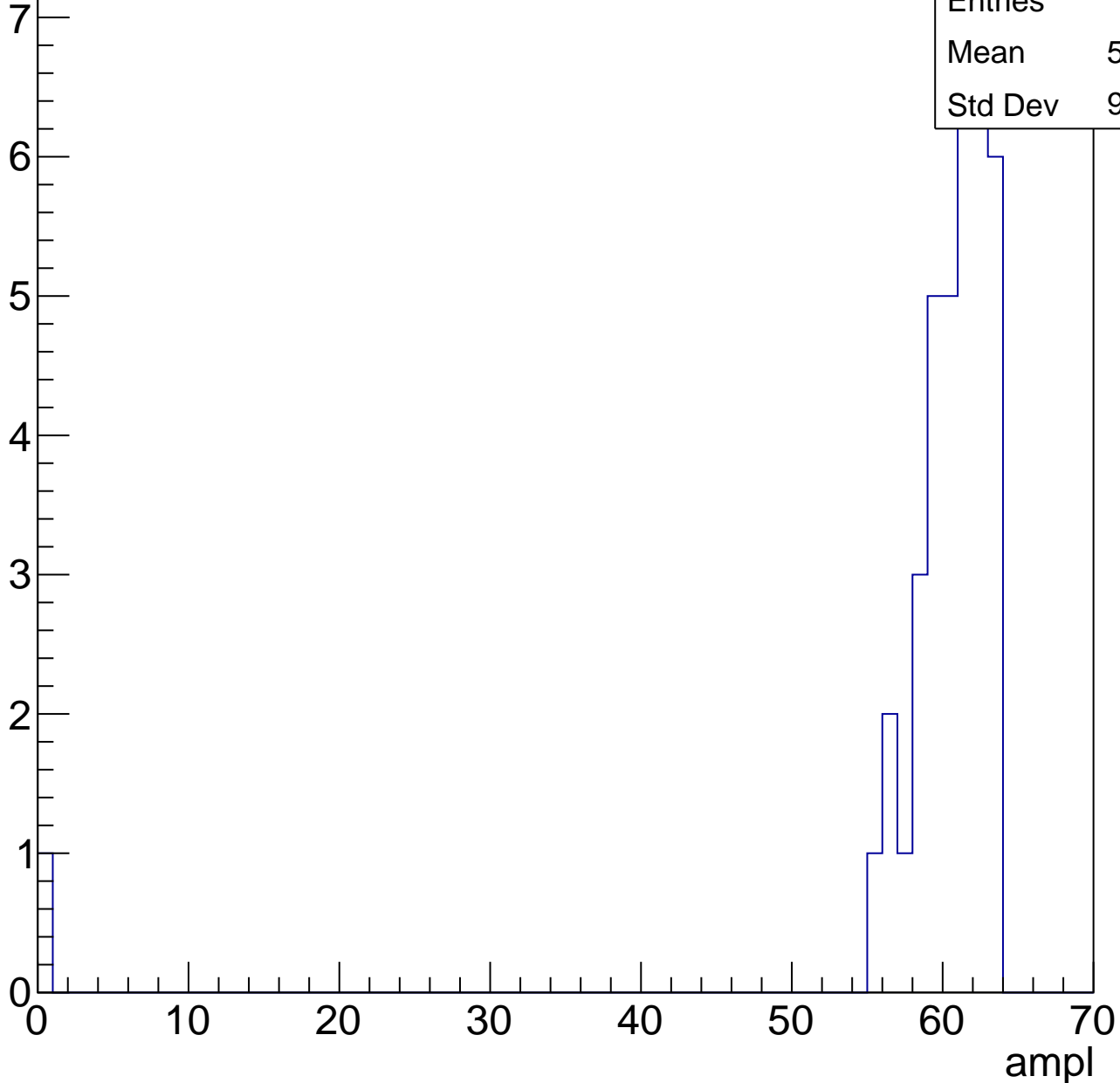


B0L002S, U18-ch101, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

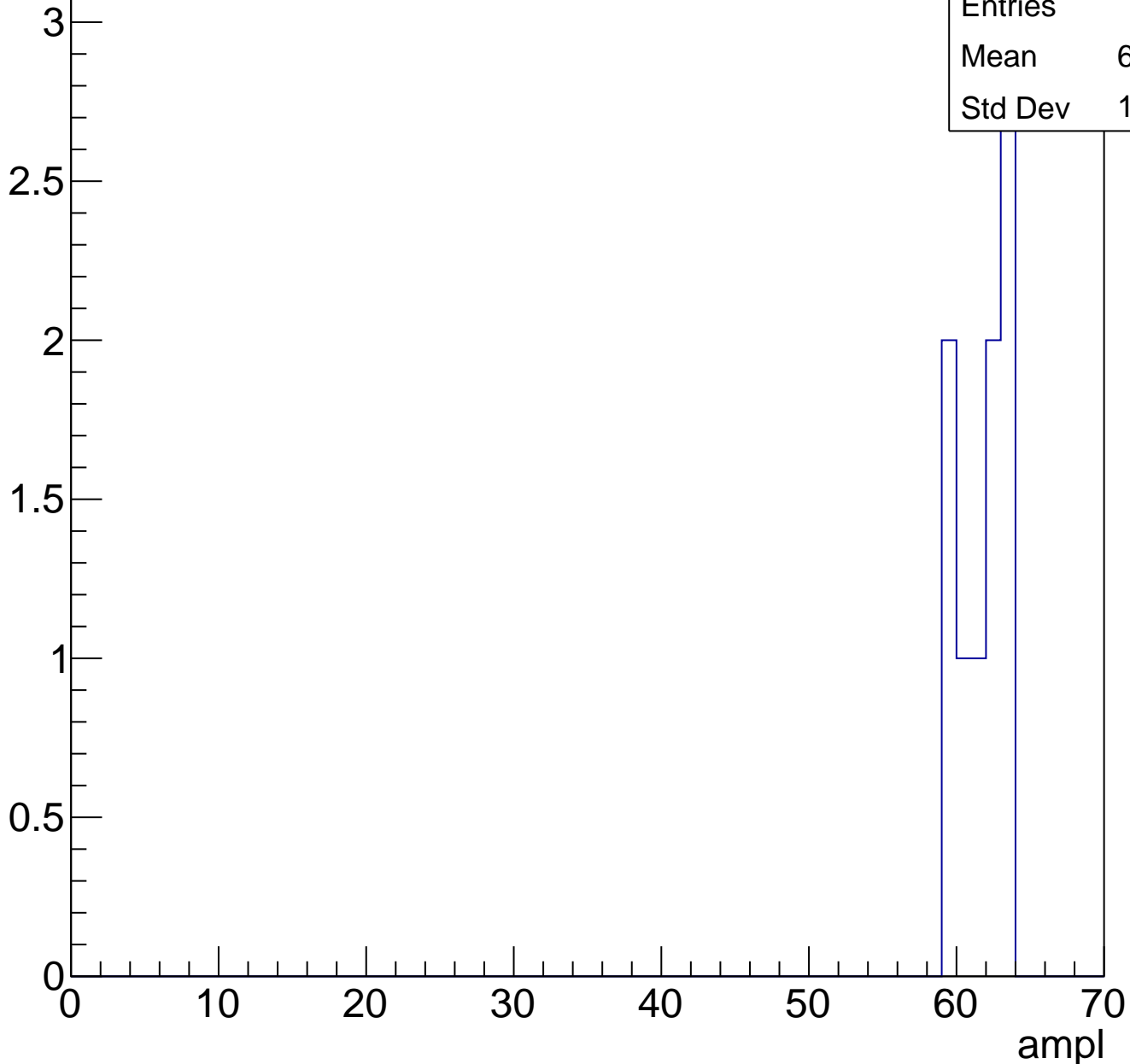
Entries	38
Mean	58.74
Std Dev	9.883



B0L002S, U18-ch101, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch101, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	63
Std Dev	0

ampl

B0L002S, U18-ch102, adc0

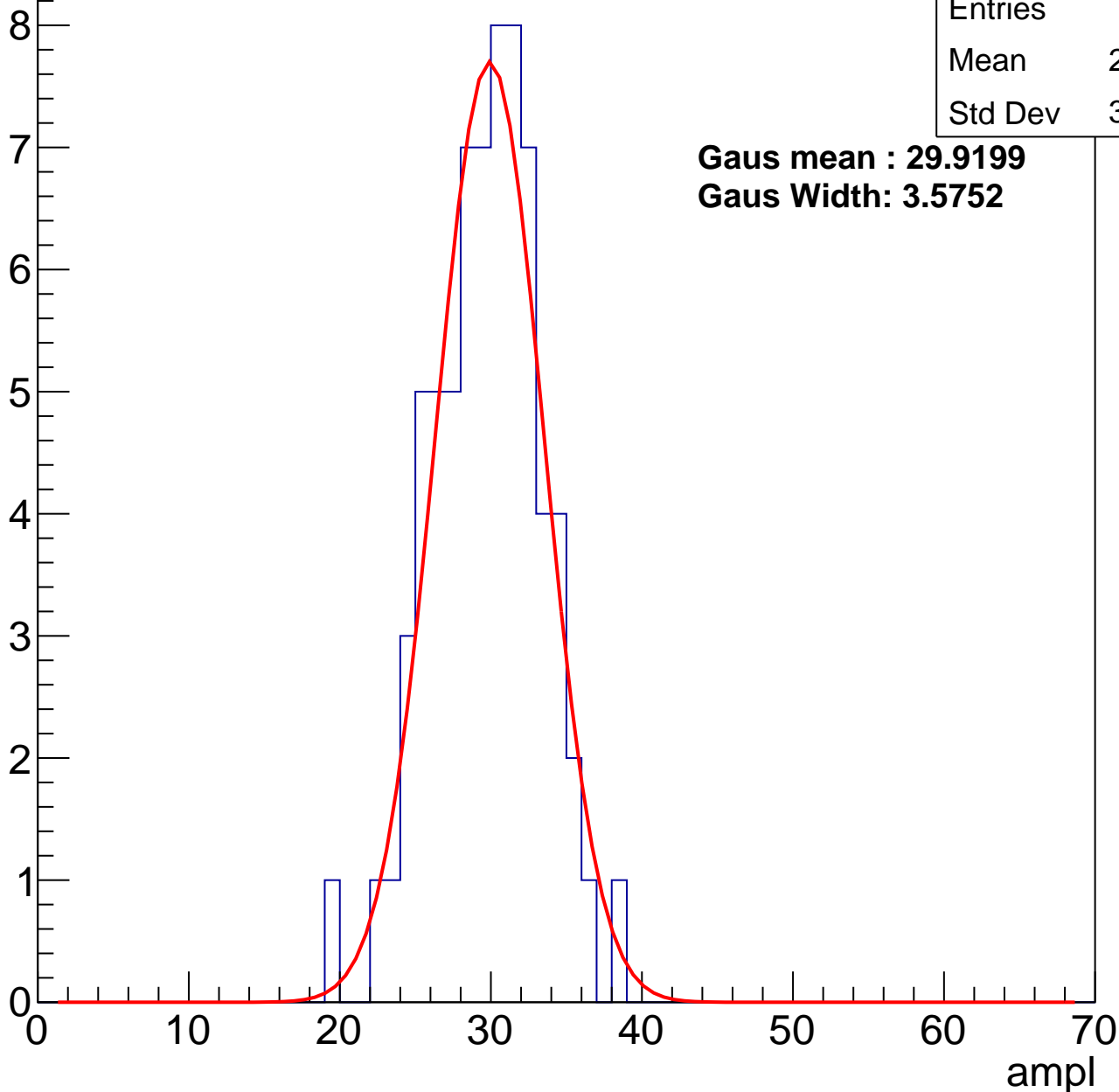
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.27
Std Dev	3.537

Gaus mean : 29.9199

Gaus Width: 3.5752



B0L002S, U18-ch102, adc1

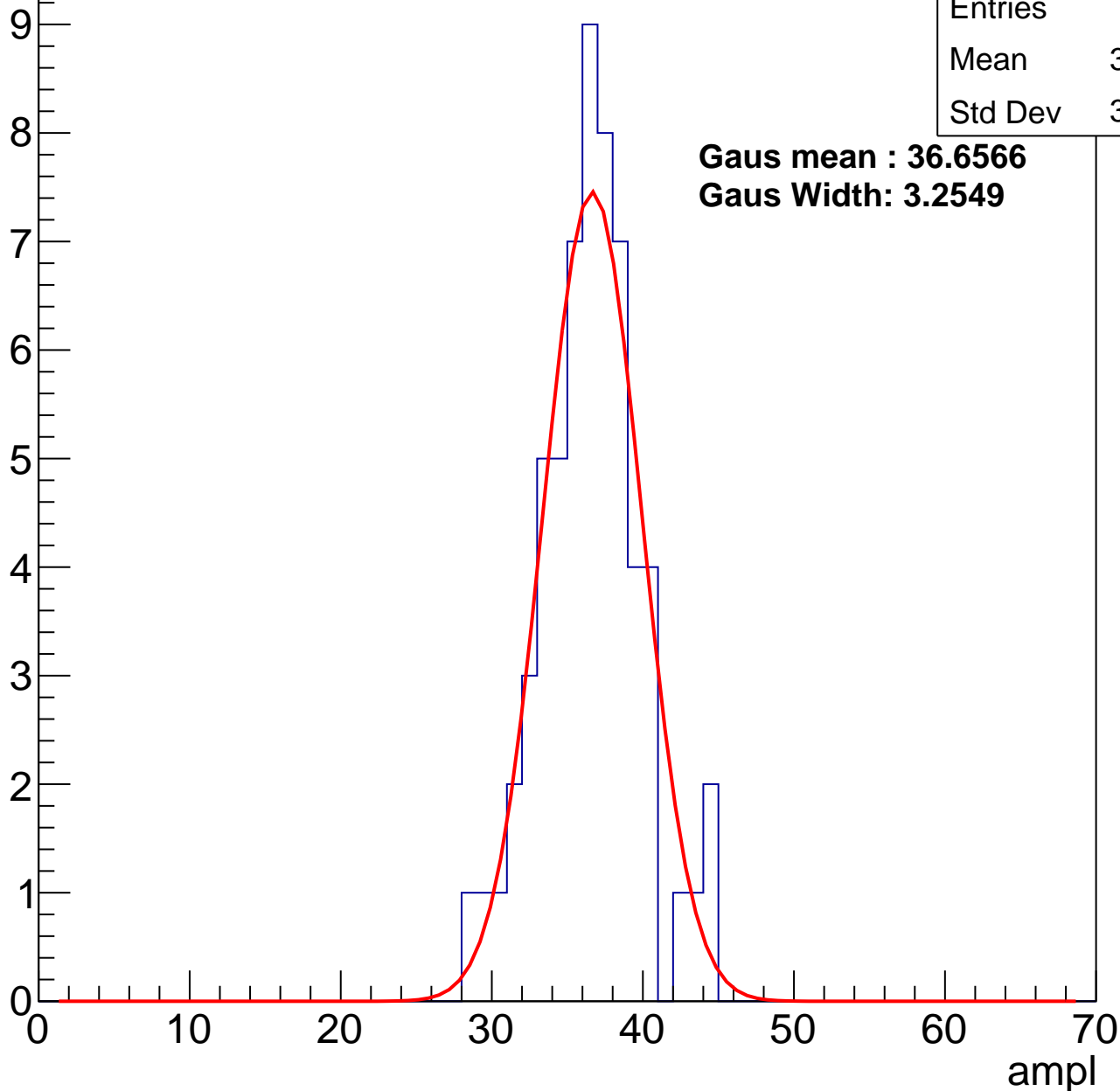
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.07
Std Dev	3.309

Gaus mean : 36.6566

Gaus Width: 3.2549



B0L002S, U18-ch102, adc2

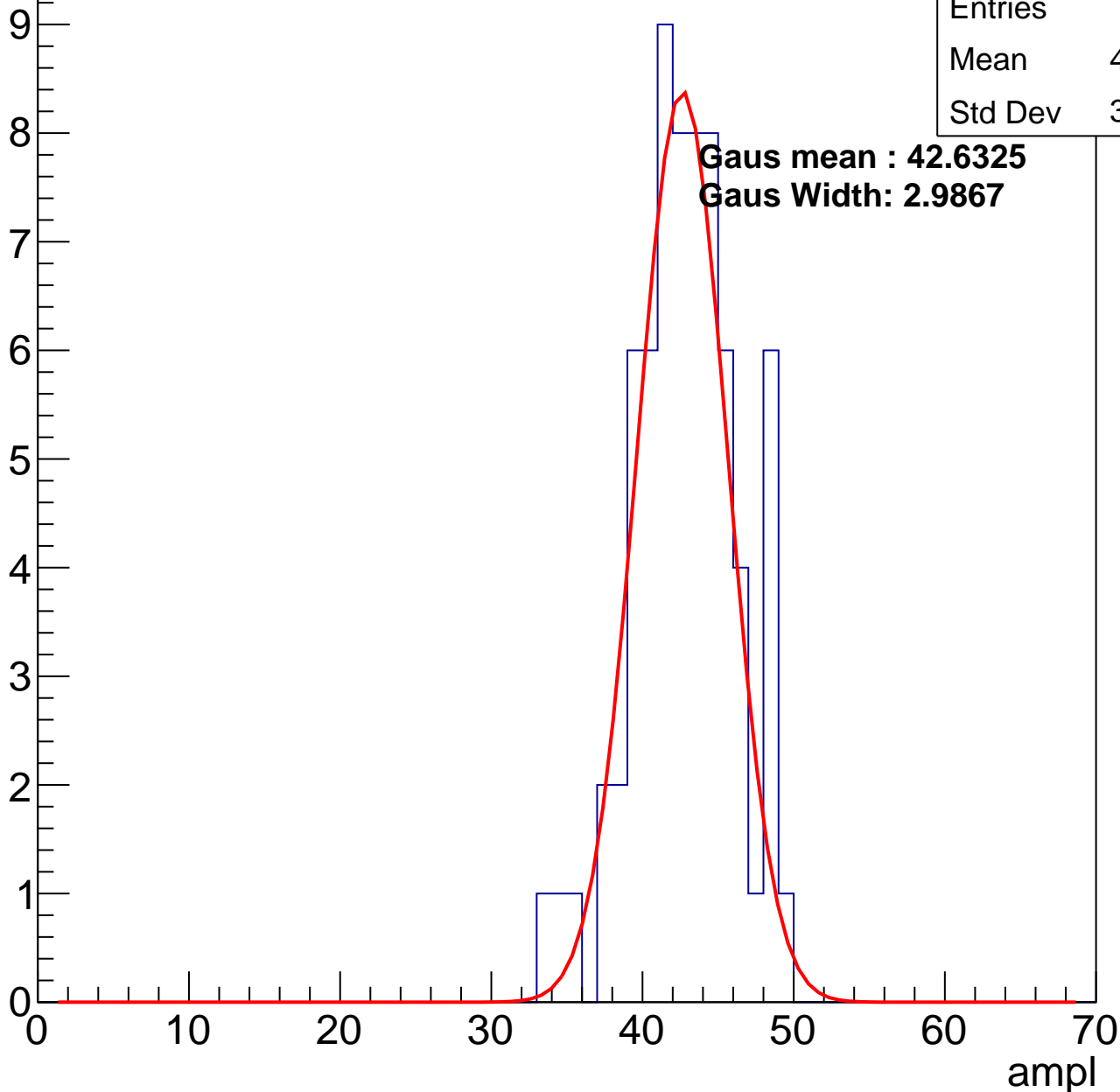
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	42.36
Std Dev	3.385

Gaus mean : 42.6325

Gaus Width: 2.9867

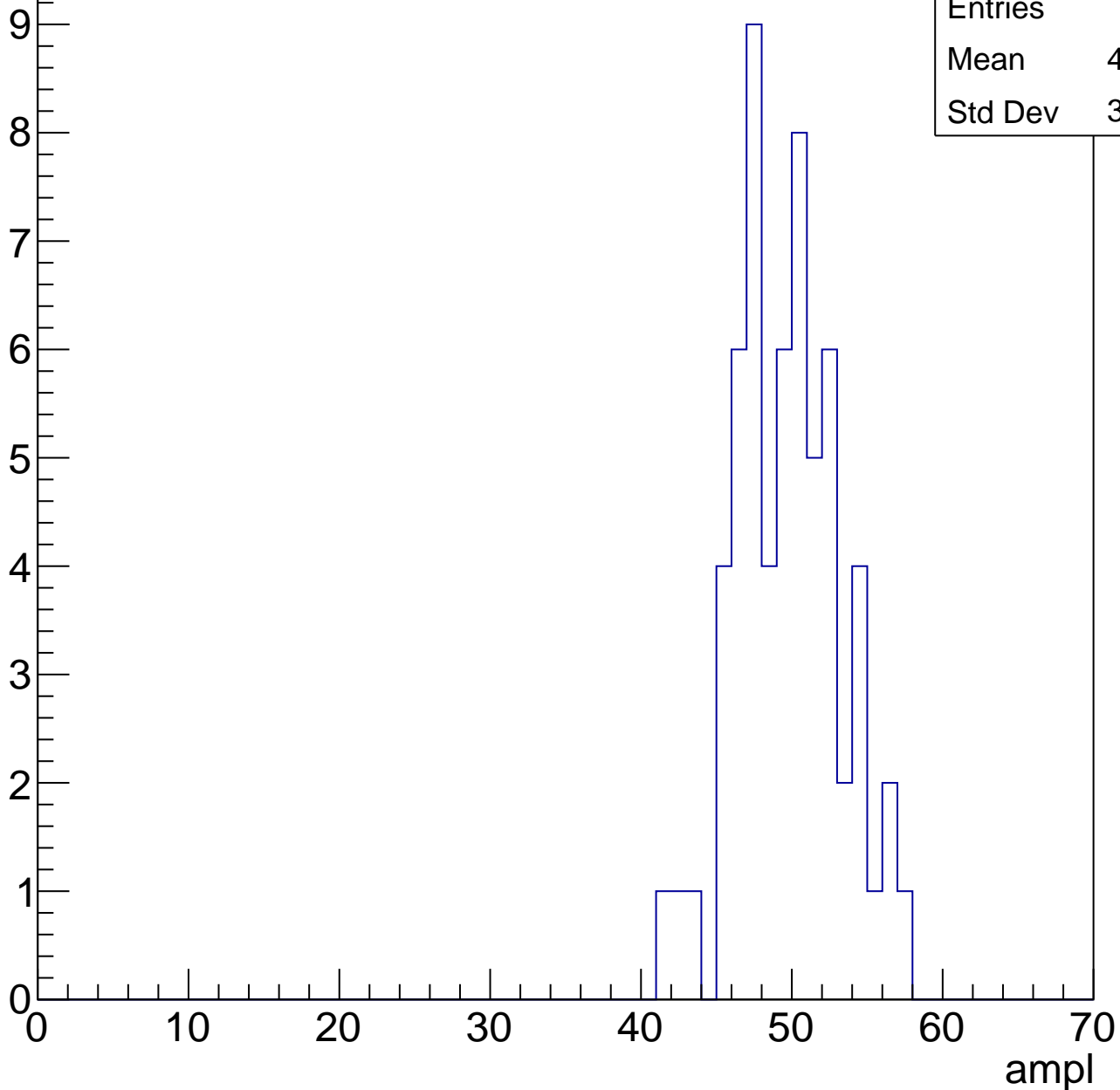


B0L002S, U18-ch102, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.25
Std Dev	3.424

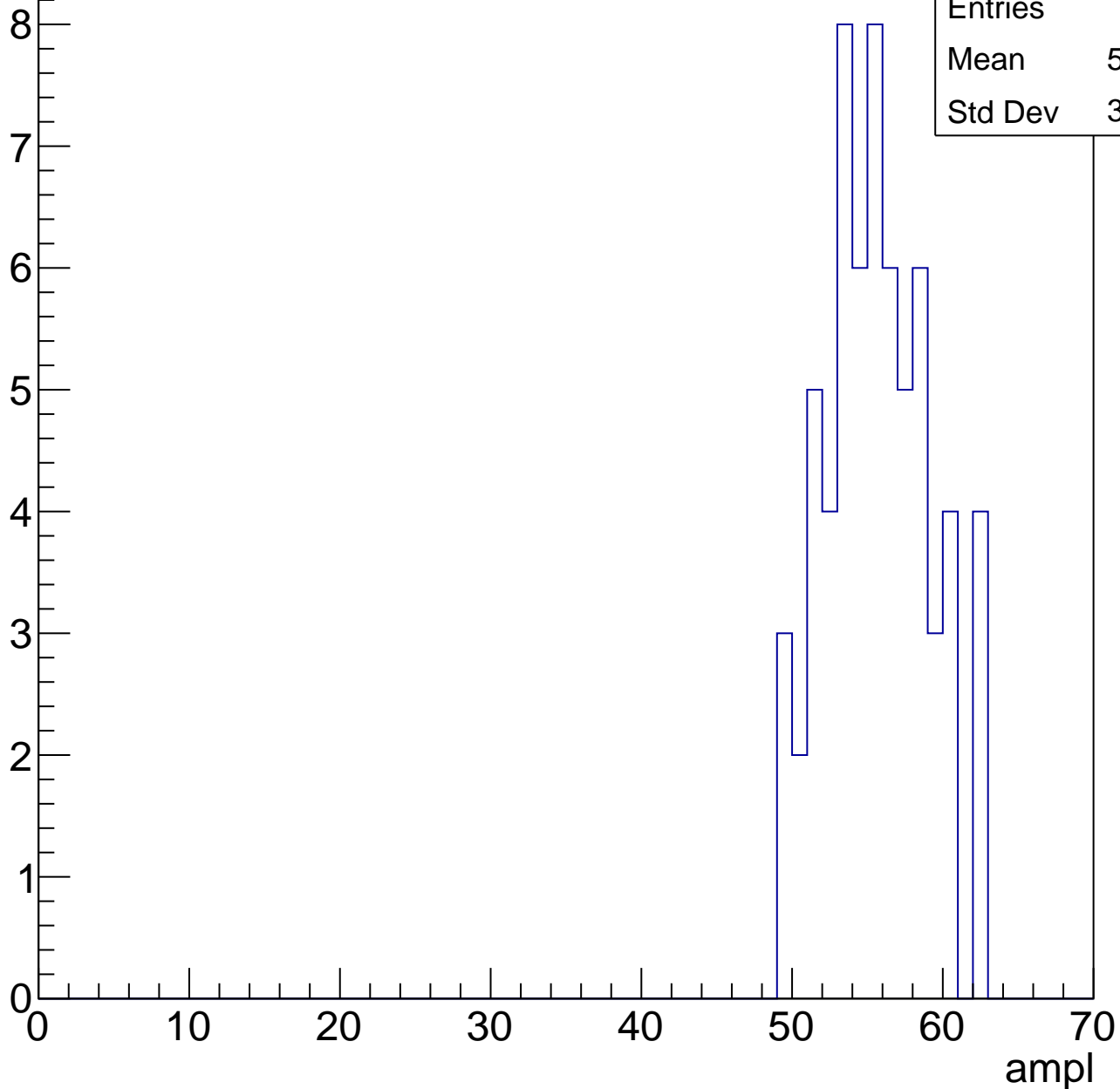


B0L002S, U18-ch102, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	55.19
Std Dev	3.386

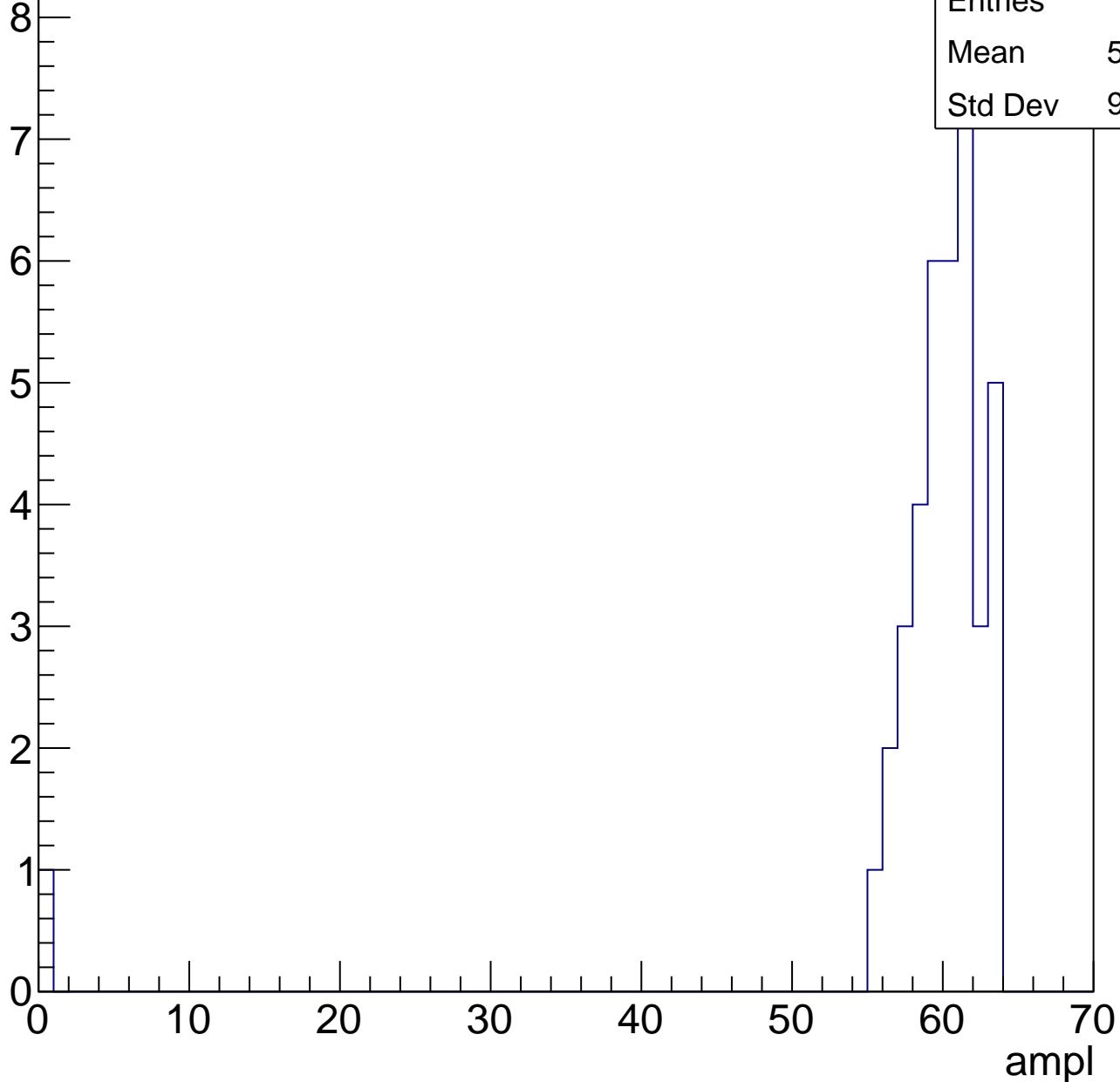


B0L002S, U18-ch102, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	39
Mean	58.28
Std Dev	9.682

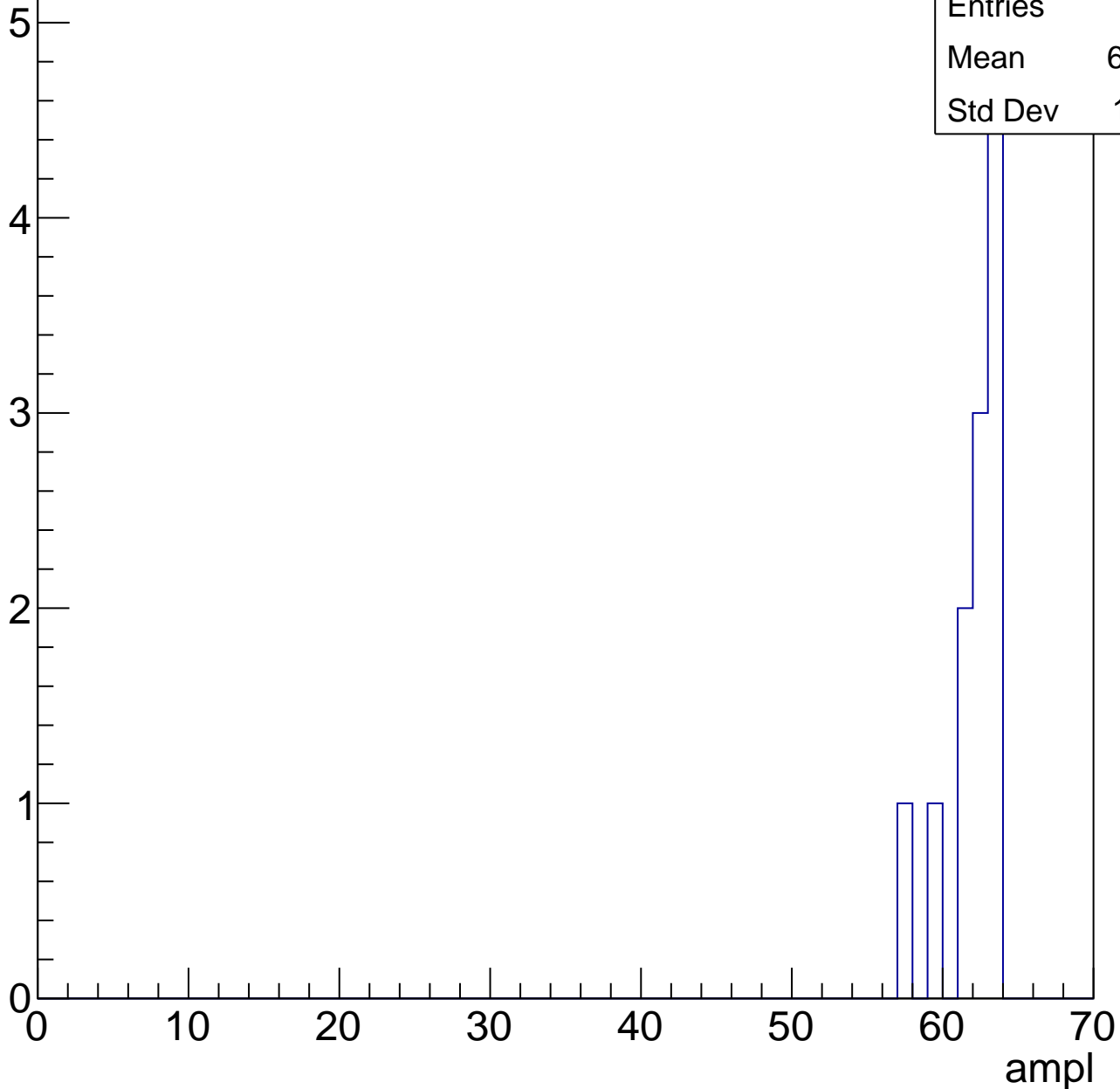


B0L002S, U18-ch102, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	12
Mean	61.58
Std Dev	1.801



B0L002S, U18-ch102, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch103, adc0

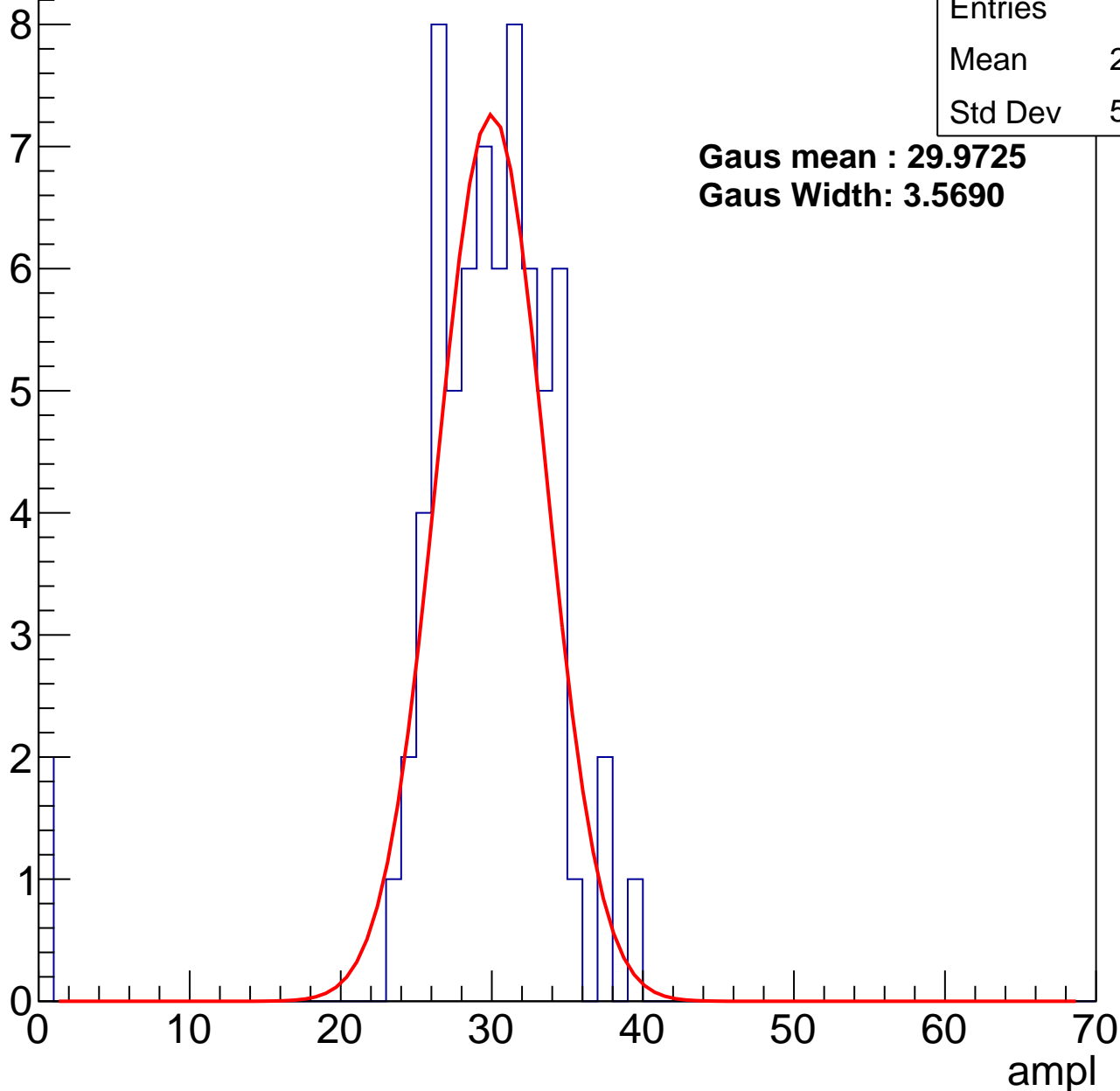
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	28.89
Std Dev	5.994

Gaus mean : 29.9725

Gaus Width: 3.5690



B0L002S, U18-ch103, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	64
Mean	36.08
Std Dev	3.555

Gaus mean : 36.5216

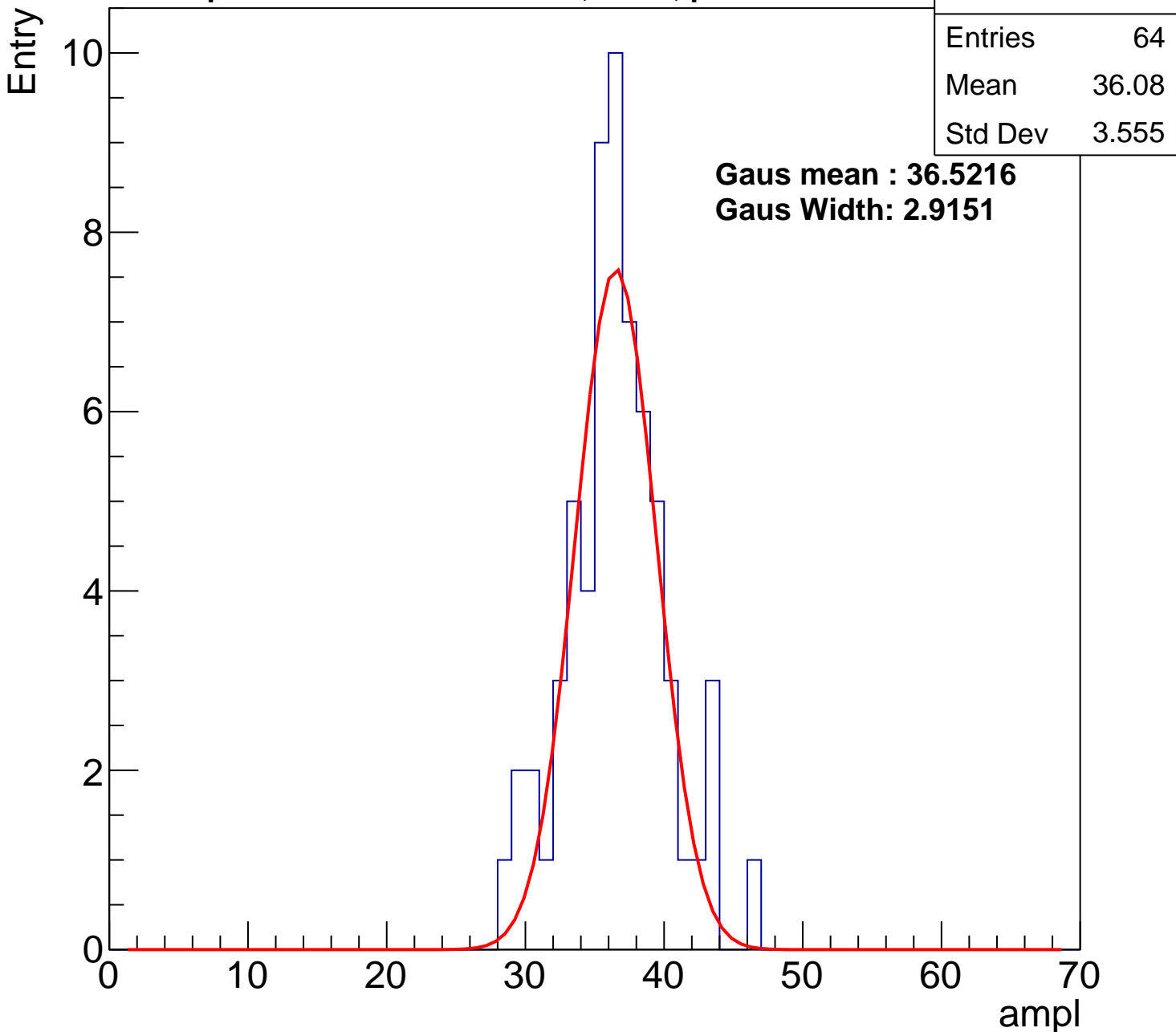
Gaus Width: 2.9151

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch103, adc2

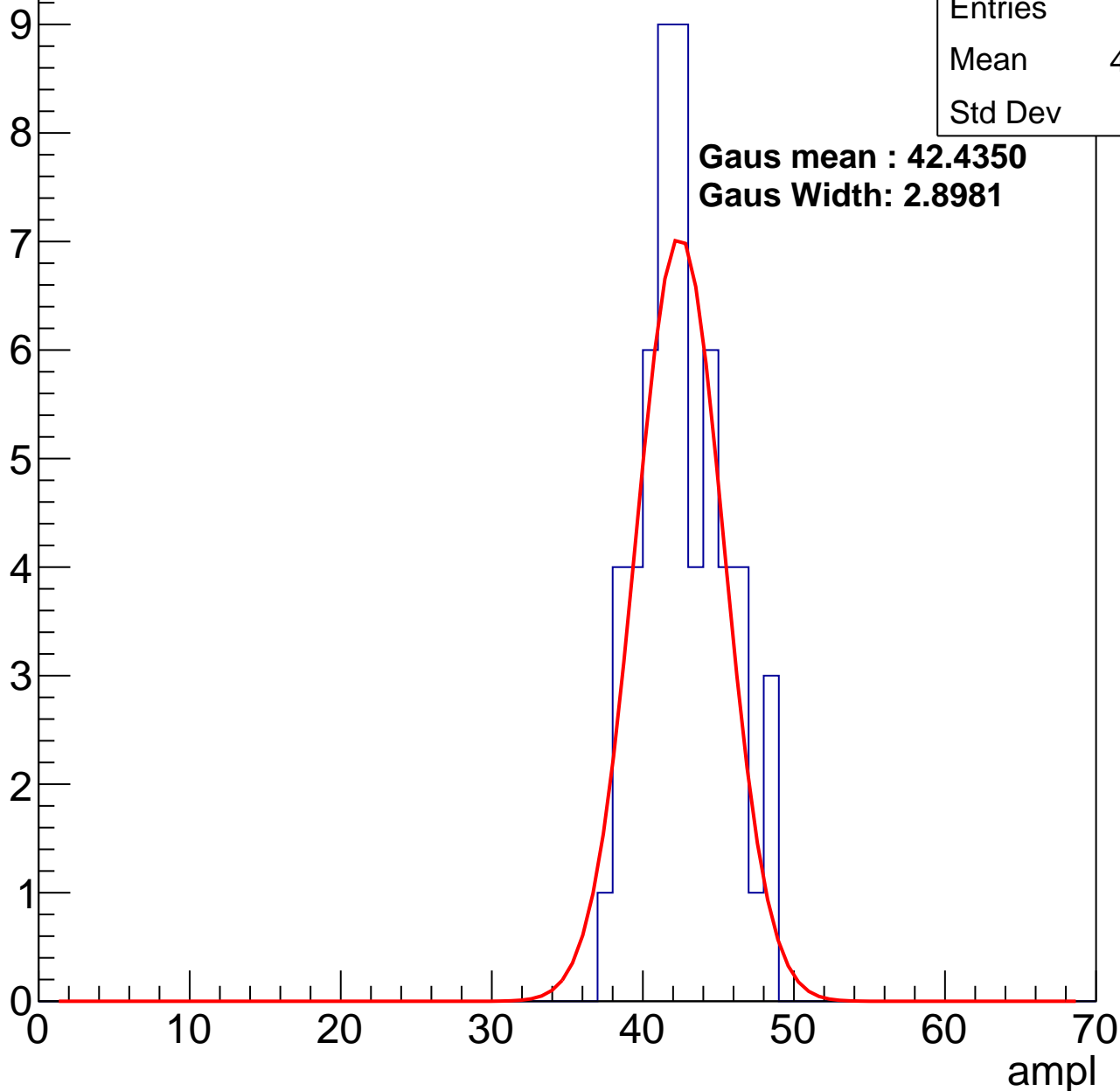
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	42.24
Std Dev	2.75

Gaus mean : 42.4350

Gaus Width: 2.8981

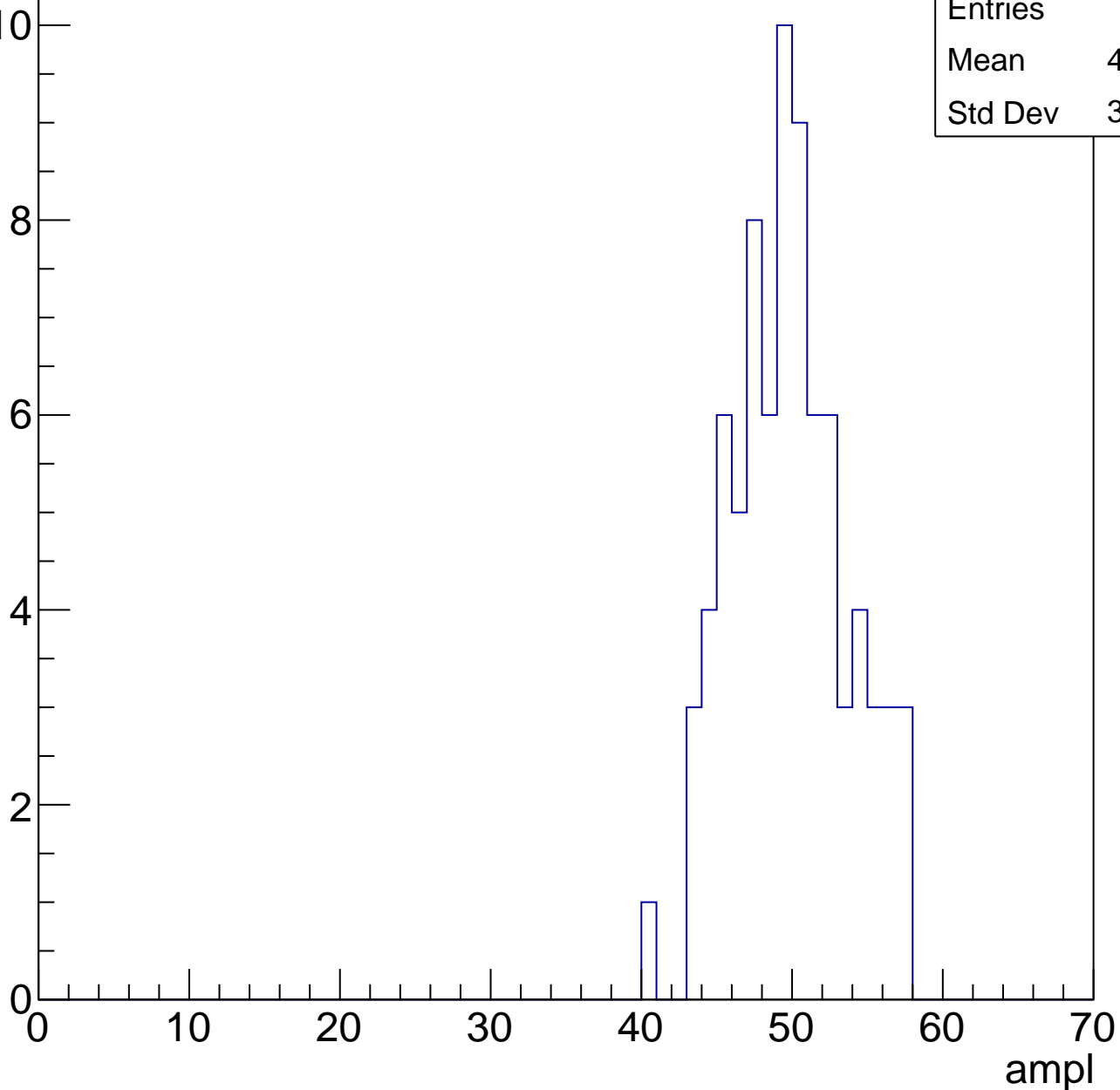


B0L002S, U18-ch103, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	80
Mean	49.33
Std Dev	3.777

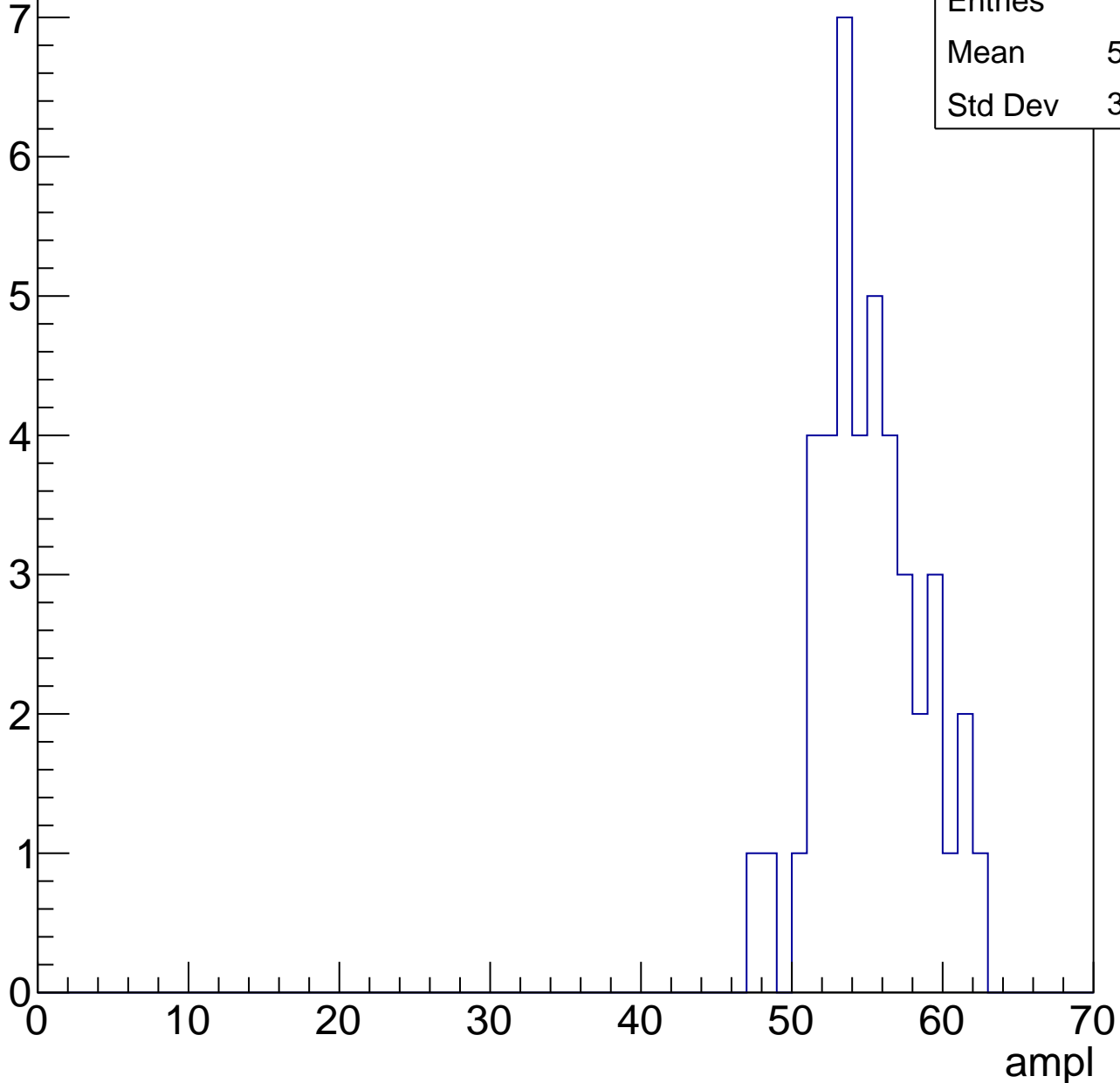


B0L002S, U18-ch103, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

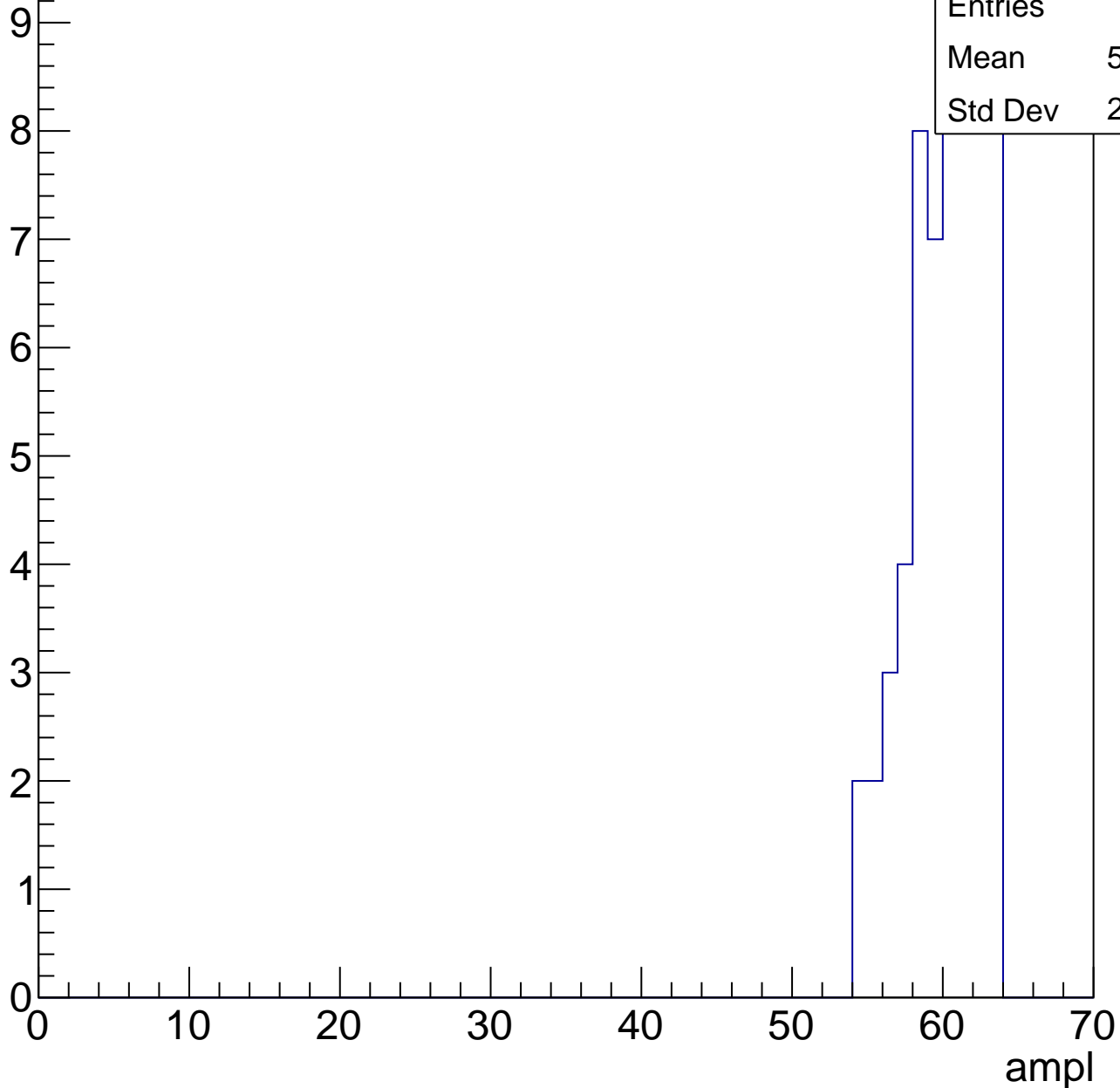
Entries	43
Mean	54.67
Std Dev	3.388



B0L002S, U18-ch103, adc5

calib_packv5_042523_0143.root, FC#8, port C1

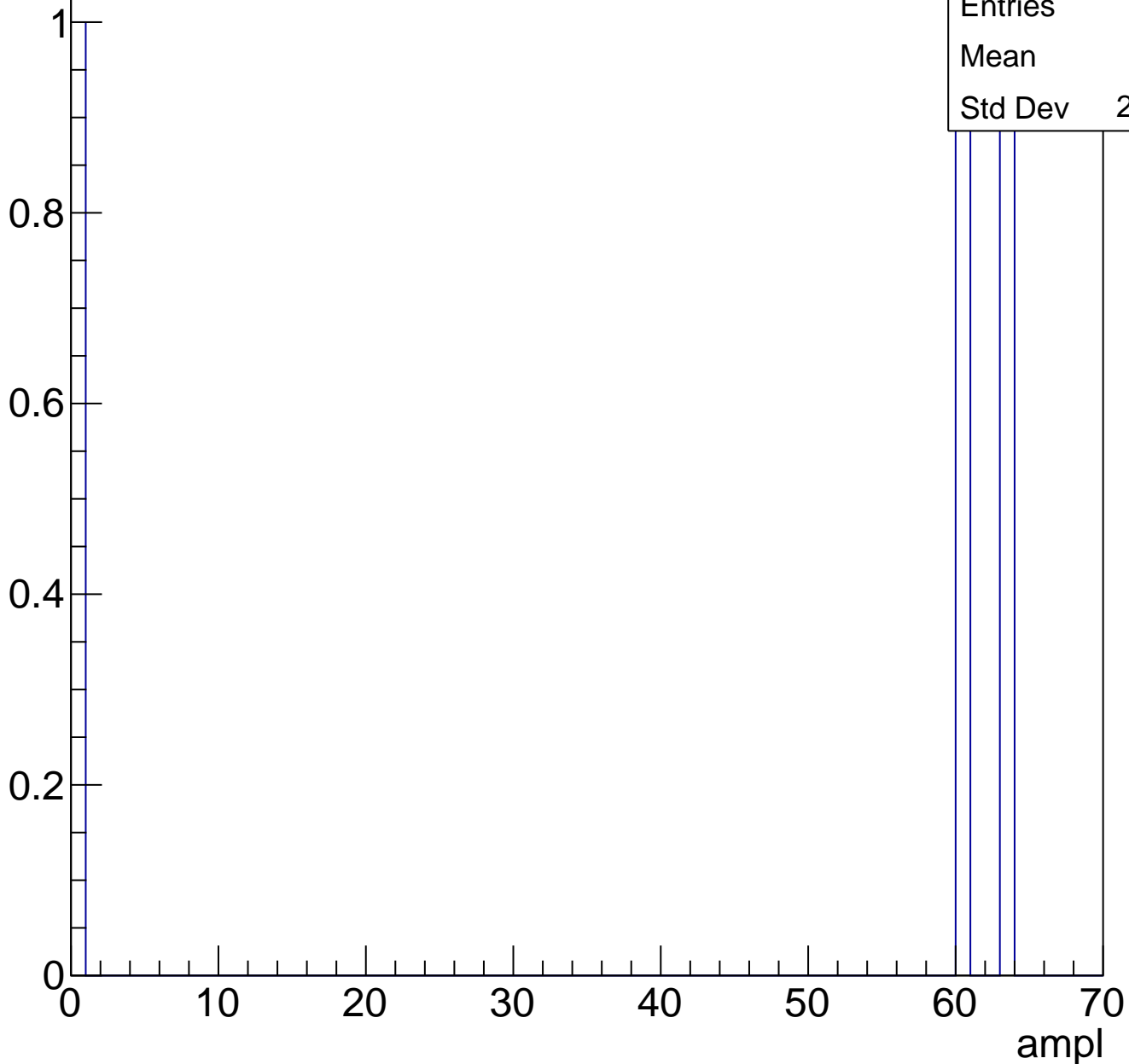
Entry



B0L002S, U18-ch103, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch103, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch104, adc0

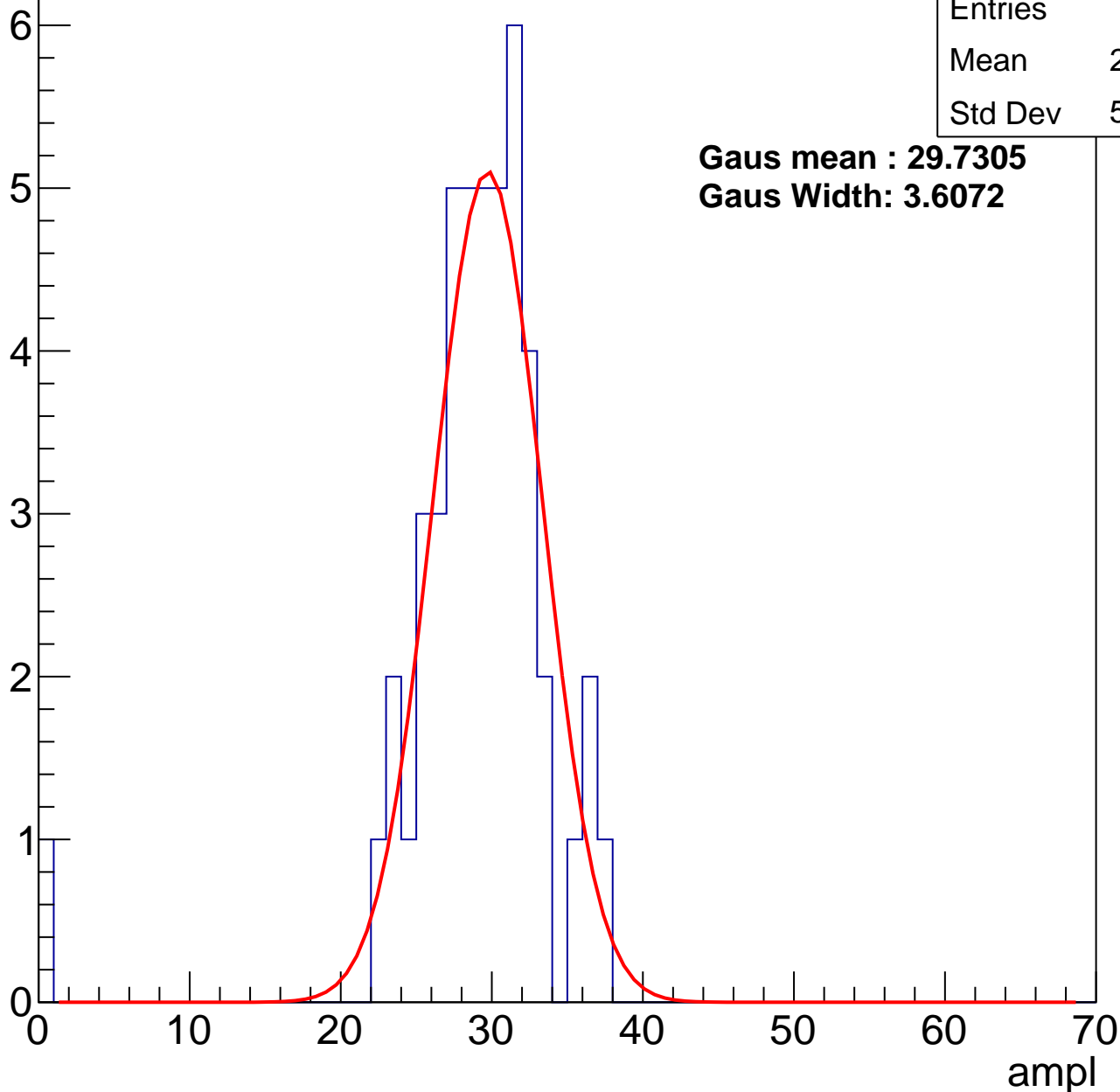
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	28.49
Std Dev	5.398

Gaus mean : 29.7305

Gaus Width: 3.6072



B0L002S, U18-ch104, adc1

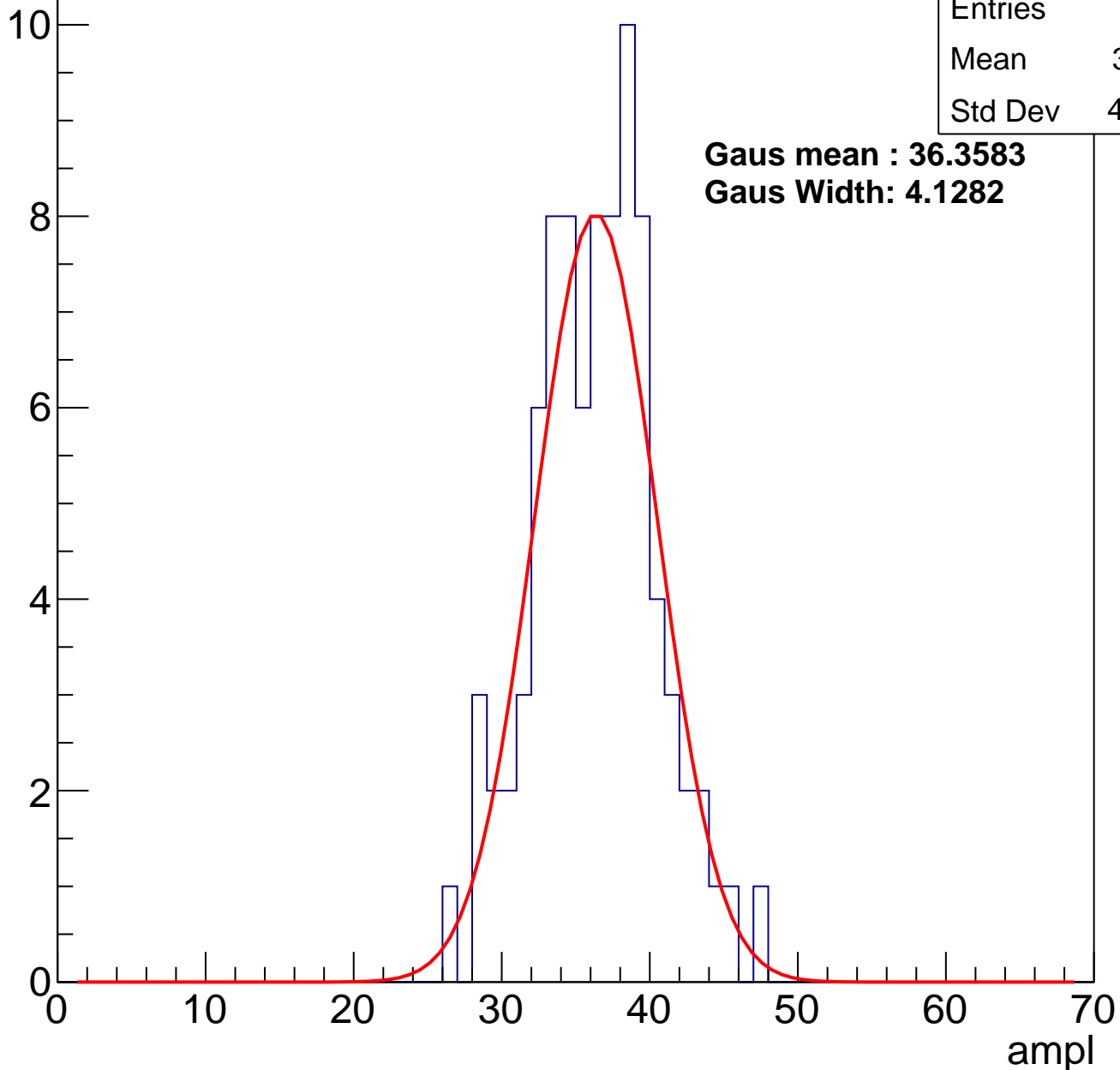
calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	35.91
Std Dev	4.065

Gaus mean : 36.3583

Gaus Width: 4.1282

Entry

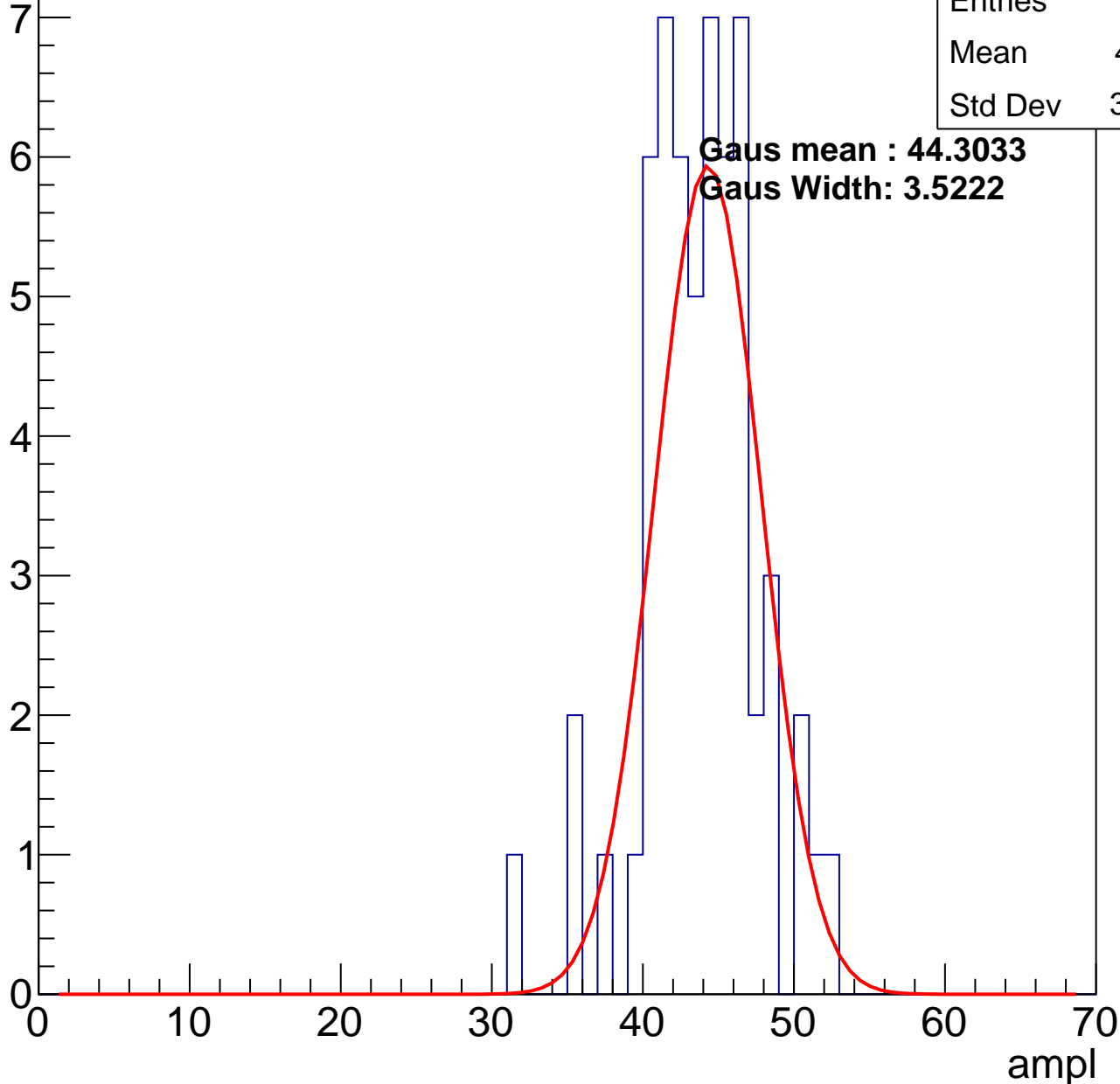


B0L002S, U18-ch104, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.31
Std Dev	3.829

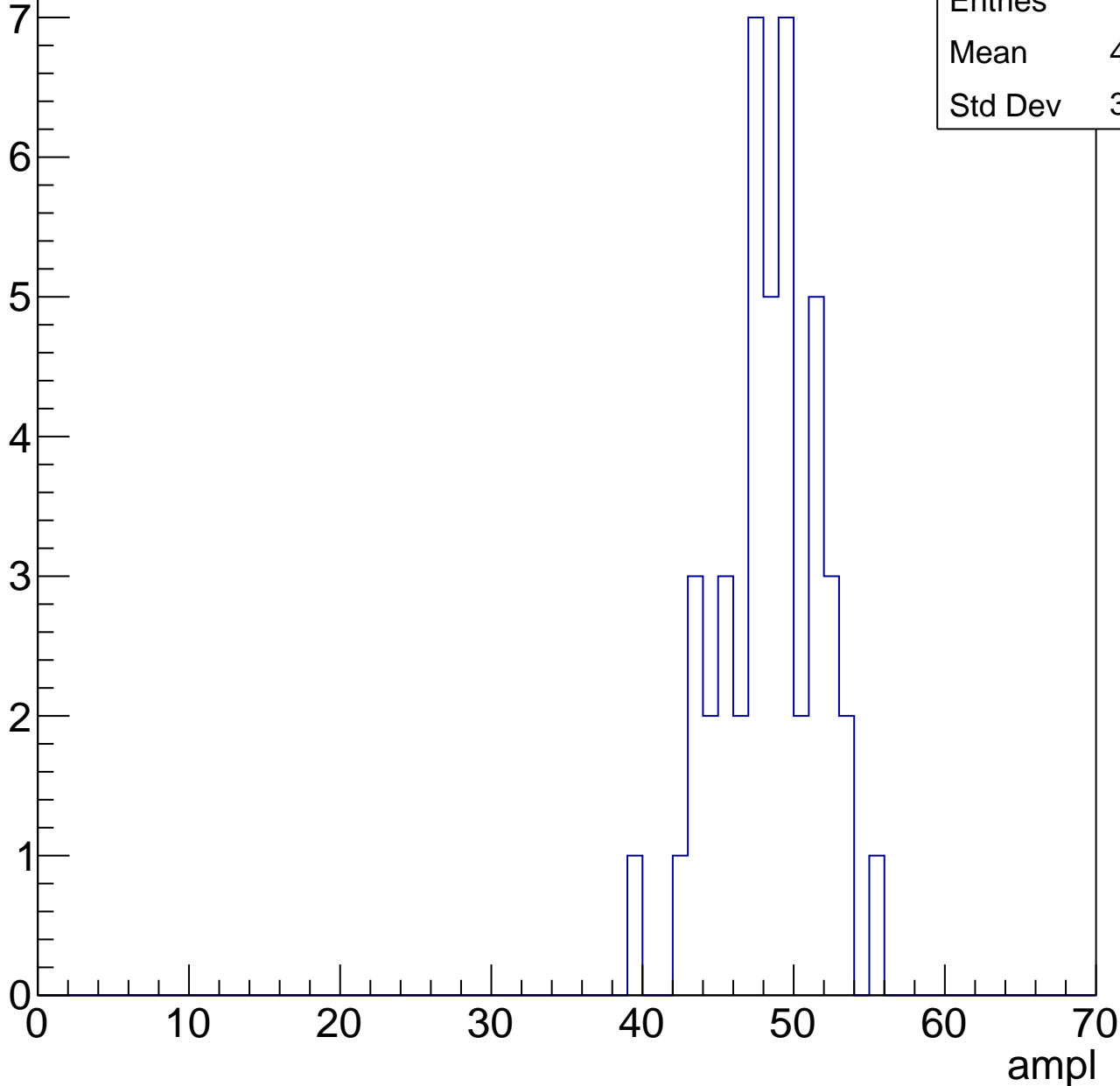


B0L002S, U18-ch104, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

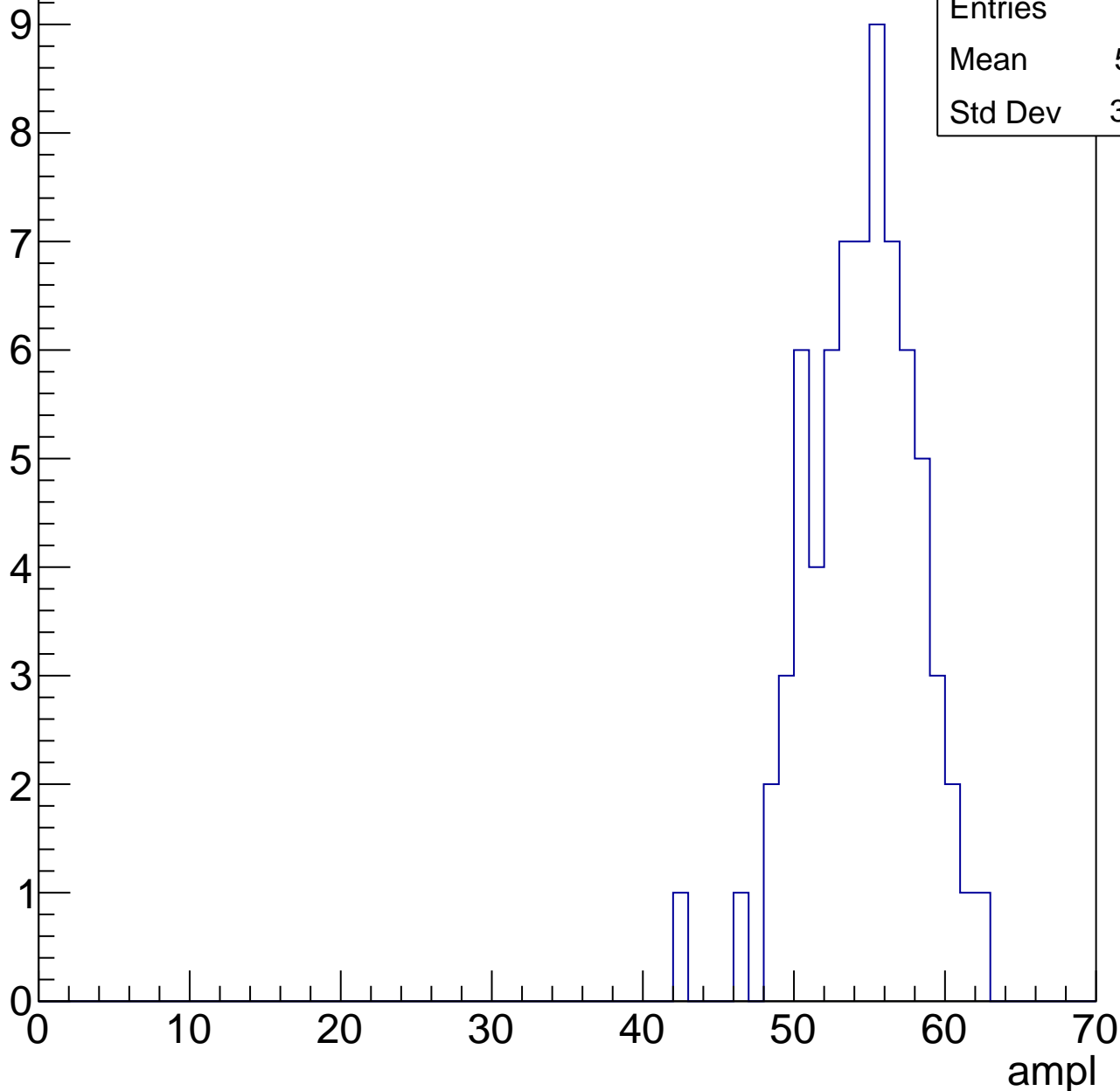
Entries	44
Mean	47.93
Std Dev	3.278



B0L002S, U18-ch104, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

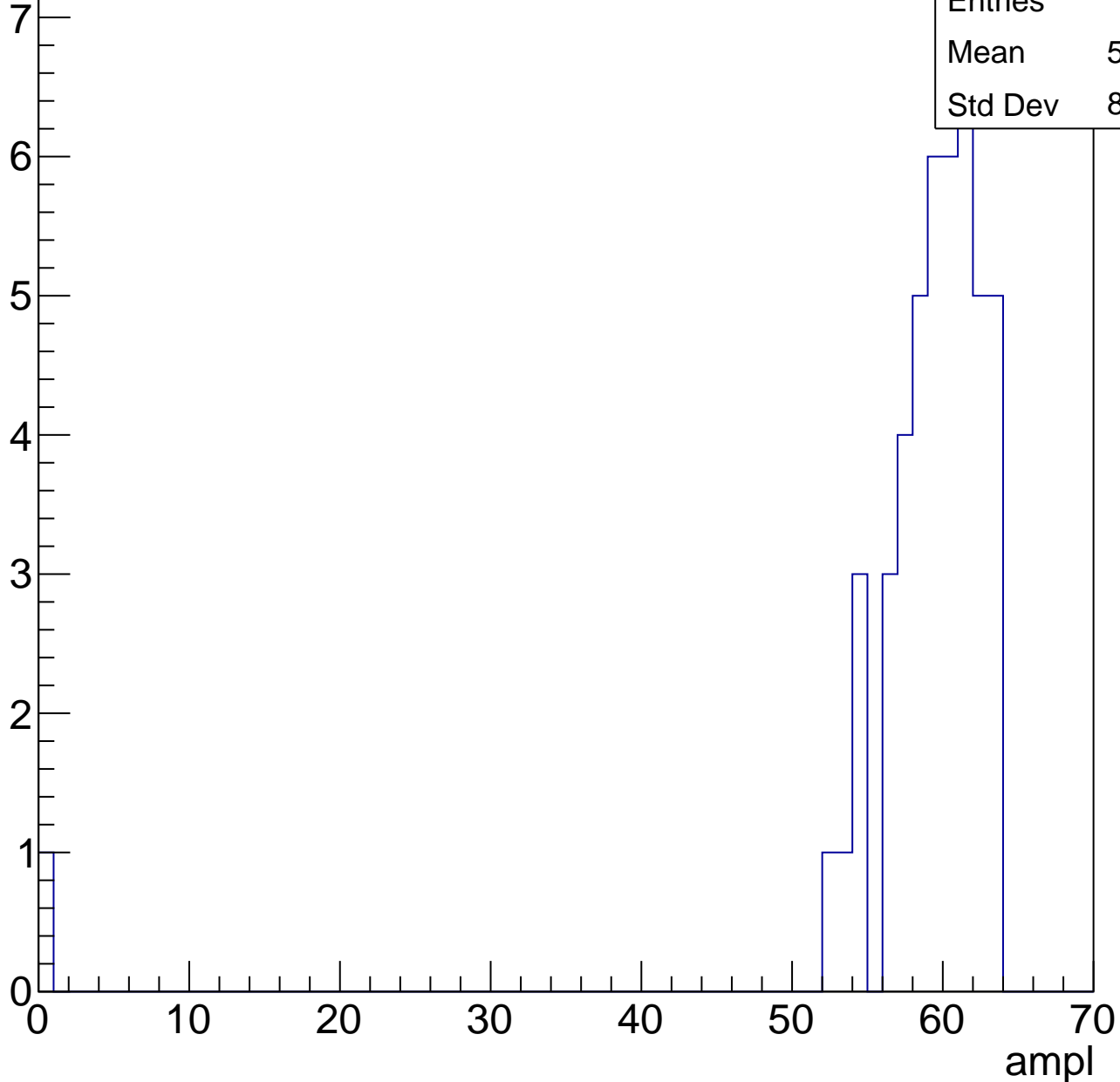


B0L002S, U18-ch104, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	57.85
Std Dev	8.975

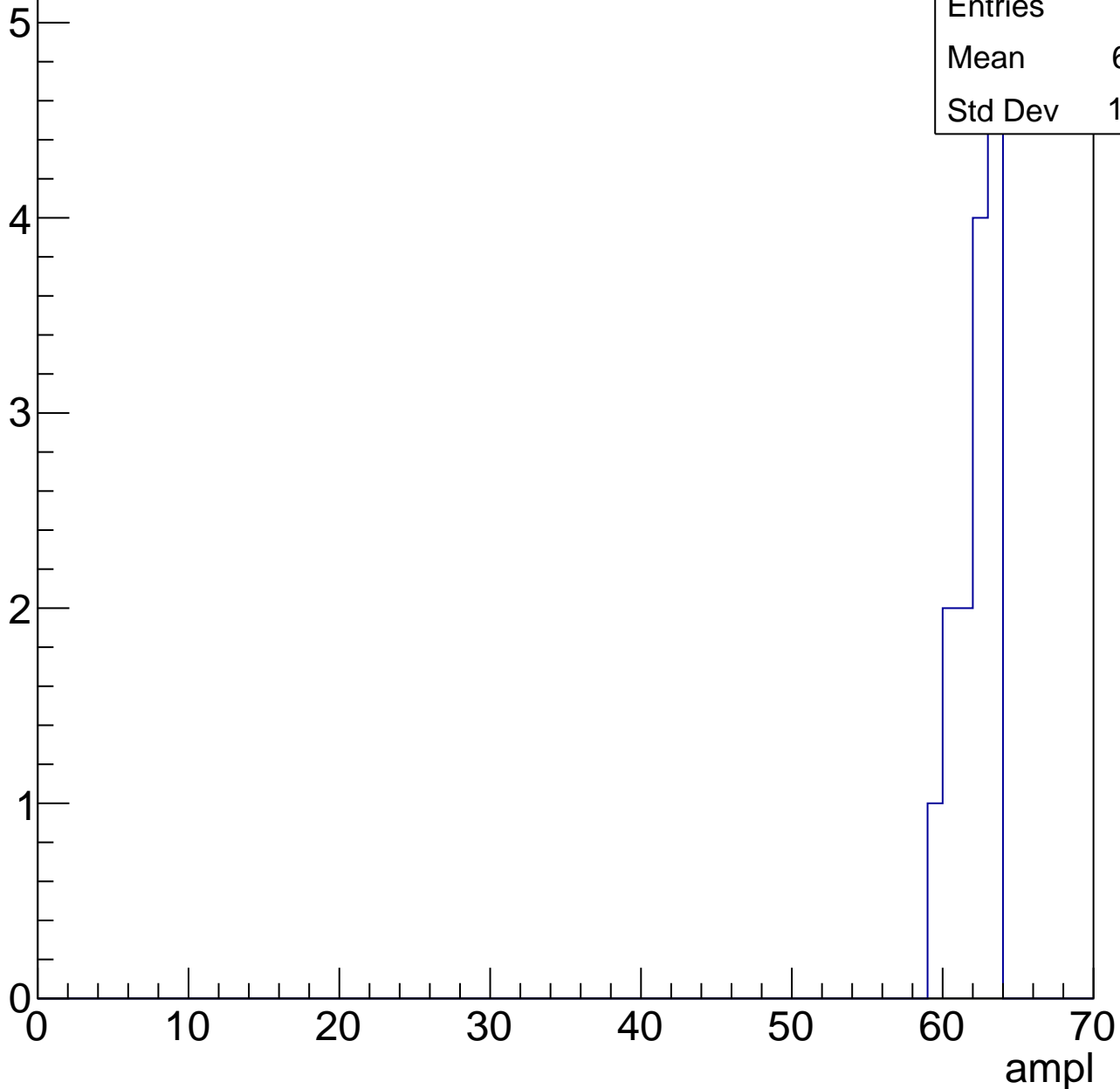


B0L002S, U18-ch104, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	14
Mean	61.71
Std Dev	1.278



B0L002S, U18-ch104, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch105, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	80
Mean	29.38
Std Dev	3.845

Gaus mean : 29.7732

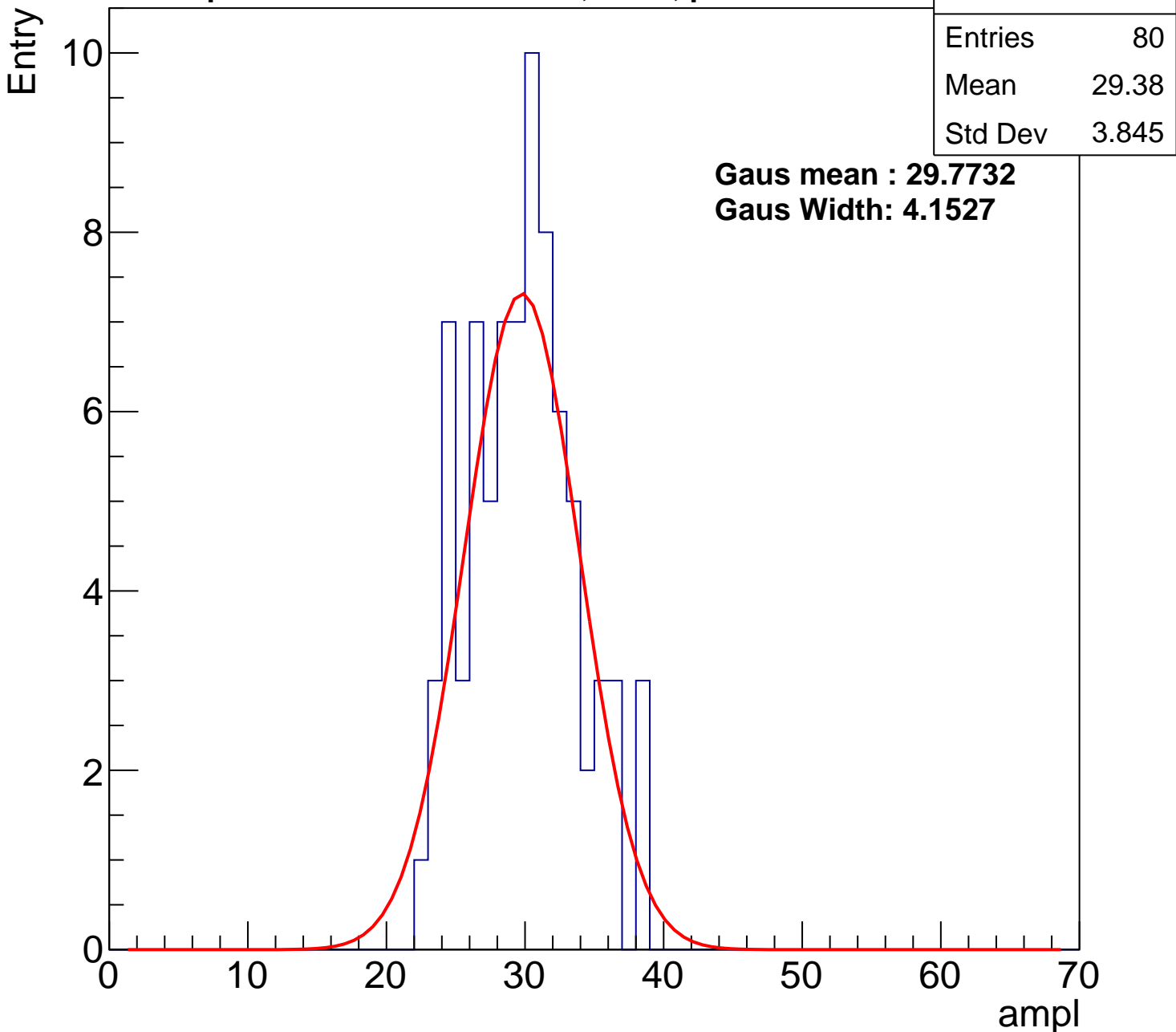
Gaus Width: 4.1527

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch105, adc1

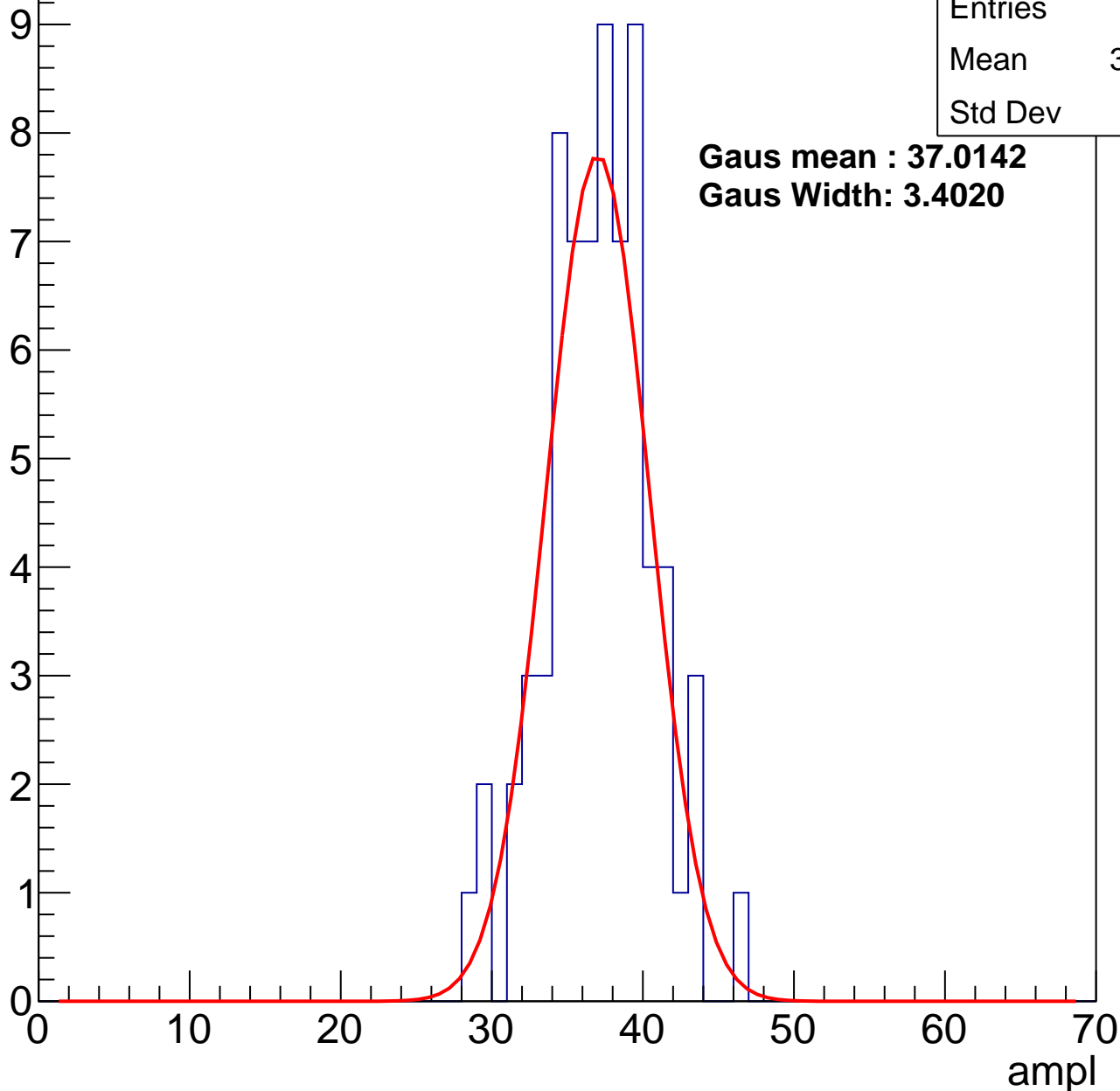
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	36.66
Std Dev	3.48

Gaus mean : 37.0142

Gaus Width: 3.4020



B0L002S, U18-ch105, adc2

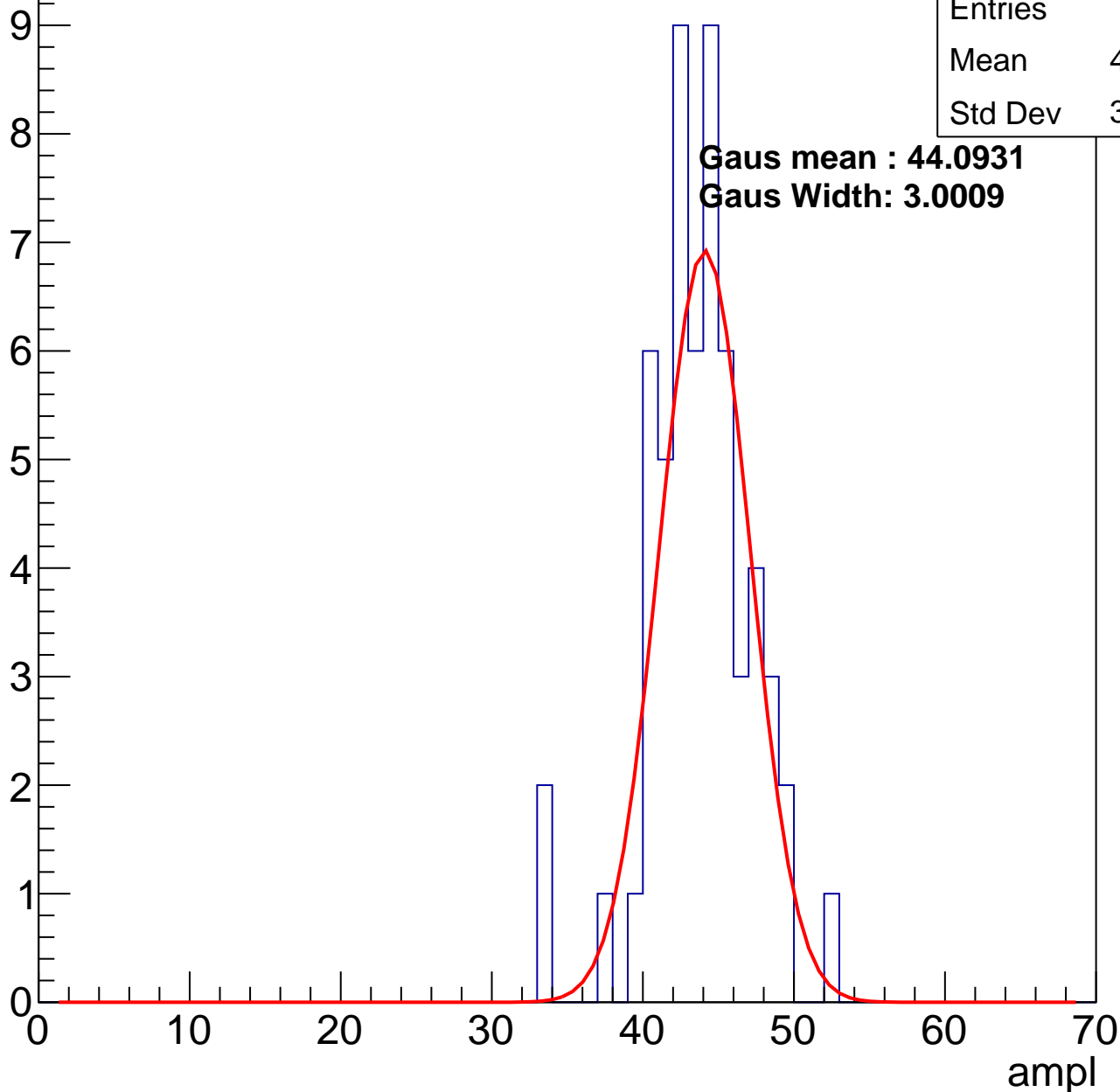
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.26
Std Dev	3.432

Gaus mean : 44.0931

Gaus Width: 3.0009

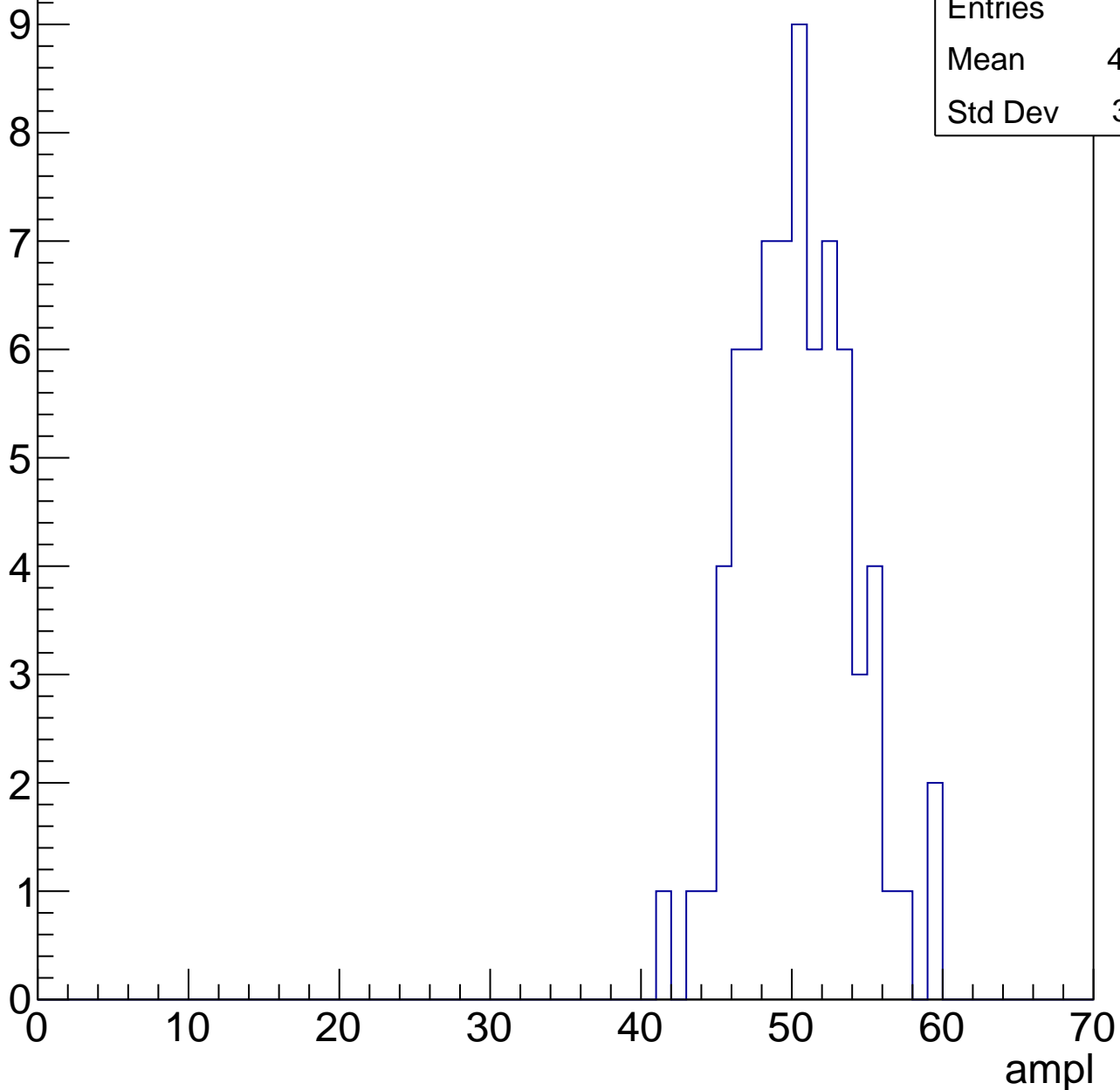


B0L002S, U18-ch105, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	49.94
Std Dev	3.601

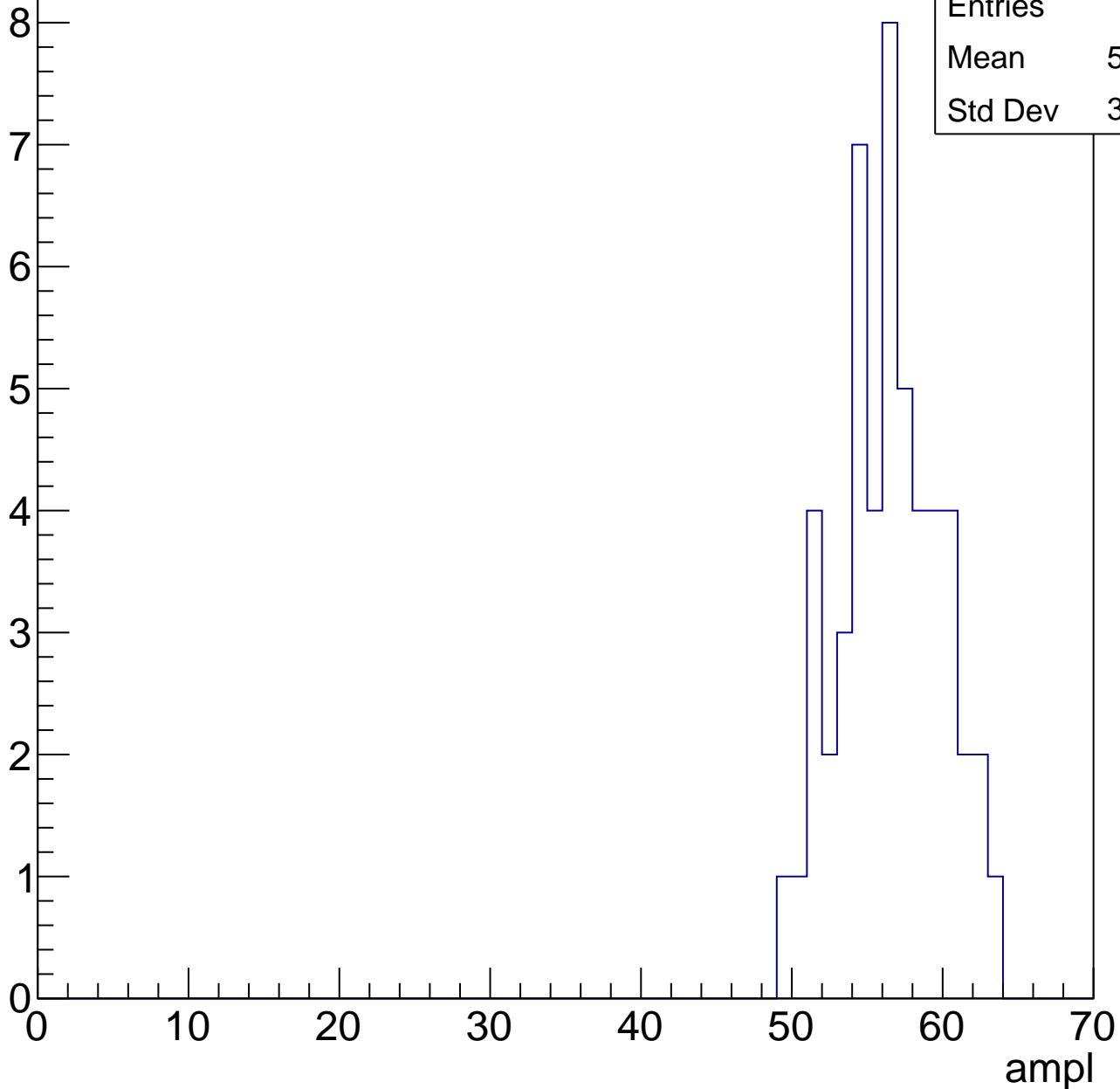


B0L002S, U18-ch105, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

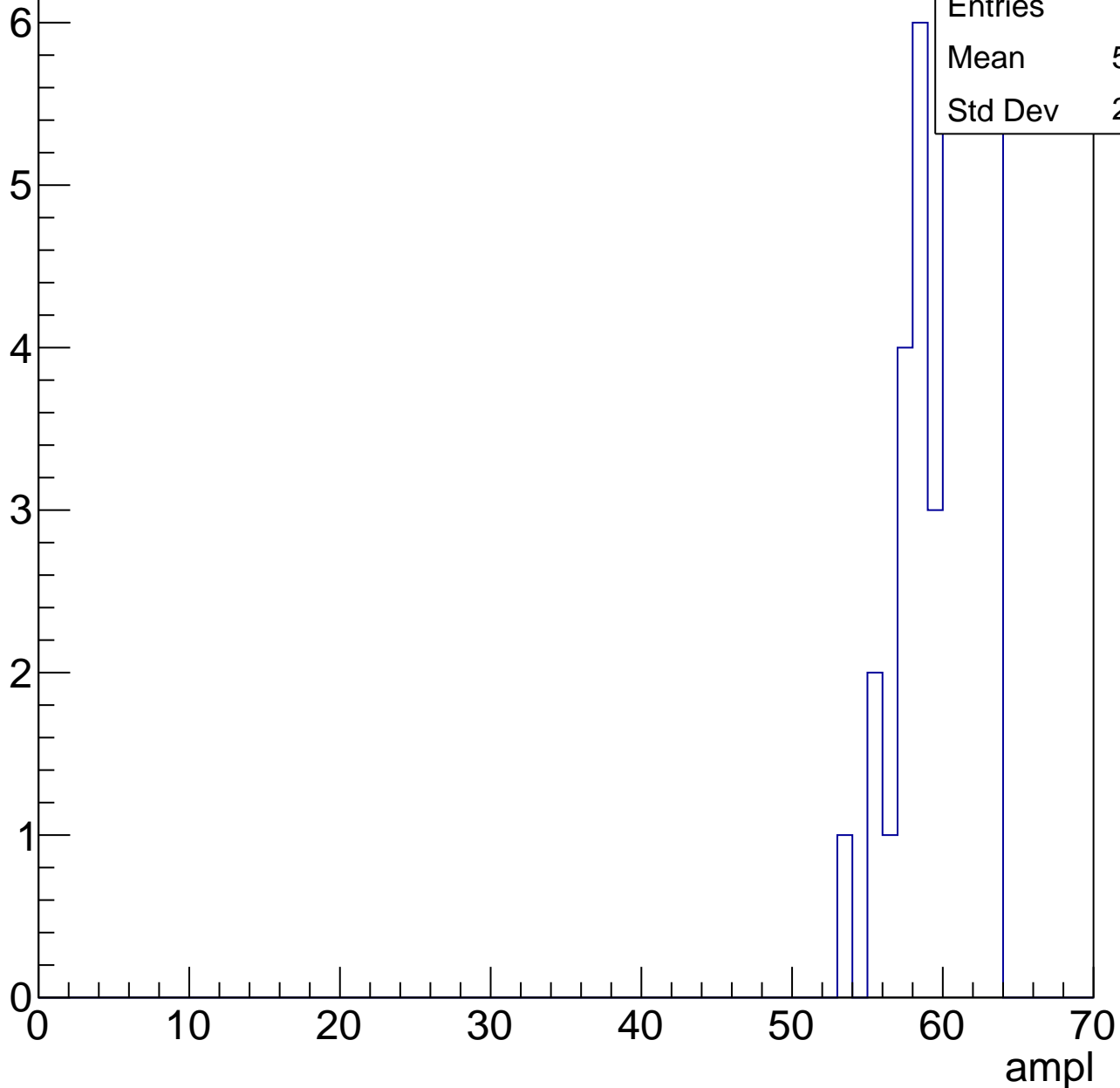
Entries	52
Mean	56.04
Std Dev	3.305



B0L002S, U18-ch105, adc5

calib_packv5_042523_0143.root, FC#8, port C1

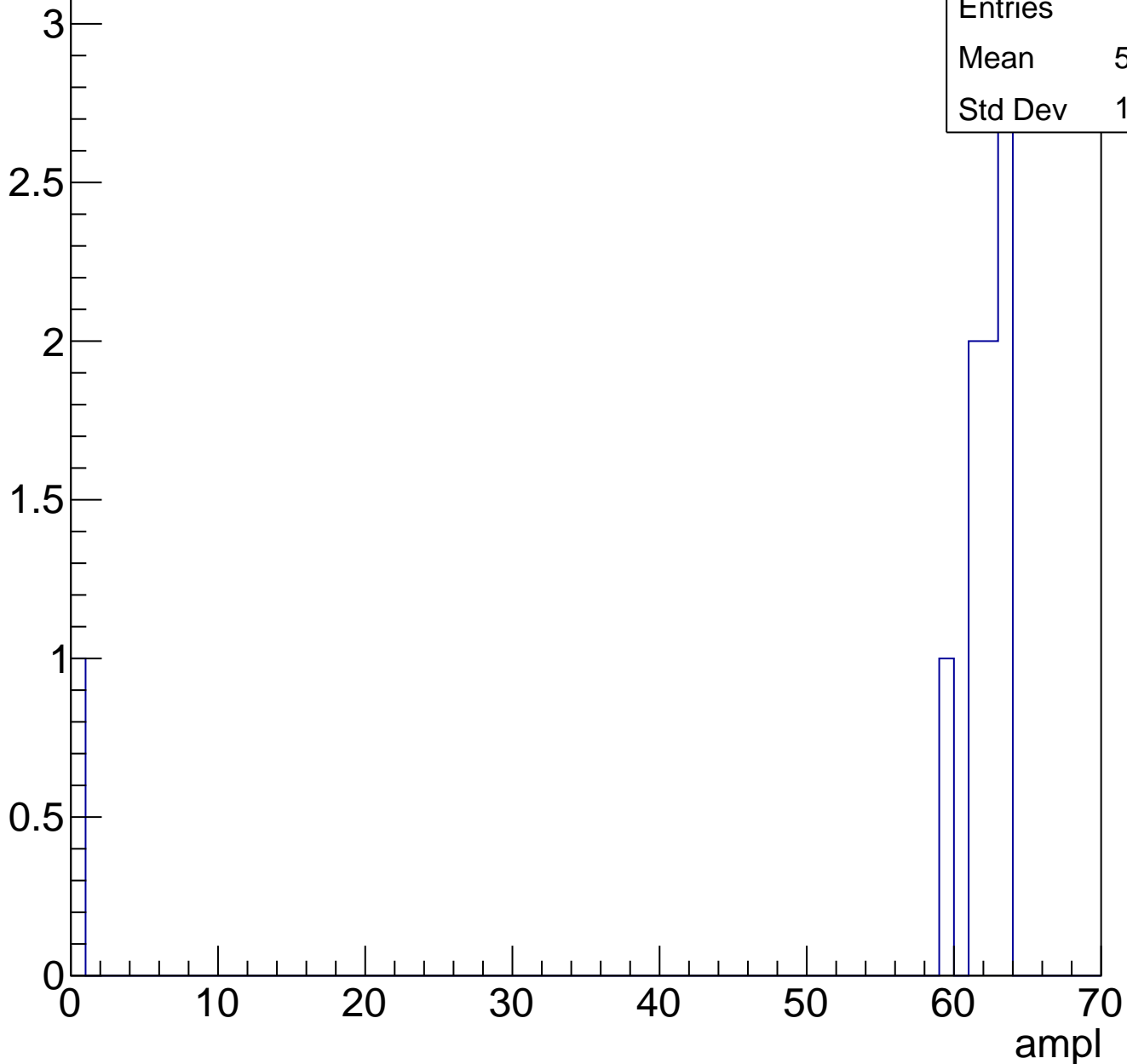
Entry



B0L002S, U18-ch105, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	9
Mean	54.89
Std Dev	19.44

B0L002S, U18-ch105, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch106, adc0

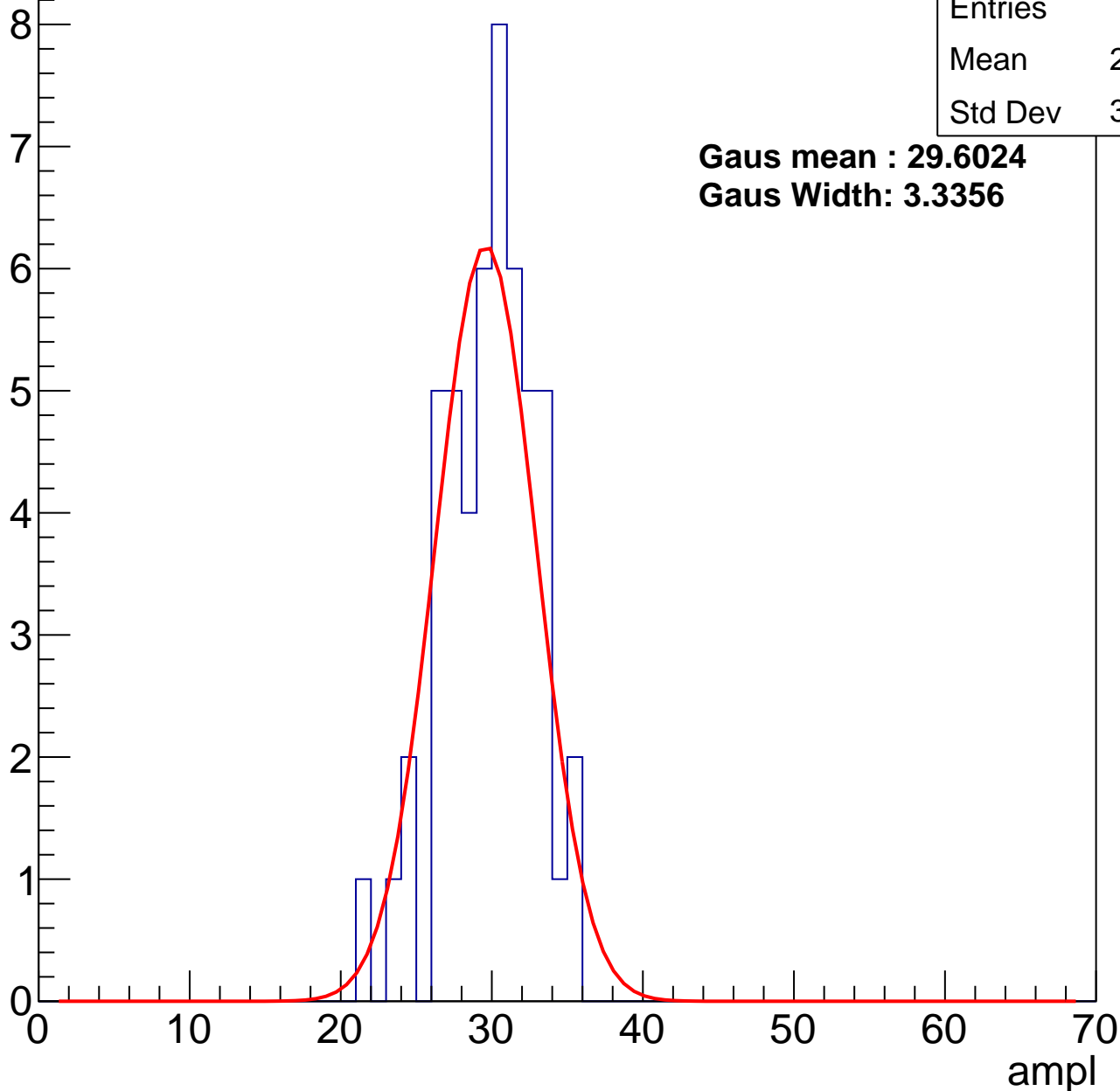
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	29.37
Std Dev	3.023

Gaus mean : 29.6024

Gaus Width: 3.3356



B0L002S, U18-ch106, adc1

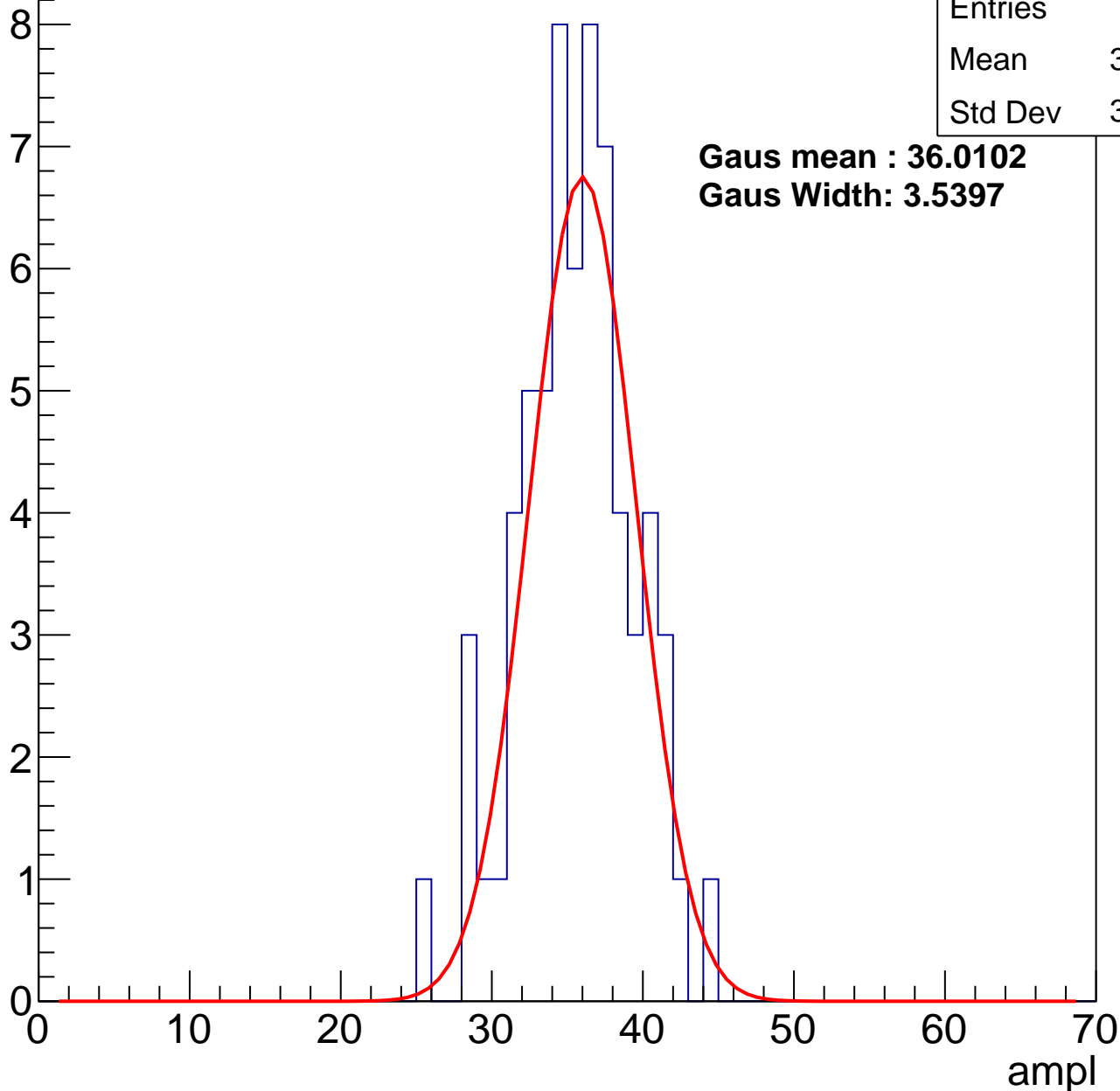
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	35.14
Std Dev	3.725

Gaus mean : 36.0102

Gaus Width: 3.5397



B0L002S, U18-ch106, adc2

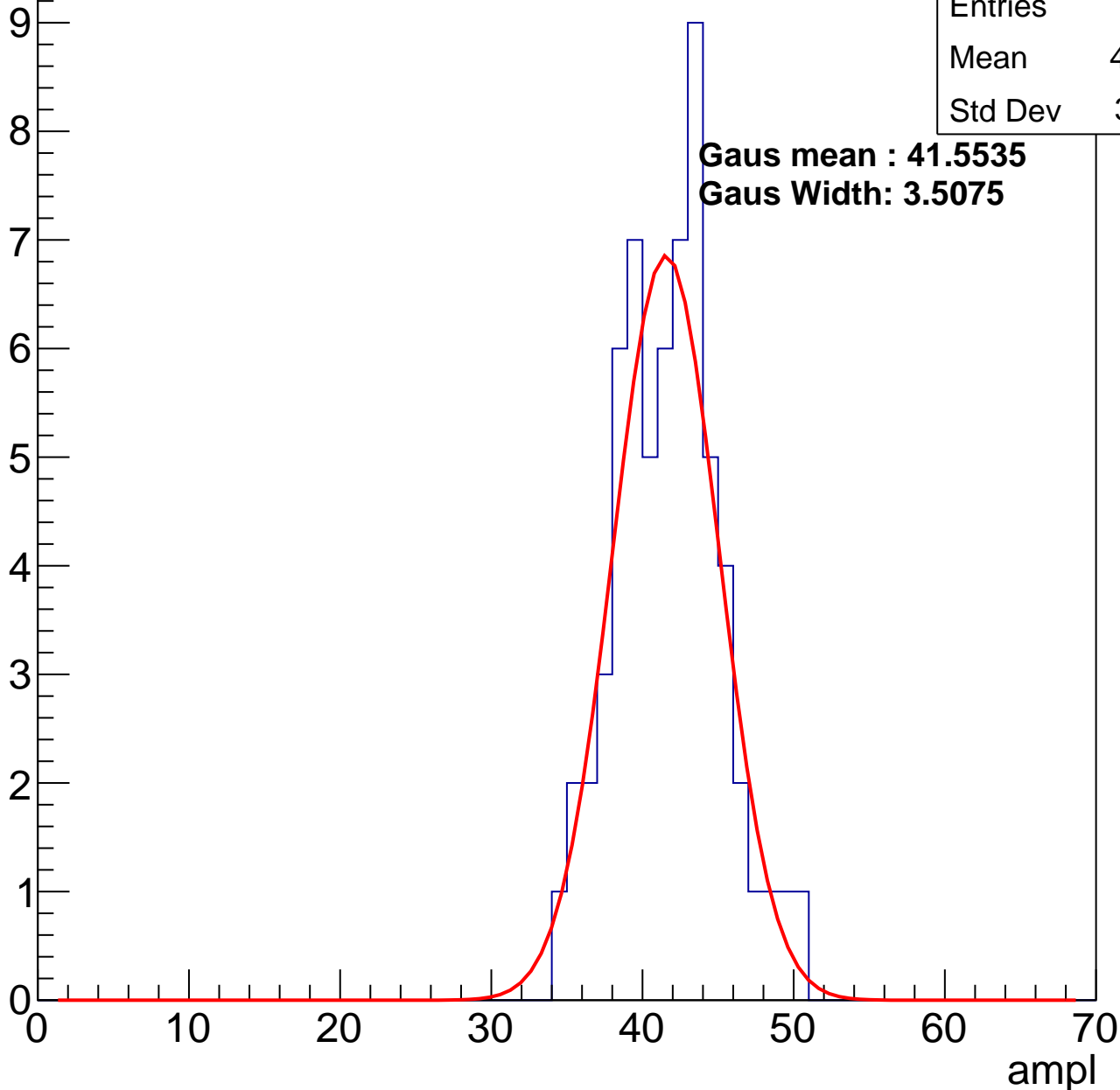
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	41.29
Std Dev	3.411

Gaus mean : 41.5535

Gaus Width: 3.5075

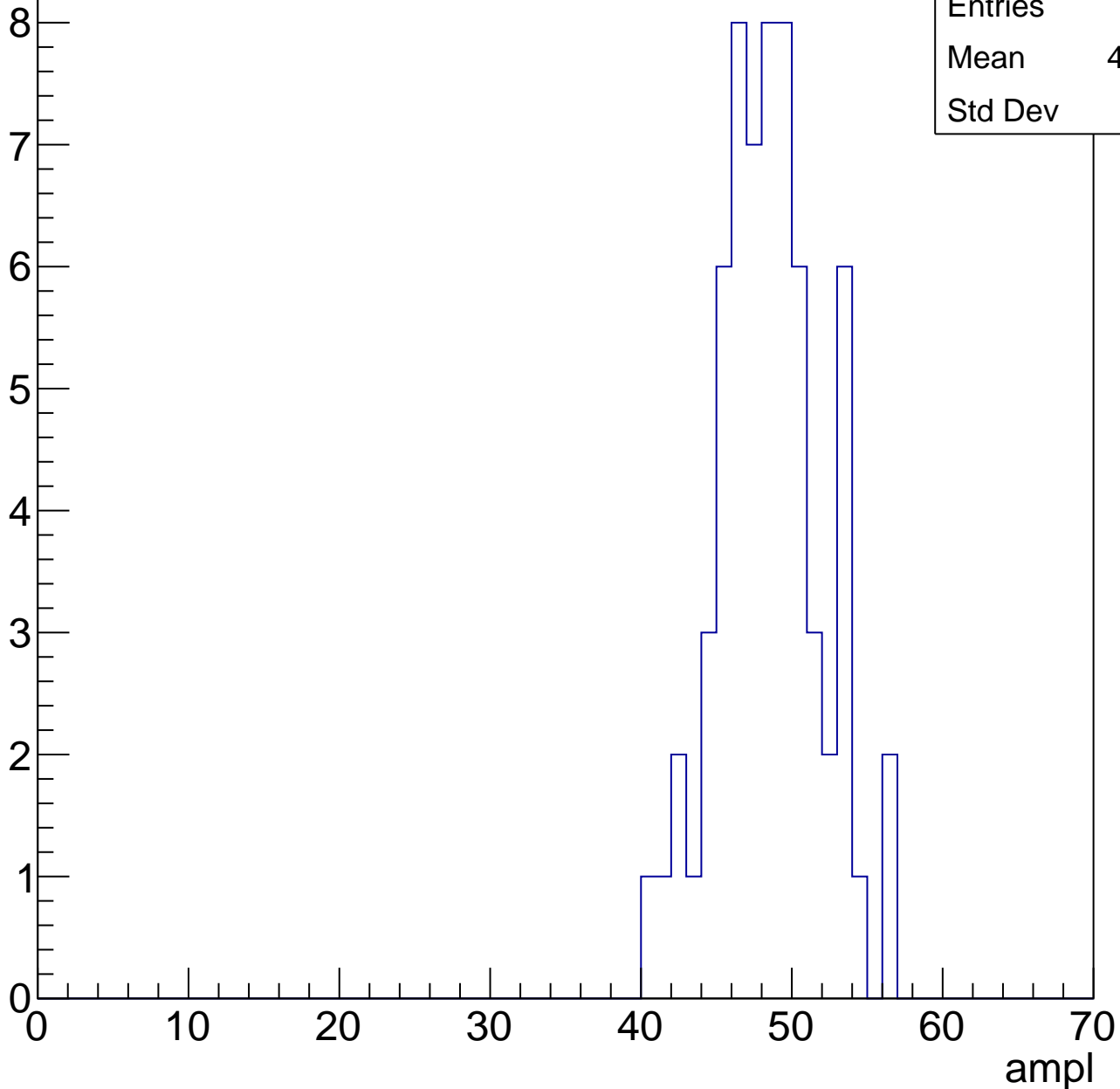


B0L002S, U18-ch106, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	48.06
Std Dev	3.41

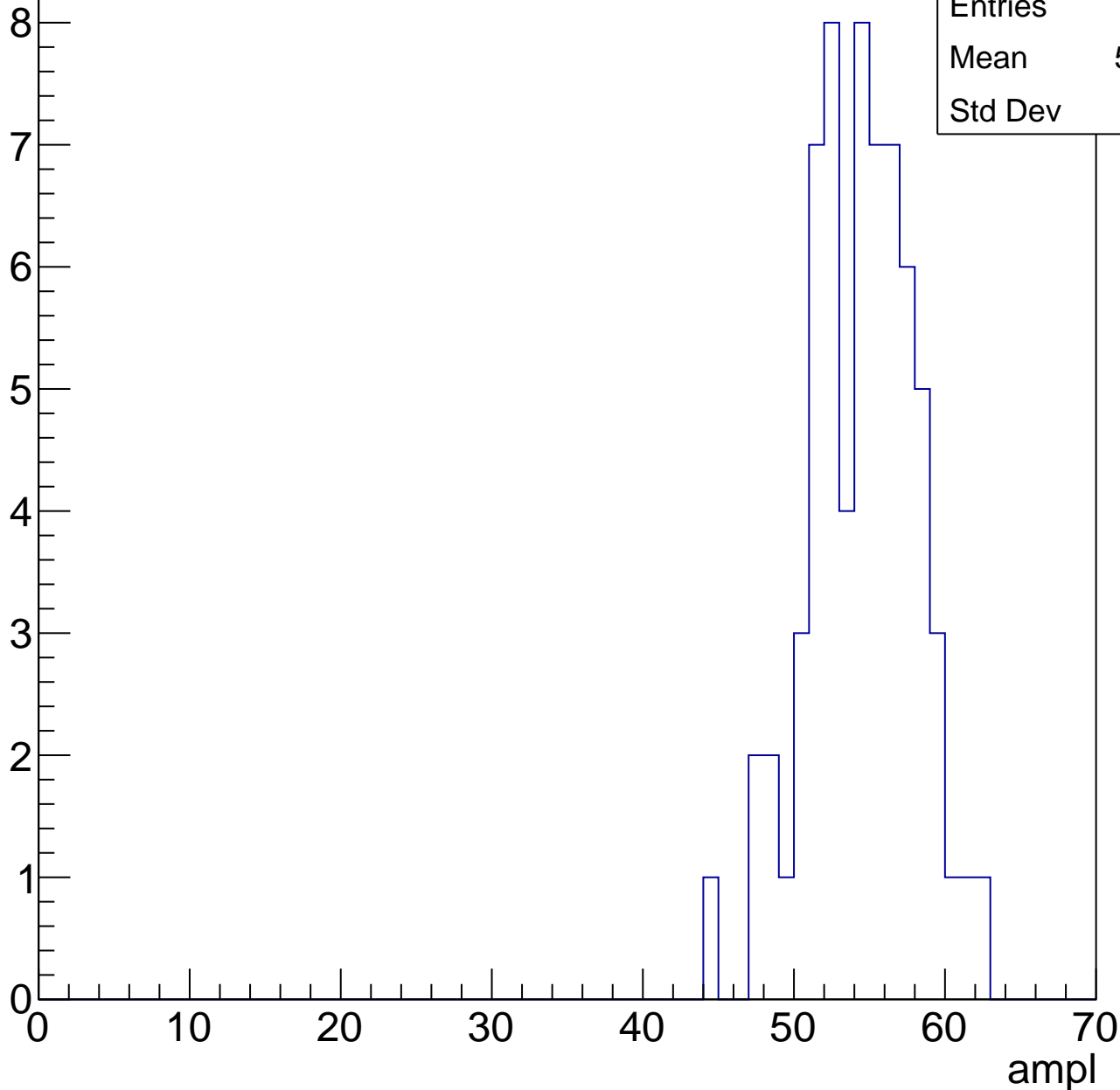


B0L002S, U18-ch106, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

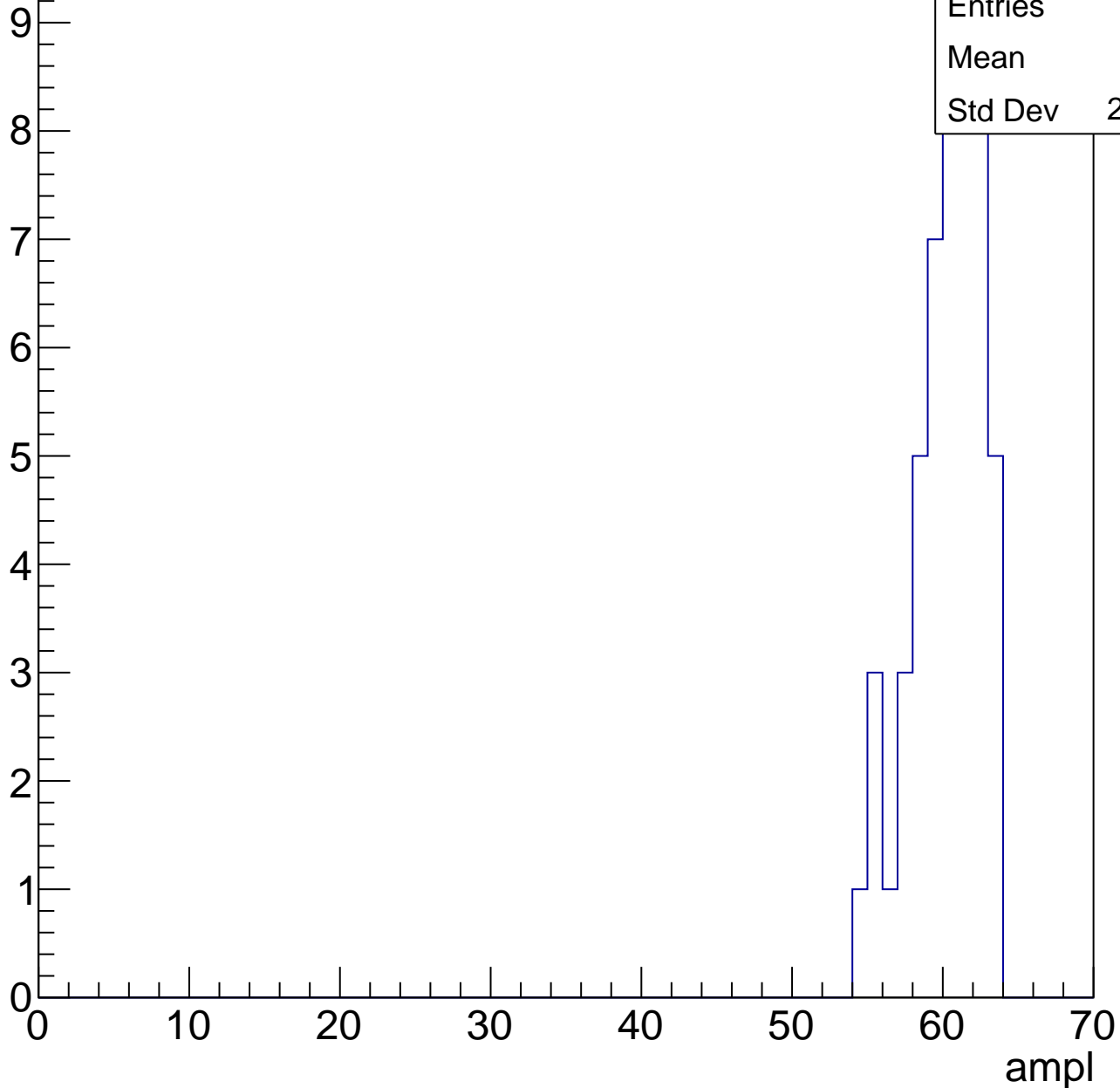
Entries	67
Mean	54.01
Std Dev	3.53



B0L002S, U18-ch106, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

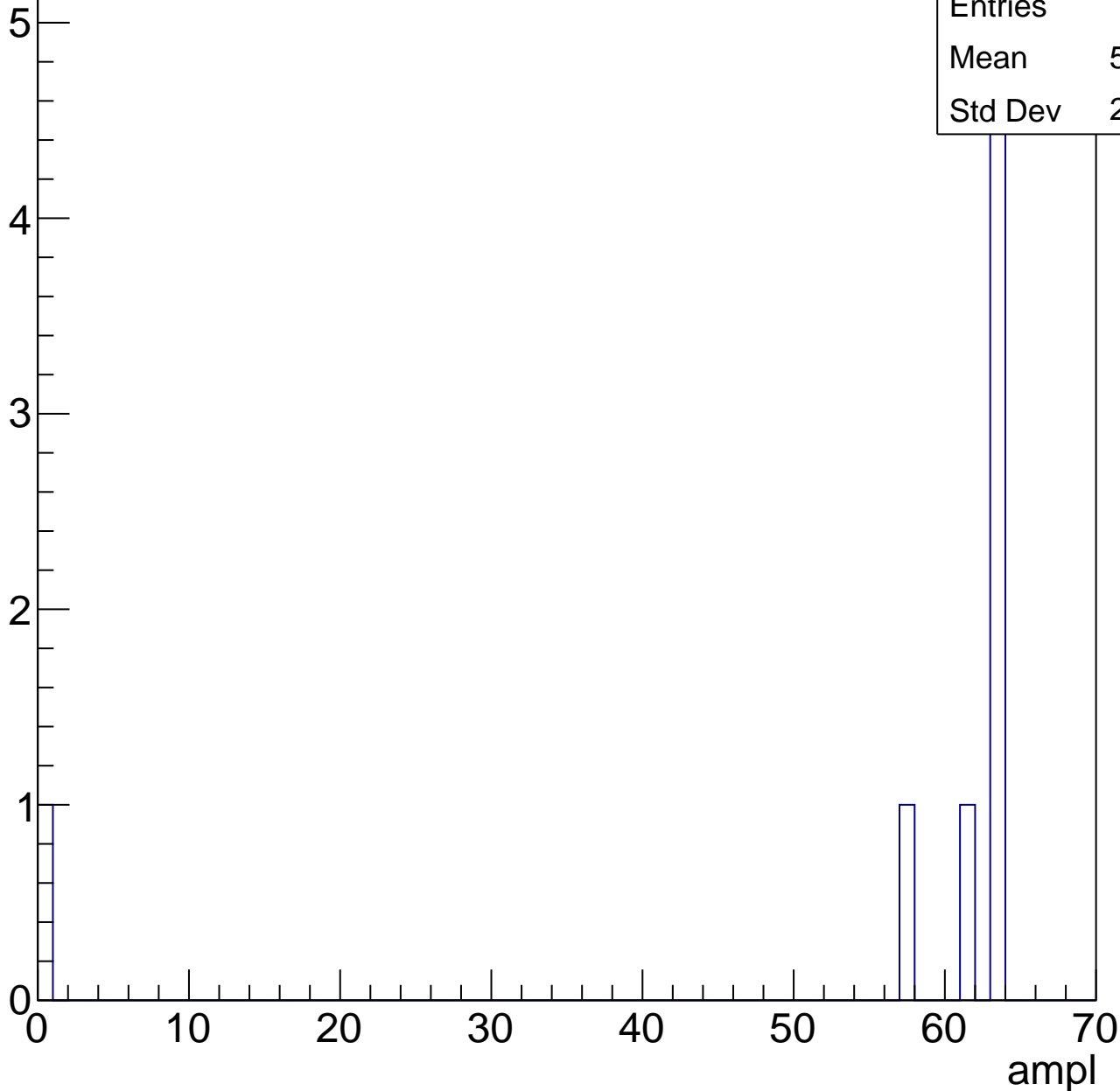


B0L002S, U18-ch106, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	8
Mean	54.12
Std Dev	20.55



B0L002S, U18-ch106, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch107, adc0

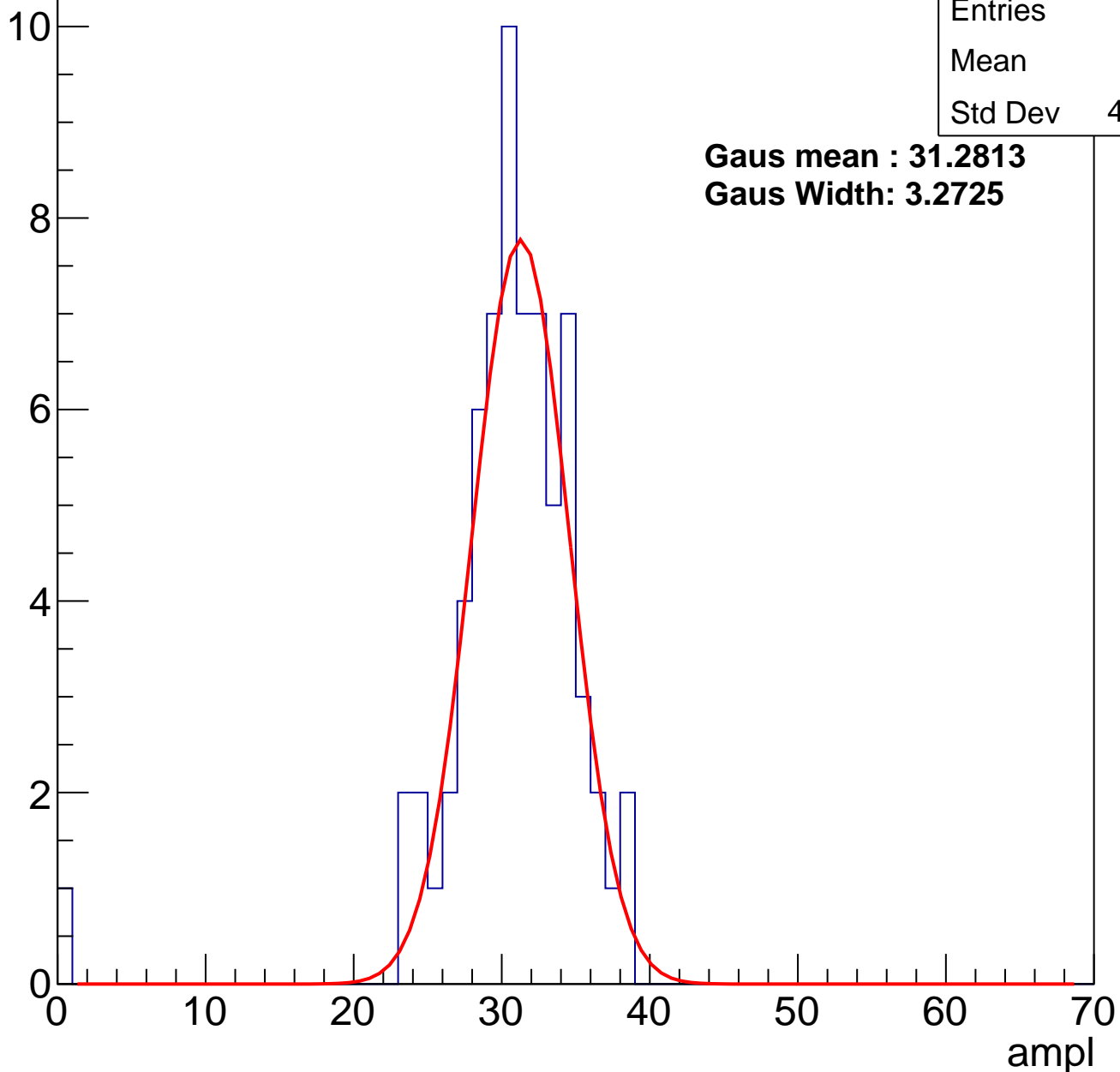
calib_packv5_042523_0143.root, FC#8, port C1

Entries	69
Mean	30.2
Std Dev	4.974

Gaus mean : 31.2813

Gaus Width: 3.2725

Entry



B0L002S, U18-ch107, adc1

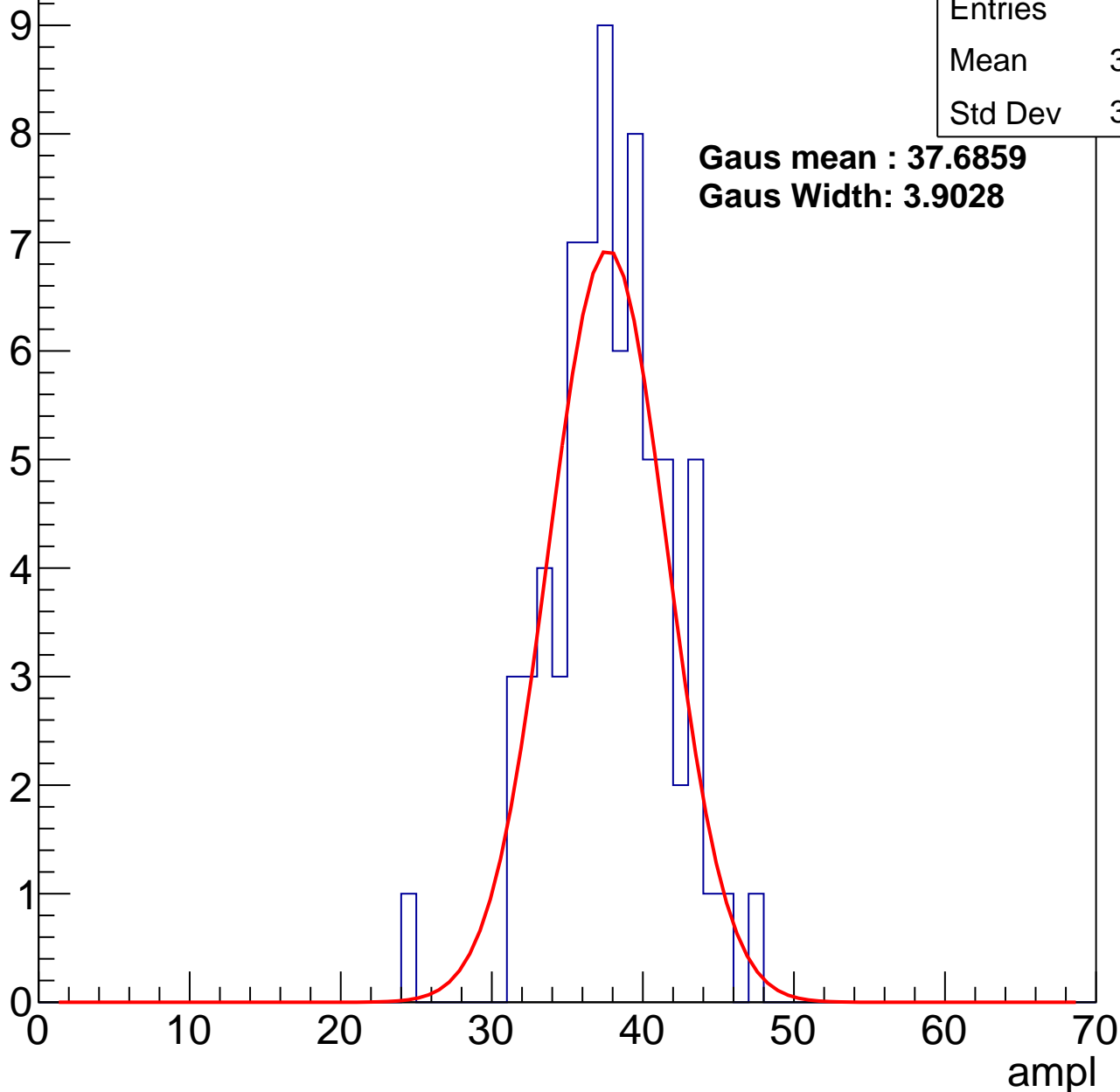
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	37.42
Std Dev	3.884

Gaus mean : 37.6859

Gaus Width: 3.9028



B0L002S, U18-ch107, adc2

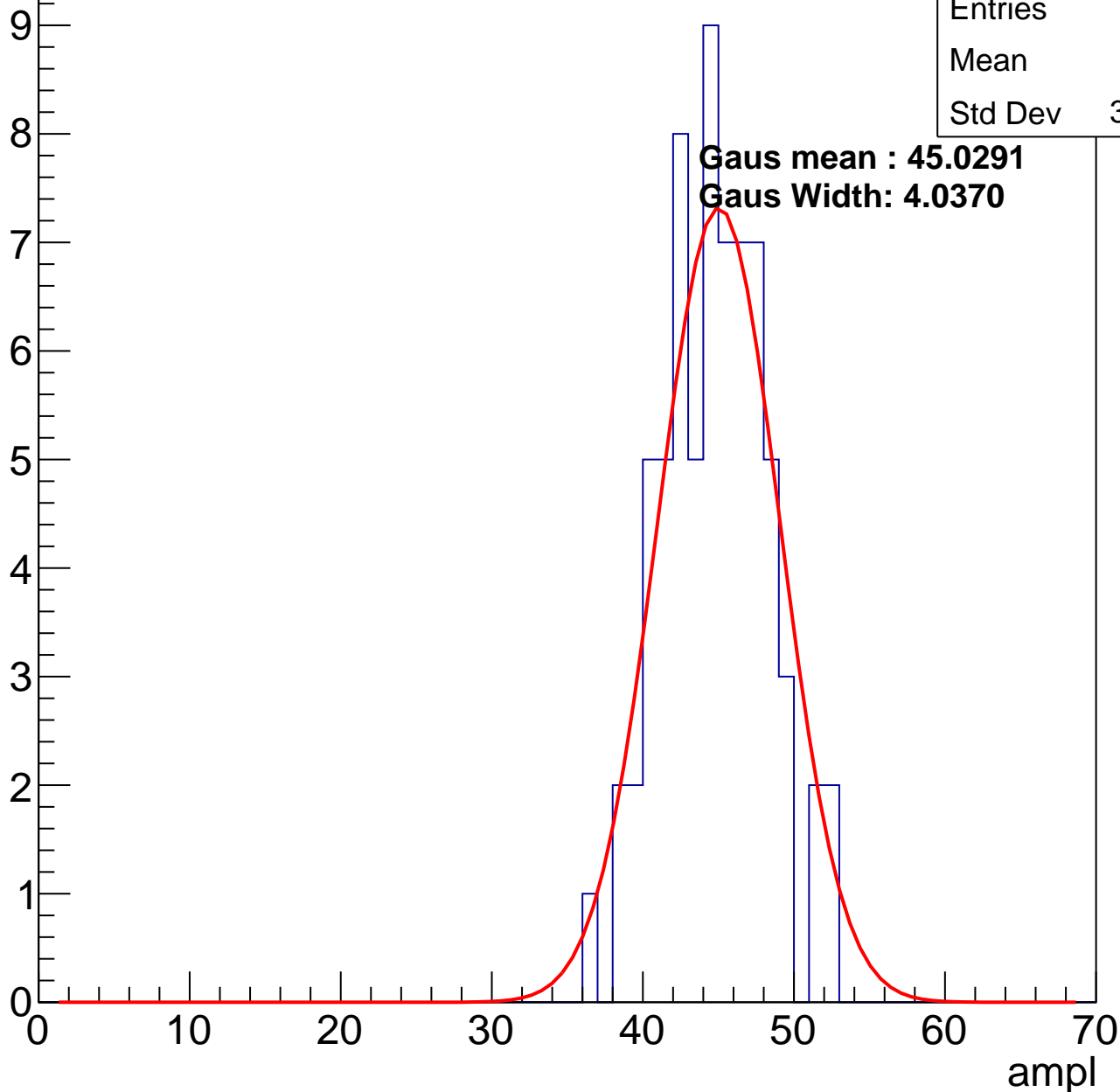
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	44.3
Std Dev	3.424

Gaus mean : 45.0291

Gaus Width: 4.0370

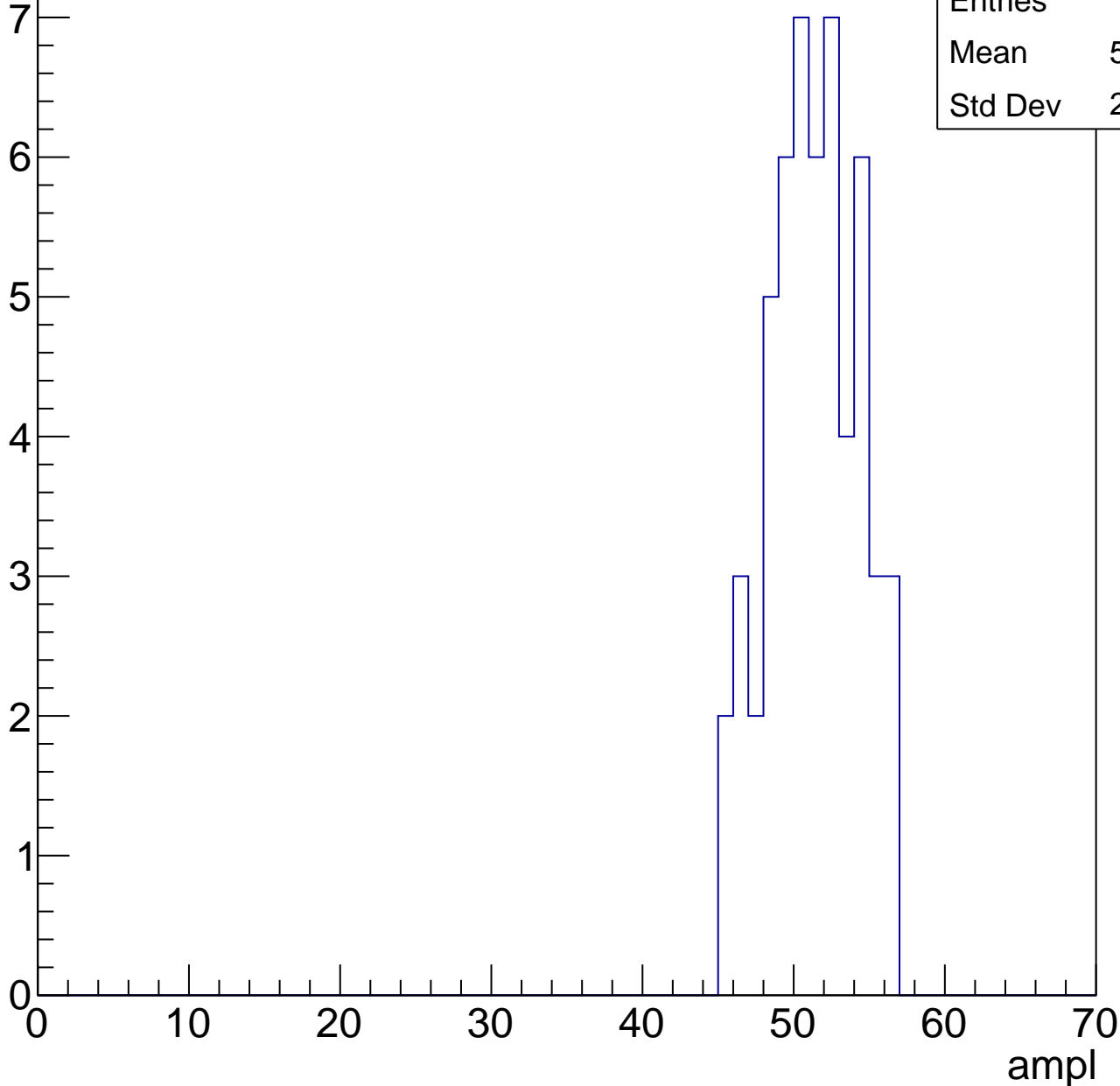


B0L002S, U18-ch107, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	50.83
Std Dev	2.898

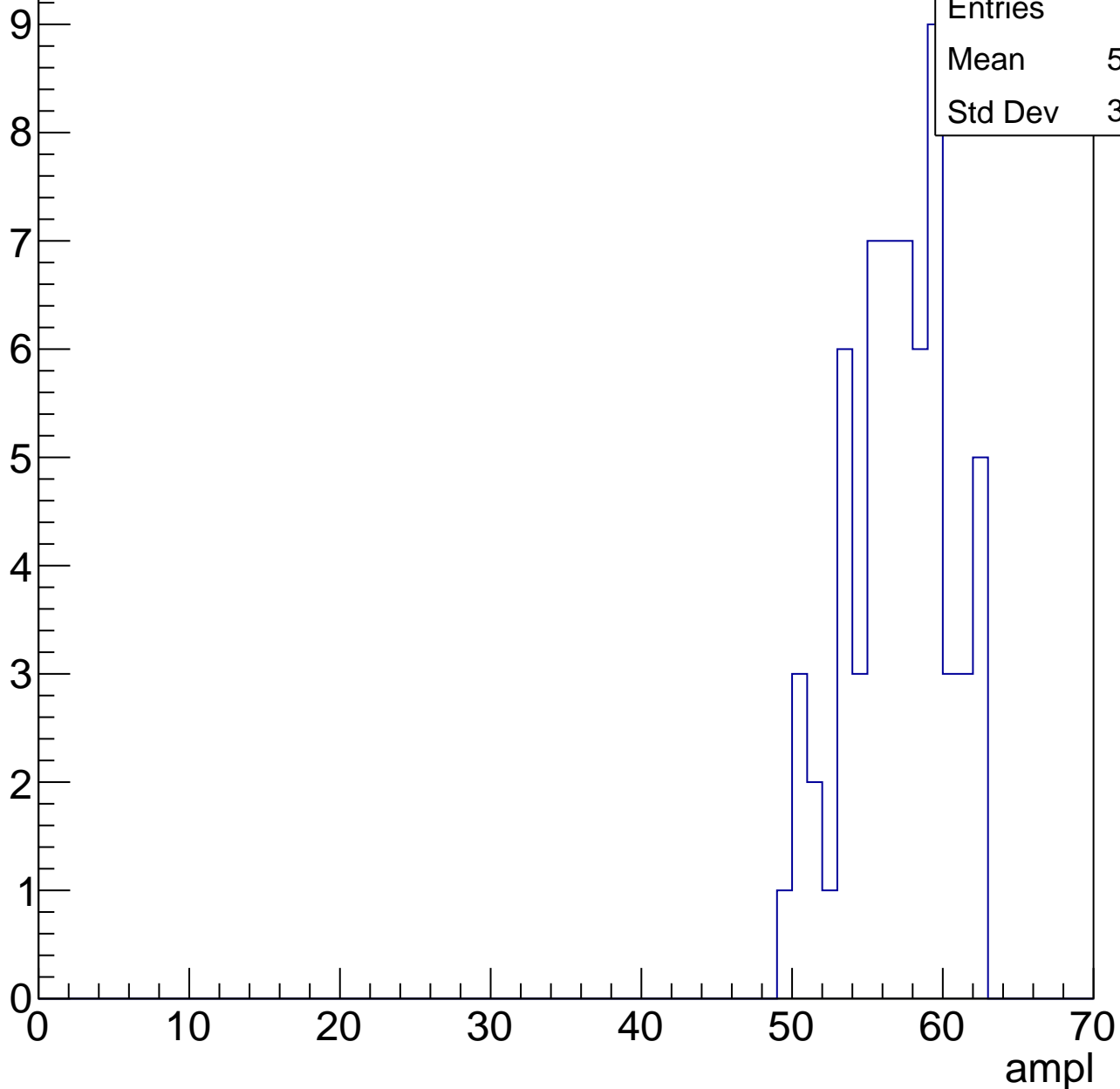


B0L002S, U18-ch107, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	56.52
Std Dev	3.318



B0L002S, U18-ch107, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	36
Mean	58.83
Std Dev	10.19

Entry

10

8

6

4

2

0

0

10

20

30

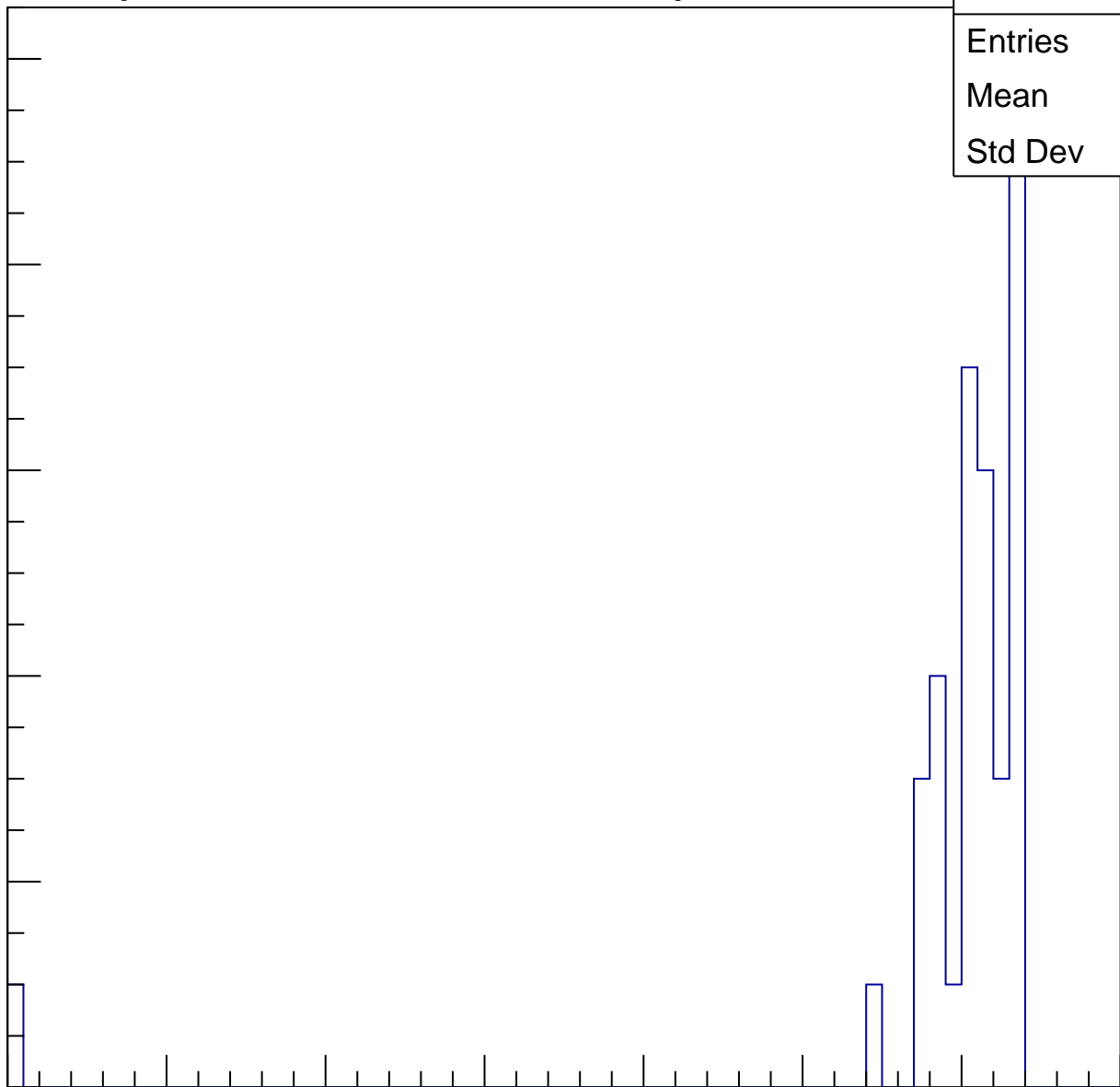
40

50

60

70

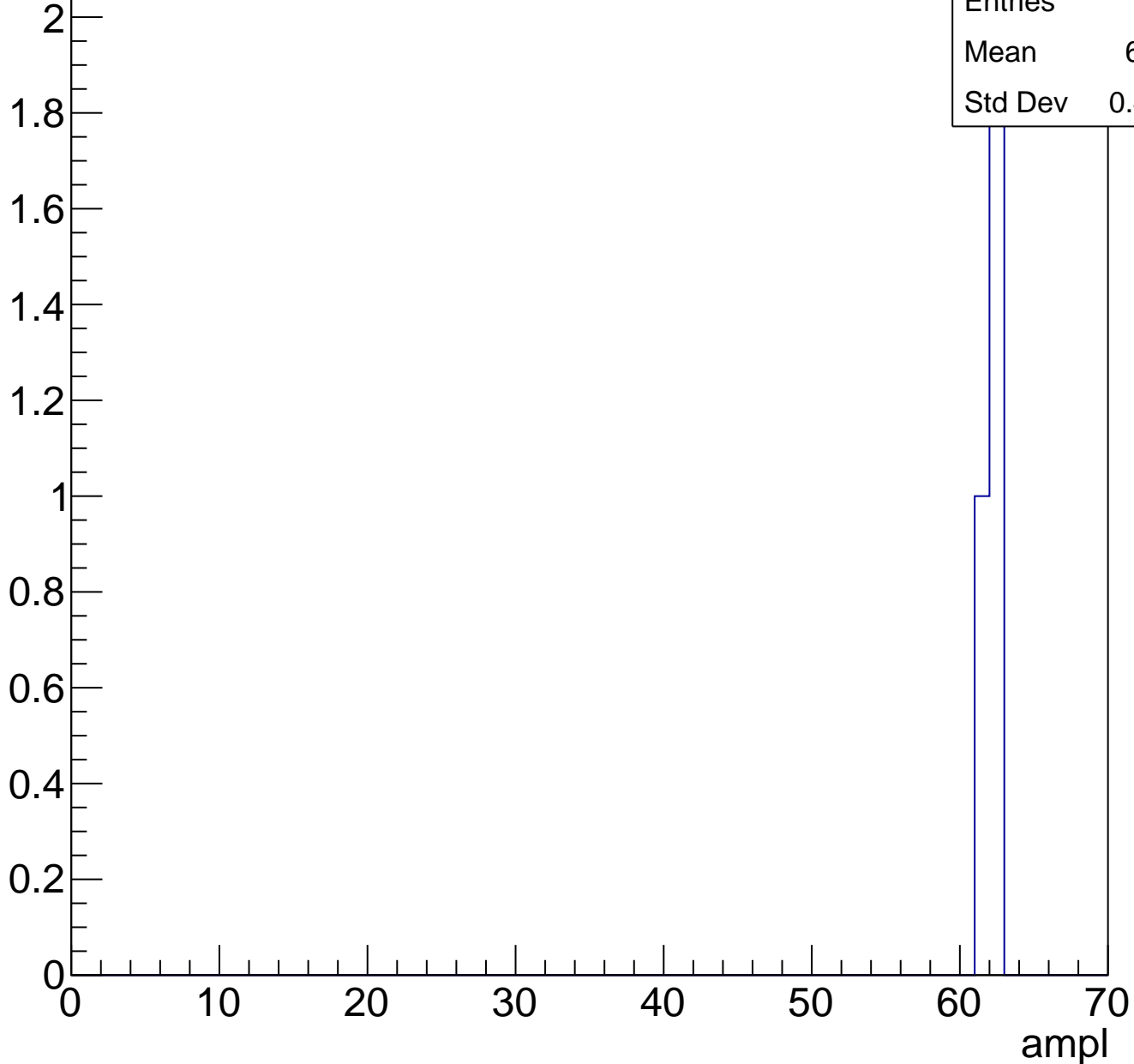
ampl



B0L002S, U18-ch107, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch107, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch108, adc0

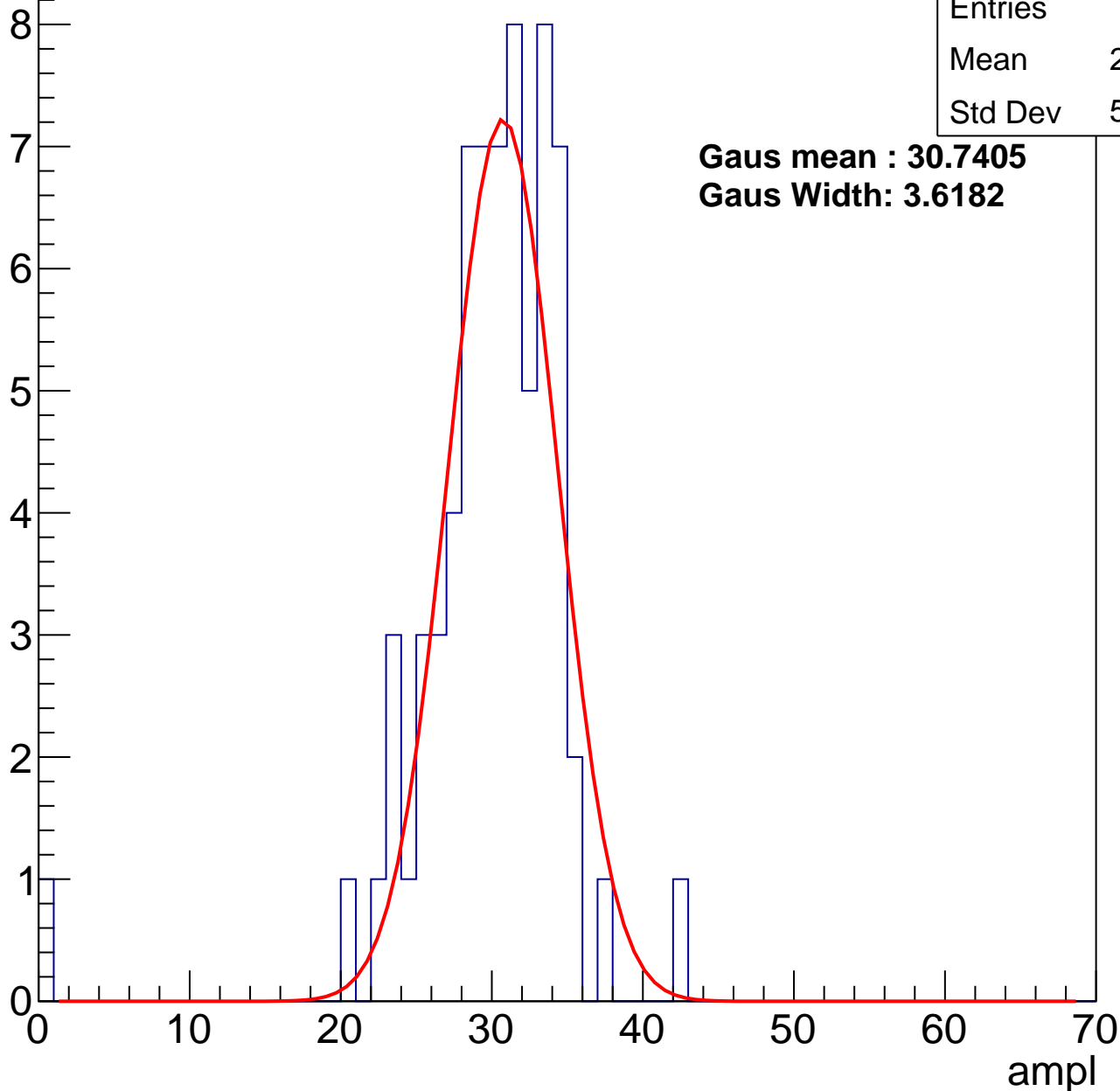
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	29.49
Std Dev	5.179

Gaus mean : 30.7405

Gaus Width: 3.6182



B0L002S, U18-ch108, adc1

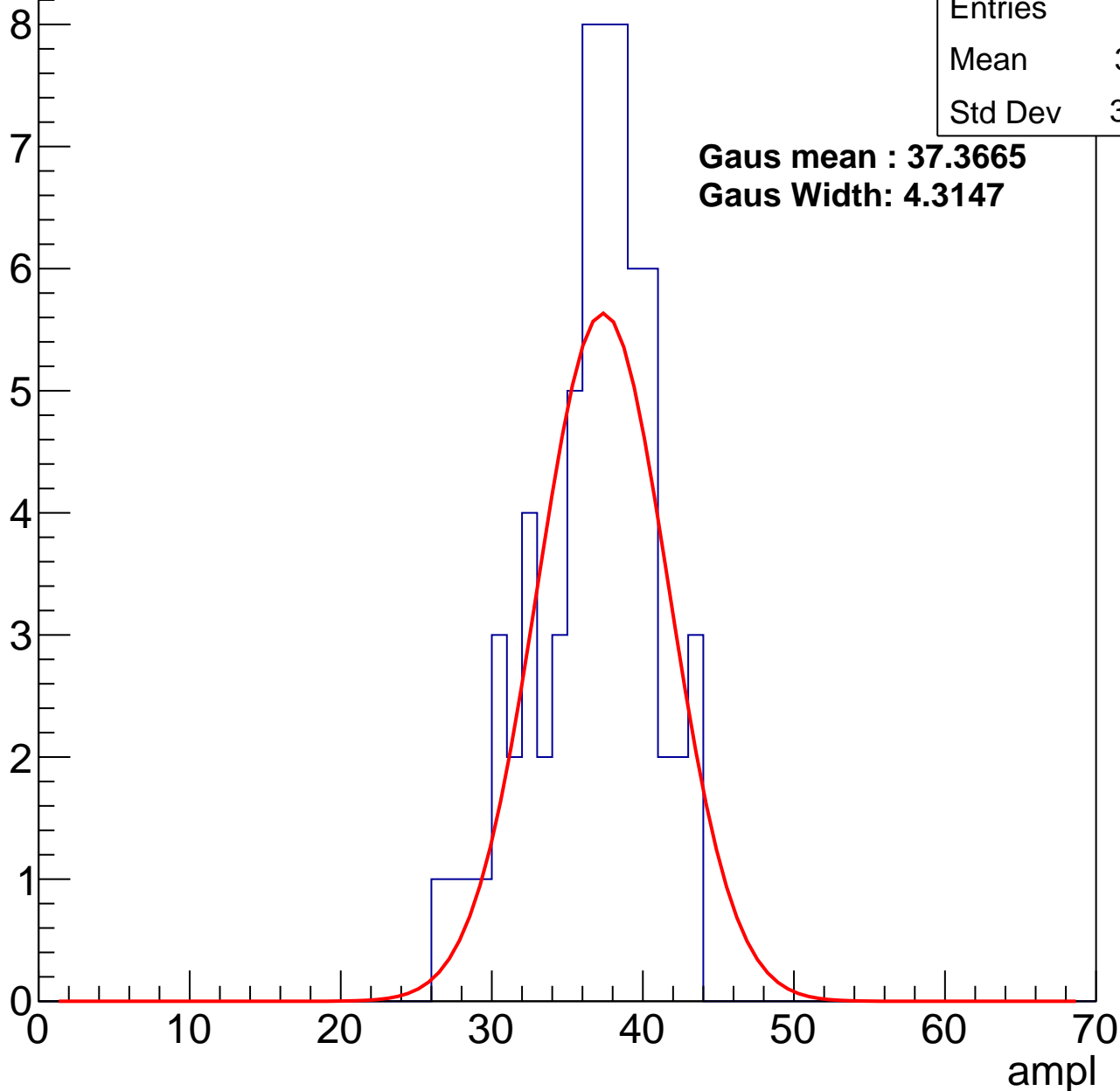
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	36.21
Std Dev	3.906

Gaus mean : 37.3665

Gaus Width: 4.3147



B0L002S, U18-ch108, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	42.87
Std Dev	3.778

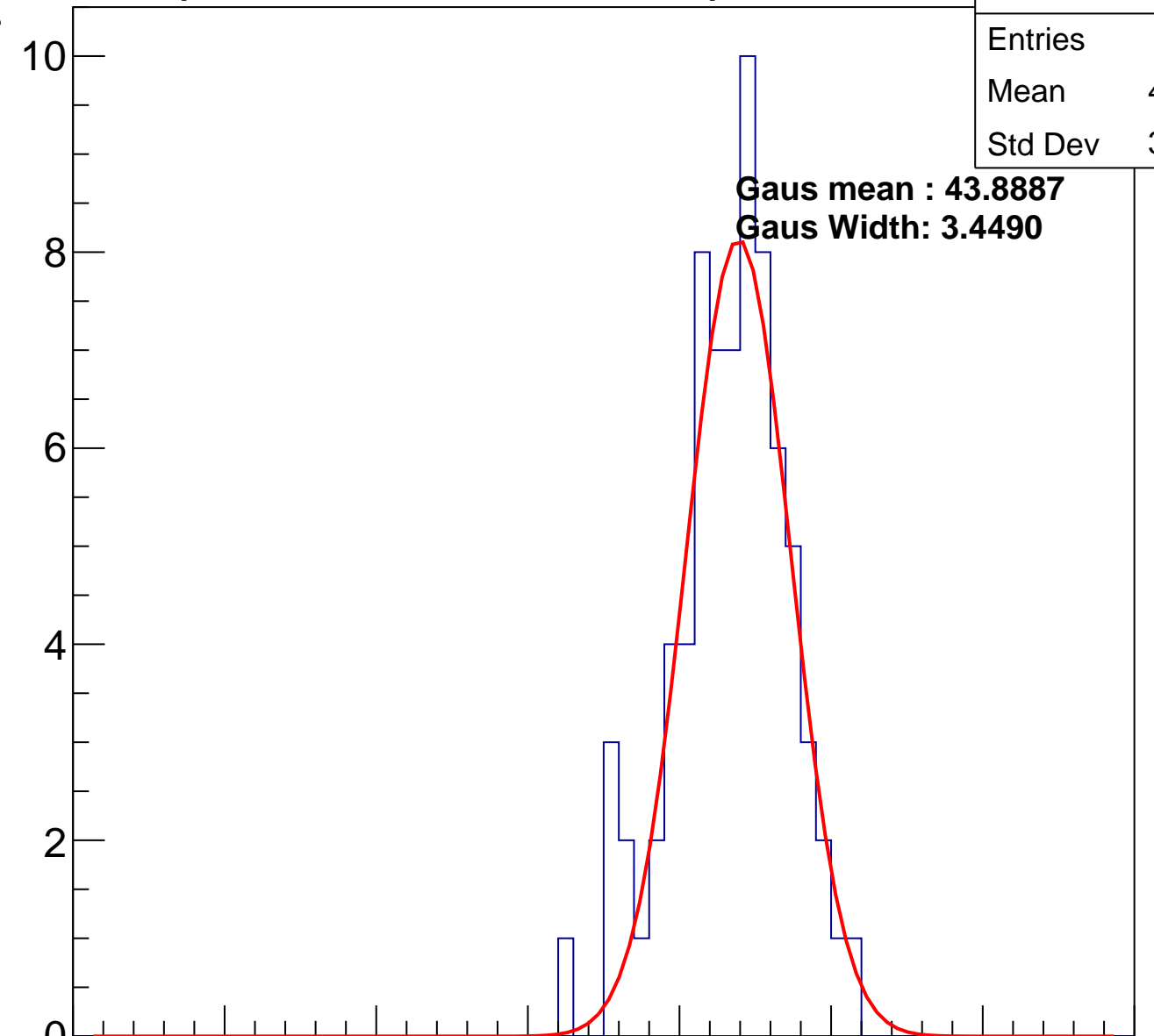
Gaus mean : 43.8887

Gaus Width: 3.4490

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70

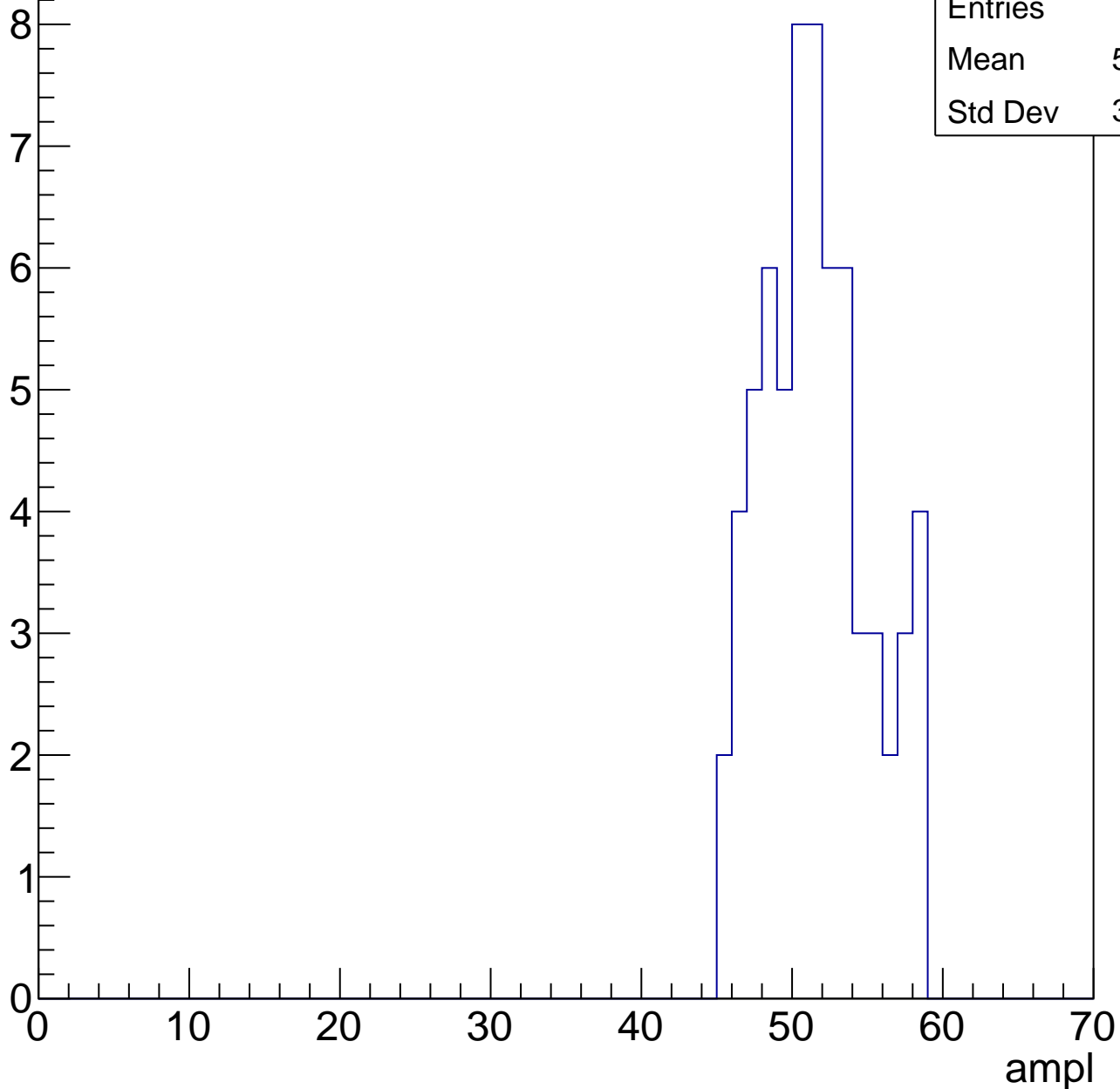


B0L002S, U18-ch108, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

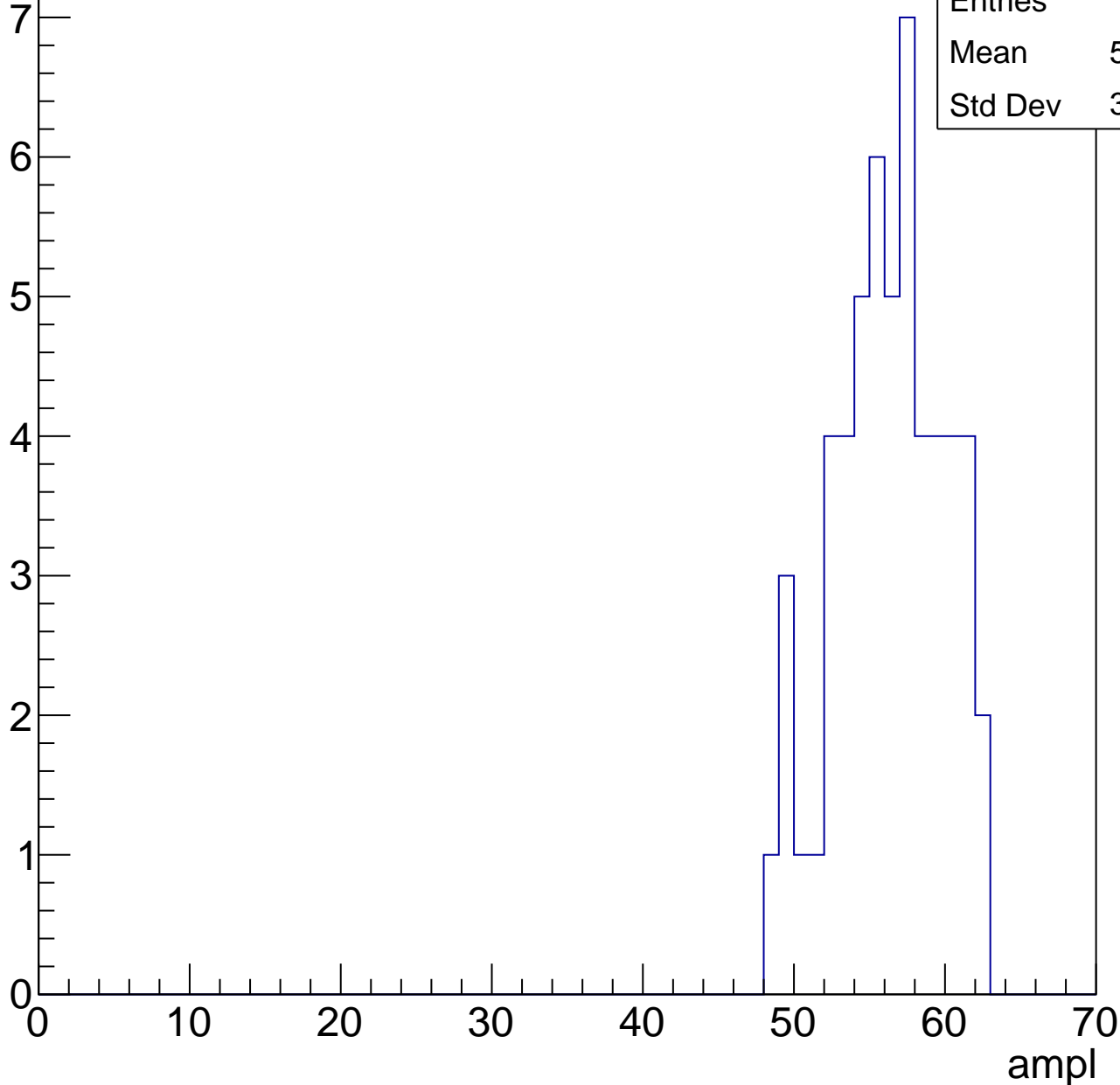
Entries	65
Mean	51.11
Std Dev	3.491



B0L002S, U18-ch108, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry



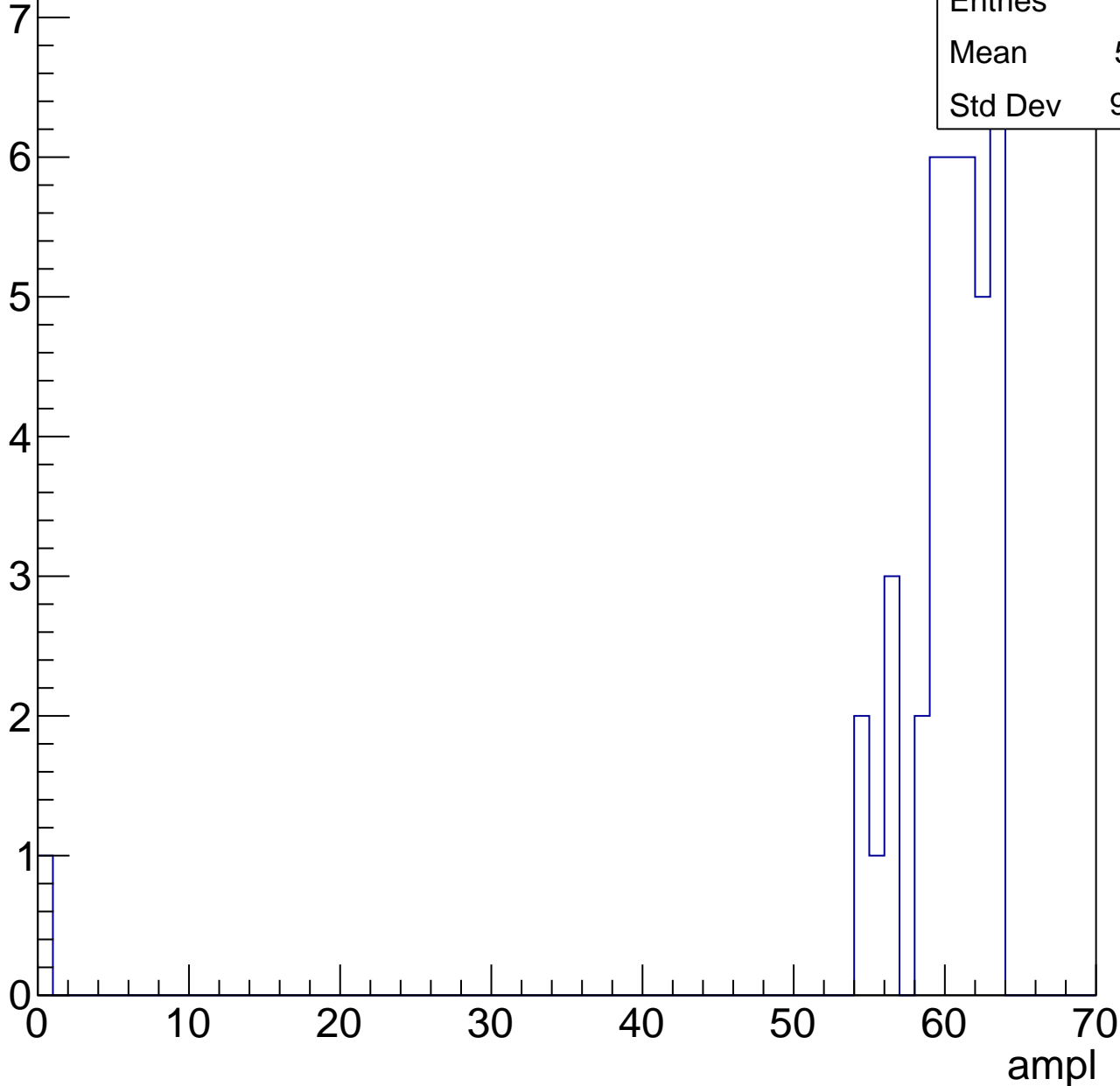
Entries	55
Mean	55.84
Std Dev	3.546

B0L002S, U18-ch108, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

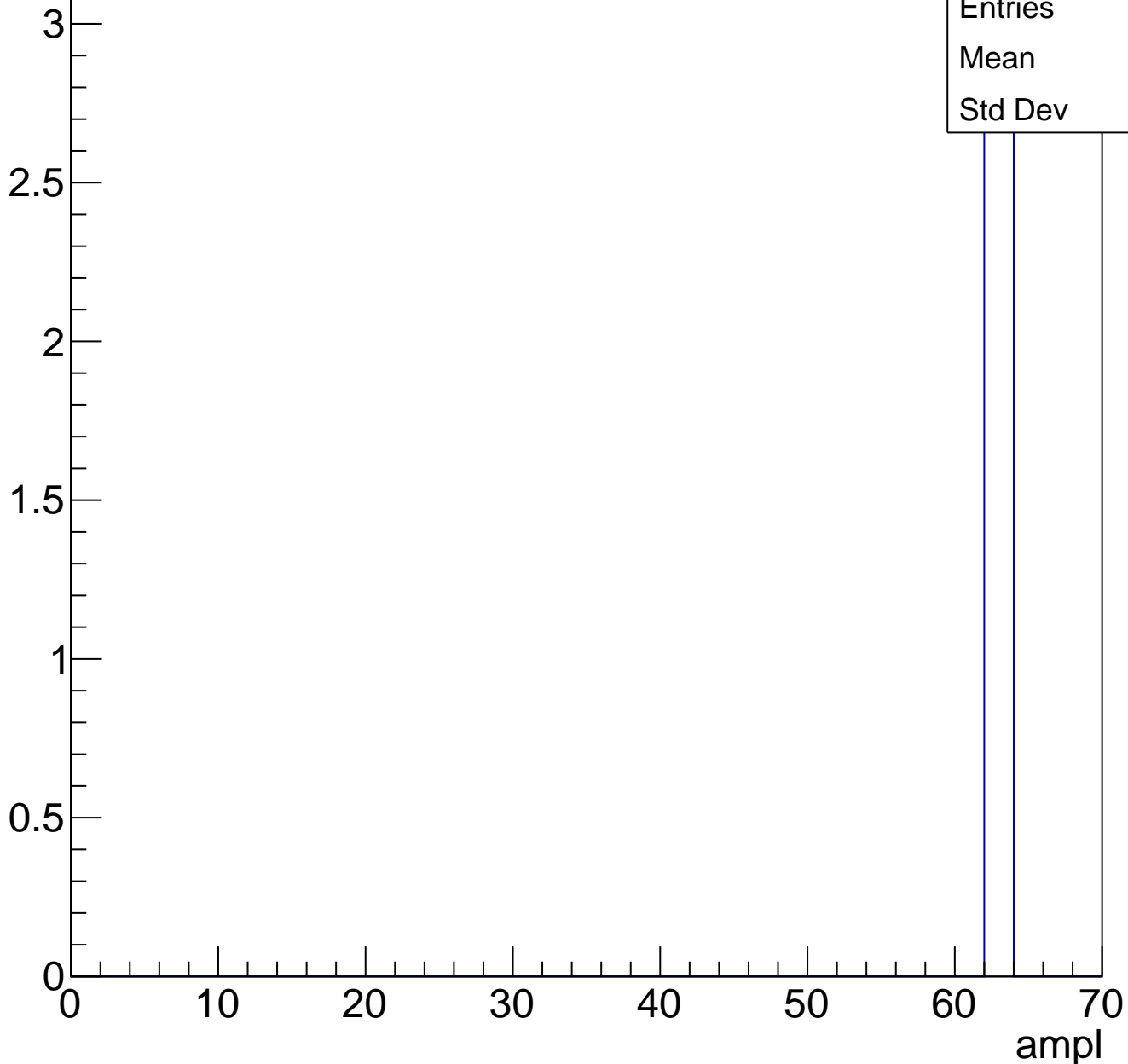
Entries	39
Mean	58.41
Std Dev	9.805



B0L002S, U18-ch108, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch108, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch109, adc0

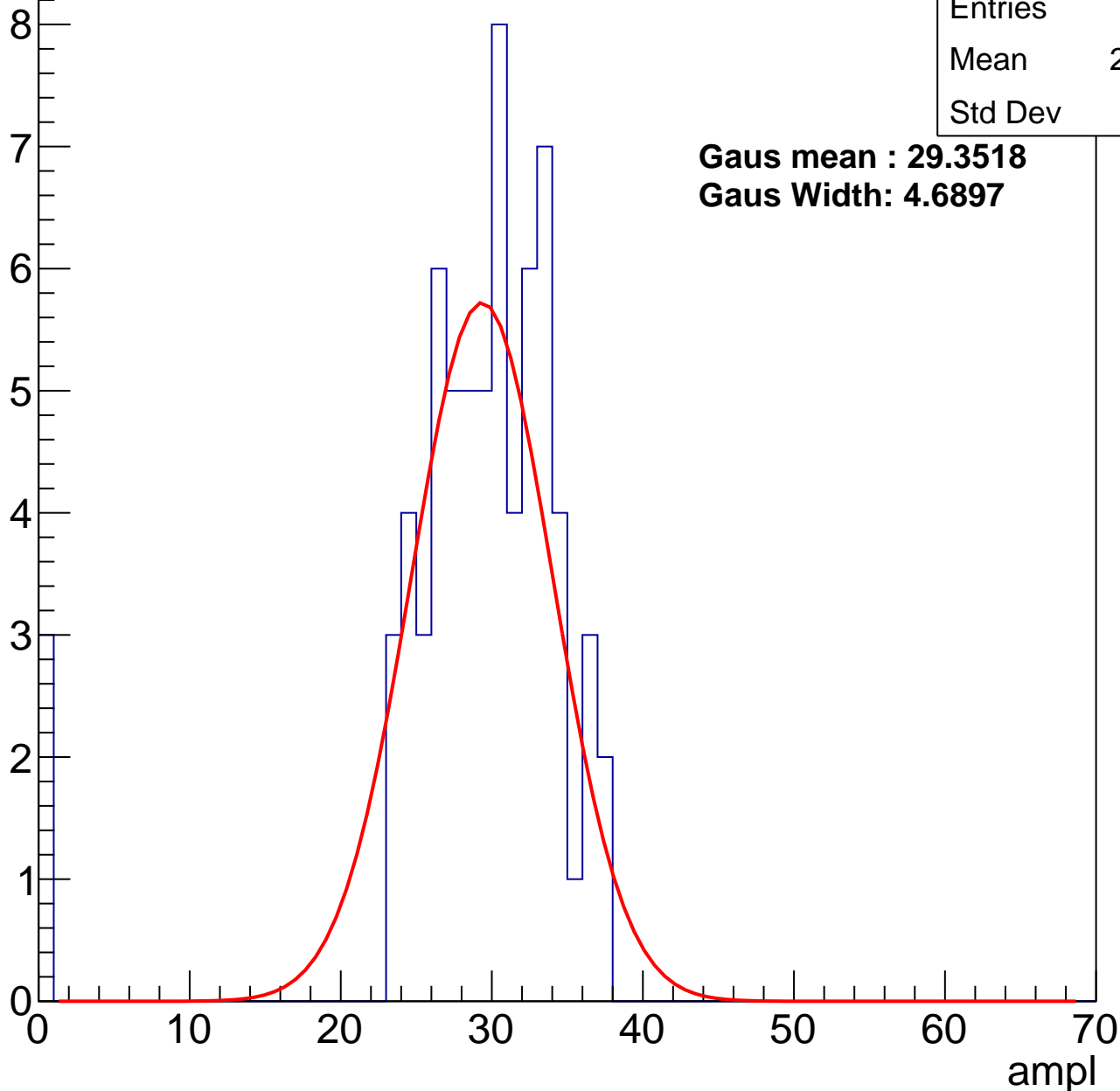
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	28.35
Std Dev	7.05

Gaus mean : 29.3518

Gaus Width: 4.6897



B0L002S, U18-ch109, adc1

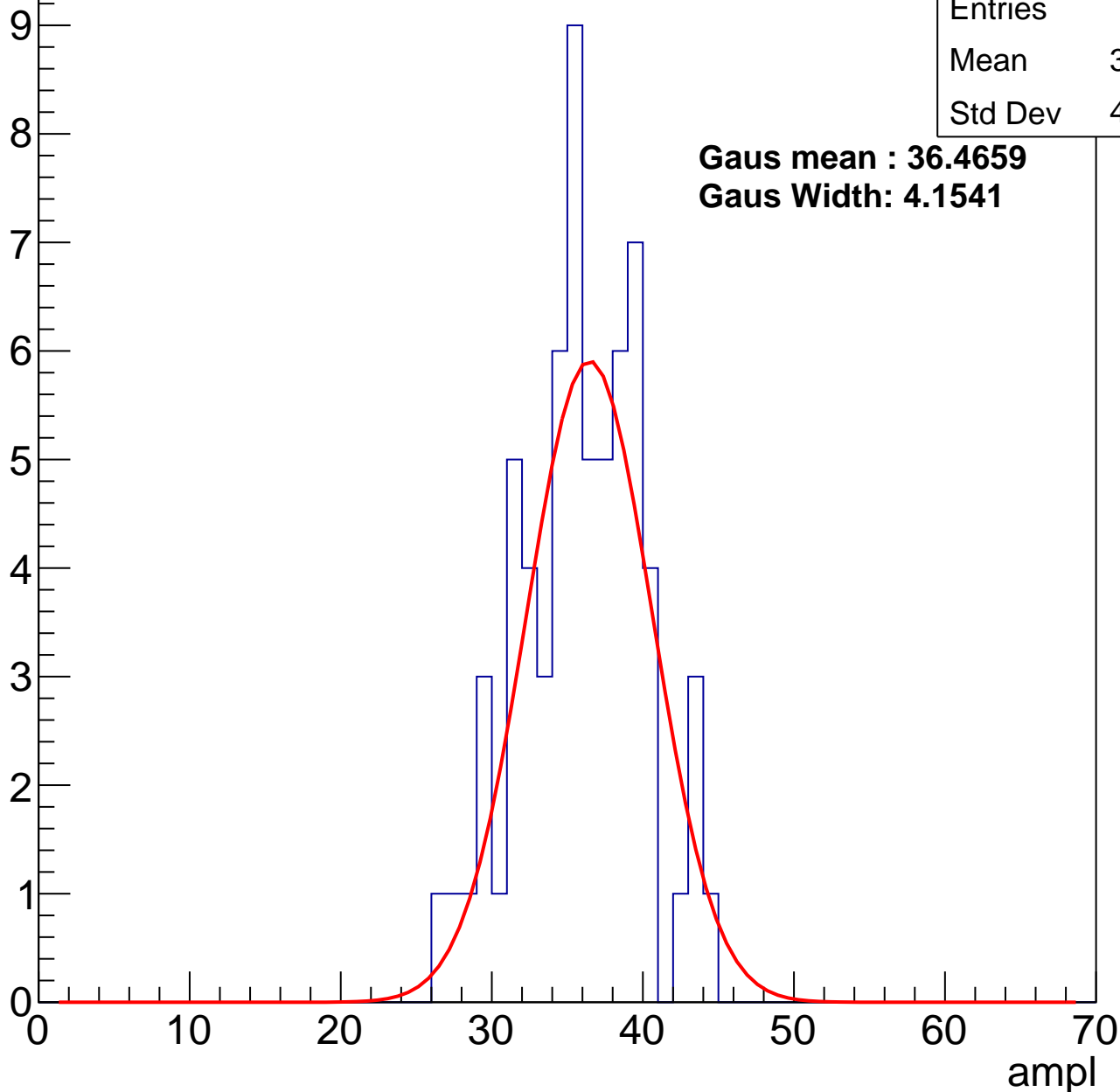
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	35.45
Std Dev	4.008

Gaus mean : 36.4659

Gaus Width: 4.1541



B0L002S, U18-ch109, adc2

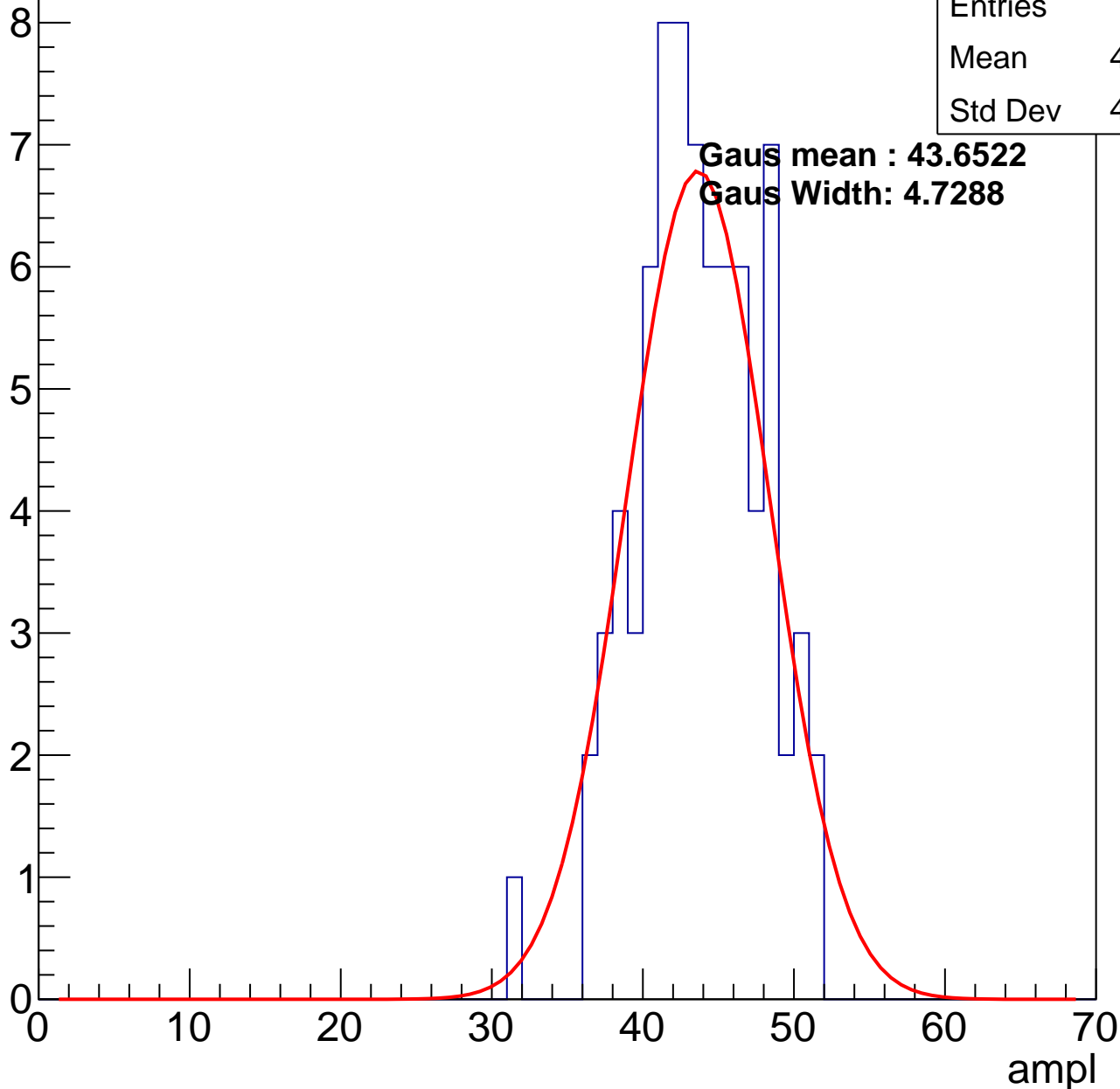
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	78
Mean	43.23
Std Dev	4.016

Gaus mean : 43.6522

Gaus Width: 4.7288

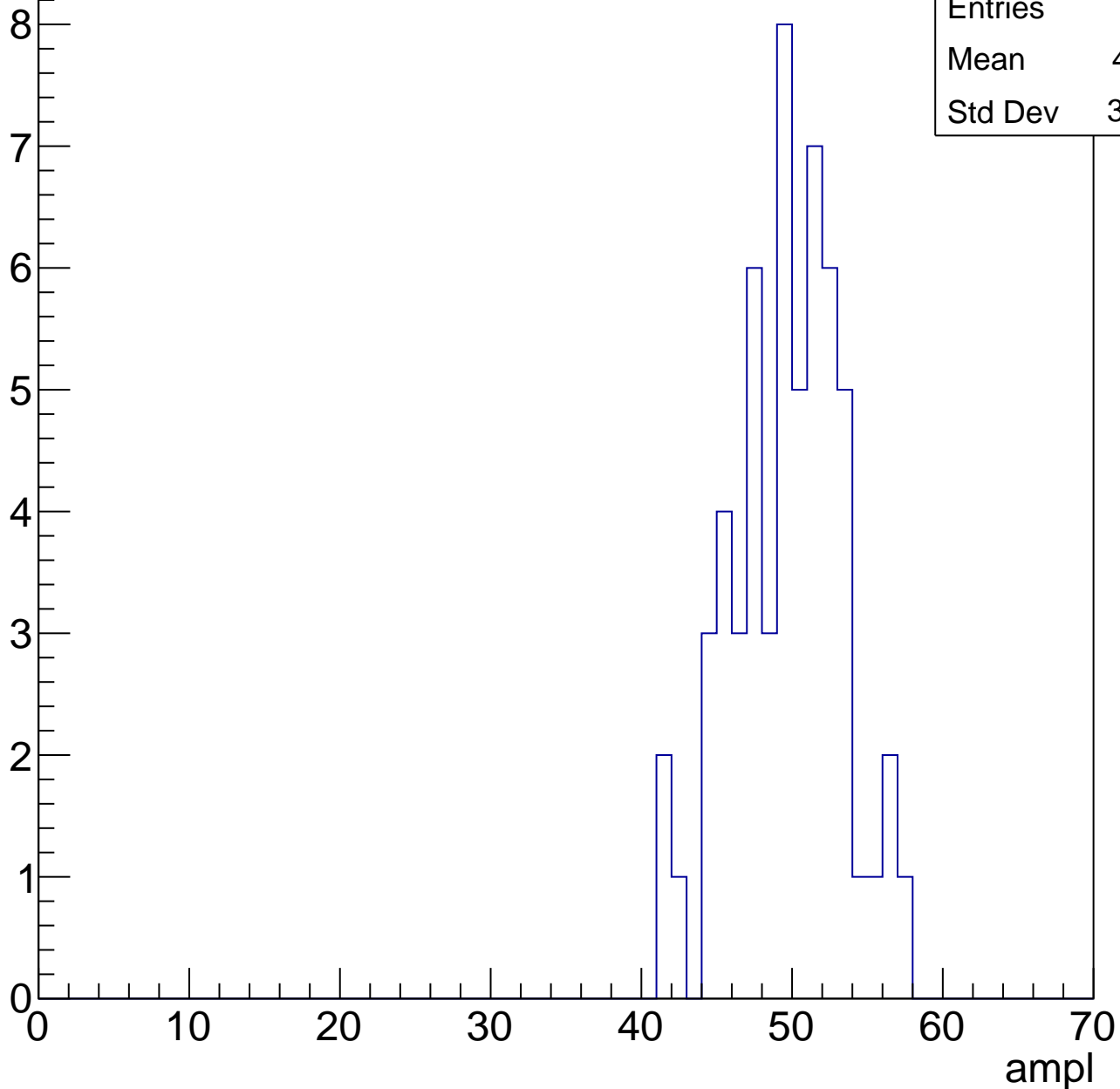


B0L002S, U18-ch109, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	49.21
Std Dev	3.614

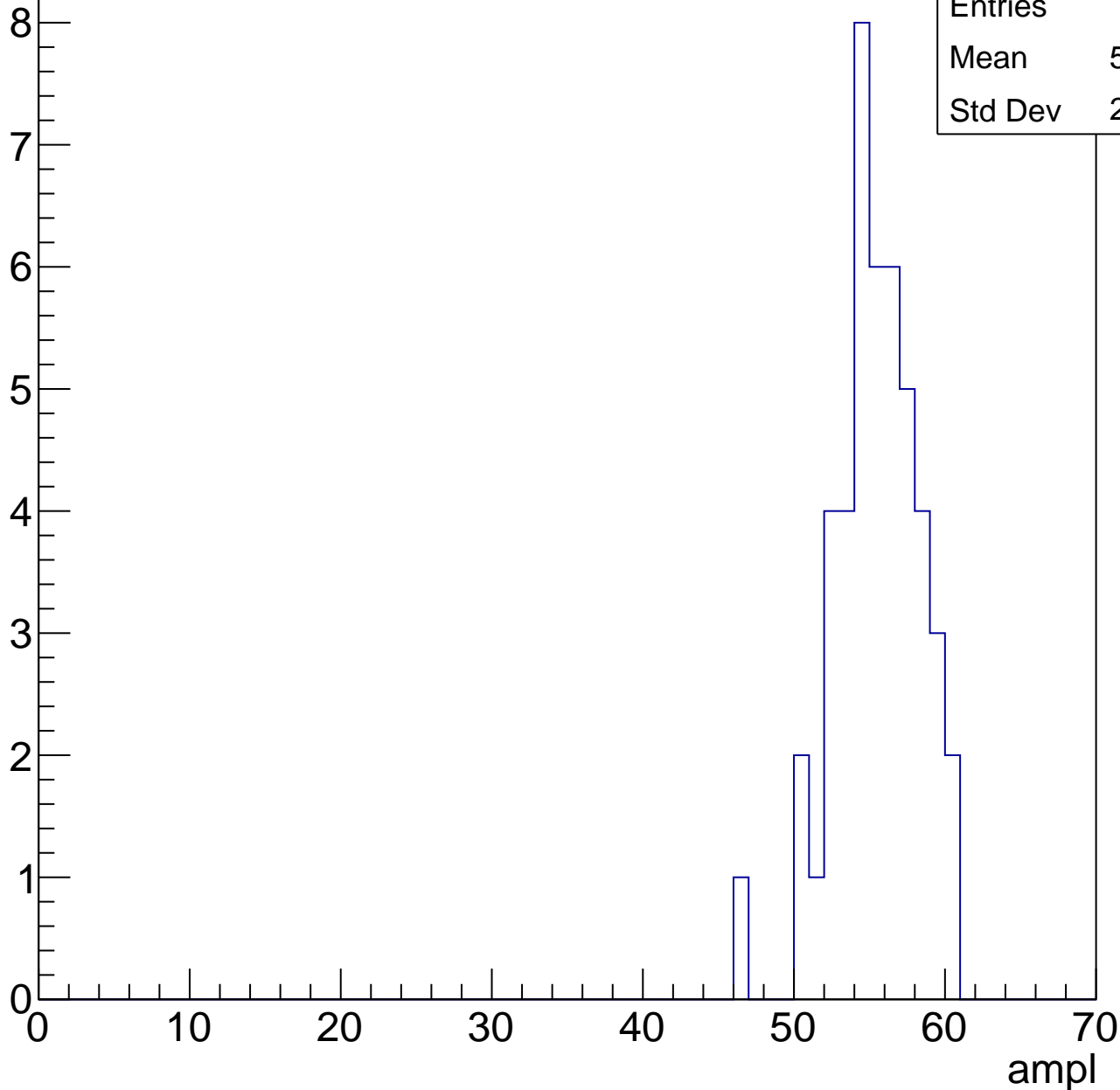


B0L002S, U18-ch109, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	46
Mean	54.98
Std Dev	2.824



B0L002S, U18-ch109, adc5

calib_packv5_042523_0143.root, FC#8, port C1

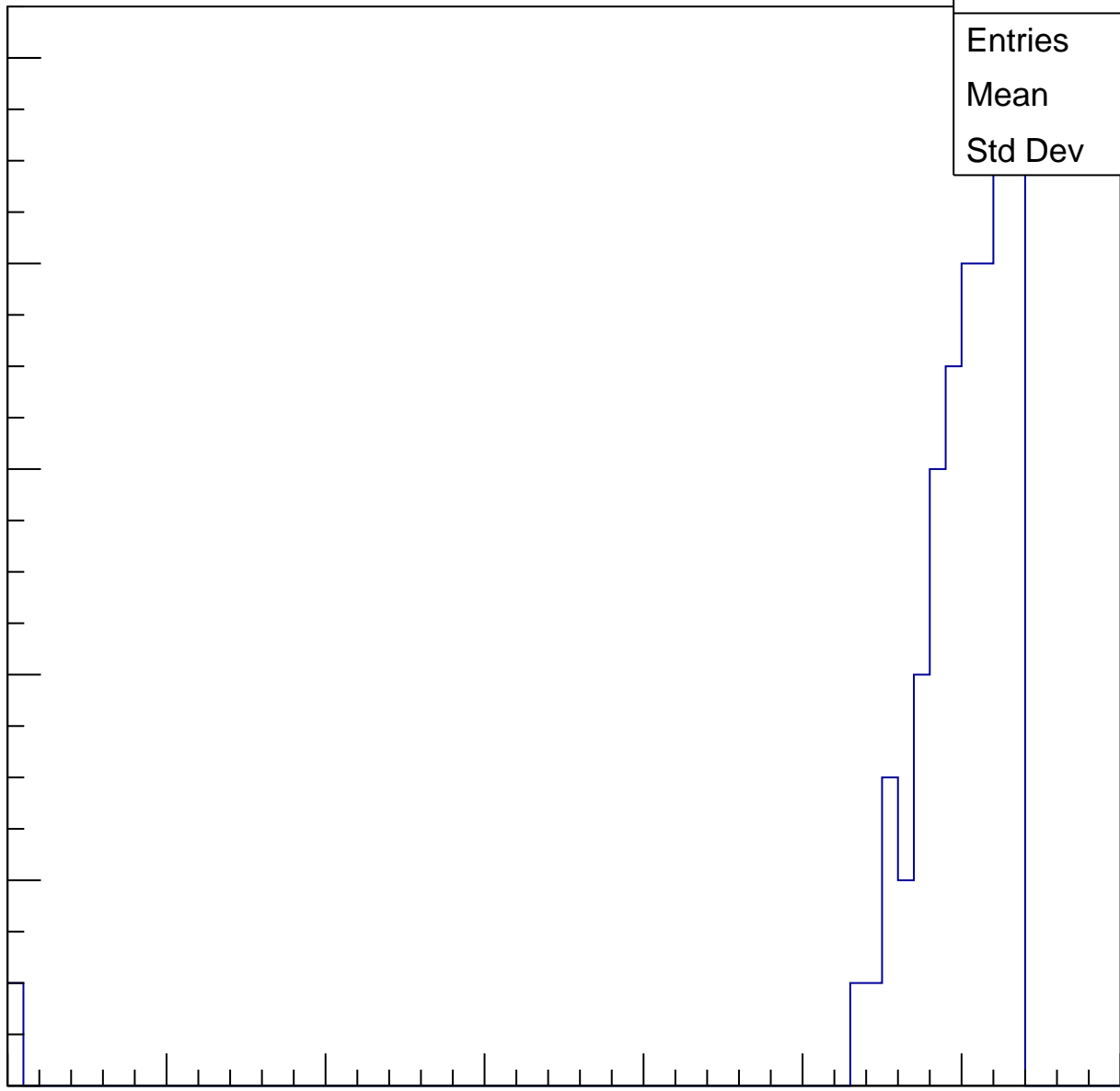
Entries	60
Mean	58.82
Std Dev	8.072

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch109, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	61.33
Std Dev	0.4714

ampl

0 10 20 30 40 50 60 70

B0L002S, U18-ch109, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch110, adc0

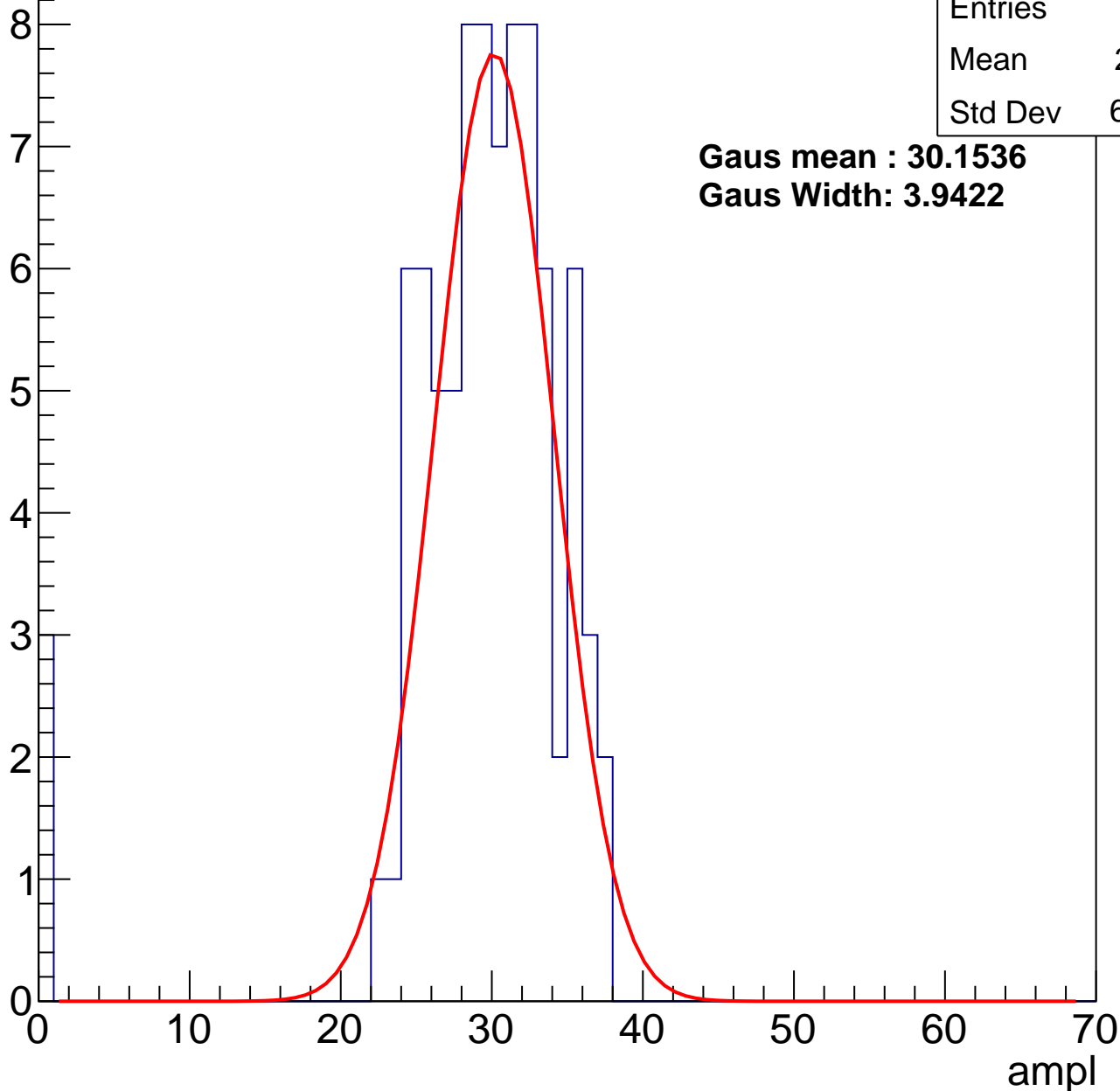
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	85
Mean	28.61
Std Dev	6.564

Gaus mean : 30.1536

Gaus Width: 3.9422



B0L002S, U18-ch110, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	37.43
Std Dev	3.654

Gaus mean : 38.2460

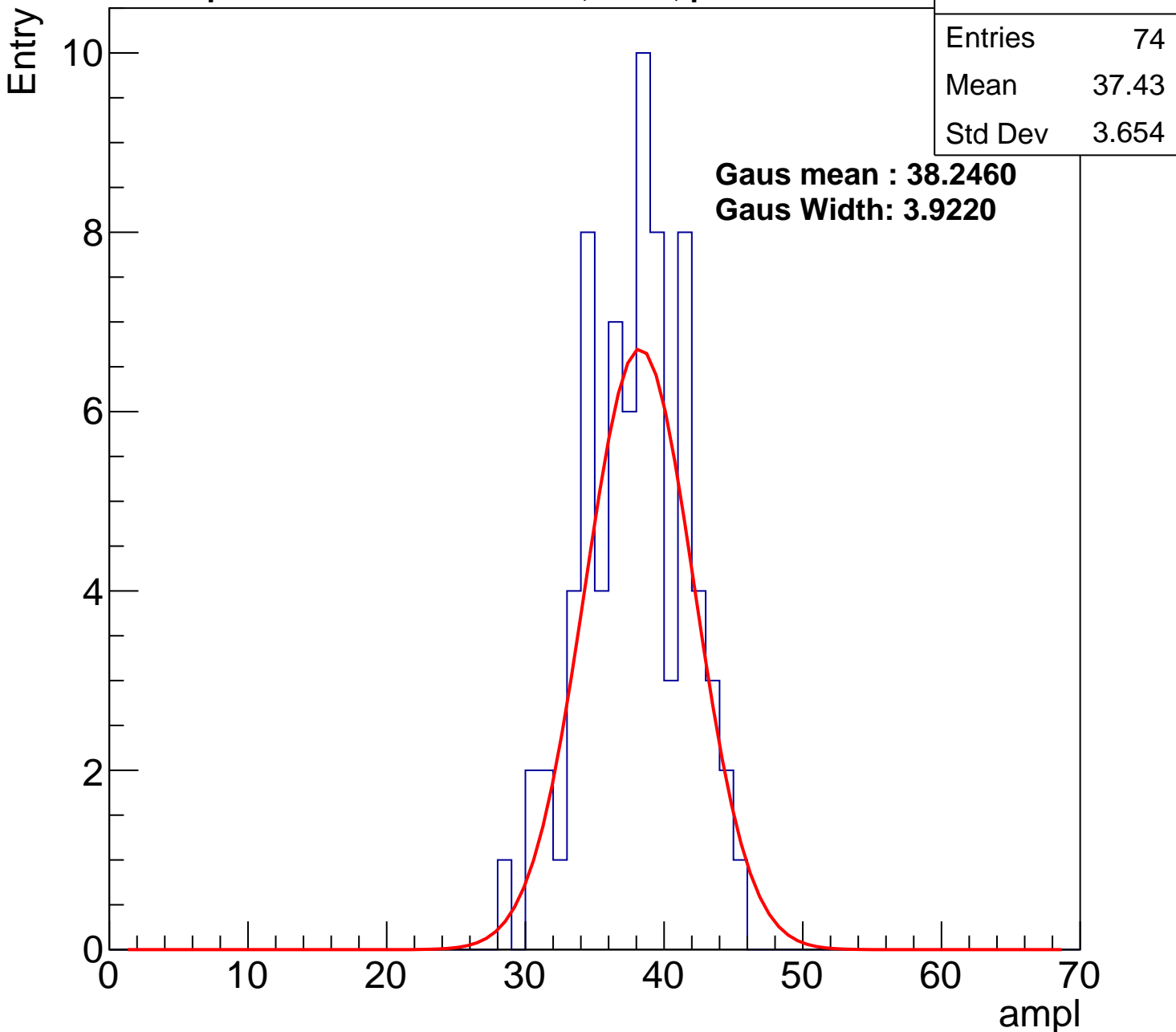
Gaus Width: 3.9220

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

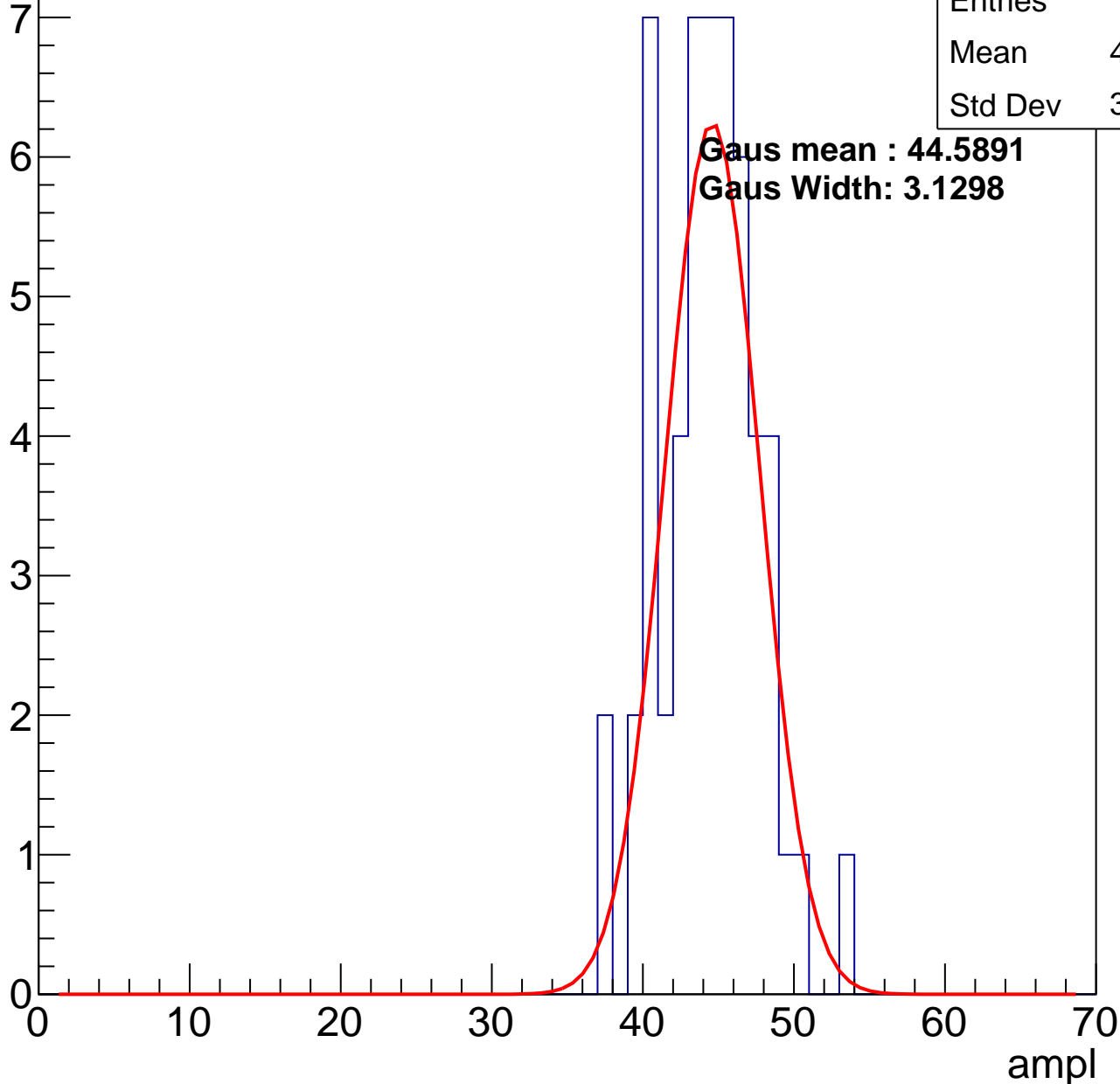


B0L002S, U18-ch110, adc2

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	55
Mean	43.89
Std Dev	3.229

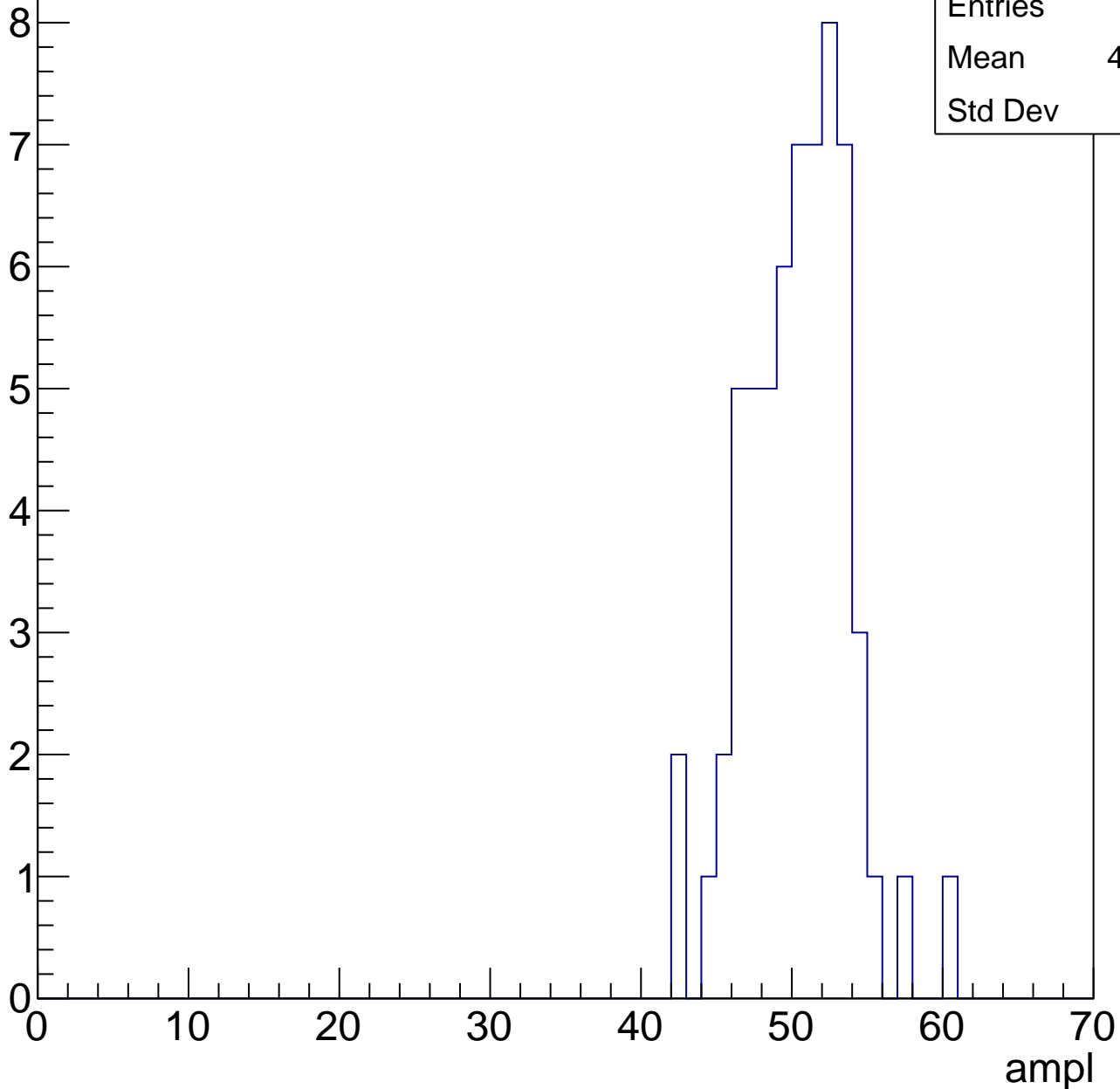


B0L002S, U18-ch110, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.92
Std Dev	3.36

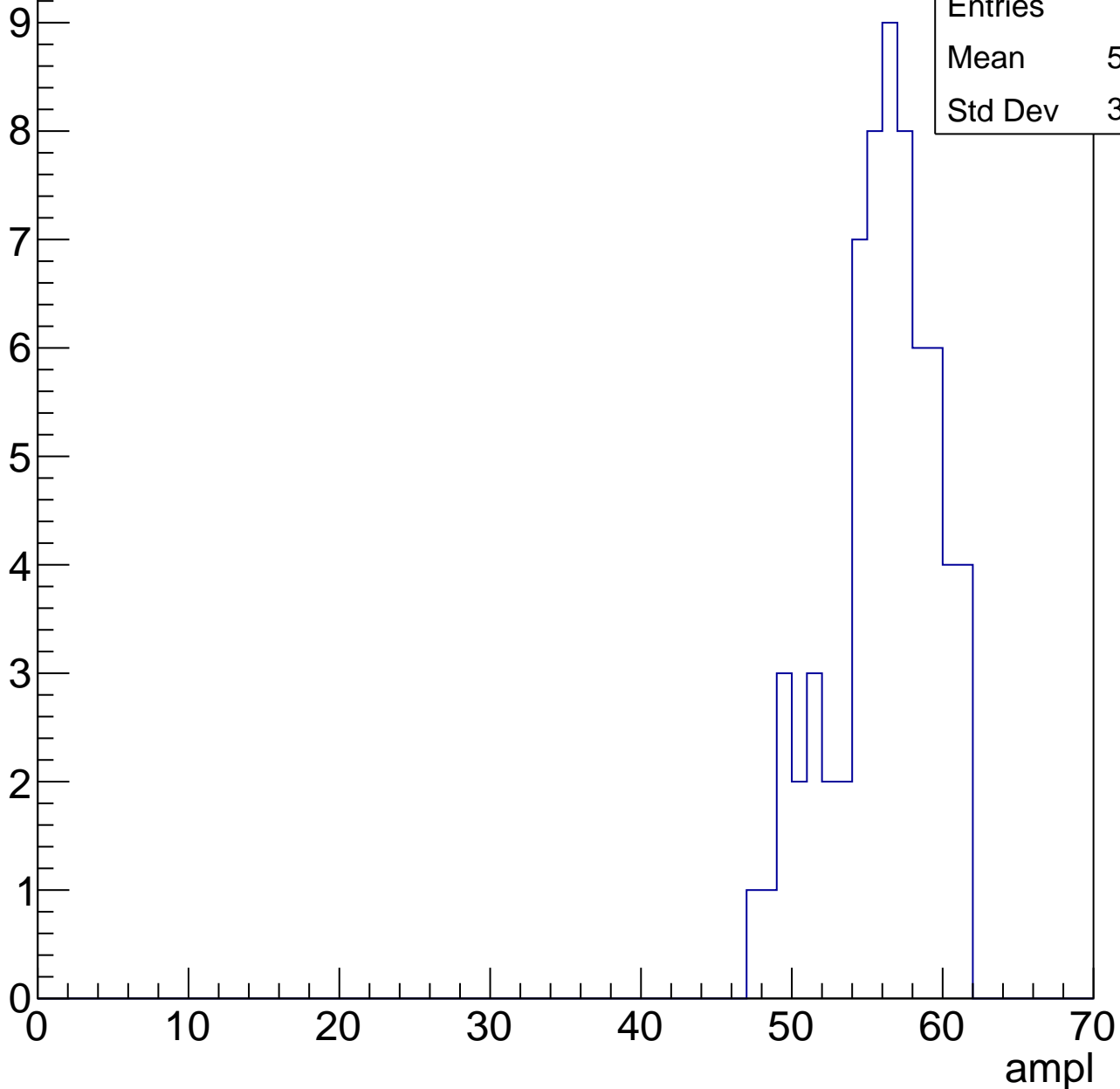


B0L002S, U18-ch110, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	55.59
Std Dev	3.402

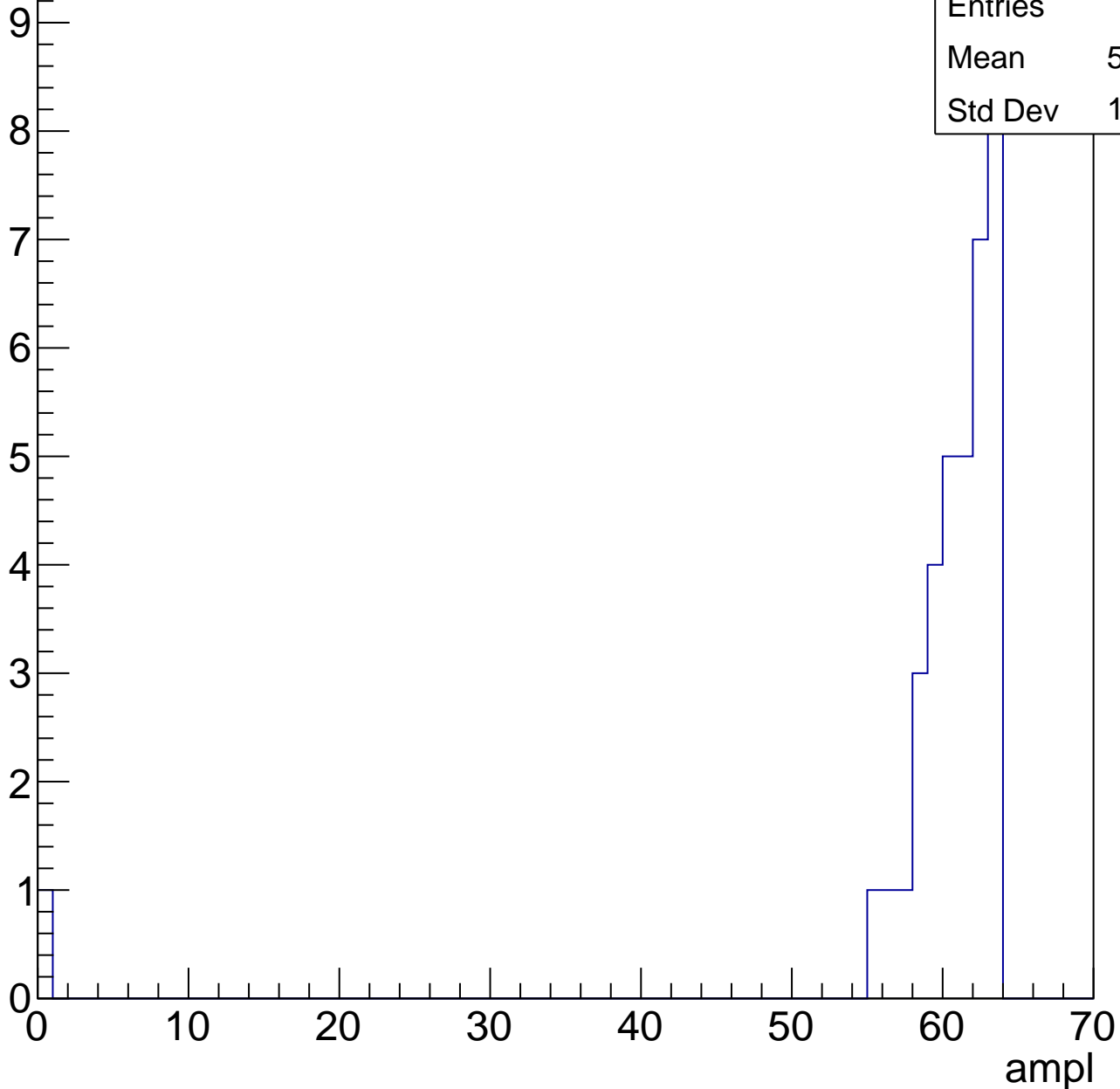


B0L002S, U18-ch110, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

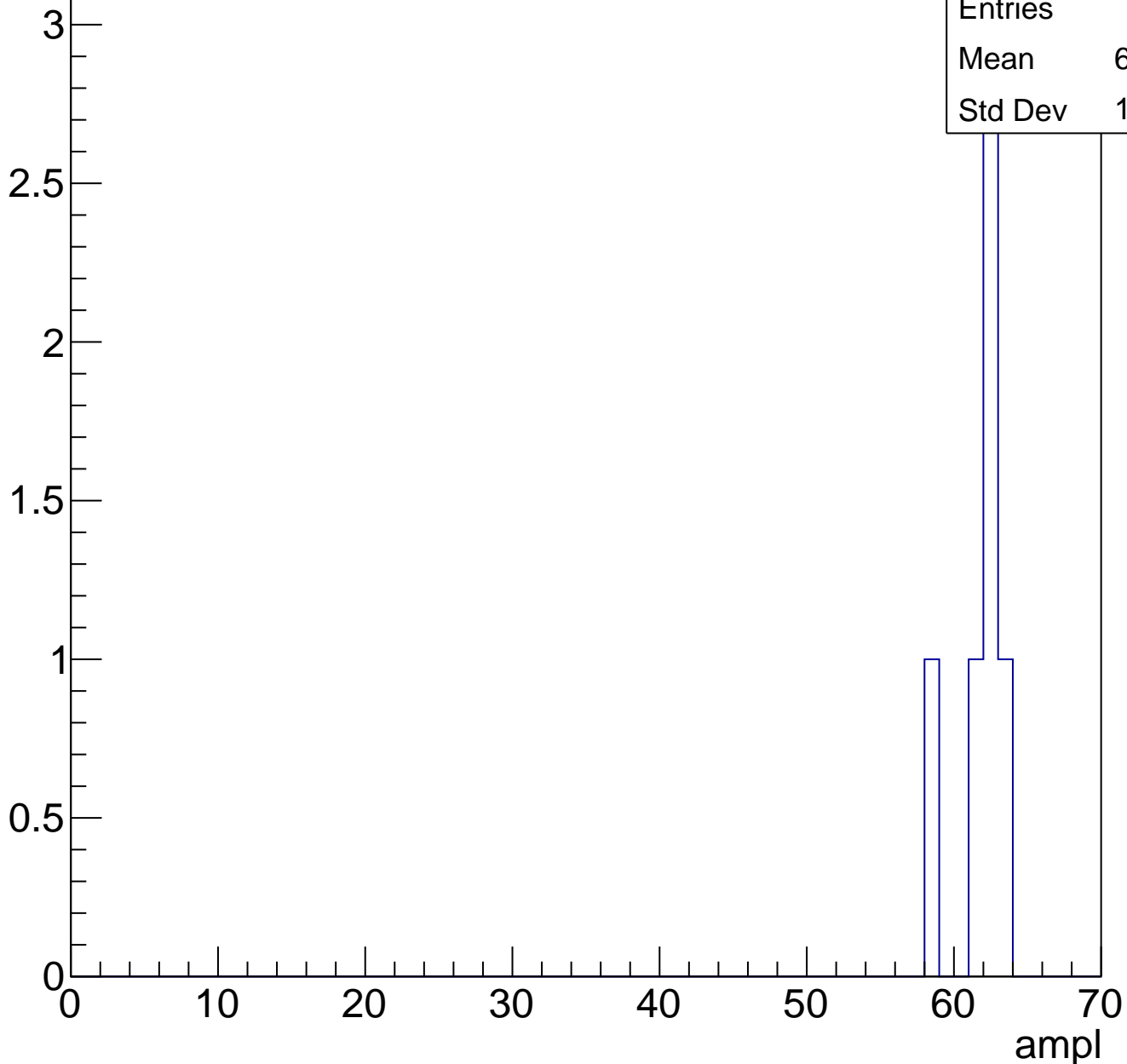
Entries	37
Mean	59.03
Std Dev	10.06



B0L002S, U18-ch110, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch110, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch111, adc0

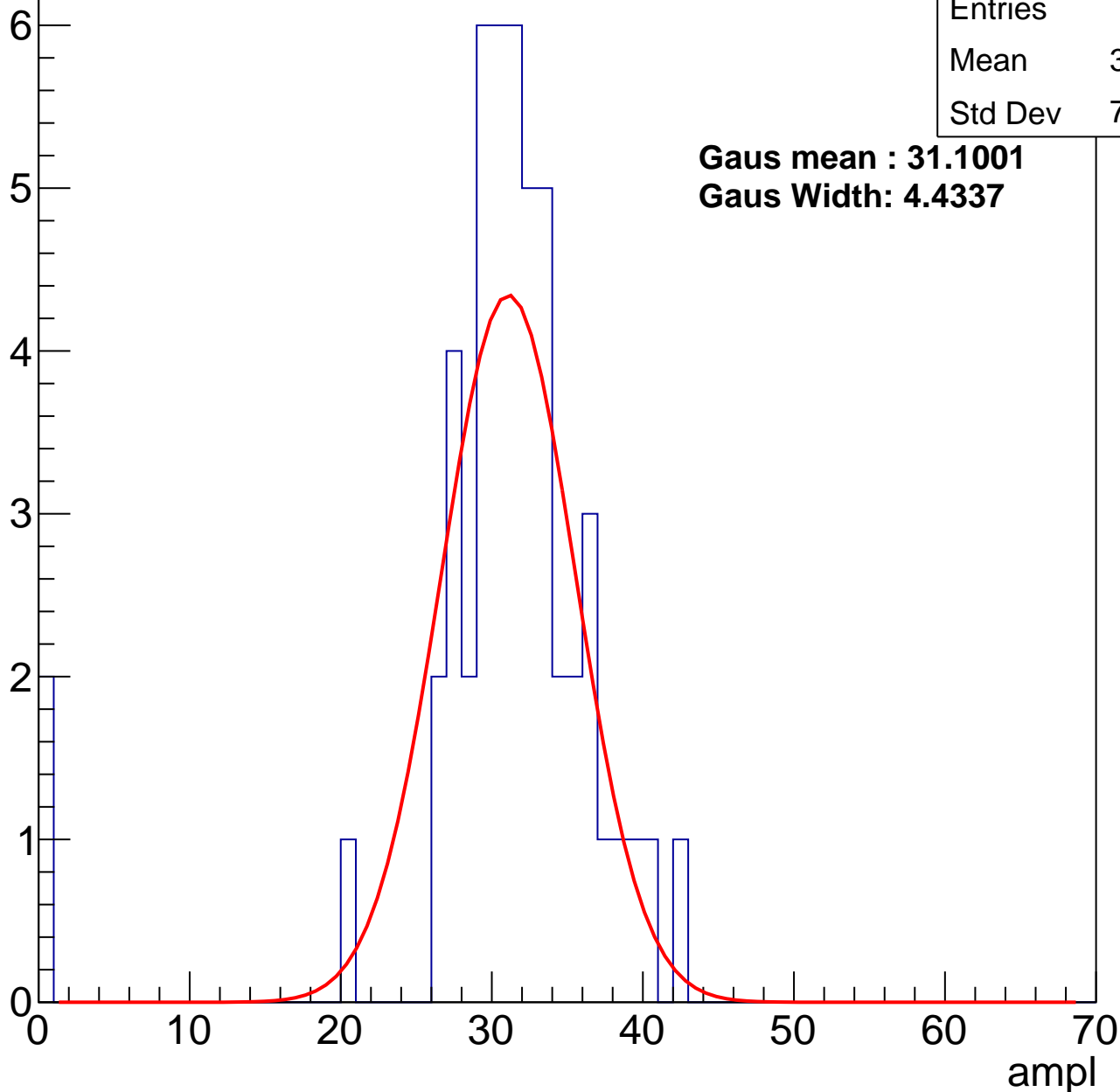
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	30.25
Std Dev	7.246

Gaus mean : 31.1001

Gaus Width: 4.4337



B0L002S, U18-ch111, adc1

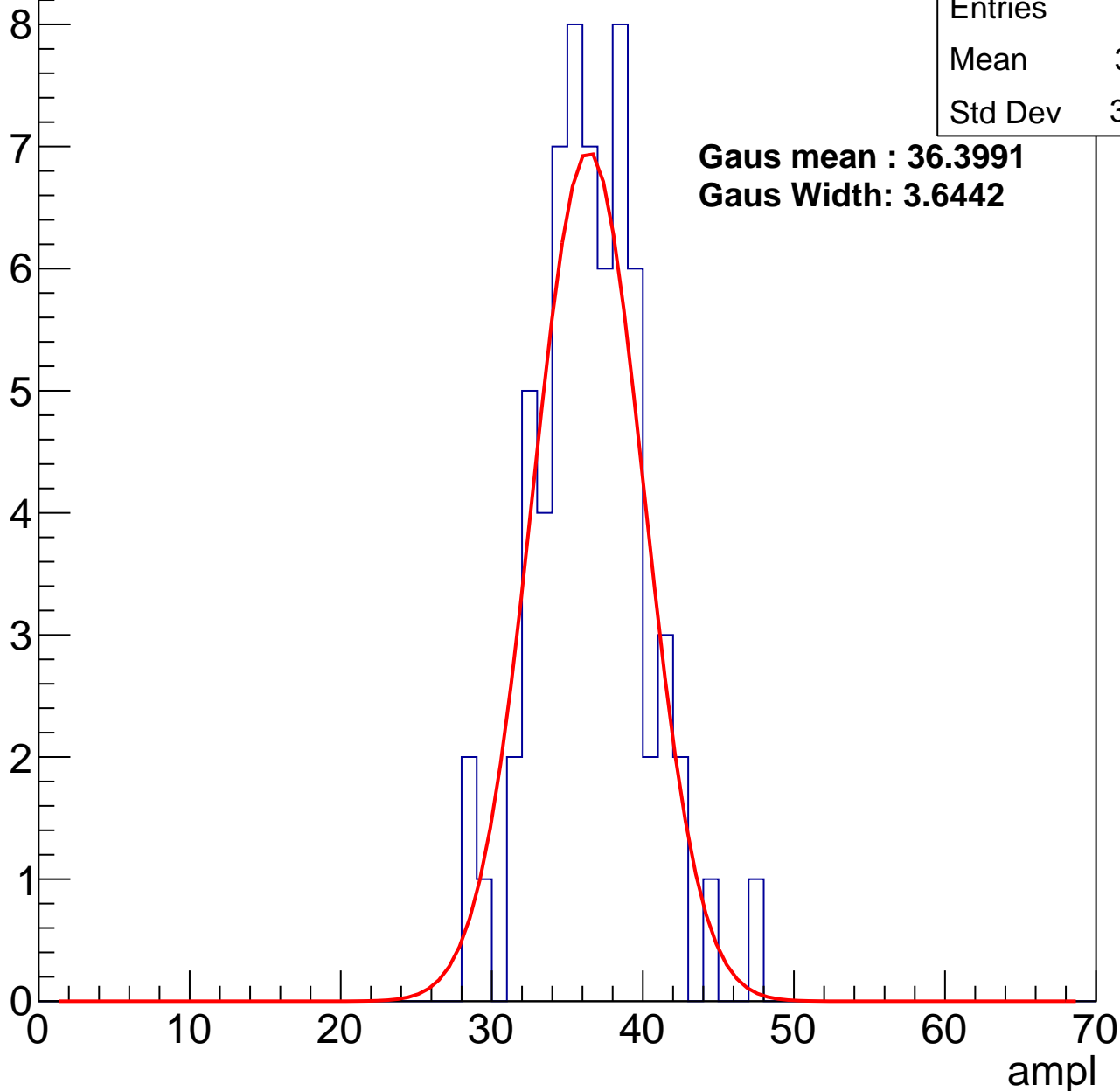
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	36.11
Std Dev	3.583

Gaus mean : 36.3991

Gaus Width: 3.6442



B0L002S, U18-ch111, adc2

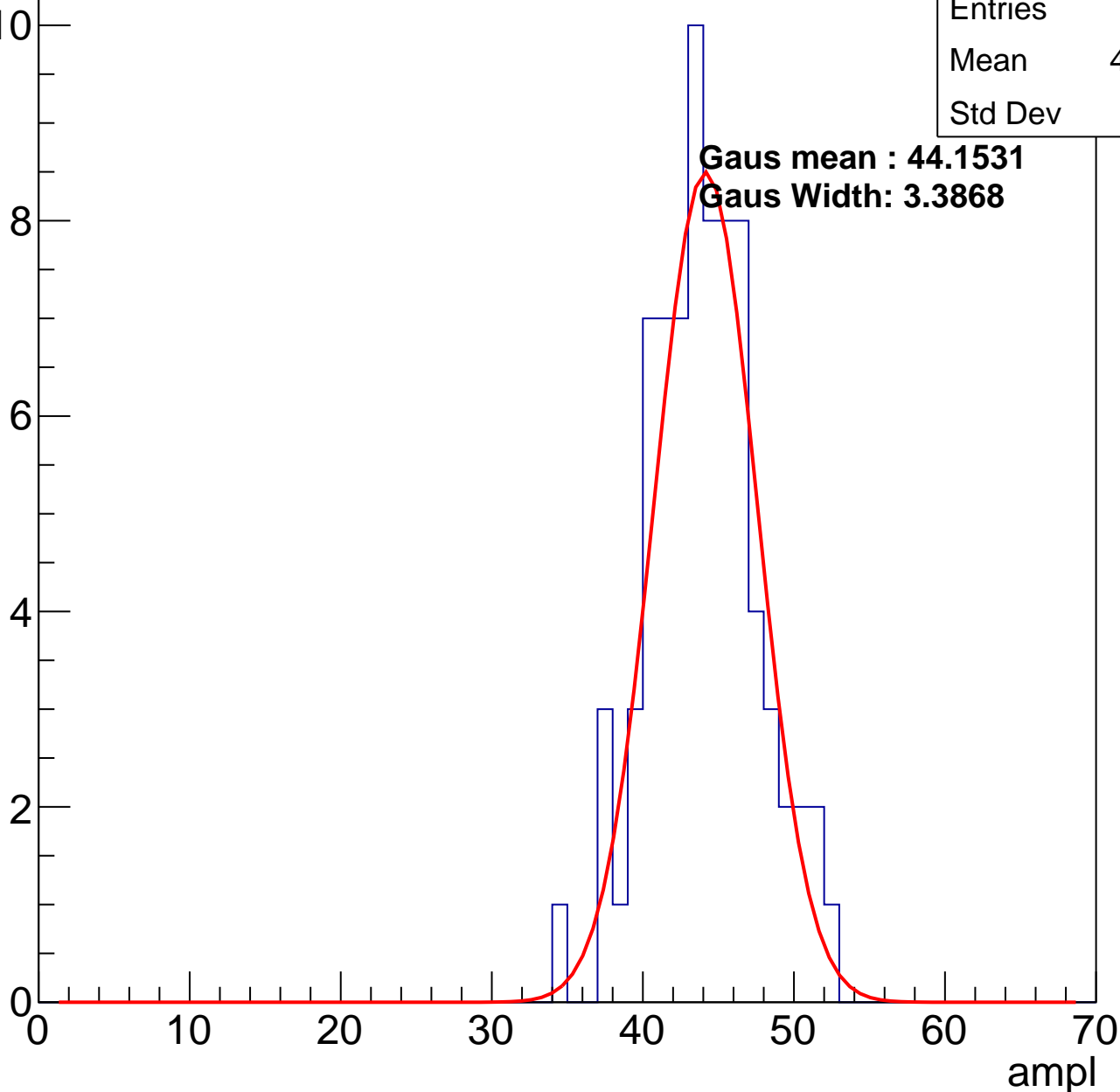
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.57
Std Dev	3.54

Gaus mean : 44.1531

Gaus Width: 3.3868

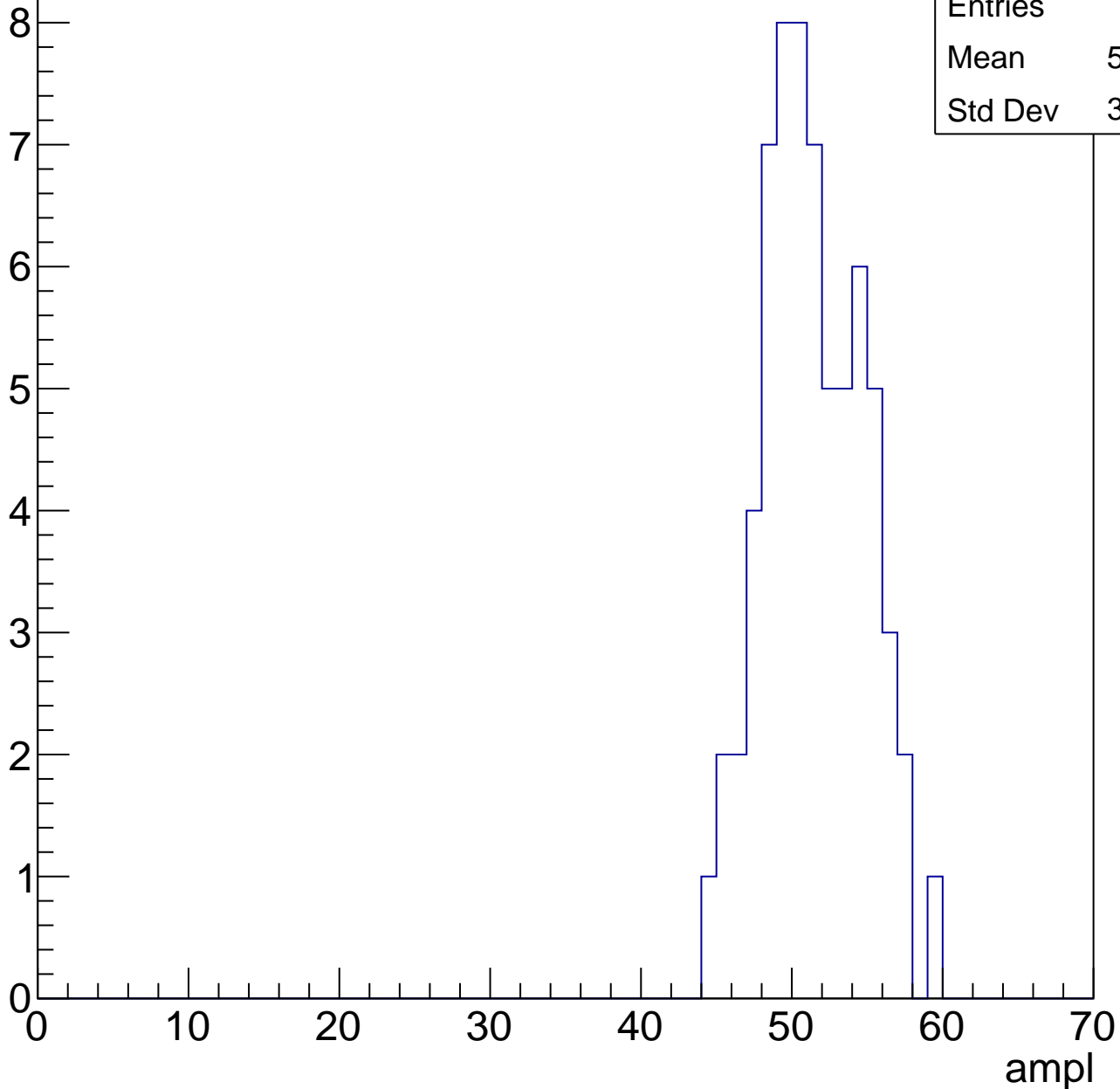


B0L002S, U18-ch111, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	50.97
Std Dev	3.275

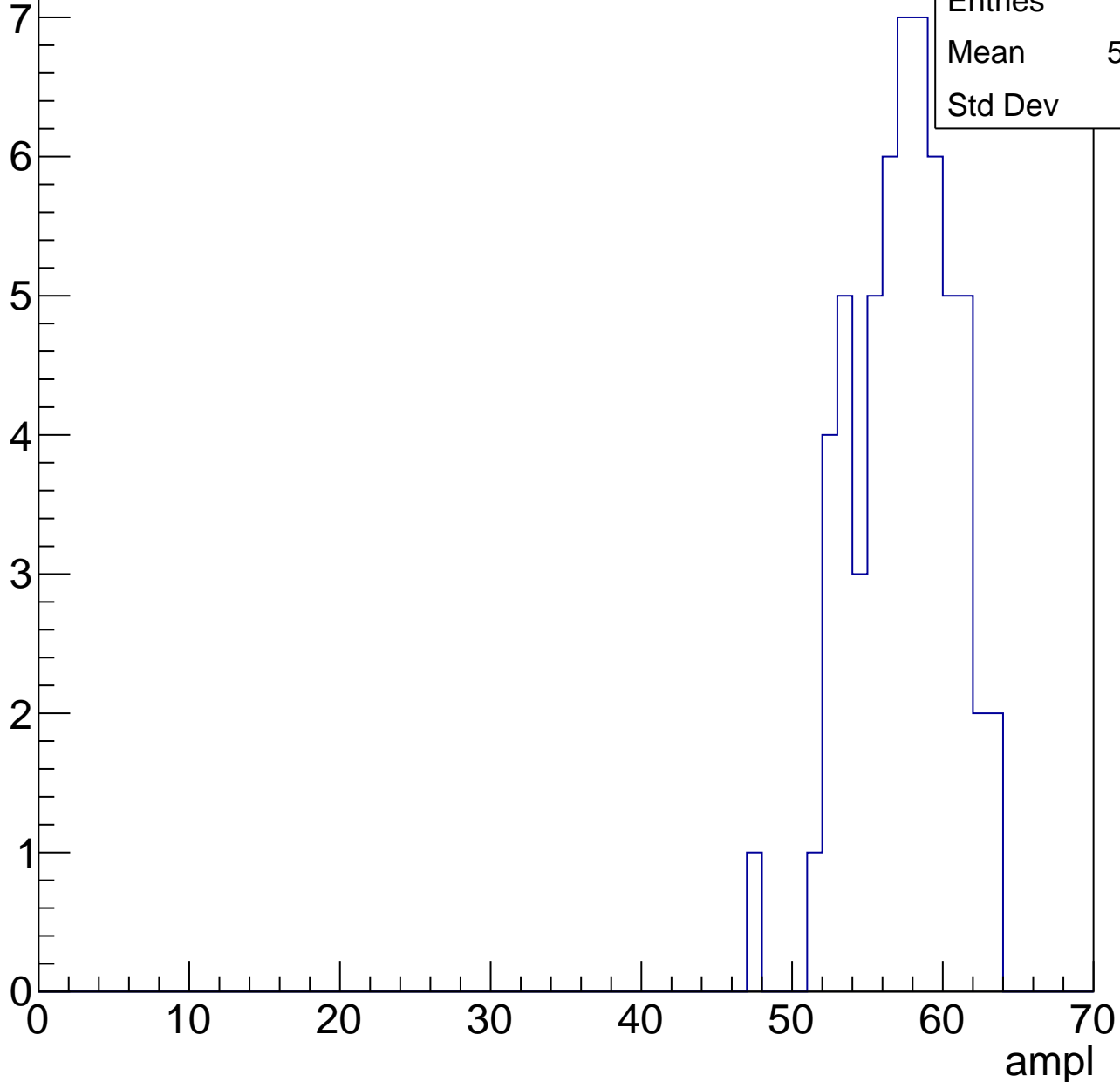


B0L002S, U18-ch111, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	56.92
Std Dev	3.31

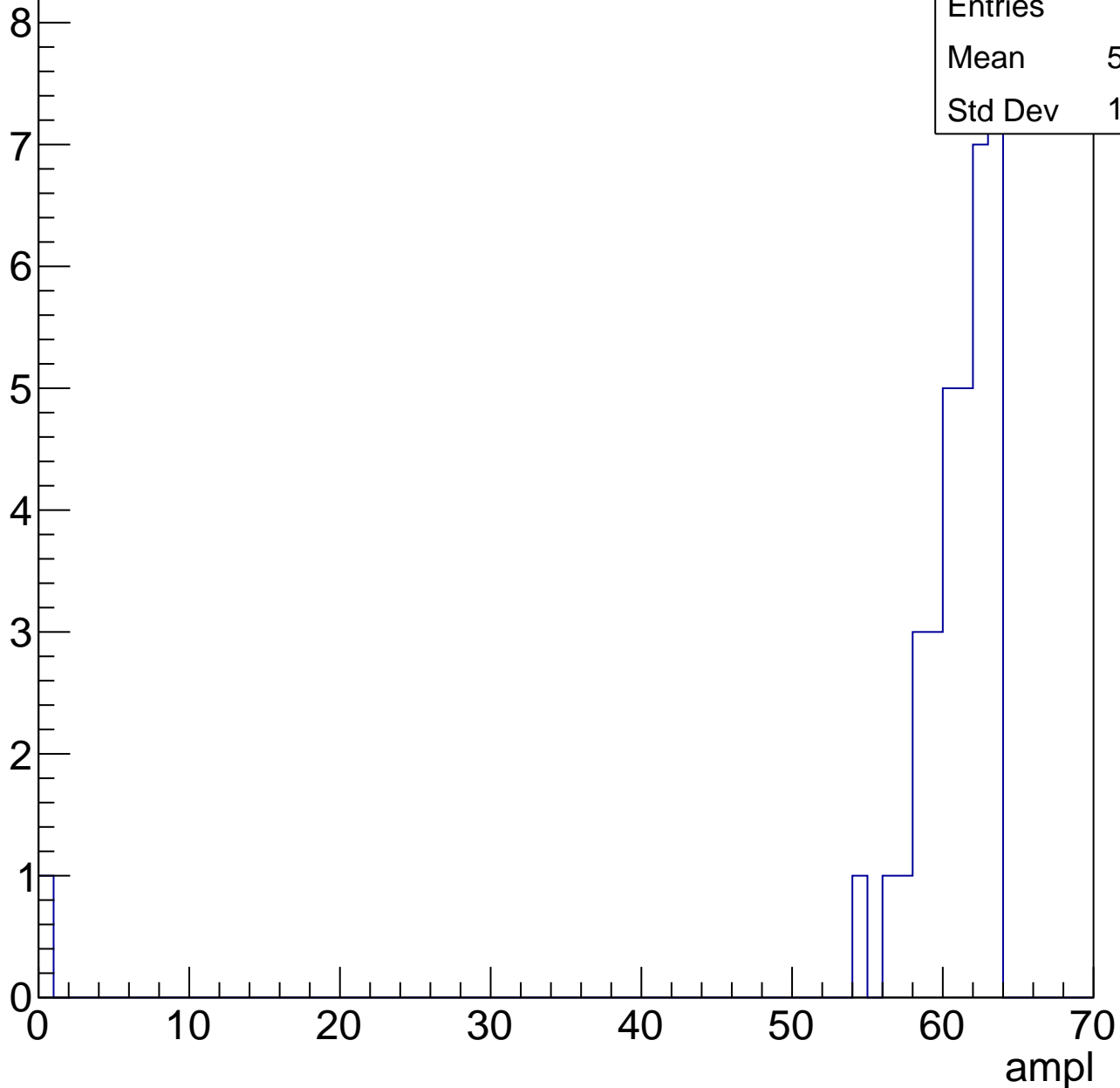


B0L002S, U18-ch111, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	35
Mean	58.89
Std Dev	10.33



B0L002S, U18-ch111, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch111, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch112, adc0

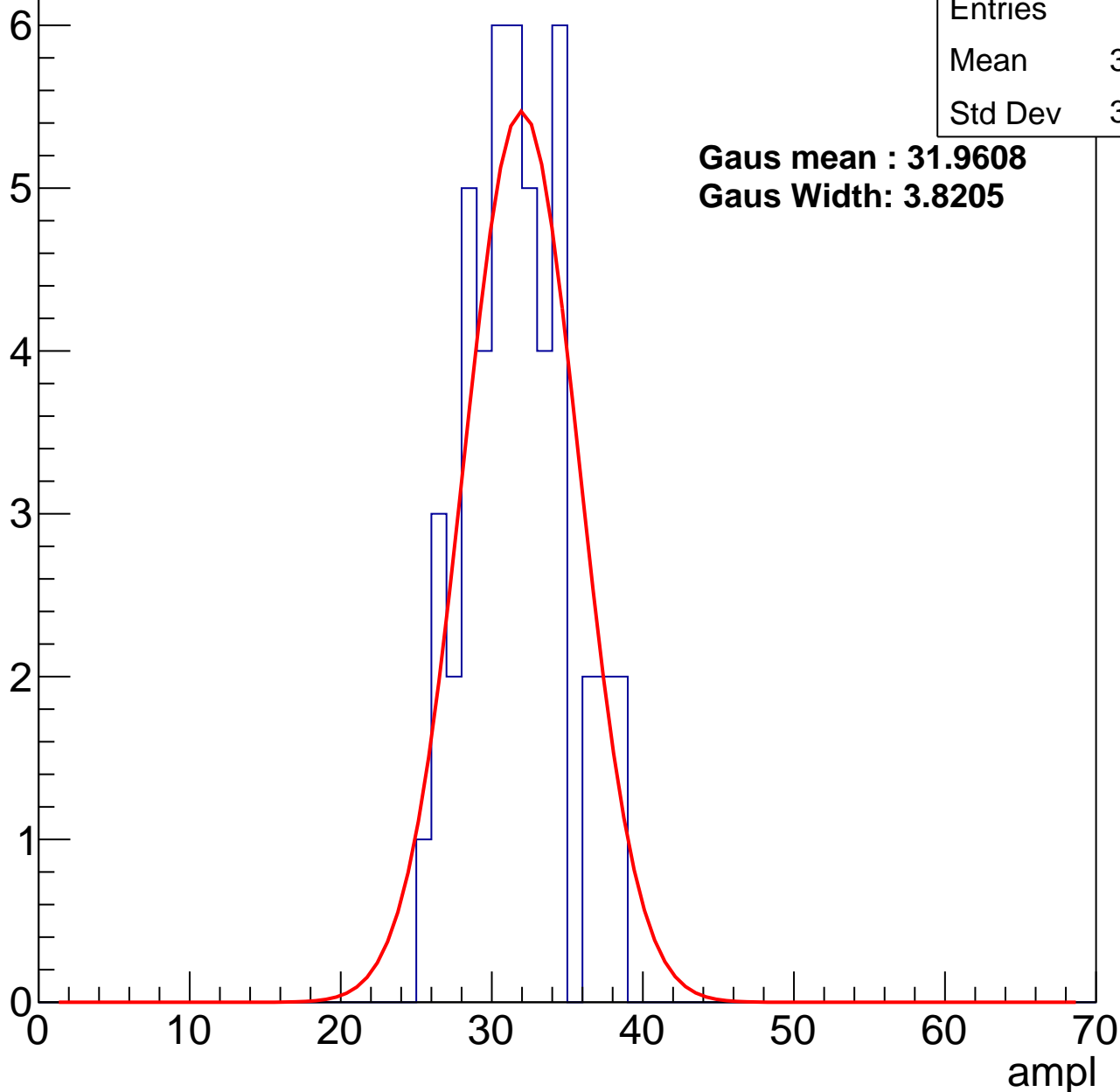
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	31.19
Std Dev	3.238

Gaus mean : 31.9608

Gaus Width: 3.8205



B0L002S, U18-ch112, adc1

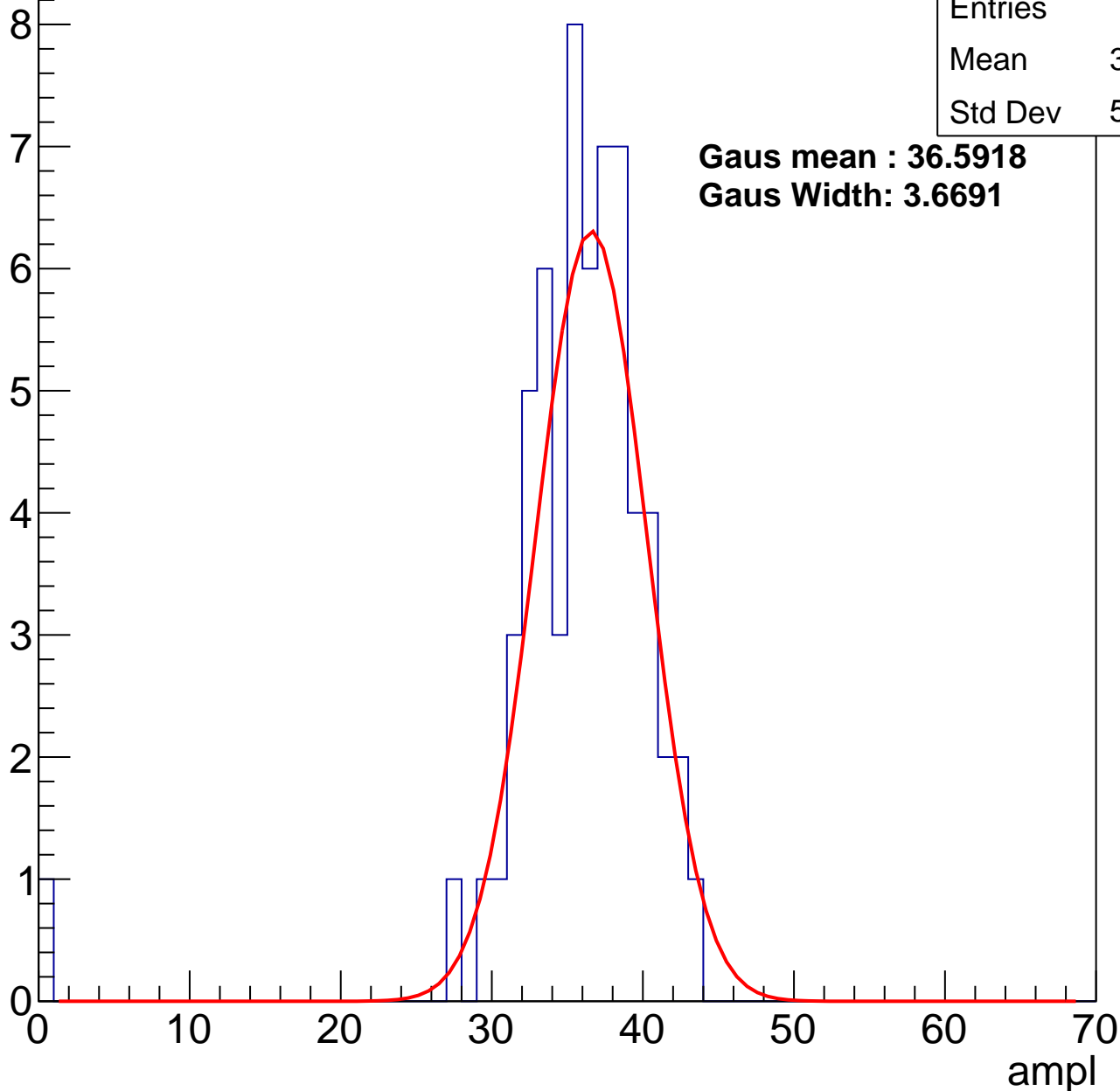
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	62
Mean	35.24
Std Dev	5.625

Gaus mean : 36.5918

Gaus Width: 3.6691



B0L002S, U18-ch112, adc2

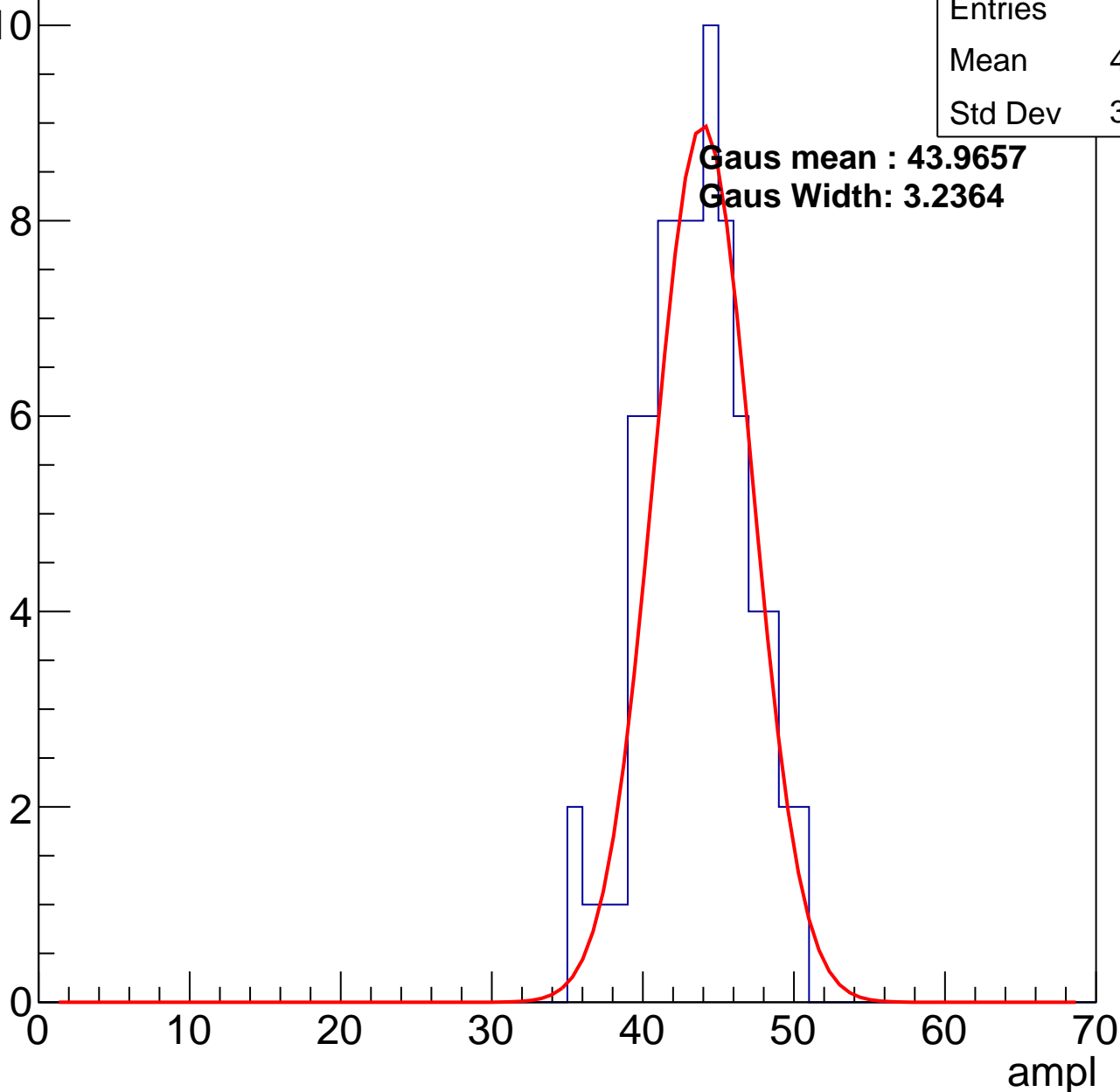
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	43.08
Std Dev	3.337

Gaus mean : 43.9657

Gaus Width: 3.2364

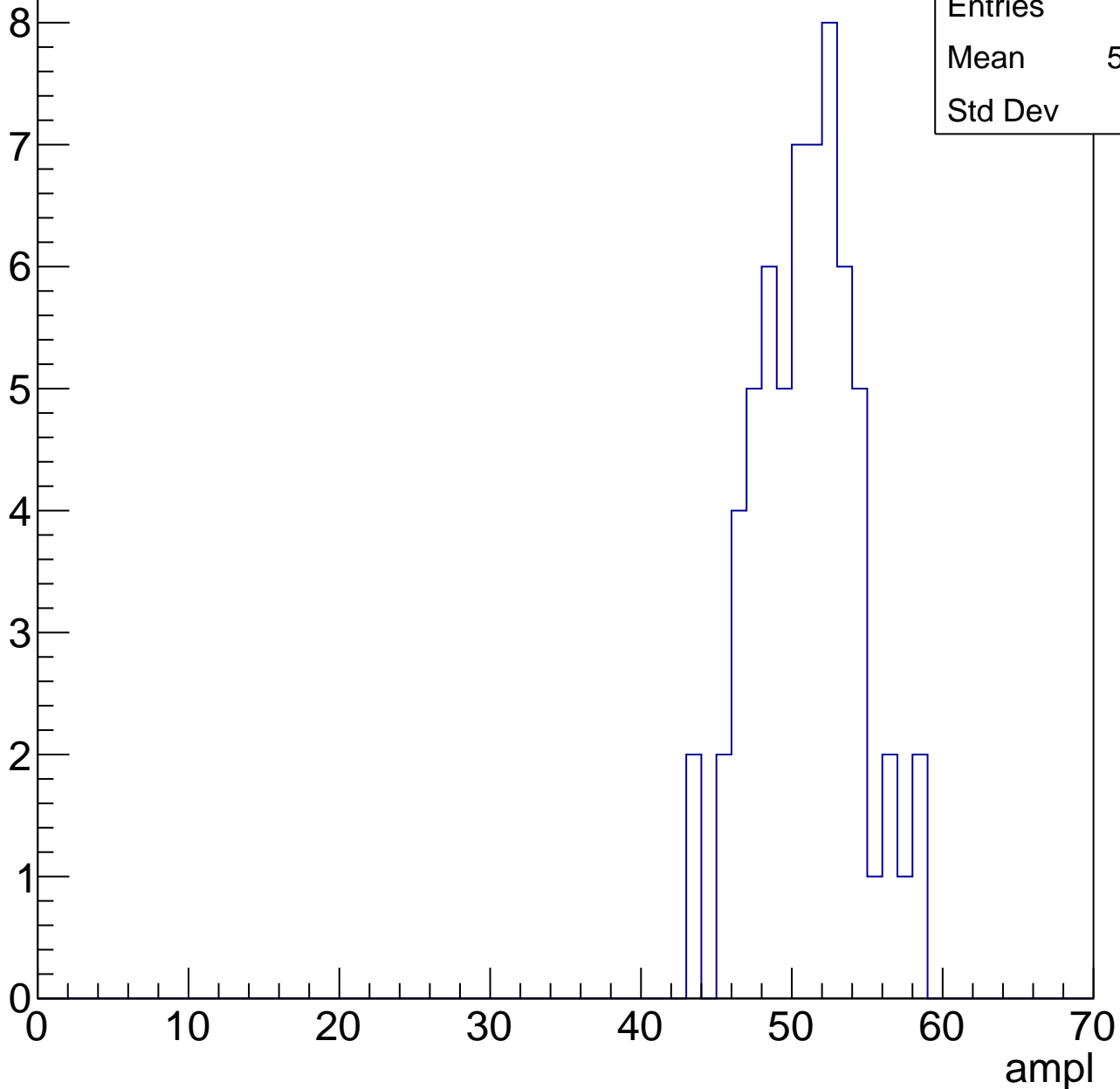


B0L002S, U18-ch112, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	50.46
Std Dev	3.38

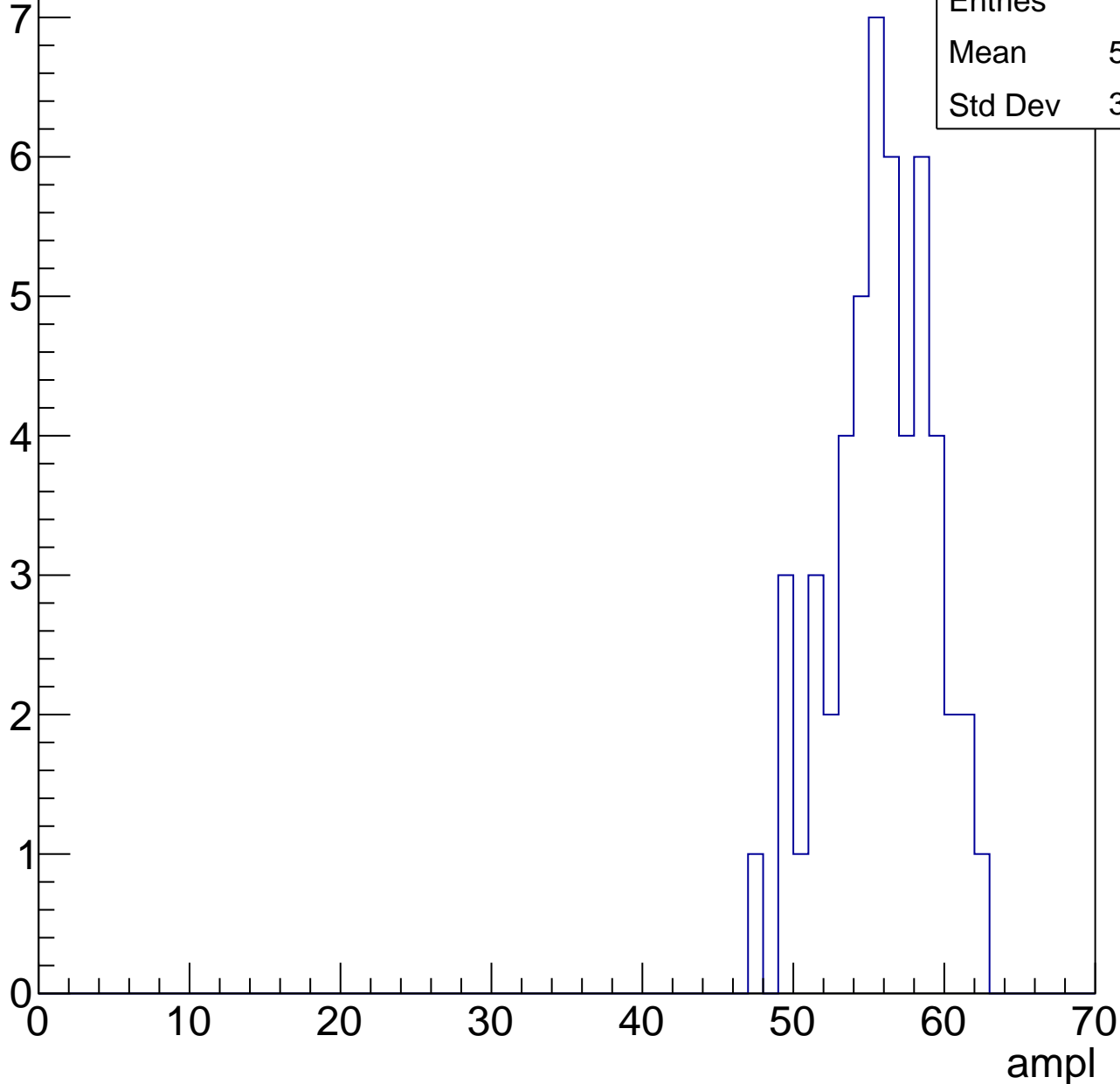


B0L002S, U18-ch112, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

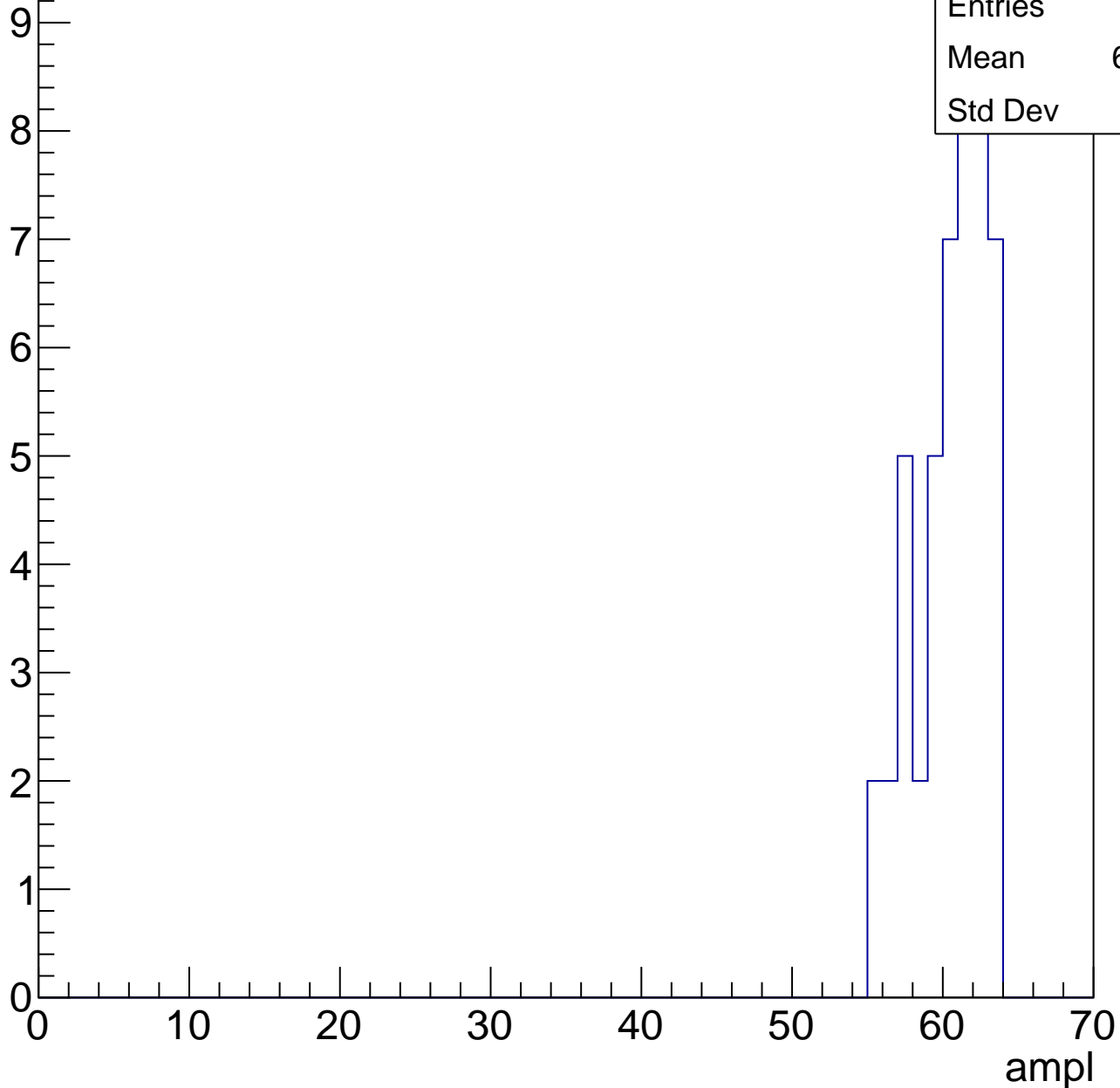
Entries	51
Mean	55.29
Std Dev	3.403



B0L002S, U18-ch112, adc5

calib_packv5_042523_0143.root, FC#8, port C1

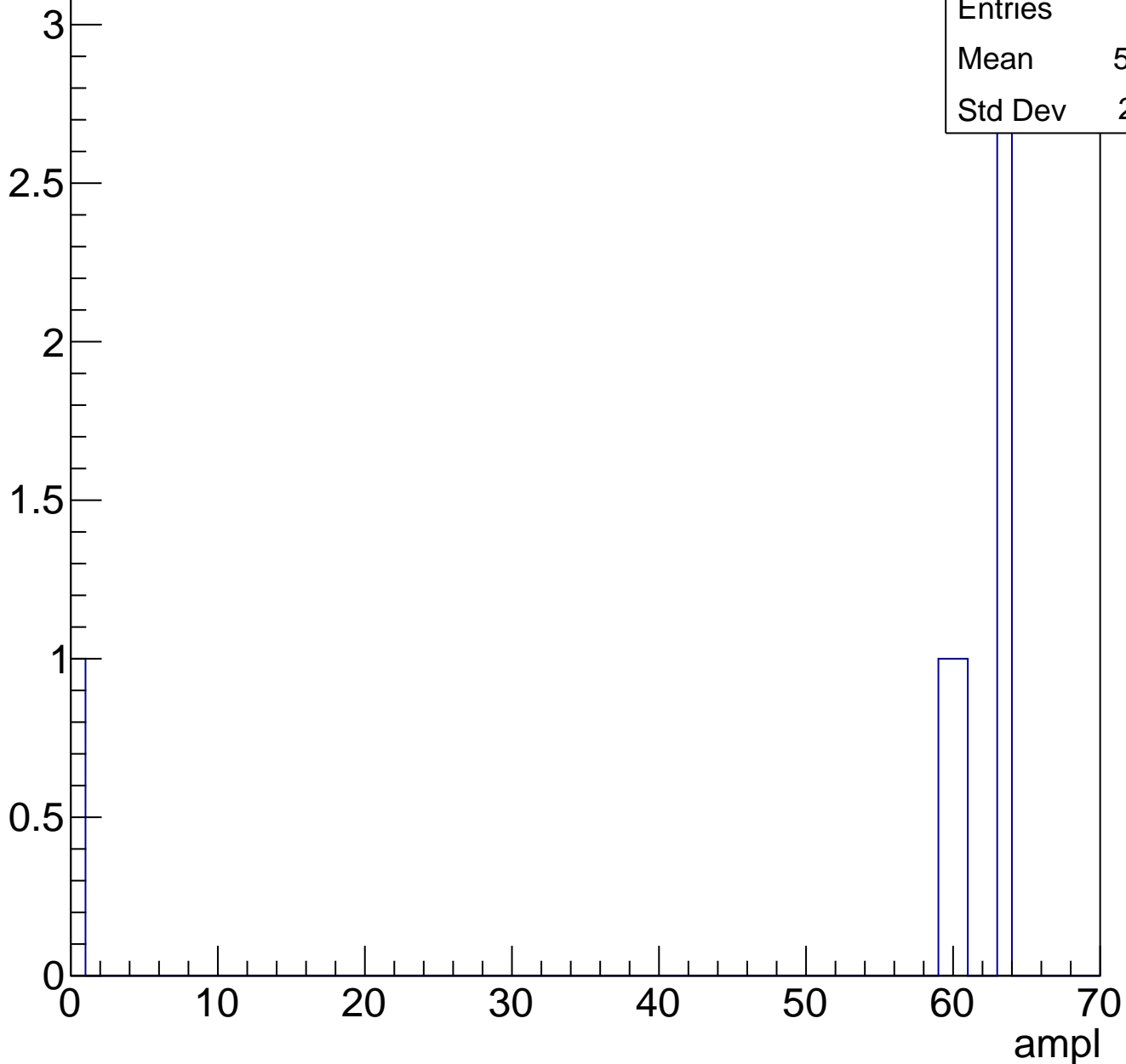
Entry



B0L002S, U18-ch112, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch112, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch113, adc0

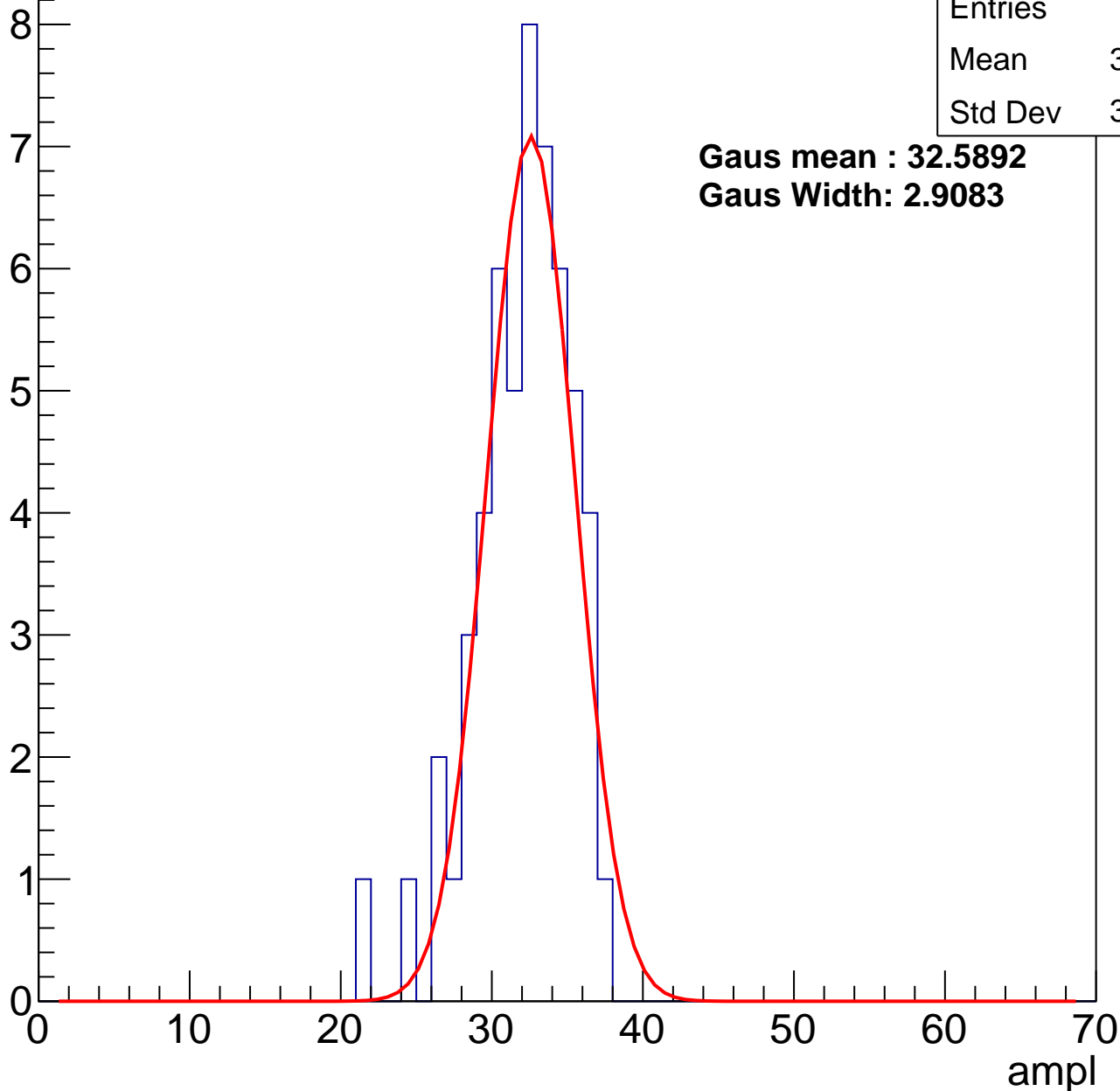
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	31.59
Std Dev	3.194

Gaus mean : 32.5892

Gaus Width: 2.9083



B0L002S, U18-ch113, adc1

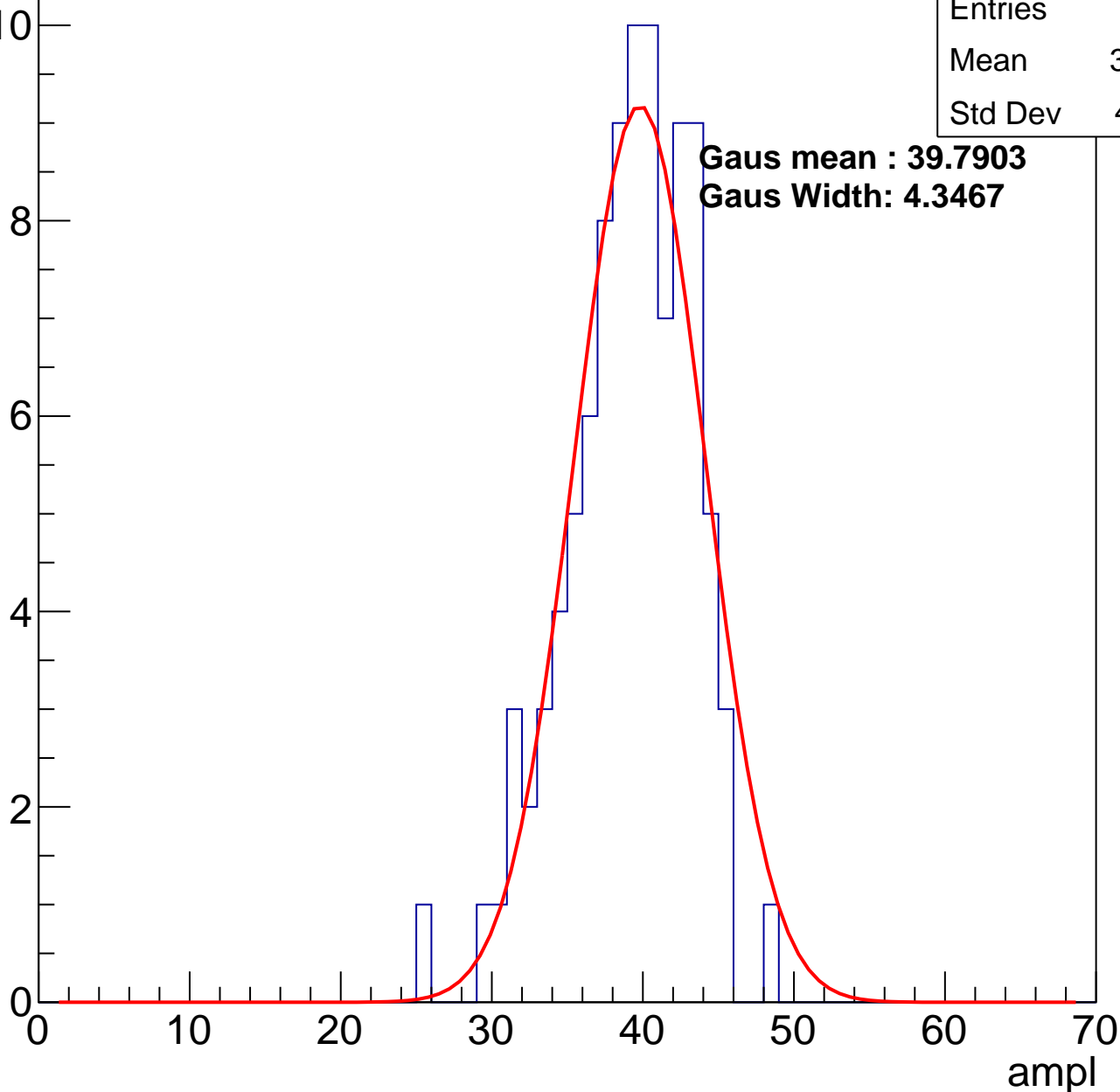
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	97
Mean	38.66
Std Dev	4.081

Gaus mean : 39.7903

Gaus Width: 4.3467



B0L002S, U18-ch113, adc2

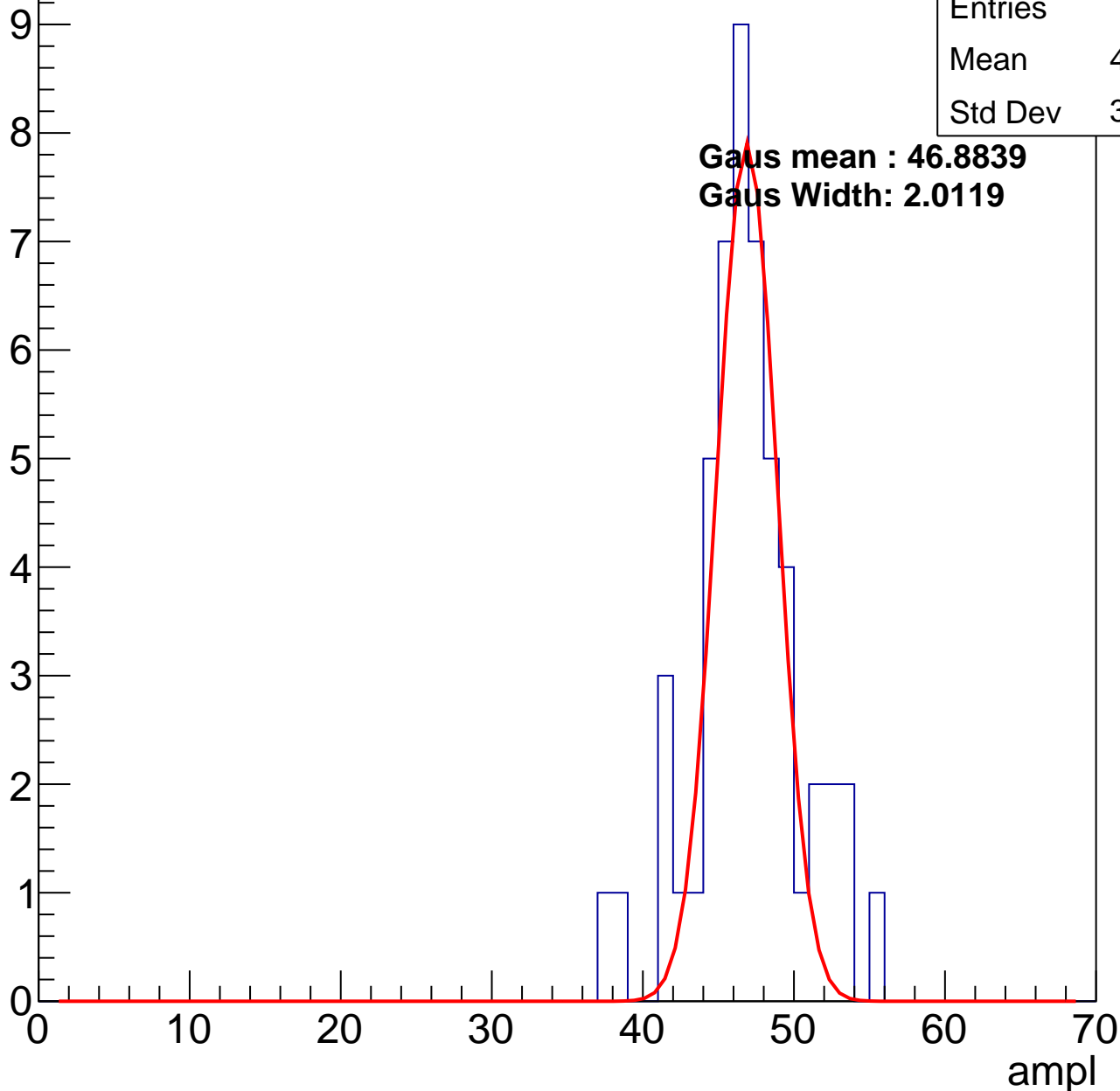
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	46.42
Std Dev	3.516

Gaus mean : 46.8839

Gaus Width: 2.0119

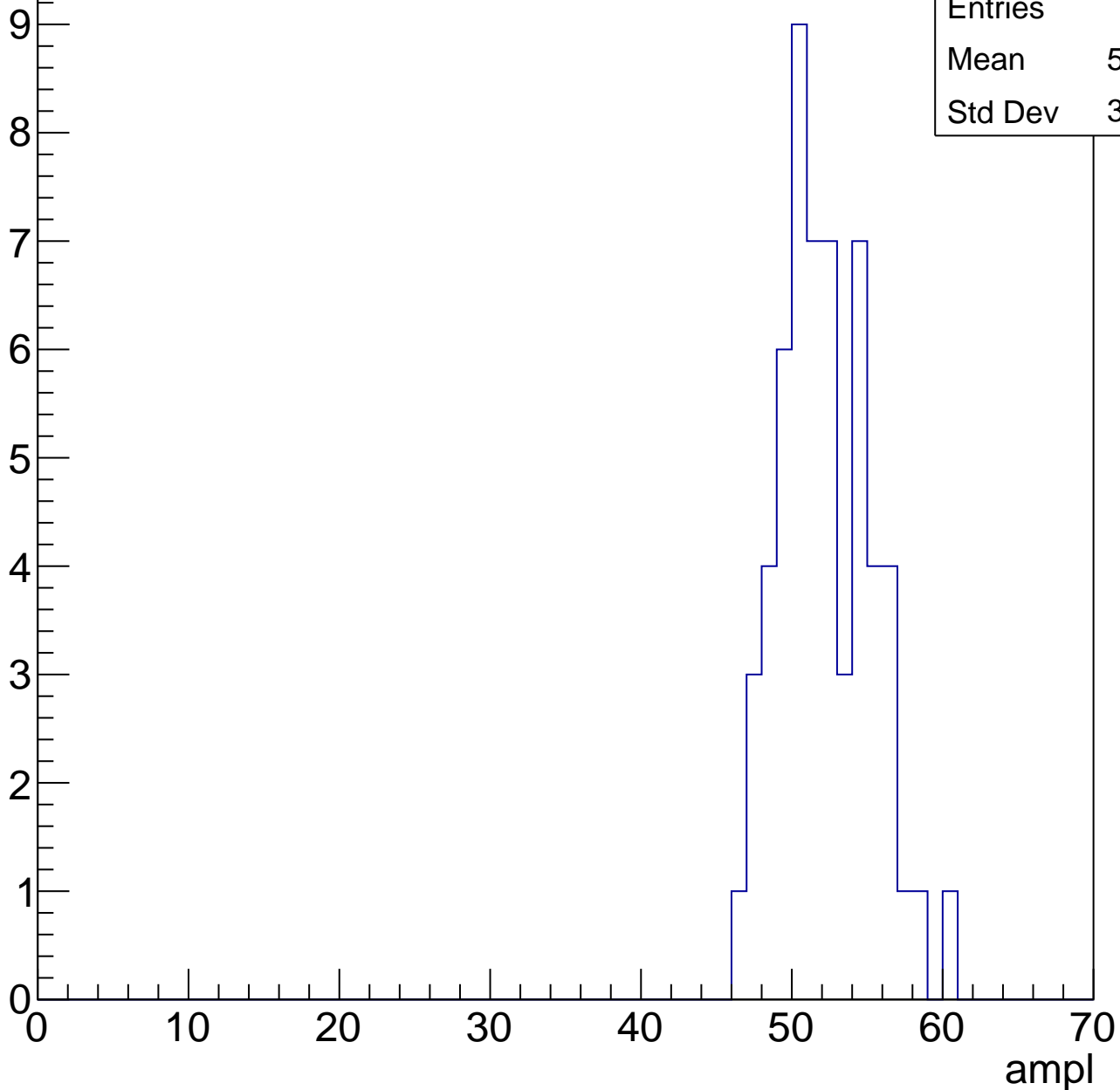


B0L002S, U18-ch113, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

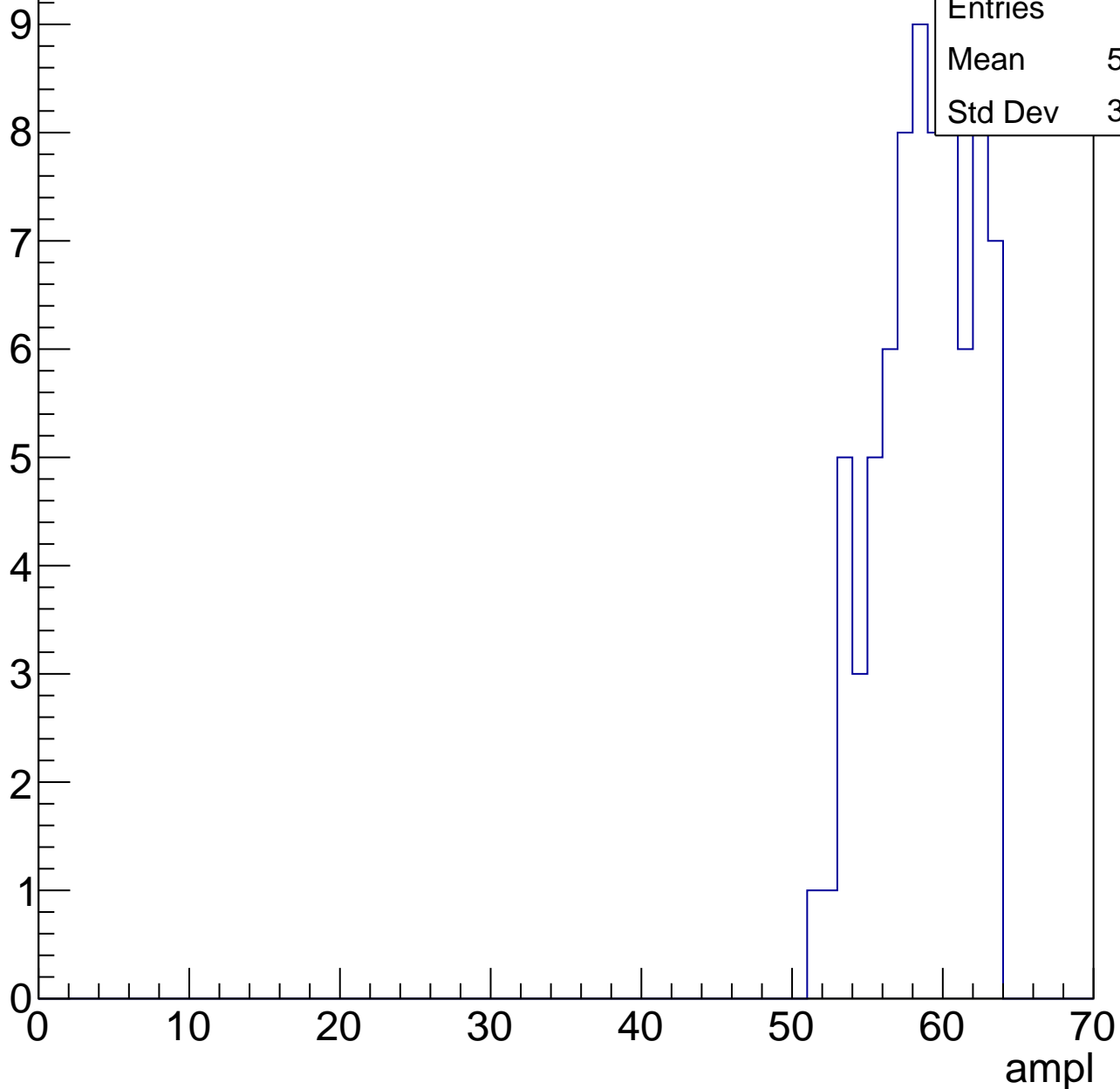
Entries	58
Mean	51.72
Std Dev	3.005



B0L002S, U18-ch113, adc4

calib_packv5_042523_0143.root, FC#8, port C1

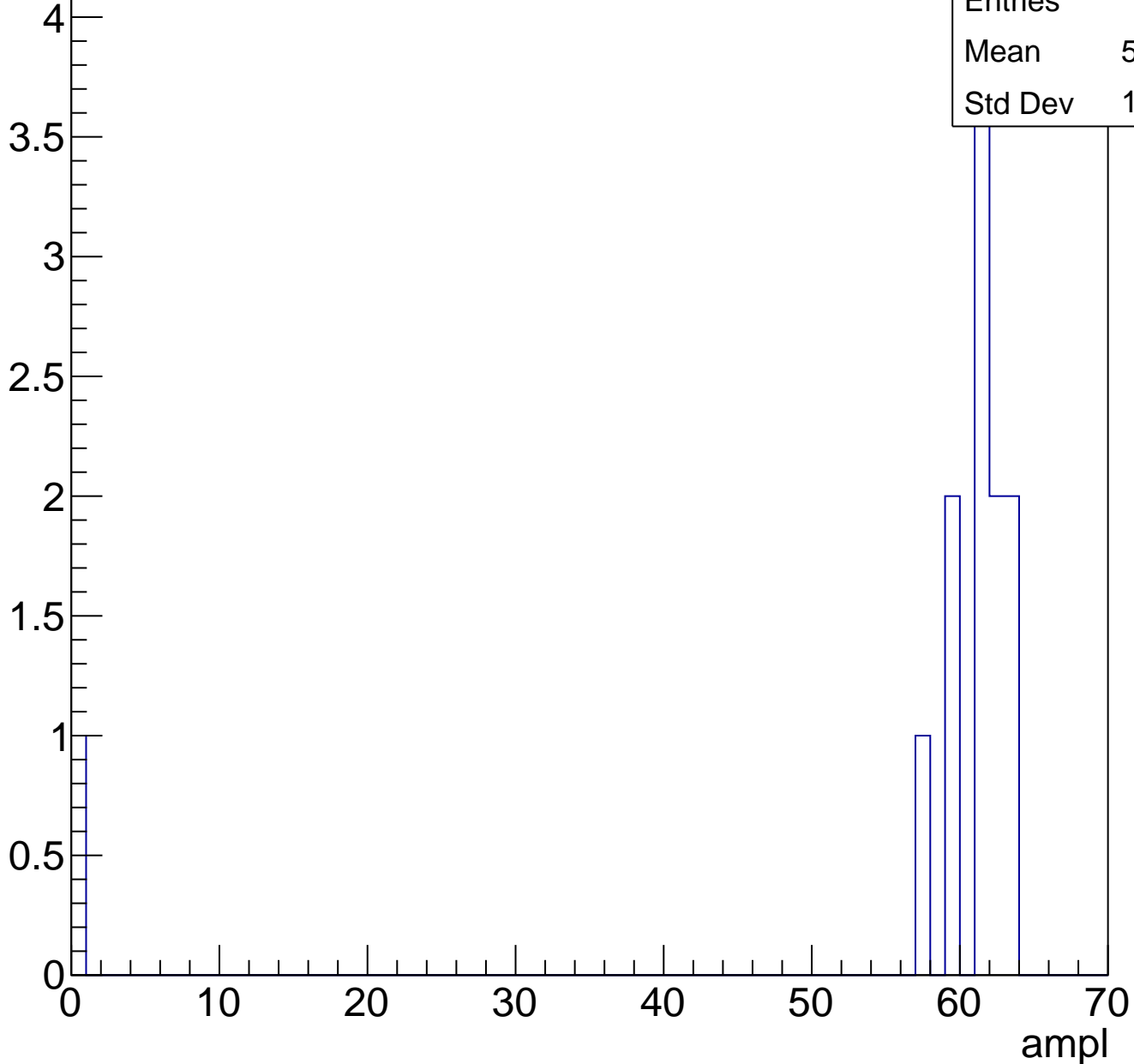
Entry



B0L002S, U18-ch113, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	12
Mean	55.75
Std Dev	16.89

B0L002S, U18-ch113, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch113, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch114, adc0

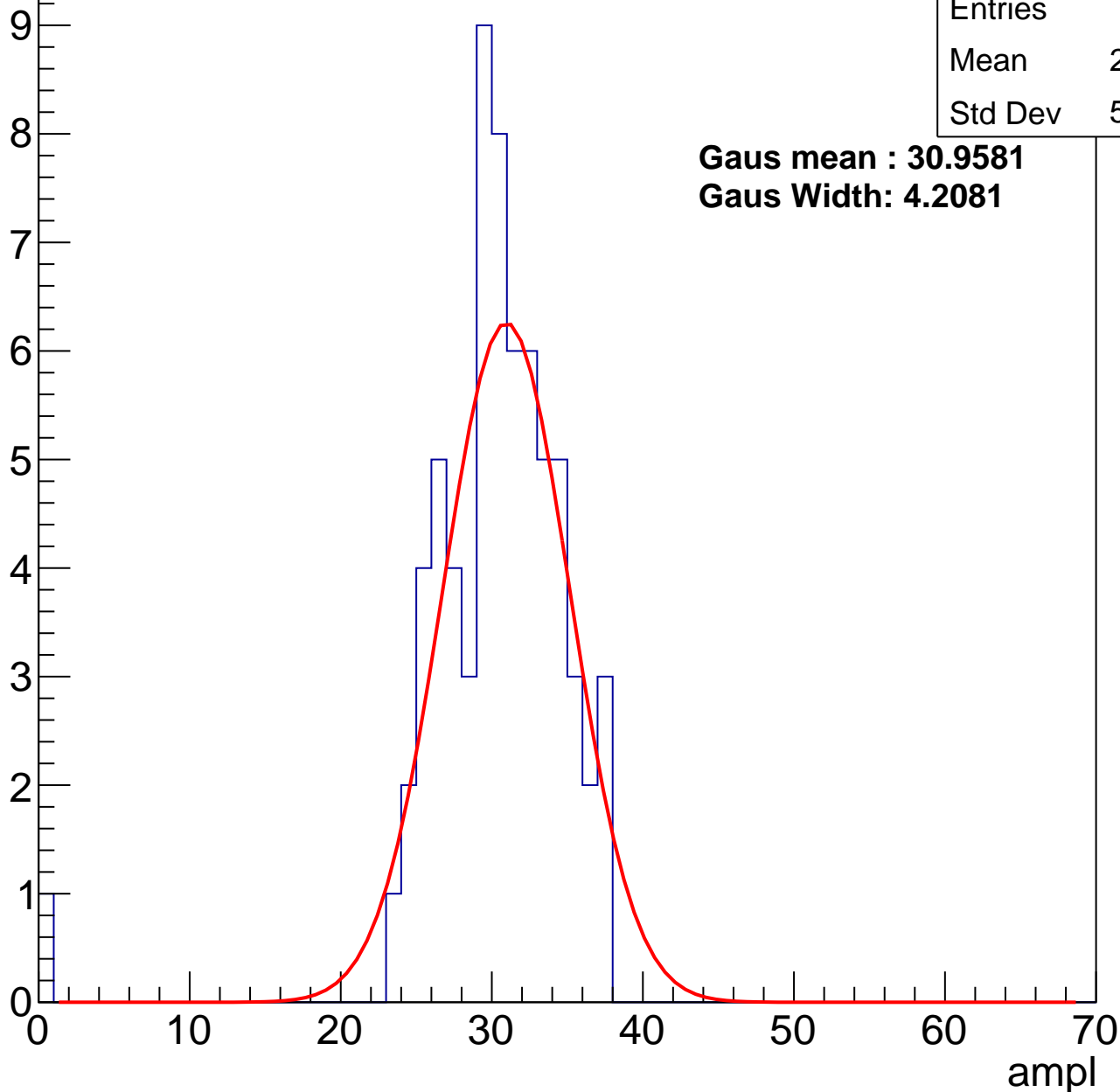
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	29.78
Std Dev	5.043

Gaus mean : 30.9581

Gaus Width: 4.2081



B0L002S, U18-ch114, adc1

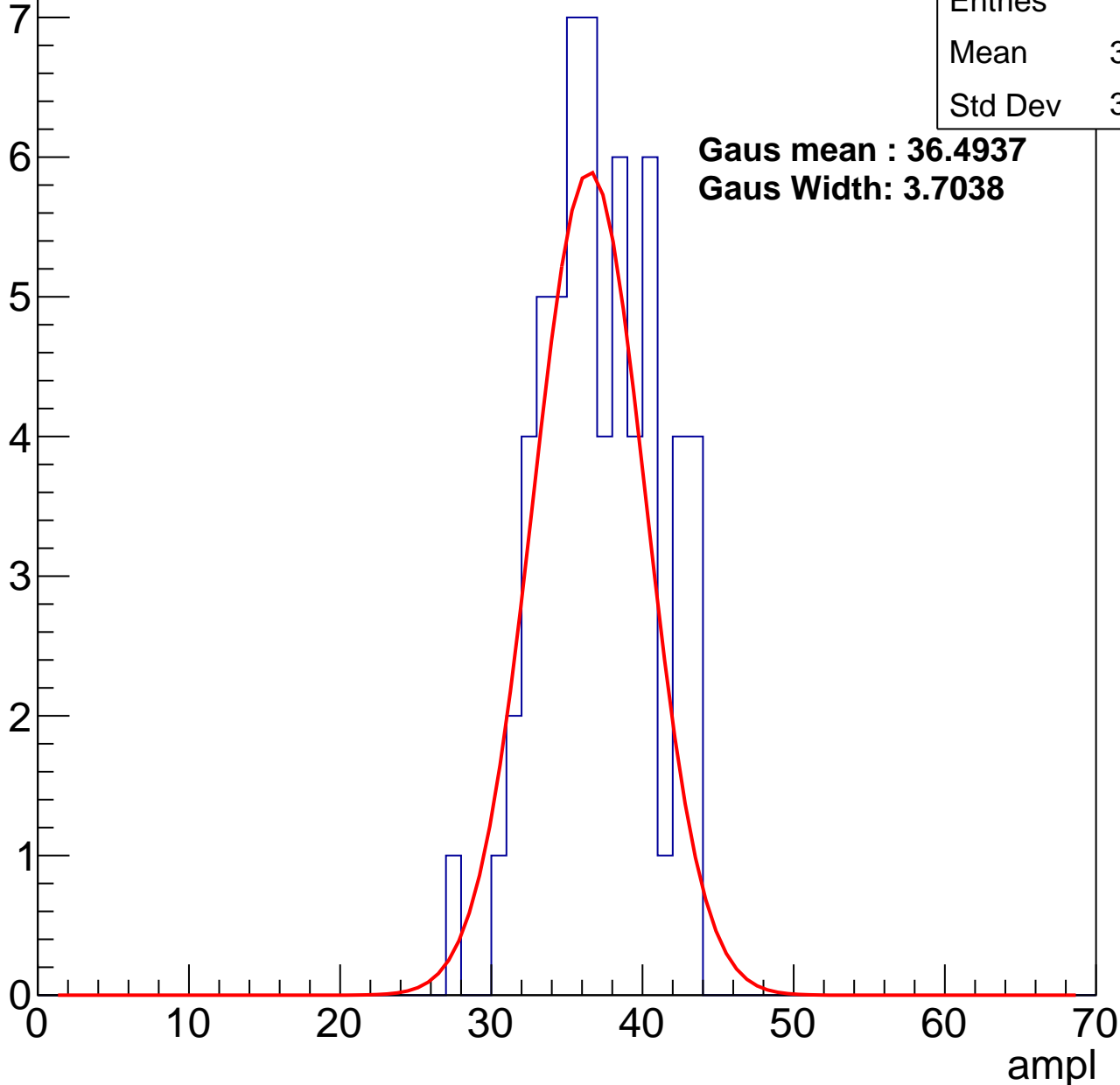
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	36.59
Std Dev	3.637

Gaus mean : 36.4937

Gaus Width: 3.7038



B0L002S, U18-ch114, adc2

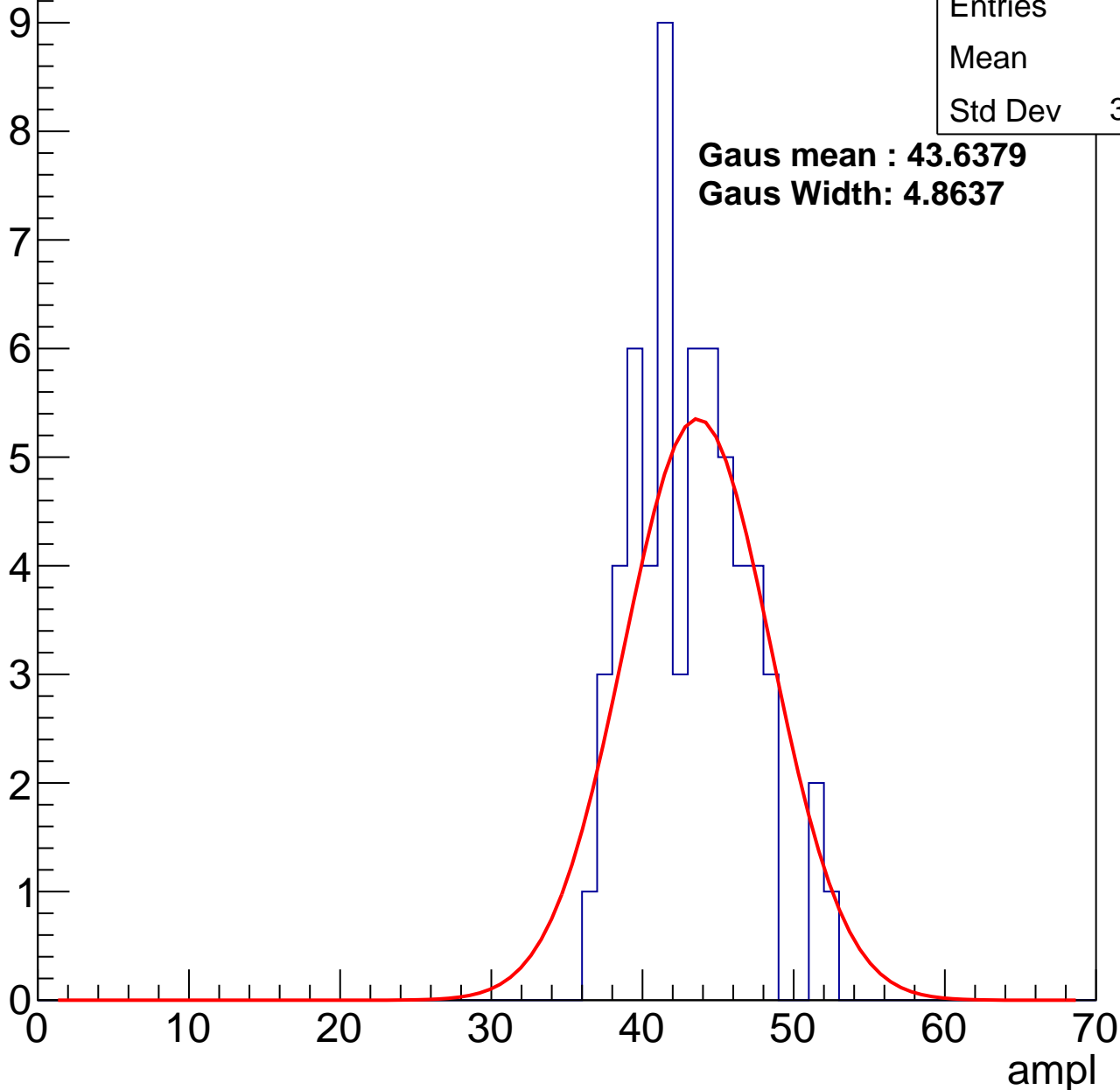
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	42.7
Std Dev	3.695

Gaus mean : 43.6379

Gaus Width: 4.8637

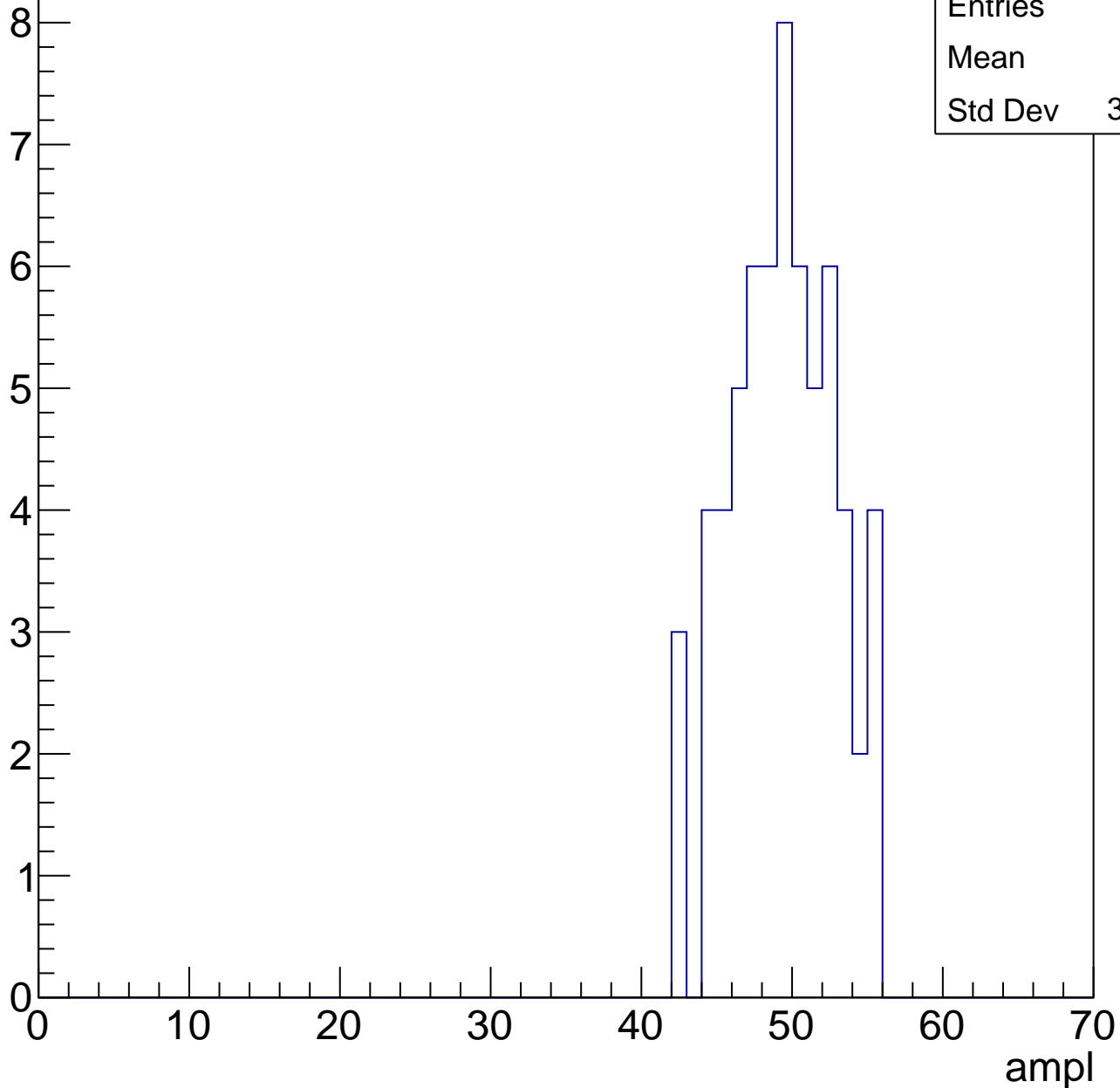


B0L002S, U18-ch114, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	48.9
Std Dev	3.389

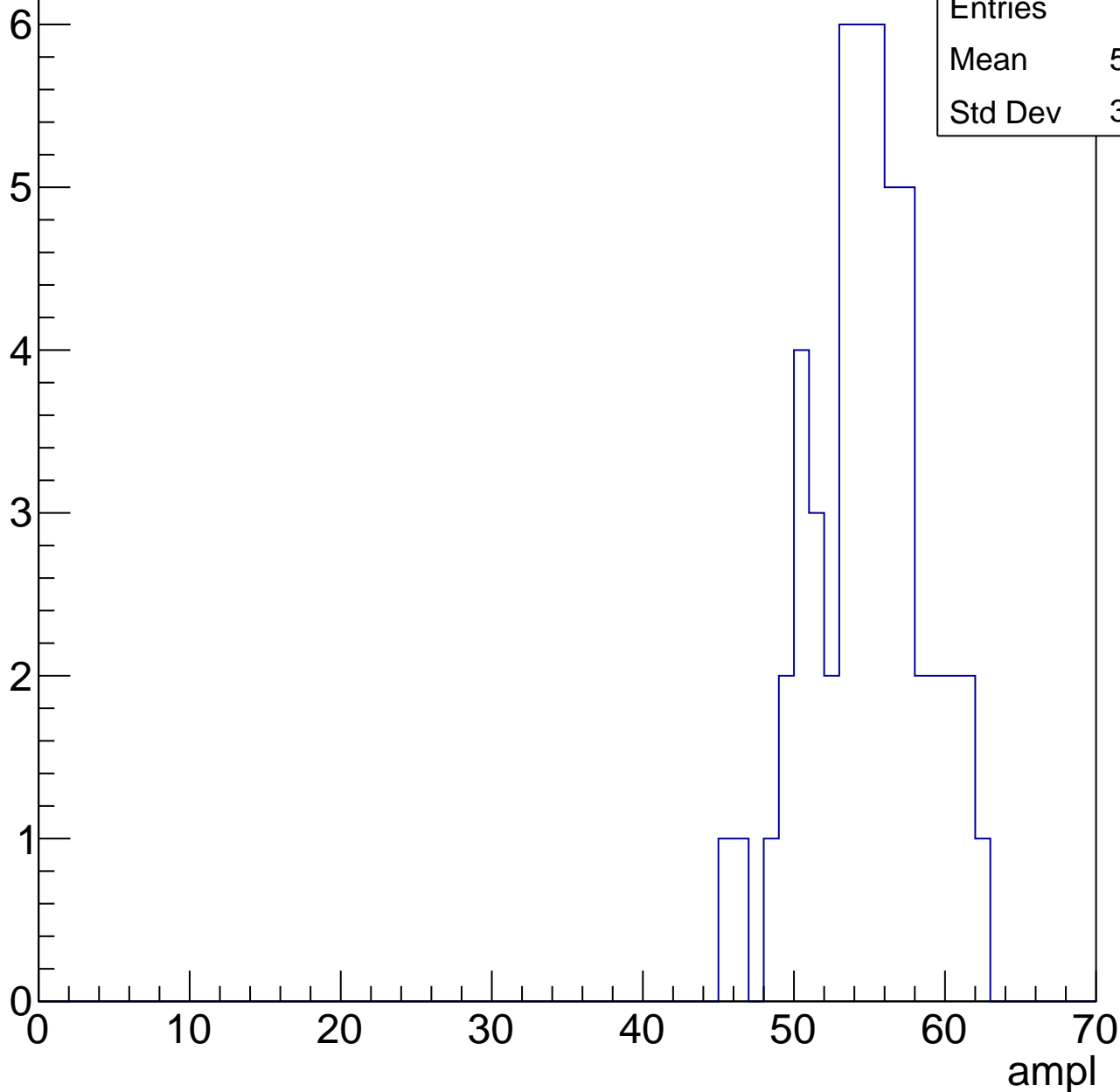


B0L002S, U18-ch114, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	54.29
Std Dev	3.754

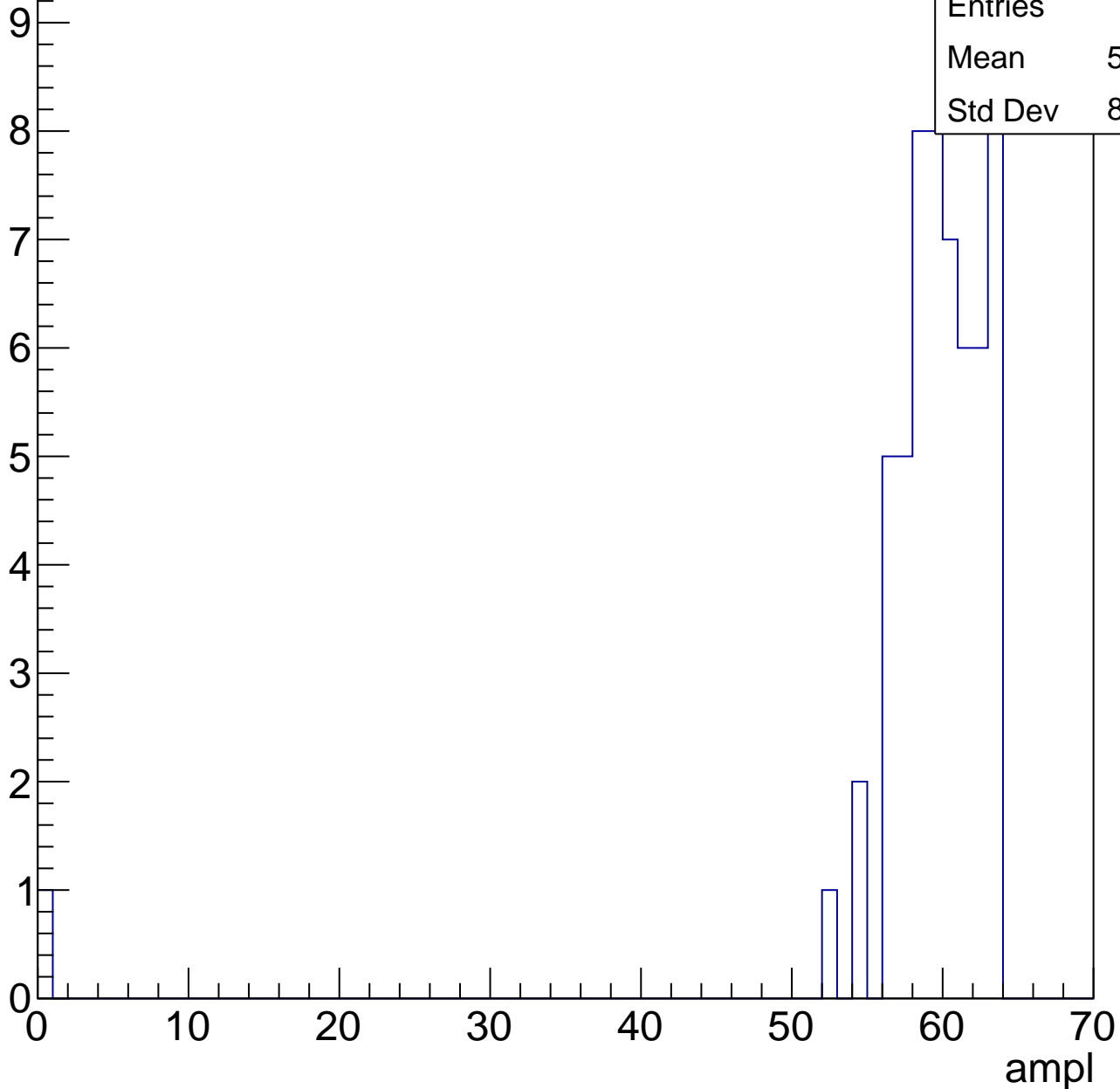


B0L002S, U18-ch114, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

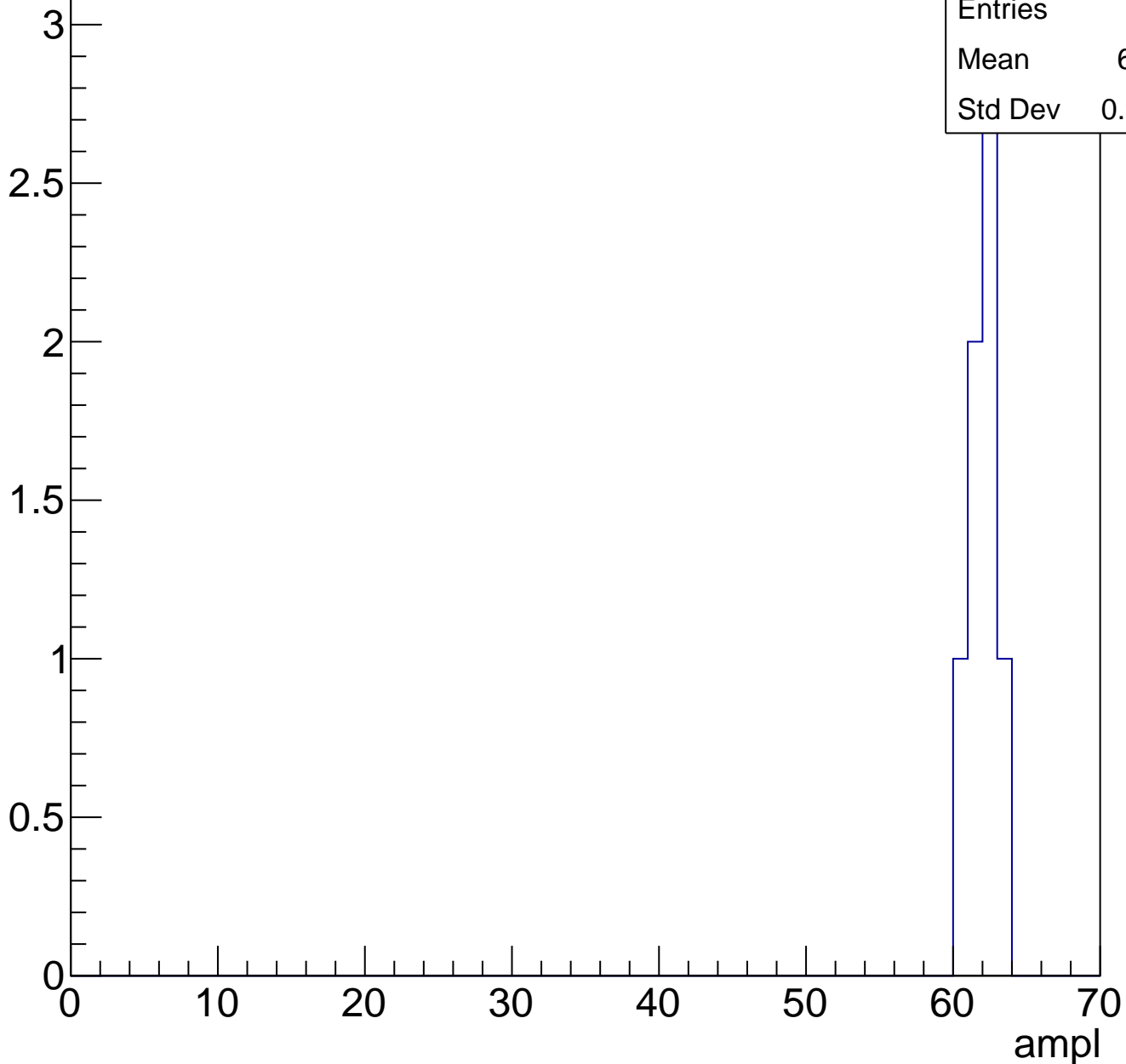
Entries	58
Mean	58.38
Std Dev	8.158



B0L002S, U18-ch114, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch114, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch115, adc0

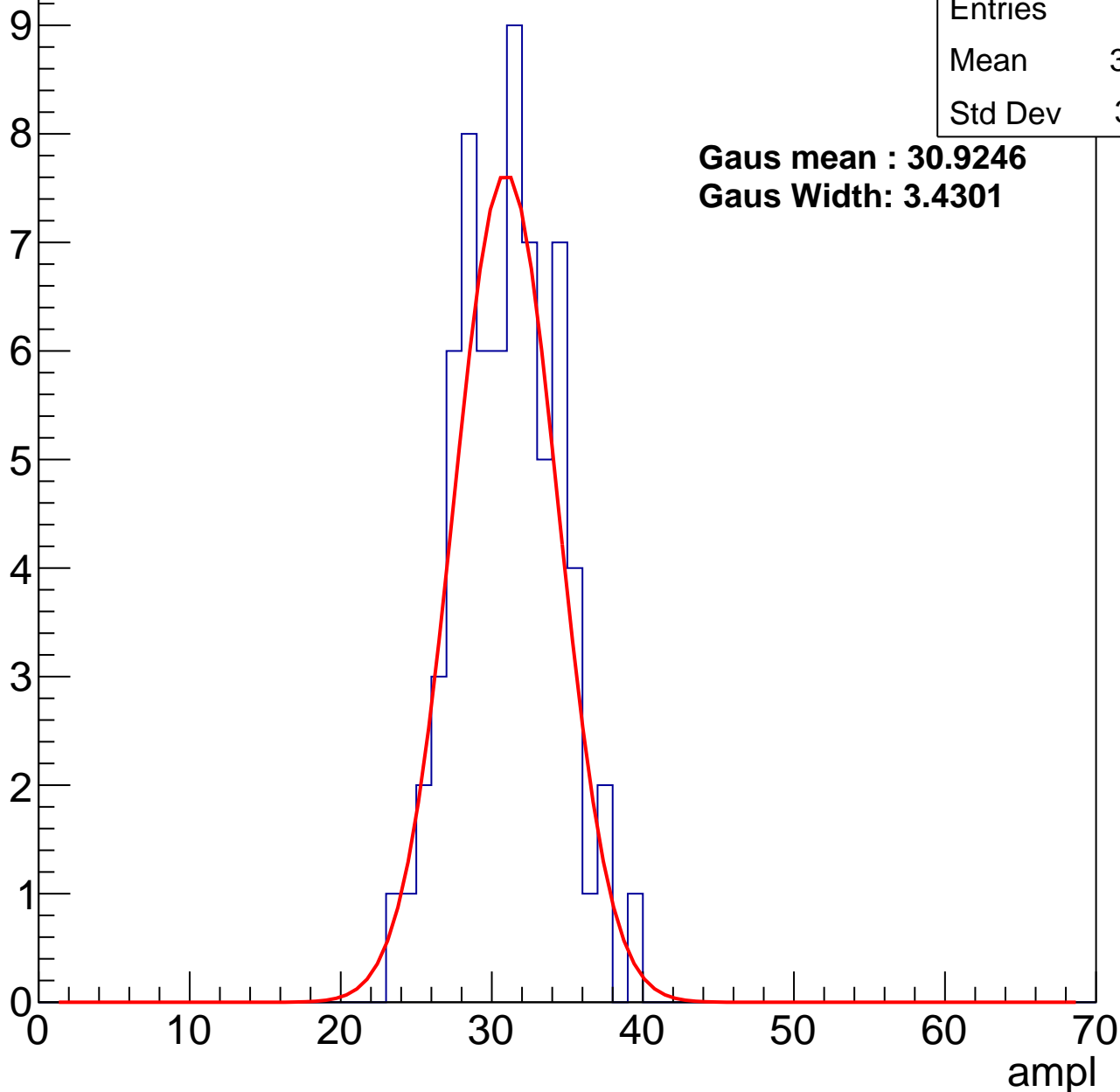
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	30.58
Std Dev	3.321

Gaus mean : 30.9246

Gaus Width: 3.4301



B0L002S, U18-ch115, adc1

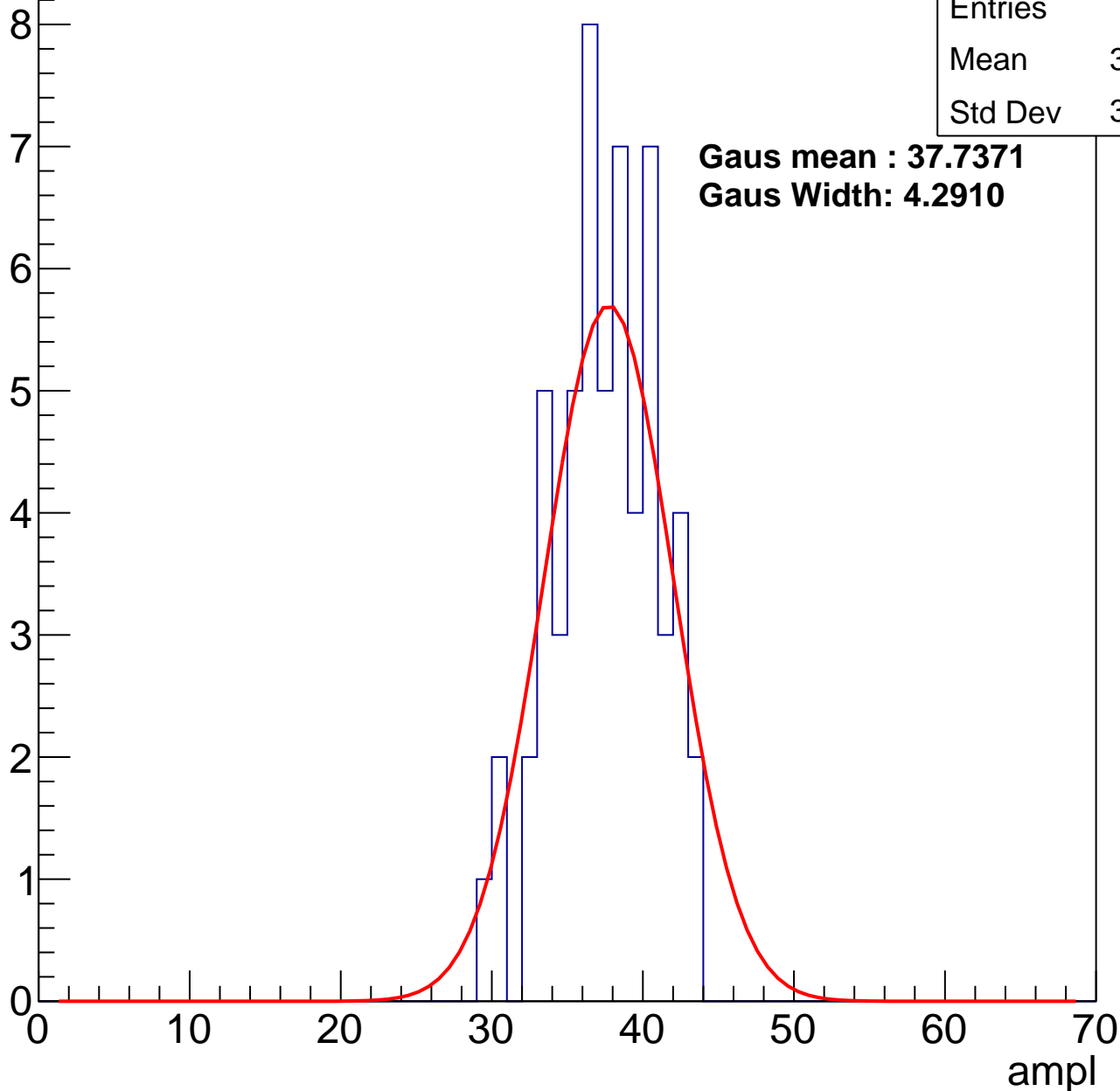
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	37.02
Std Dev	3.355

Gaus mean : 37.7371

Gaus Width: 4.2910



B0L002S, U18-ch115, adc2

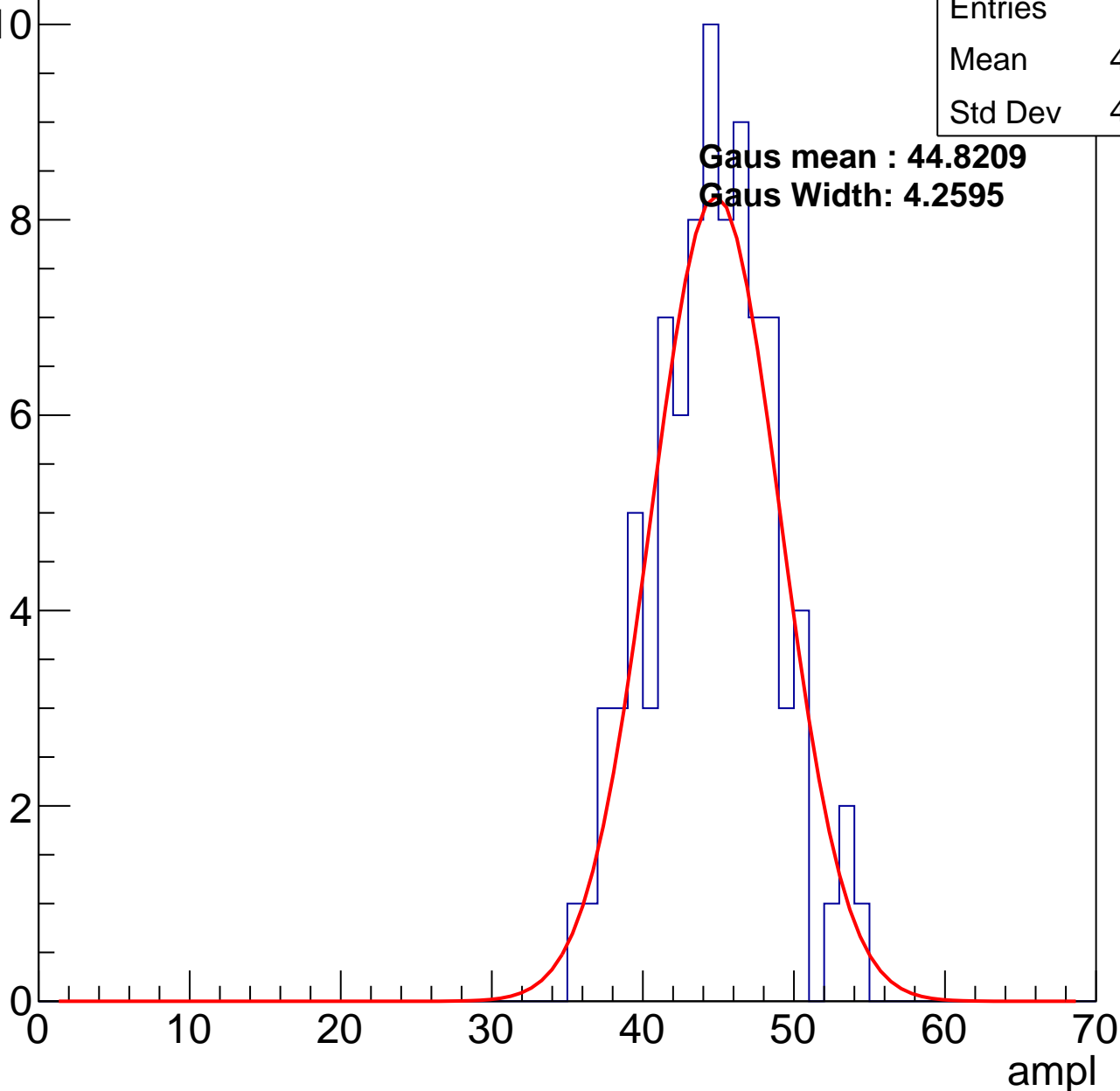
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	89
Mean	44.18
Std Dev	4.007

Gaus mean : 44.8209

Gaus Width: 4.2595

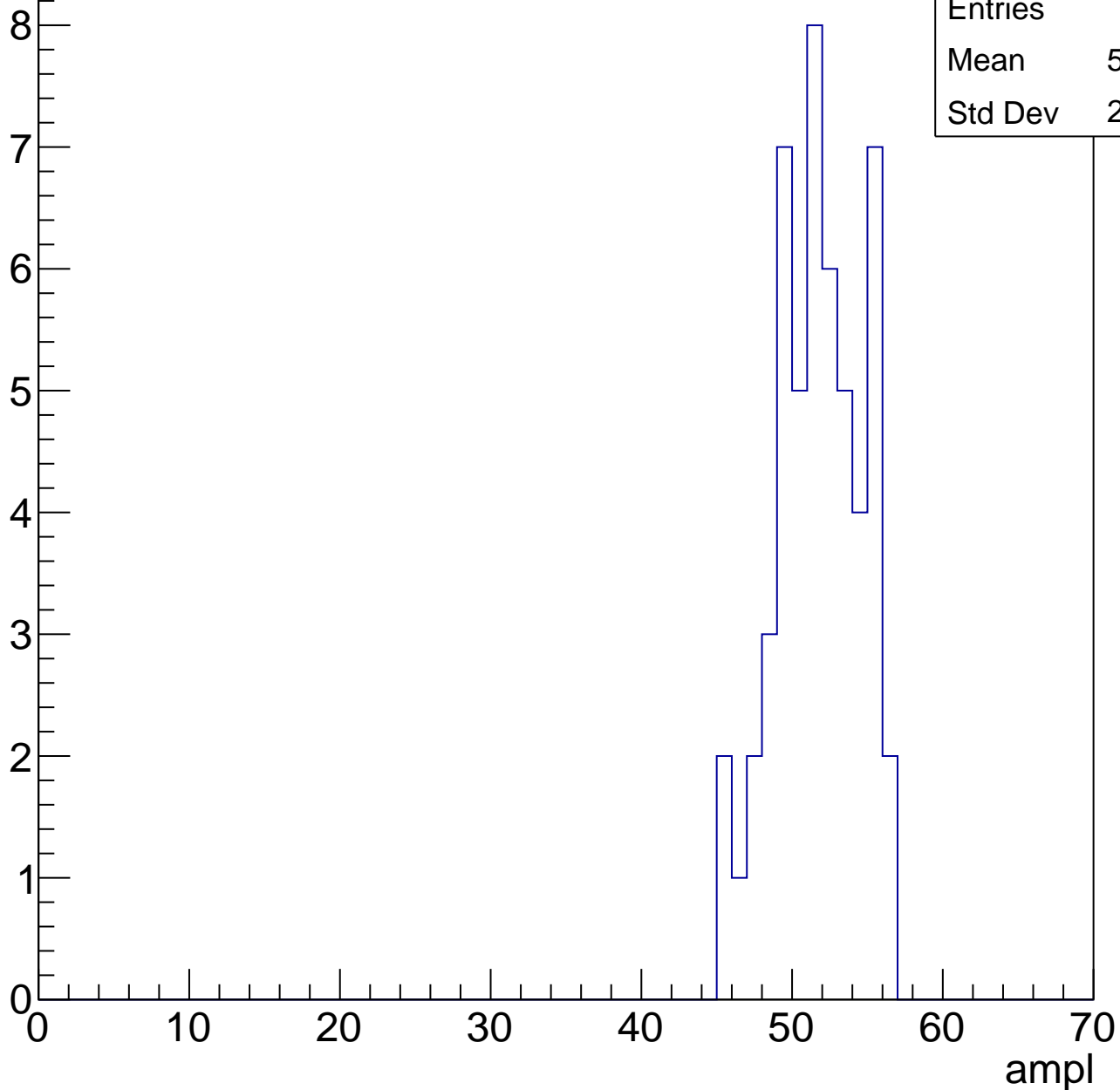


B0L002S, U18-ch115, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	52
Mean	51.25
Std Dev	2.807

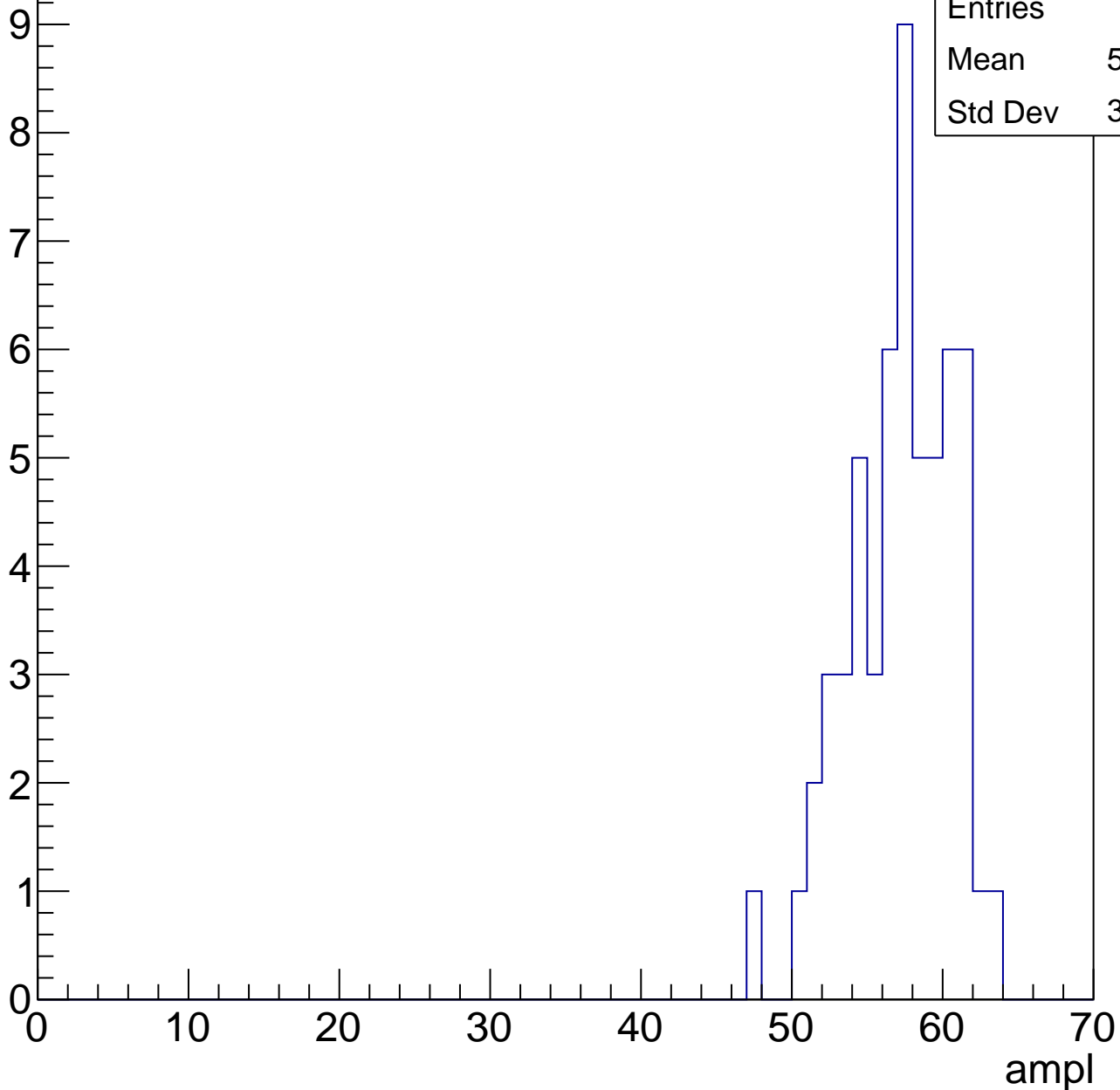


B0L002S, U18-ch115, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	56.74
Std Dev	3.348

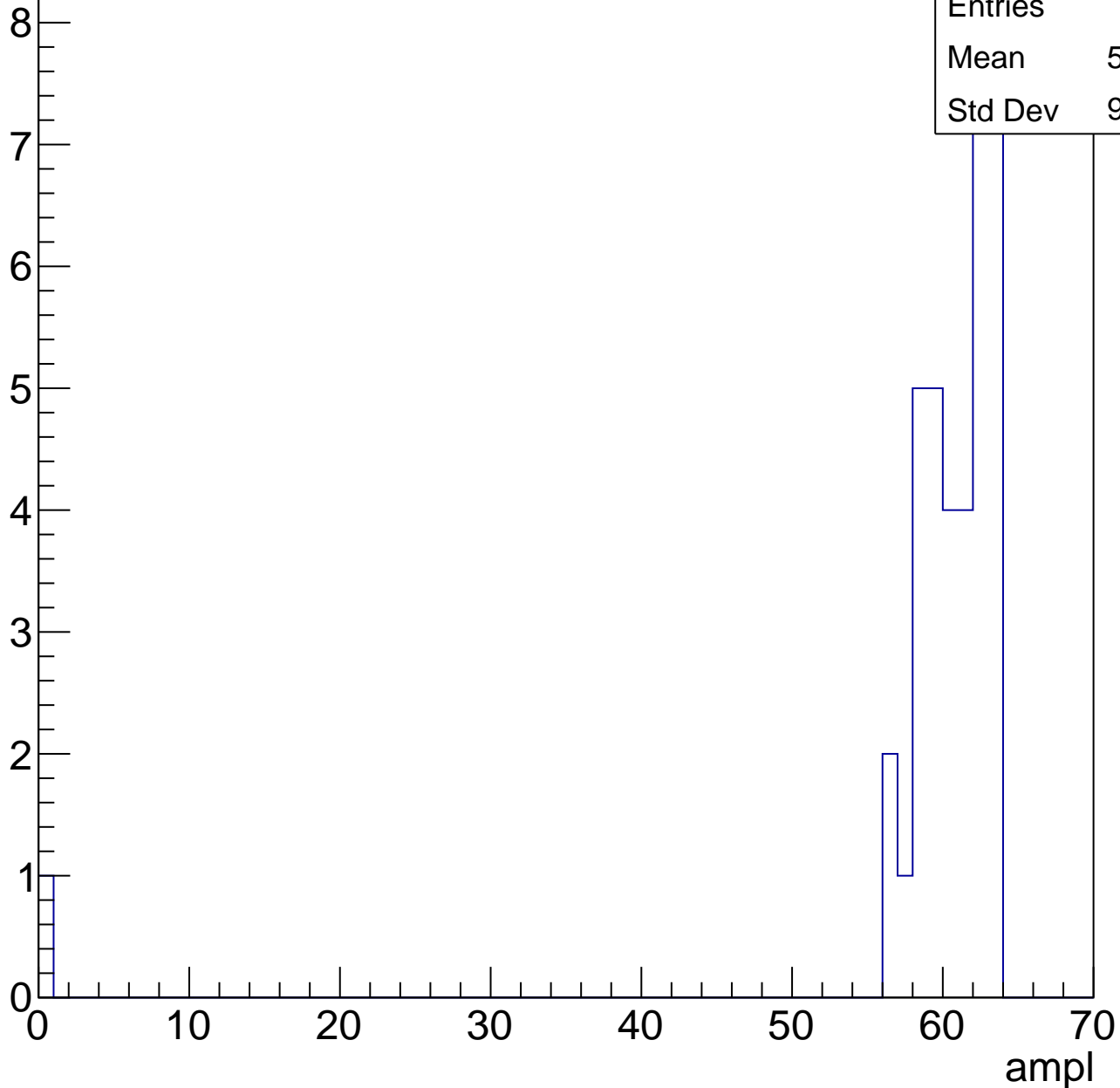


B0L002S, U18-ch115, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

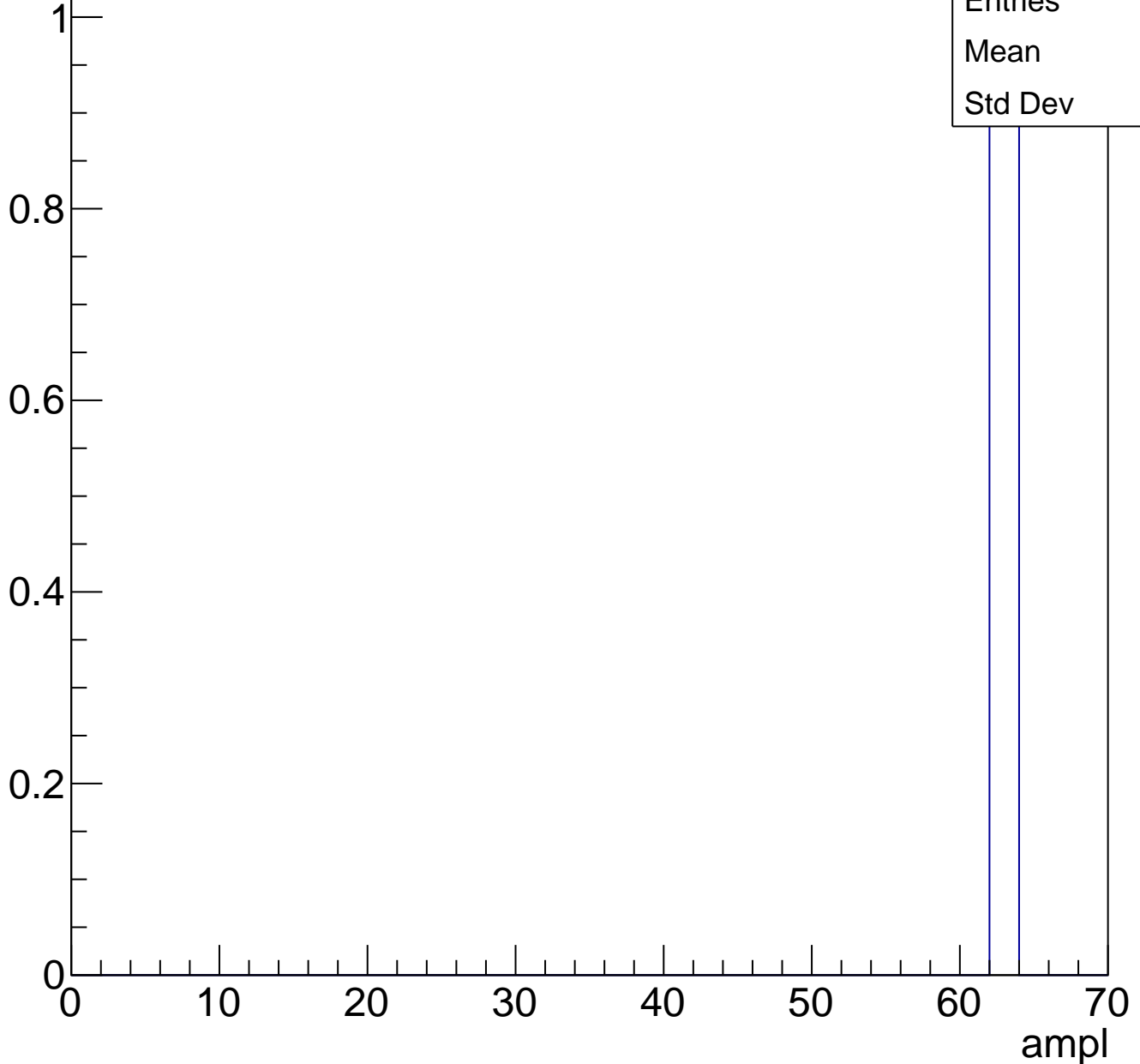
Entries	38
Mean	58.89
Std Dev	9.904



B0L002S, U18-ch115, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch115, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch116, adc0

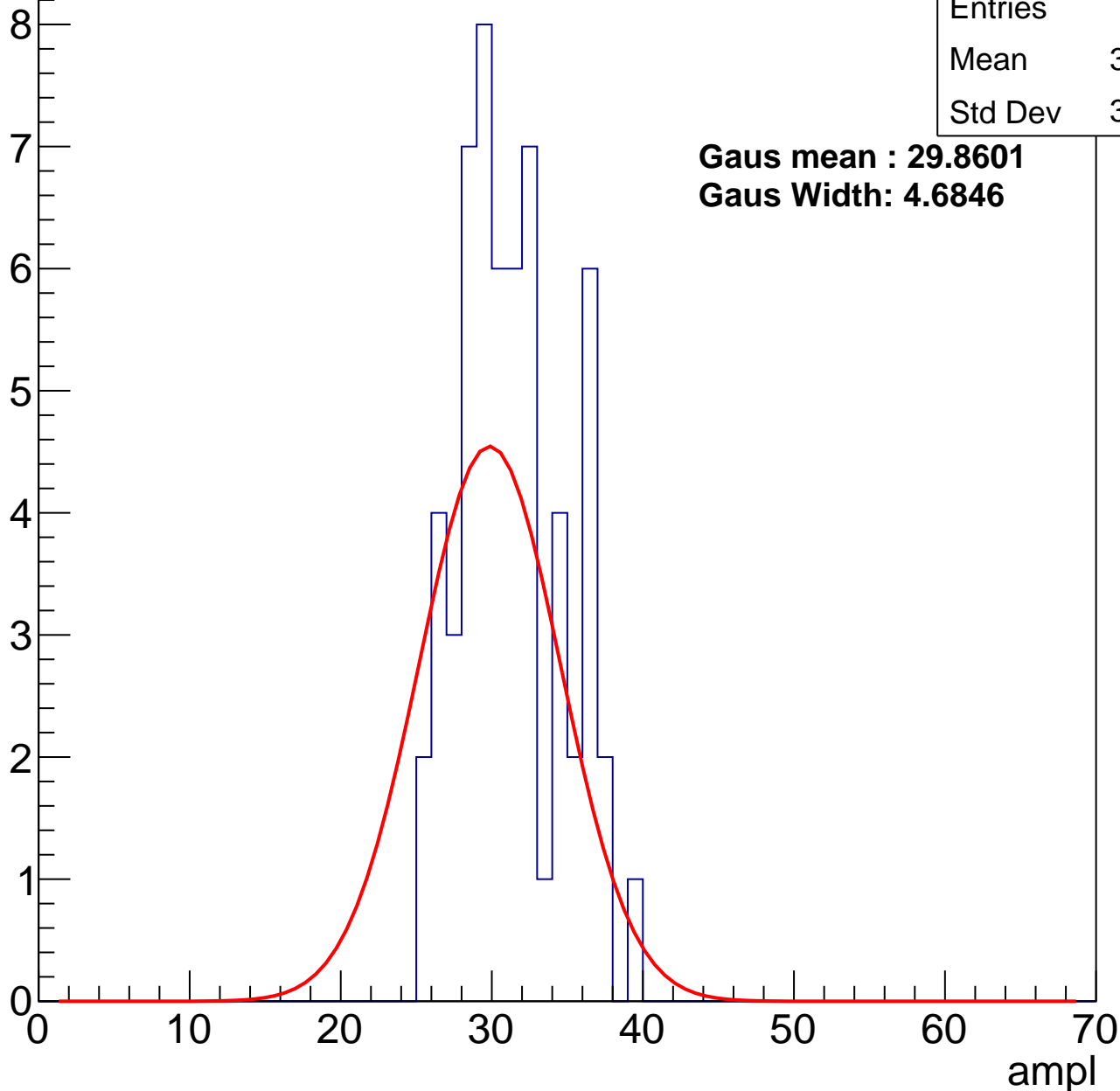
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	30.86
Std Dev	3.407

Gaus mean : 29.8601

Gaus Width: 4.6846



B0L002S, U18-ch116, adc1

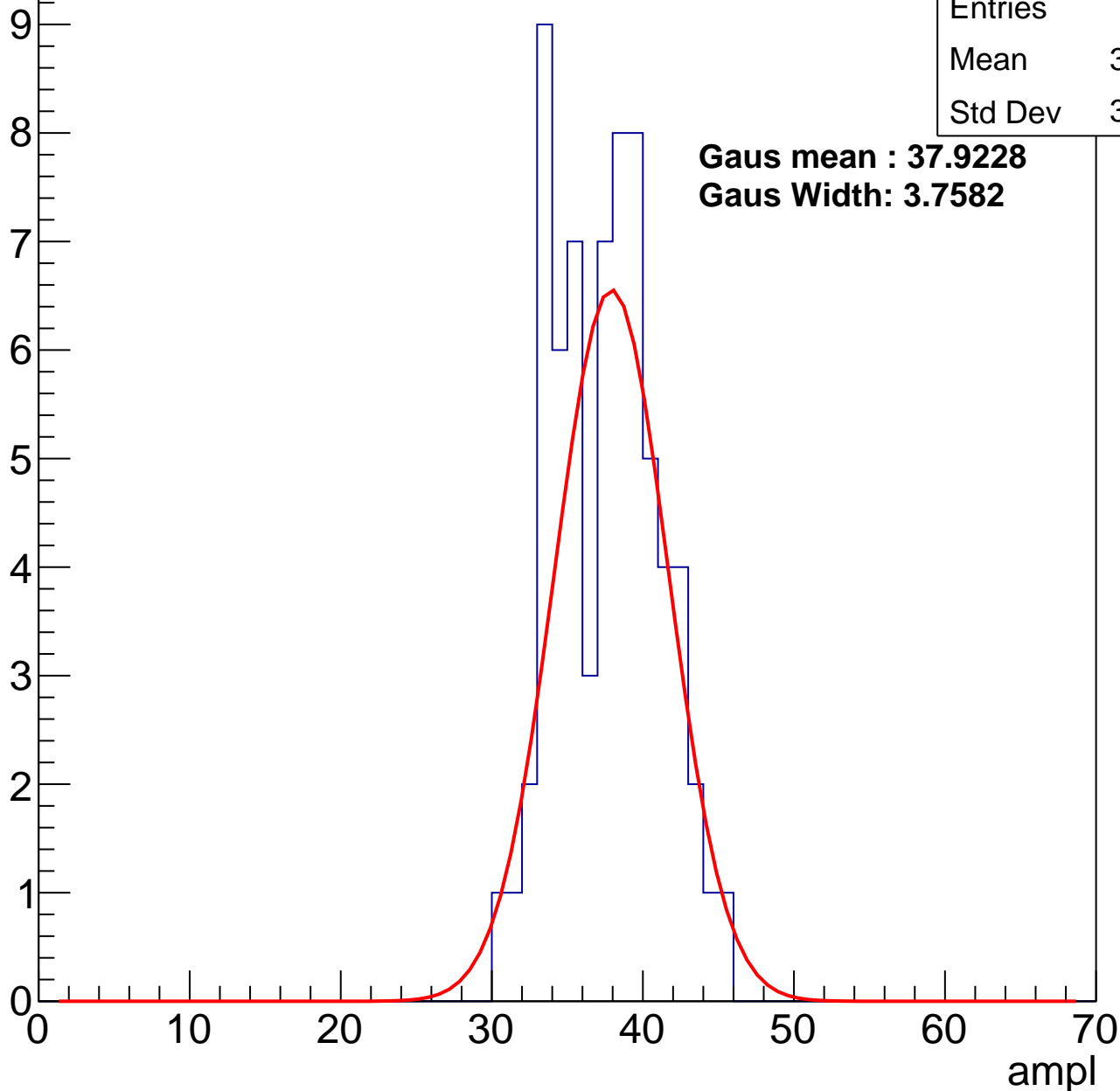
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	69
Mean	37.12
Std Dev	3.399

Gaus mean : 37.9228

Gaus Width: 3.7582



B0L002S, U18-ch116, adc2

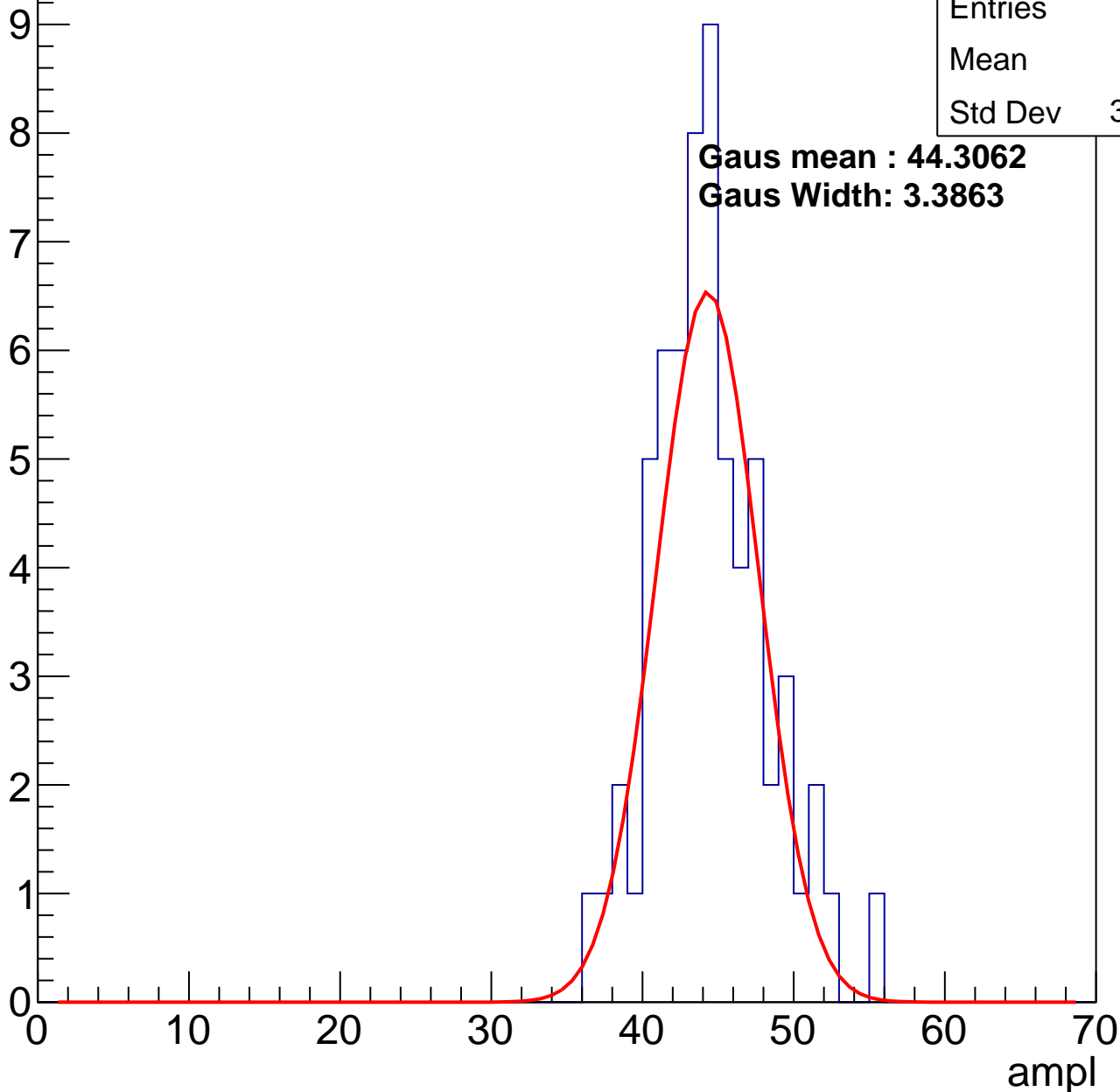
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	44
Std Dev	3.712

Gaus mean : 44.3062

Gaus Width: 3.3863

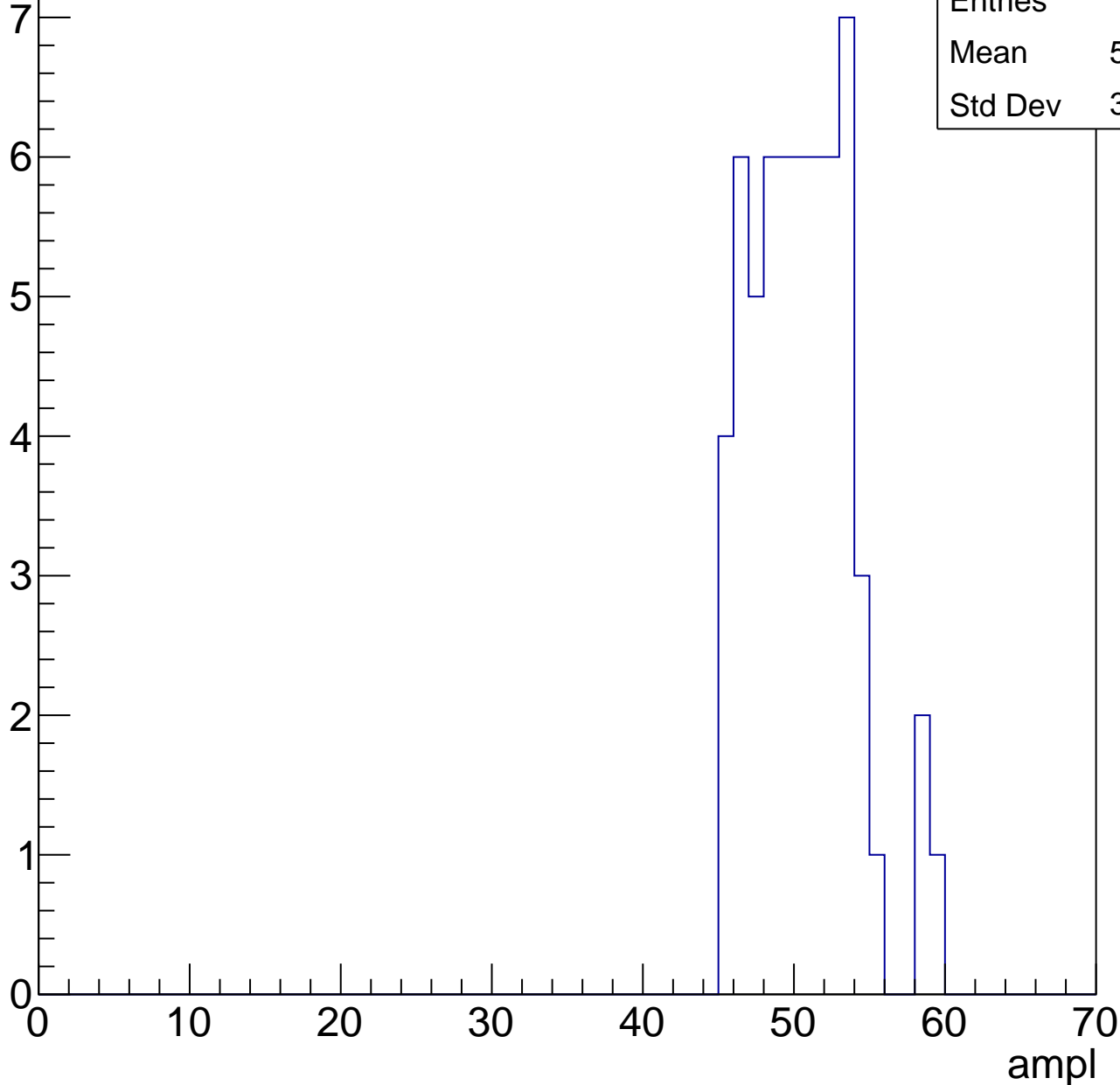


B0L002S, U18-ch116, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

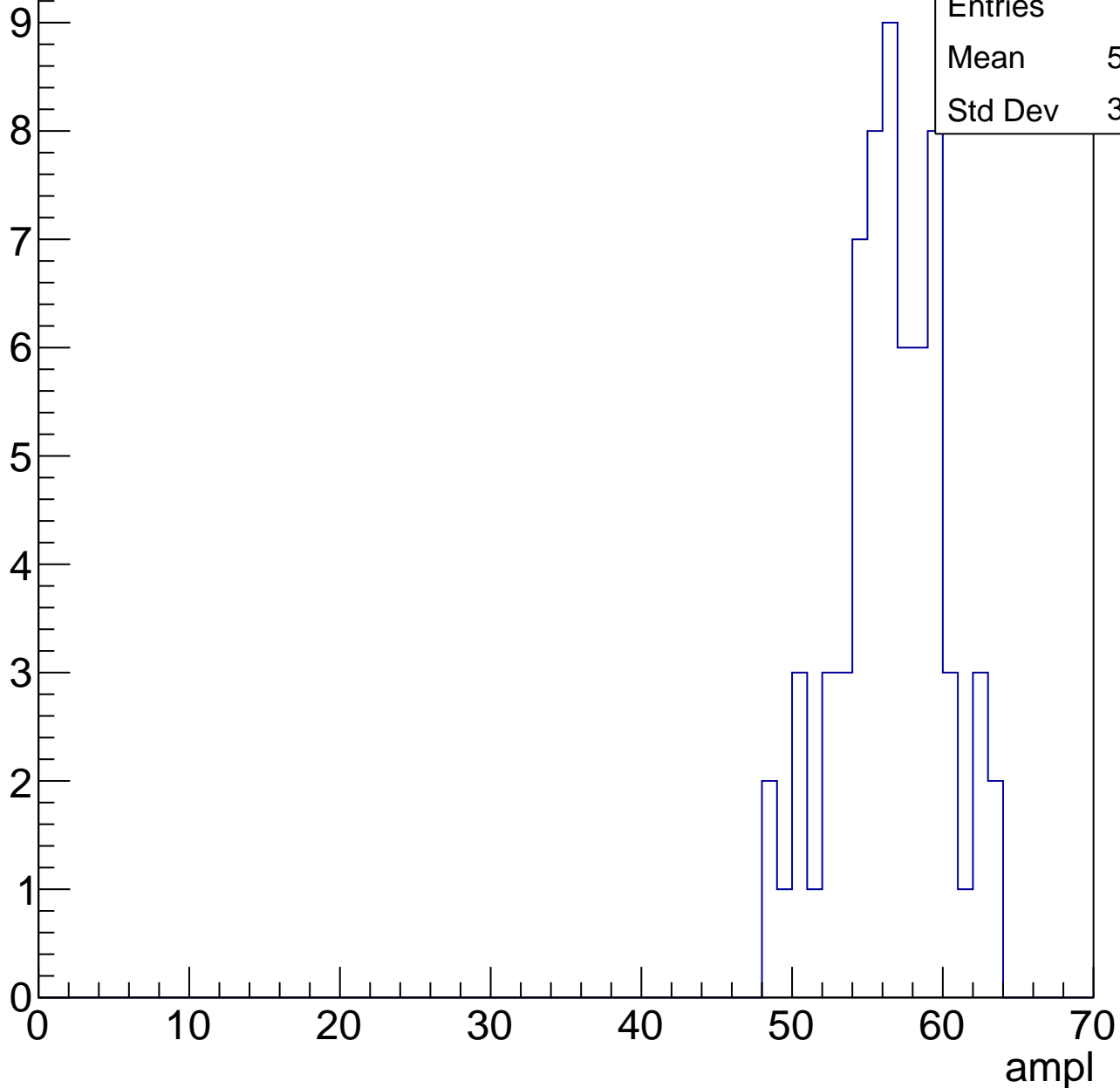
Entries	59
Mean	50.07
Std Dev	3.308



B0L002S, U18-ch116, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

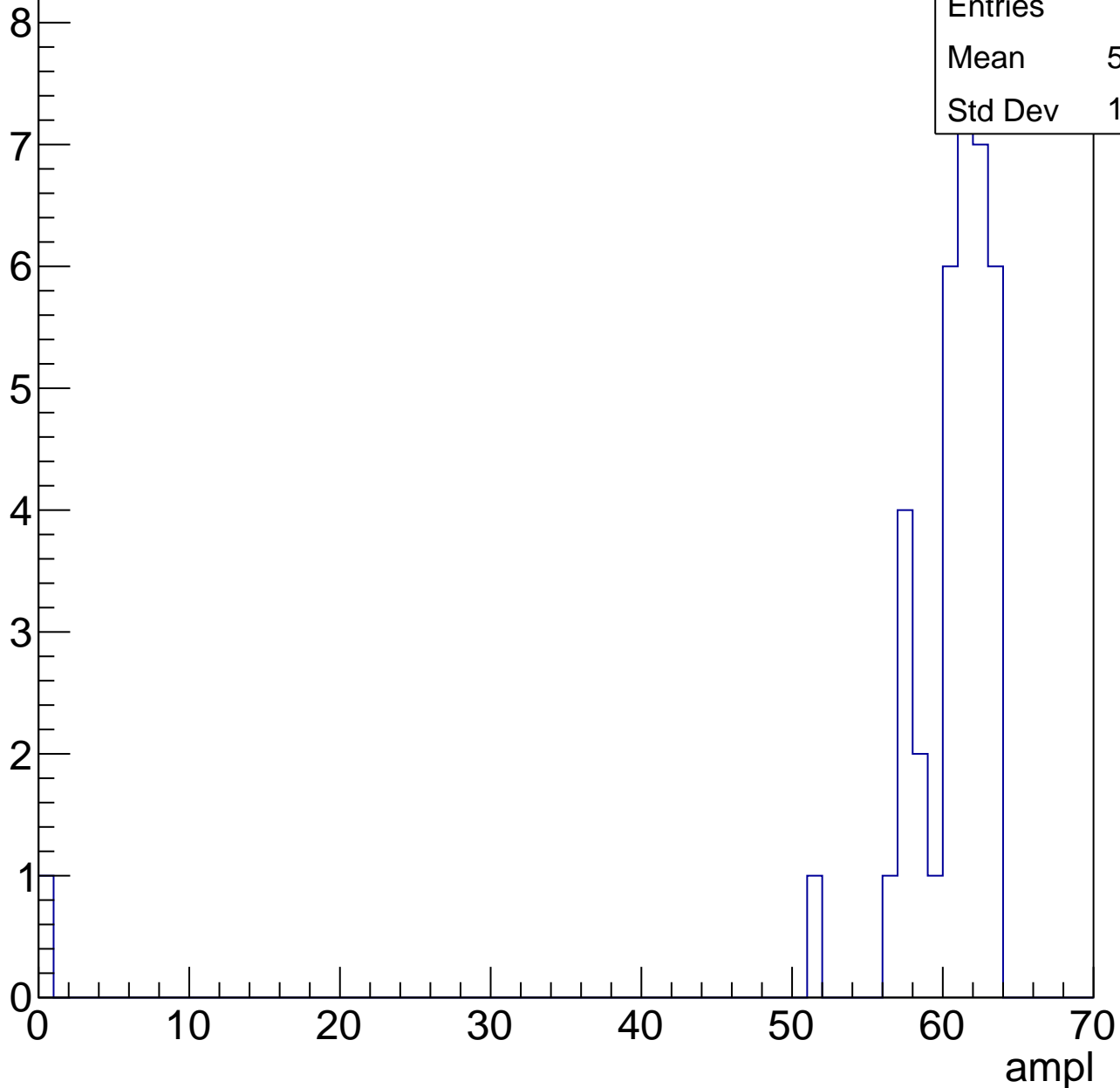


B0L002S, U18-ch116, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.65
Std Dev	10.09



B0L002S, U18-ch116, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch116, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch117, adc0

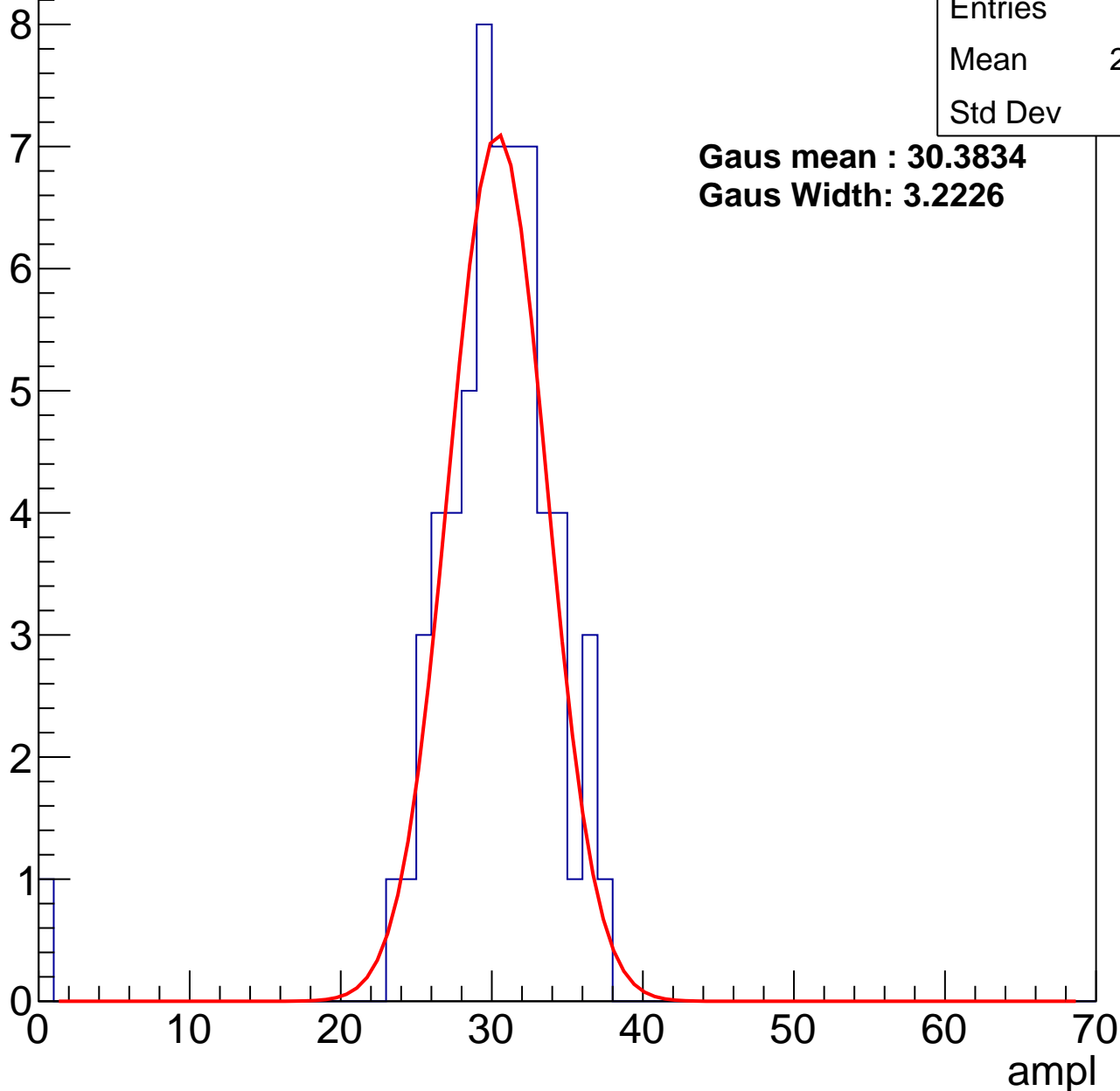
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	29.59
Std Dev	4.95

Gaus mean : 30.3834

Gaus Width: 3.2226



B0L002S, U18-ch117, adc1

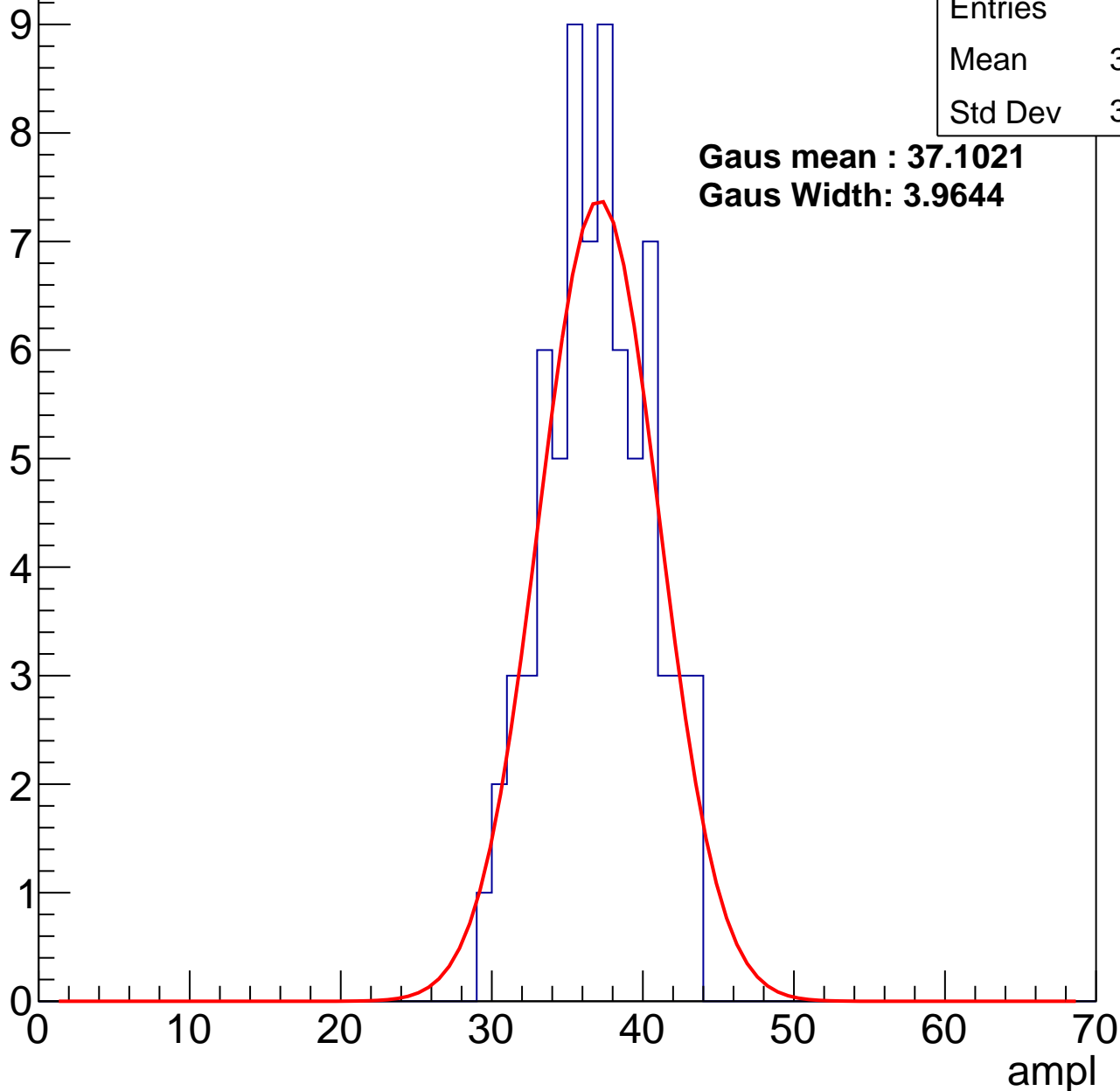
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	72
Mean	36.49
Std Dev	3.395

Gaus mean : 37.1021

Gaus Width: 3.9644



B0L002S, U18-ch117, adc2

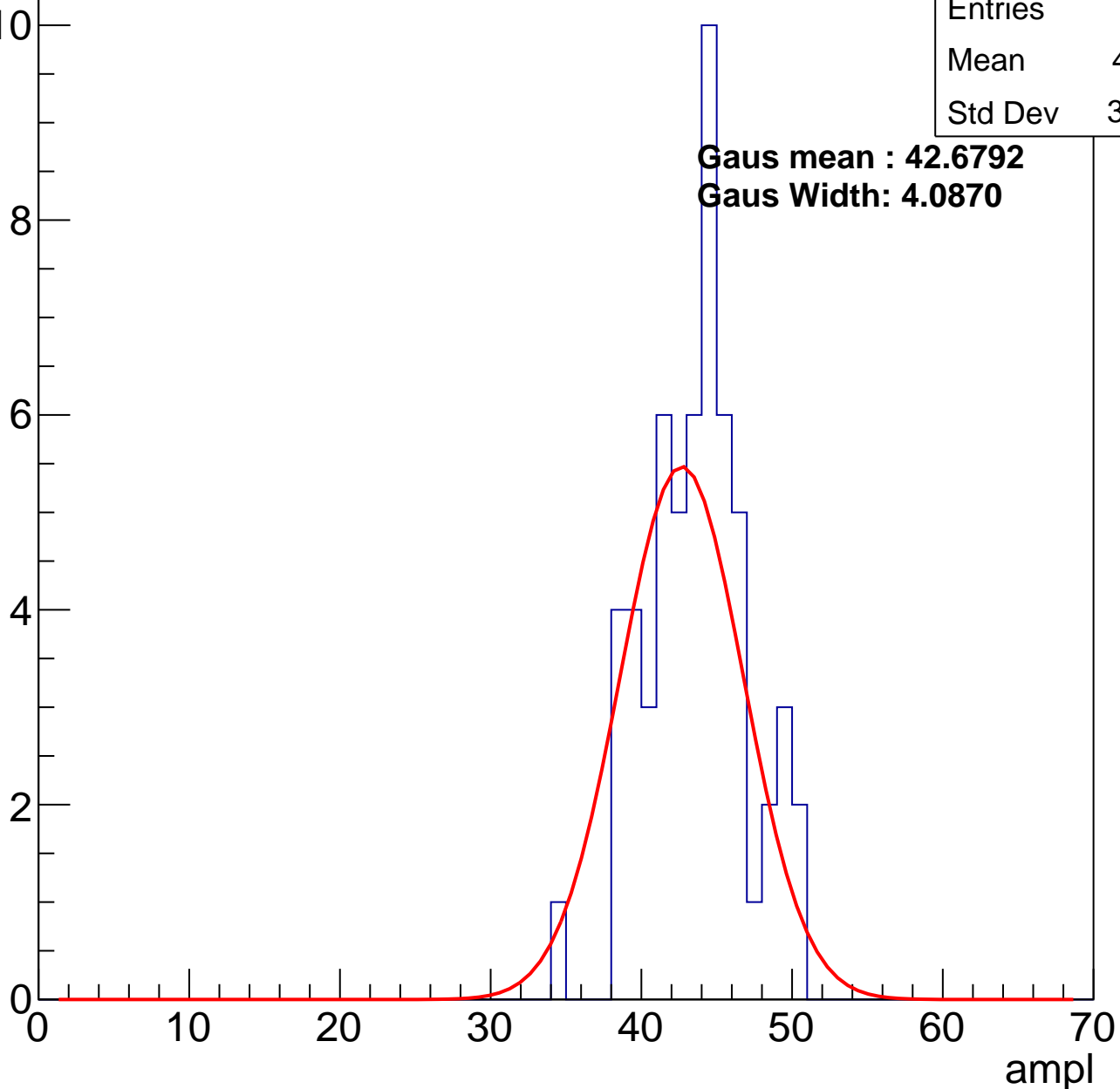
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	43.21
Std Dev	3.346

Gaus mean : 42.6792

Gaus Width: 4.0870

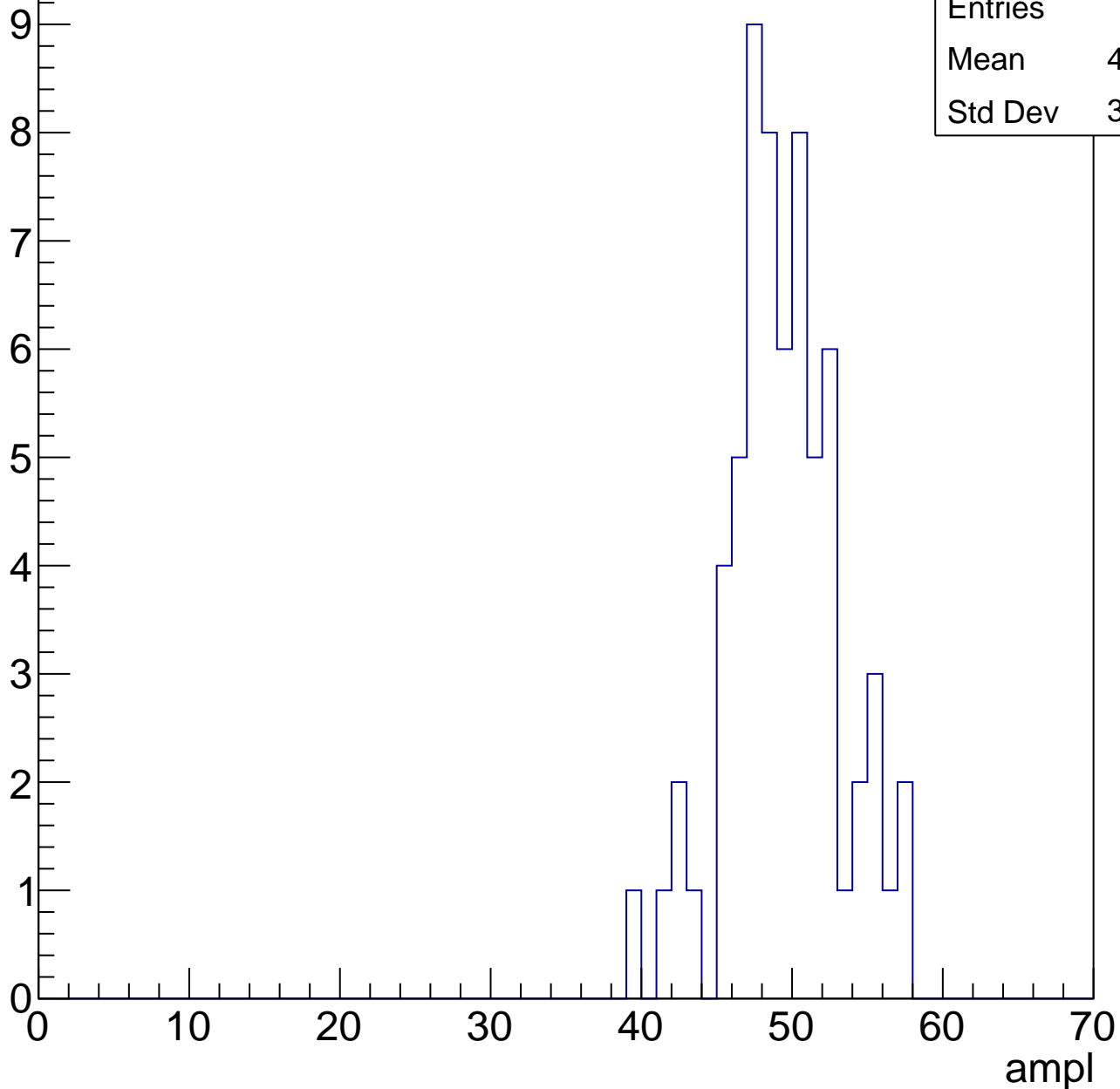


B0L002S, U18-ch117, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	65
Mean	48.94
Std Dev	3.687

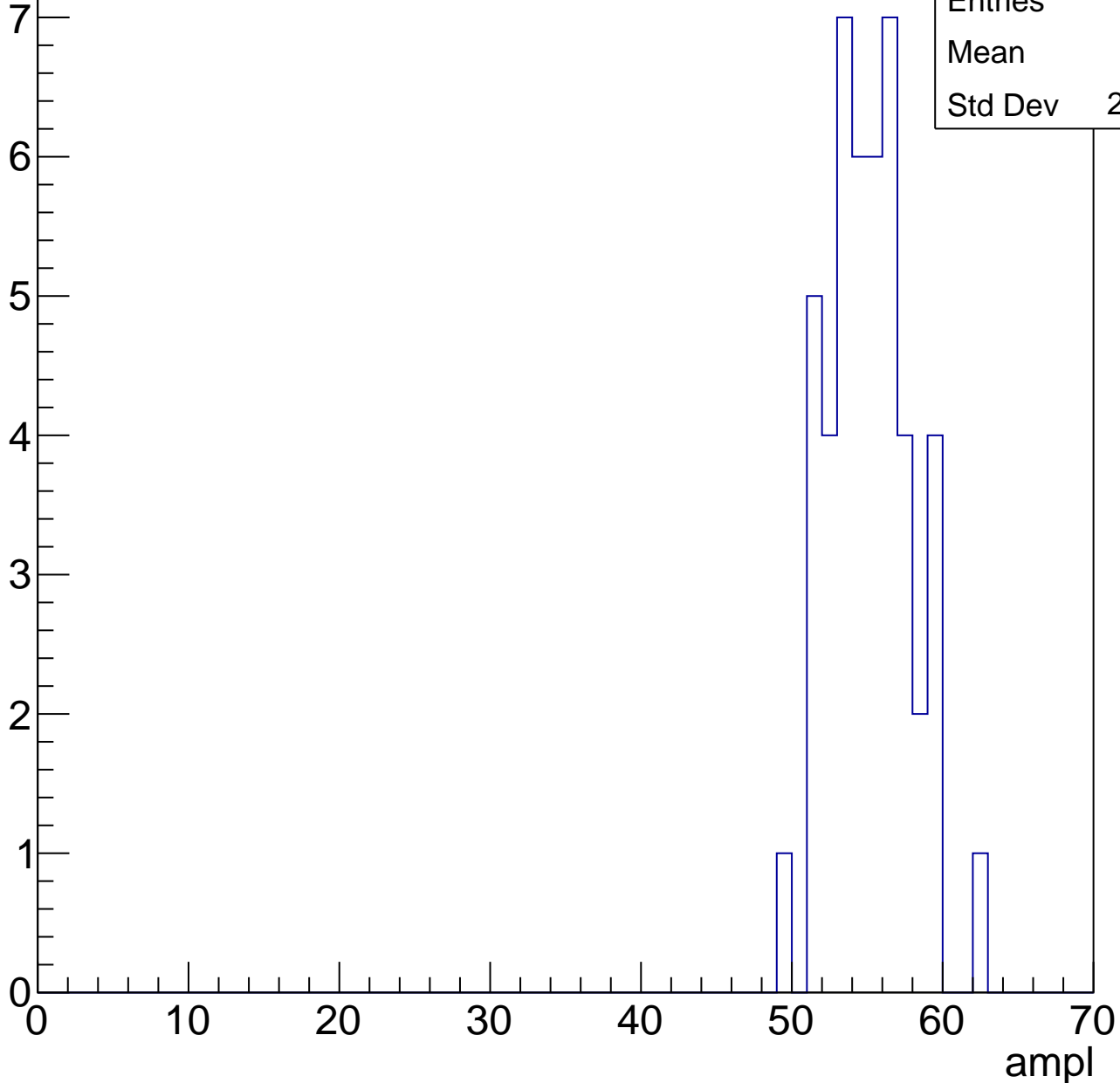


B0L002S, U18-ch117, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	47
Mean	54.7
Std Dev	2.673



B0L002S, U18-ch117, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	55
Mean	58.25
Std Dev	8.334

Entry

10

8

6

4

2

0

0

10

20

30

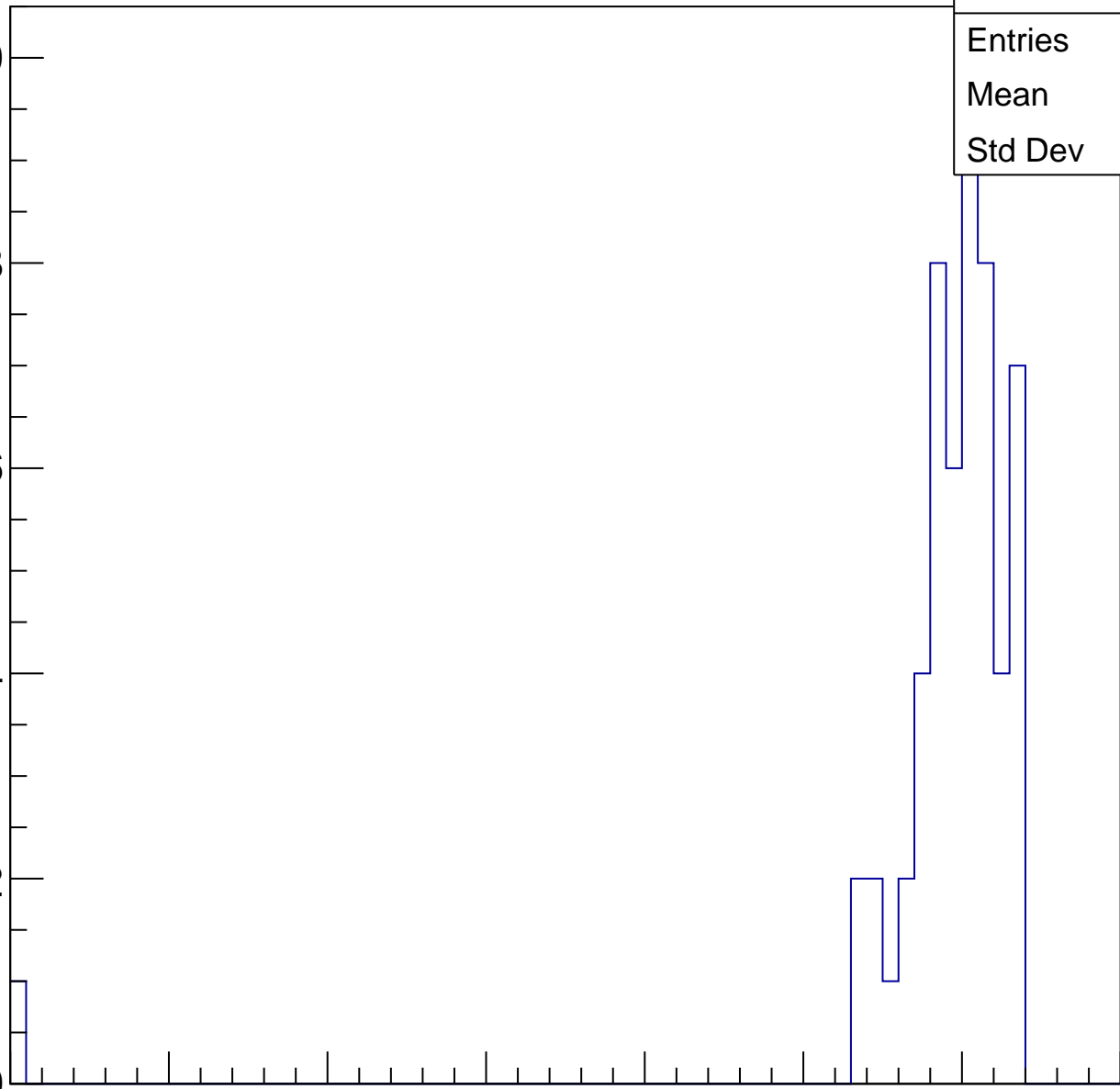
40

50

ampl

60

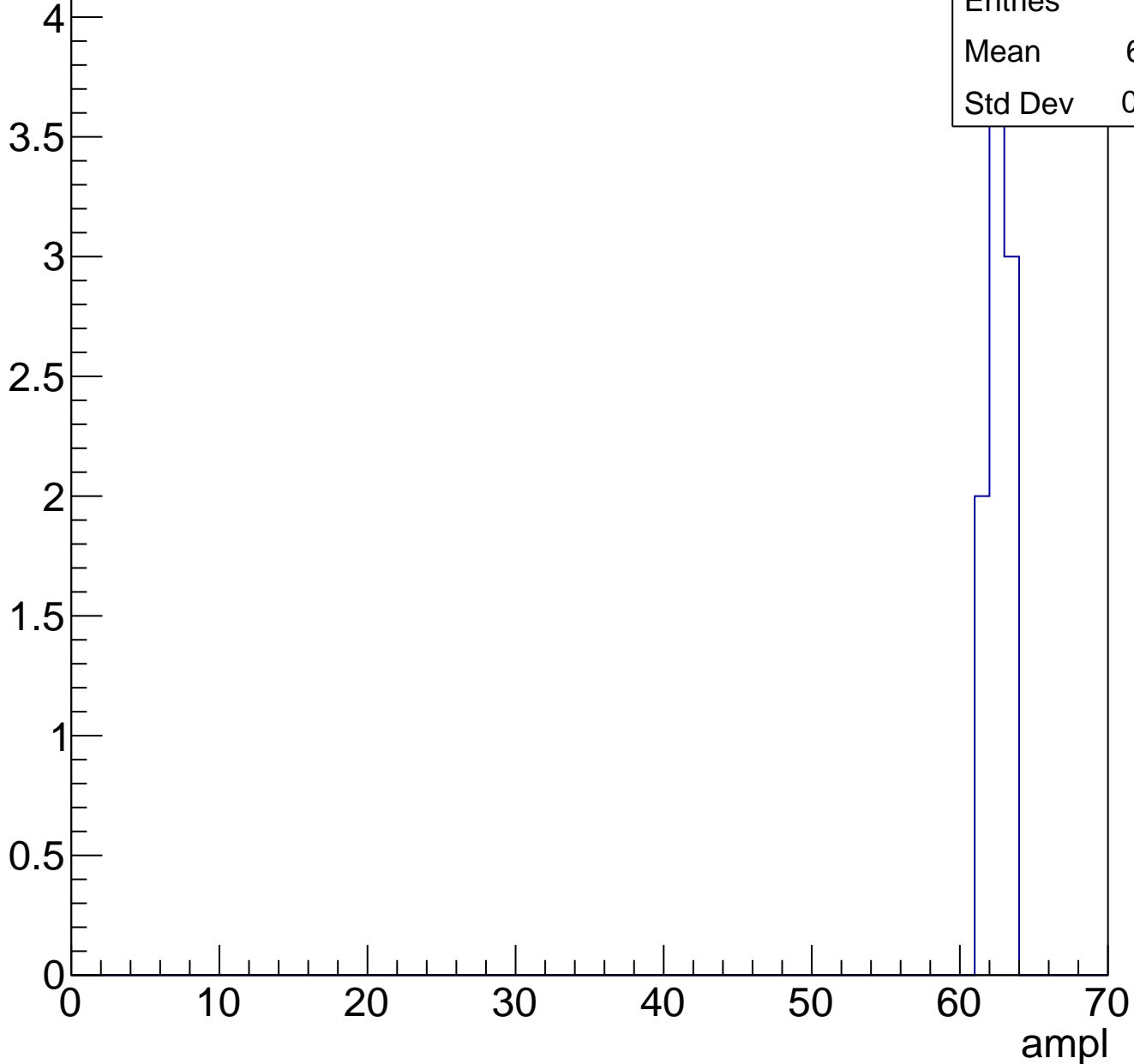
70



B0L002S, U18-ch117, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch117, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B0L002S, U18-ch118, adc0

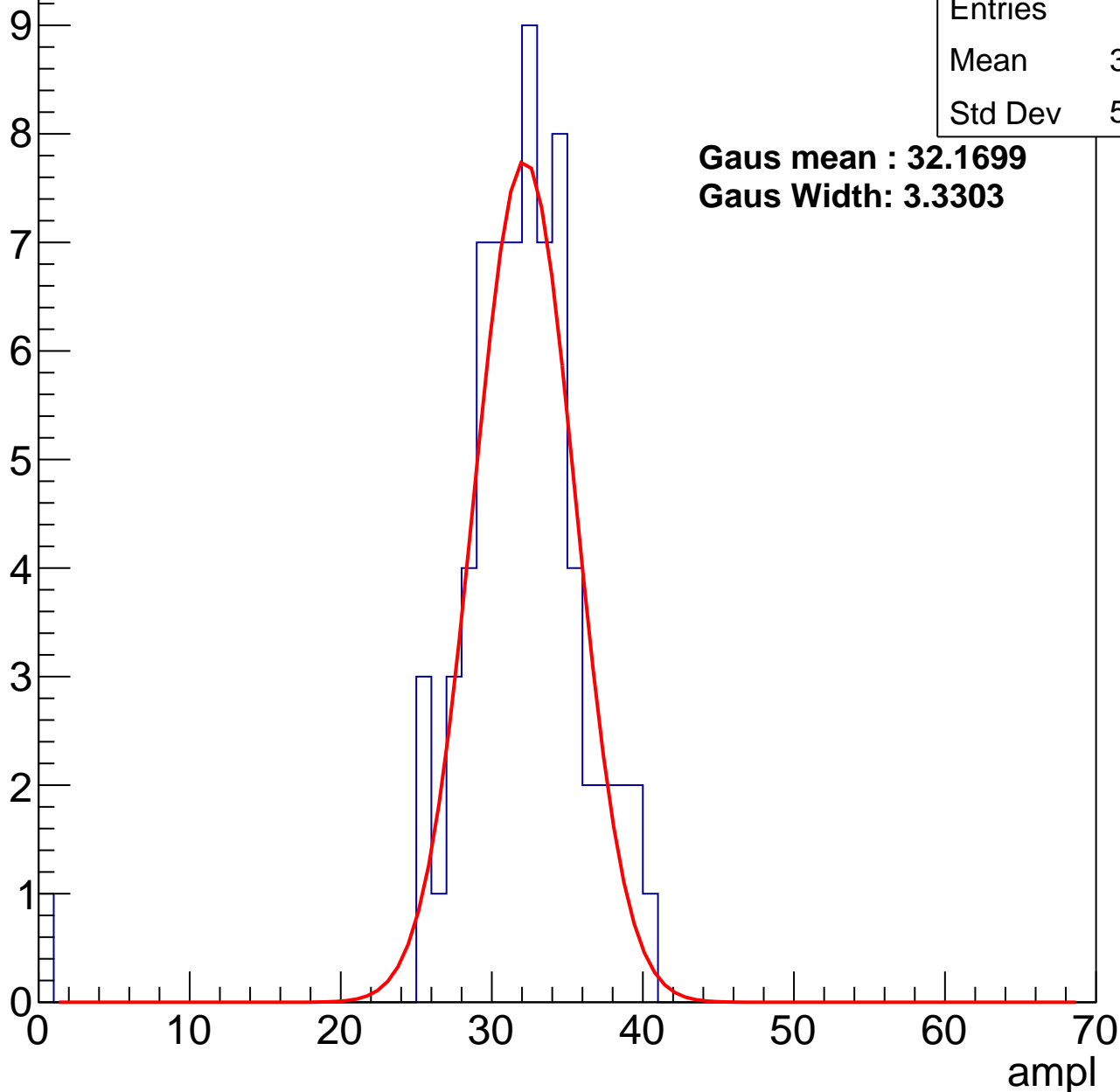
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	31.36
Std Dev	5.079

Gaus mean : 32.1699

Gaus Width: 3.3303



B0L002S, U18-ch118, adc1

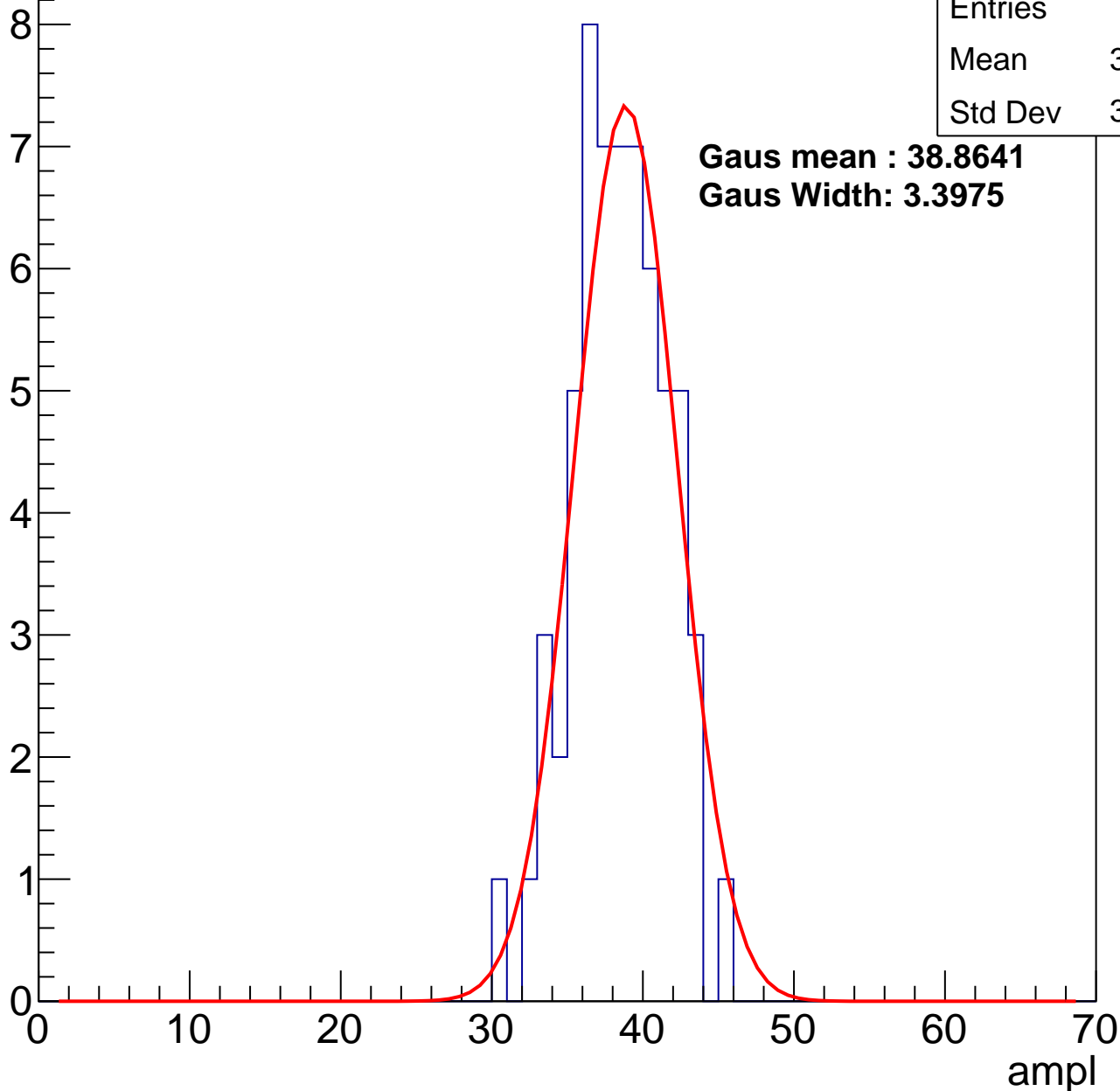
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	38.02
Std Dev	3.059

Gaus mean : 38.8641

Gaus Width: 3.3975



B0L002S, U18-ch118, adc2

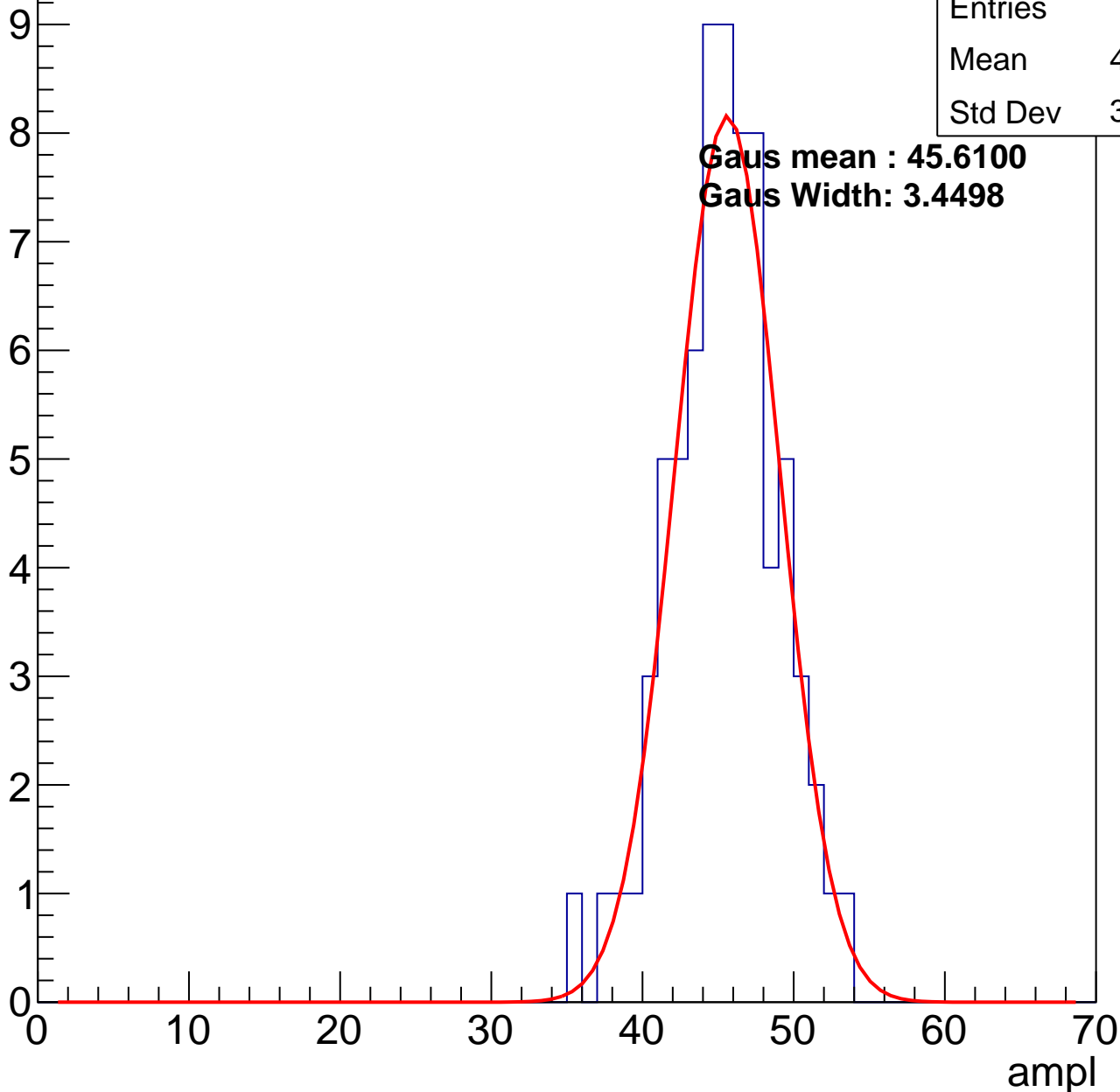
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.95
Std Dev	3.507

Gaus mean : 45.6100

Gaus Width: 3.4498

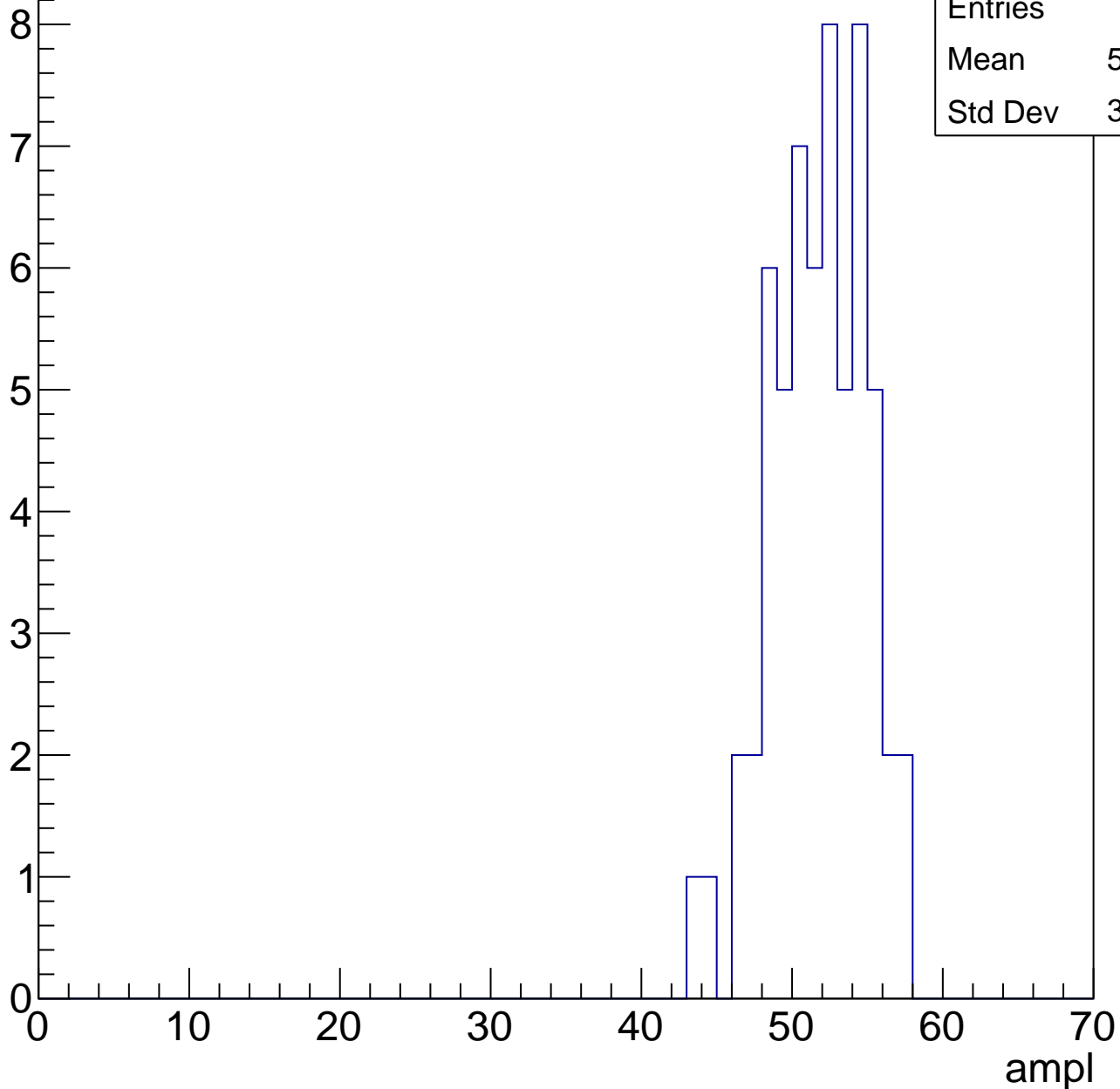


B0L002S, U18-ch118, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	60
Mean	51.27
Std Dev	3.092

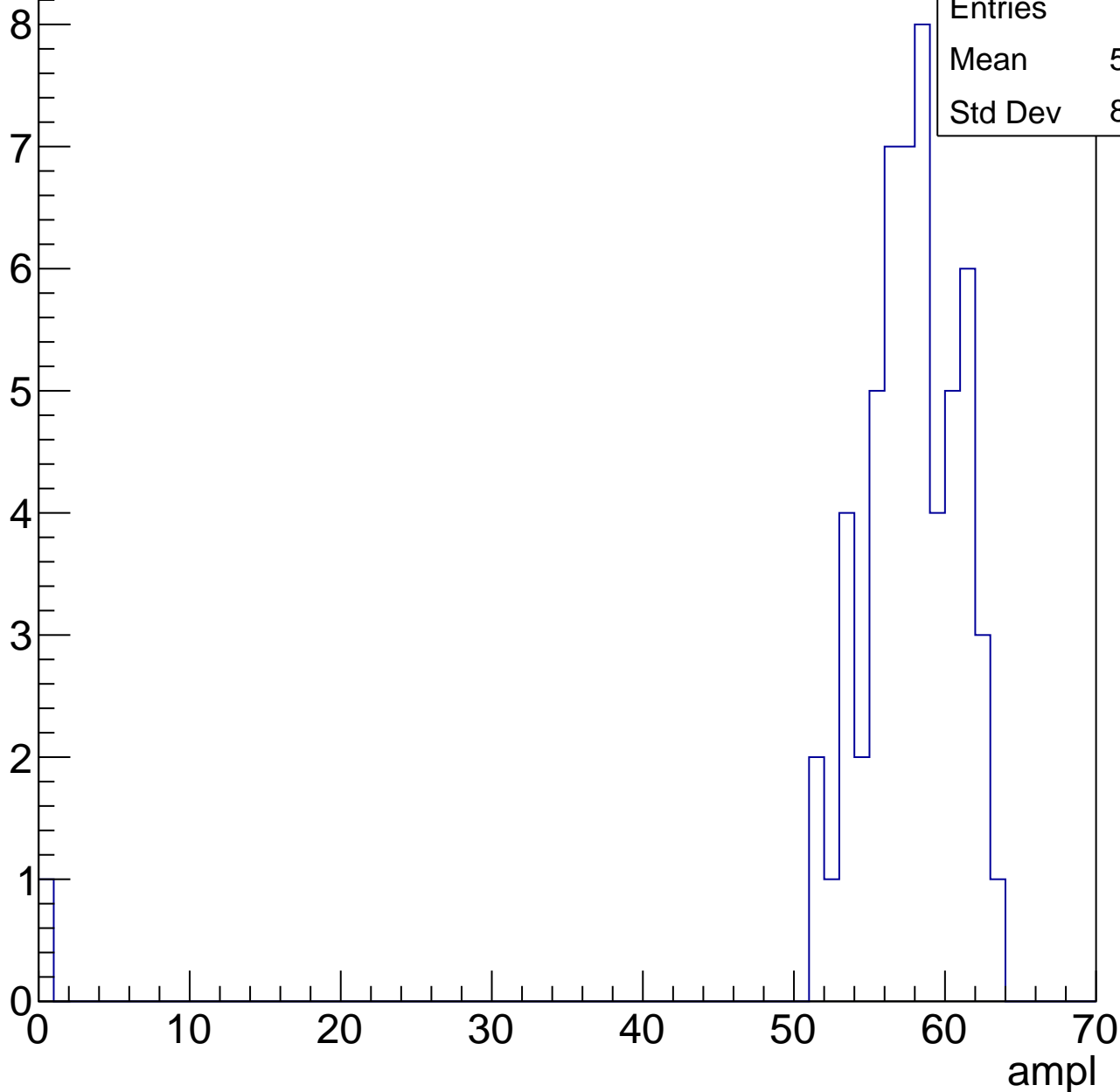


B0L002S, U18-ch118, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	56.34
Std Dev	8.136

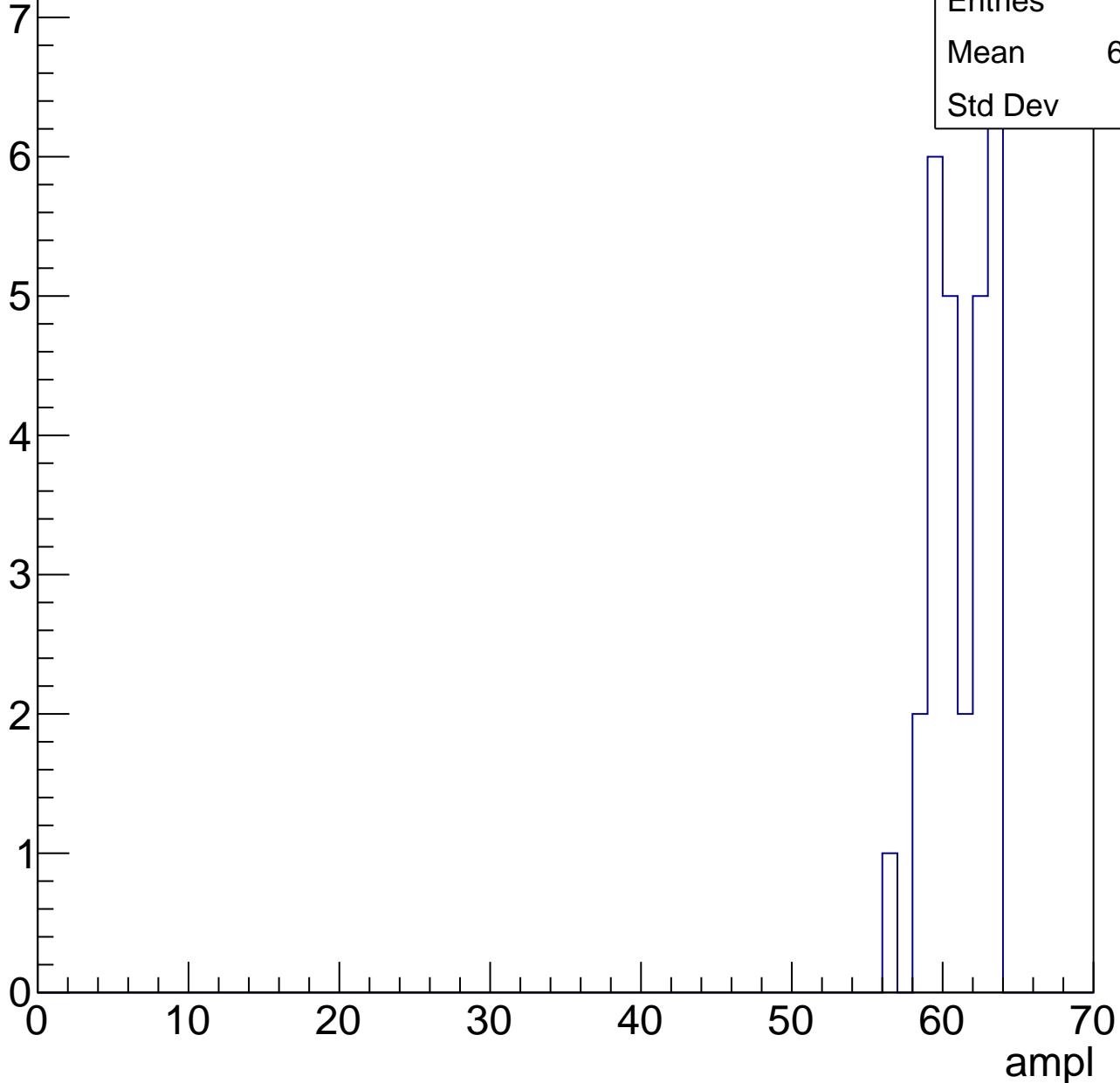


B0L002S, U18-ch118, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

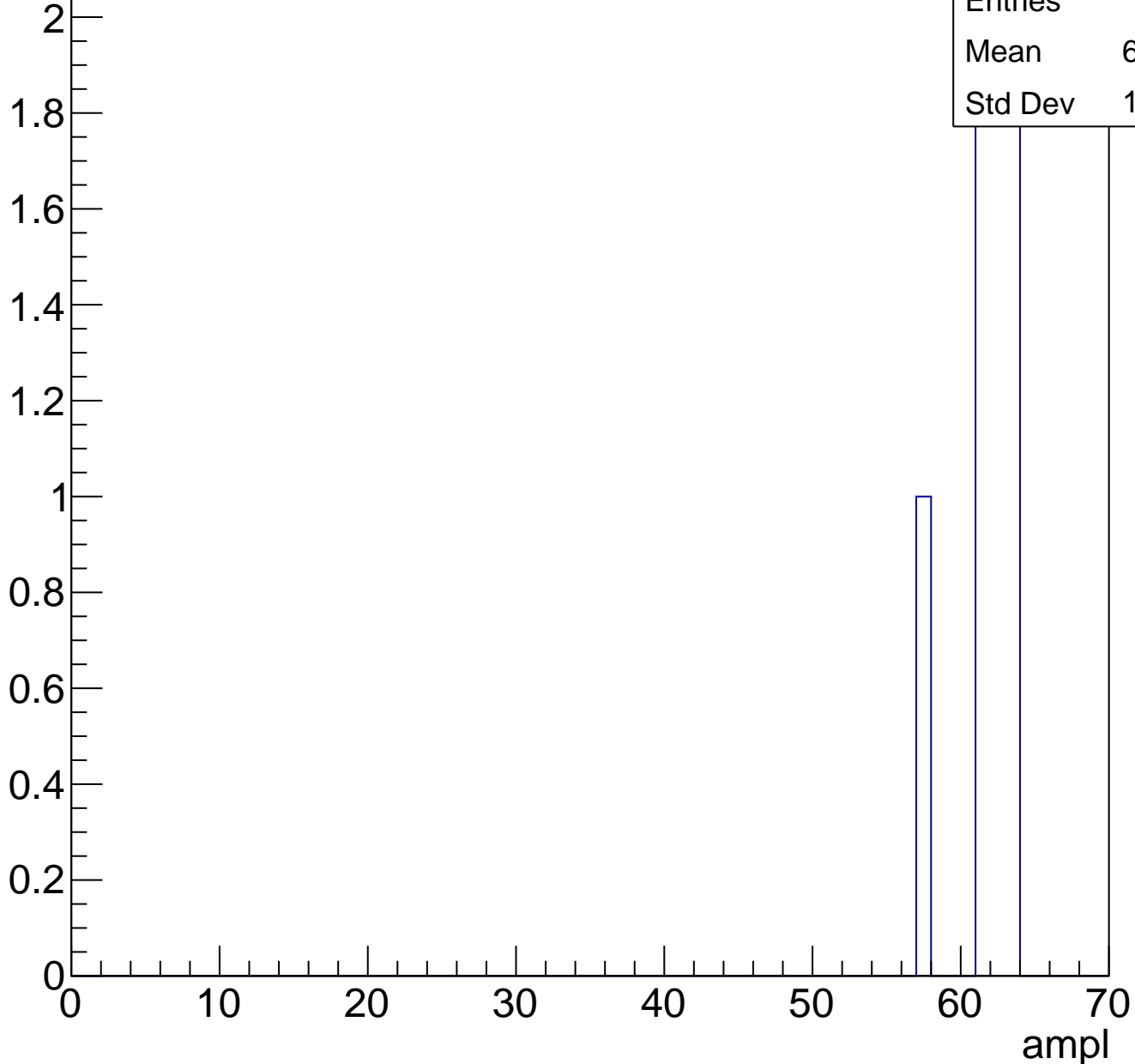
Entries	28
Mean	60.68
Std Dev	1.91



B0L002S, U18-ch118, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch118, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch119, adc0

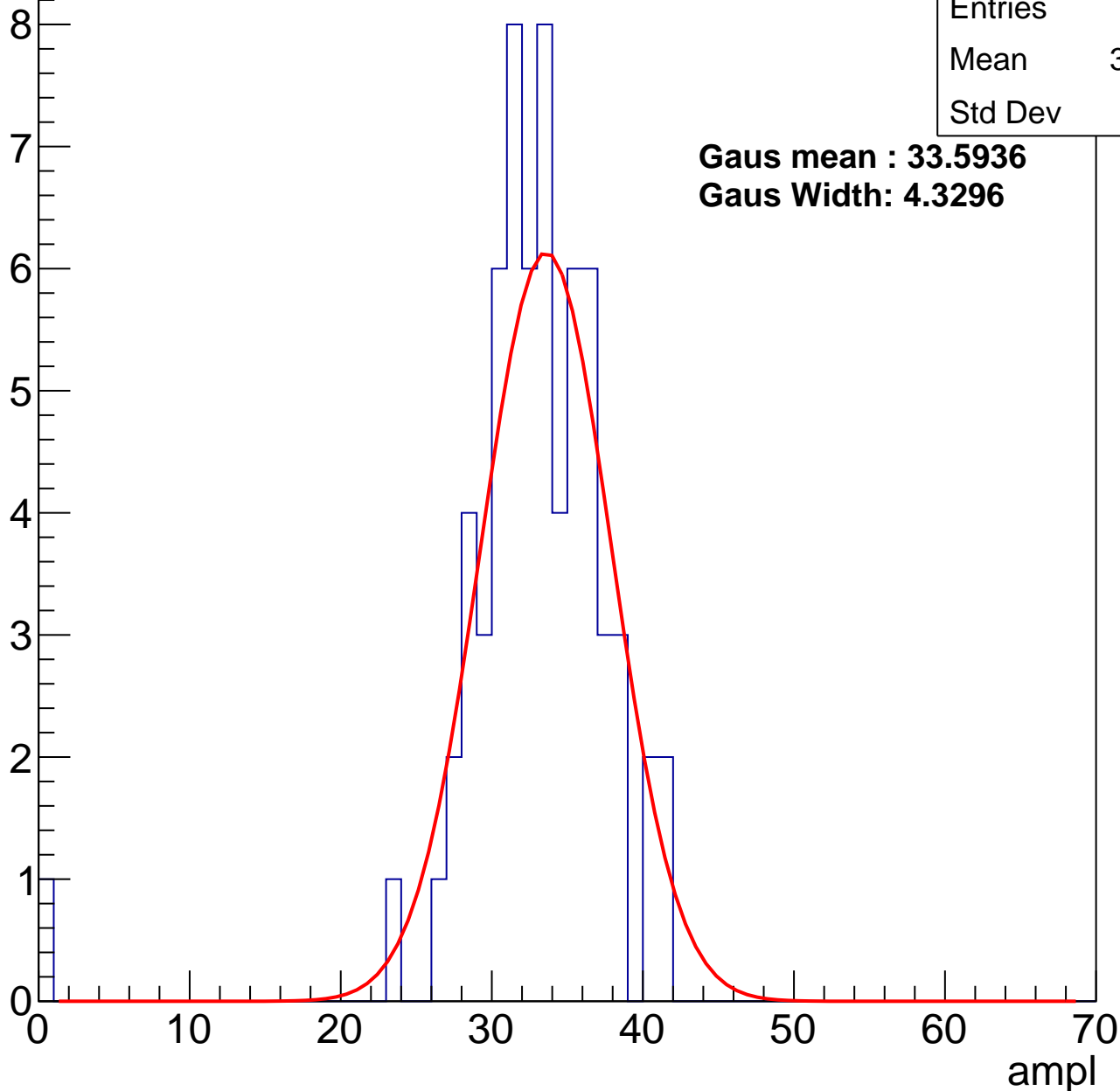
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	66
Mean	32.35
Std Dev	5.44

Gaus mean : 33.5936

Gaus Width: 4.3296



B0L002S, U18-ch119, adc1

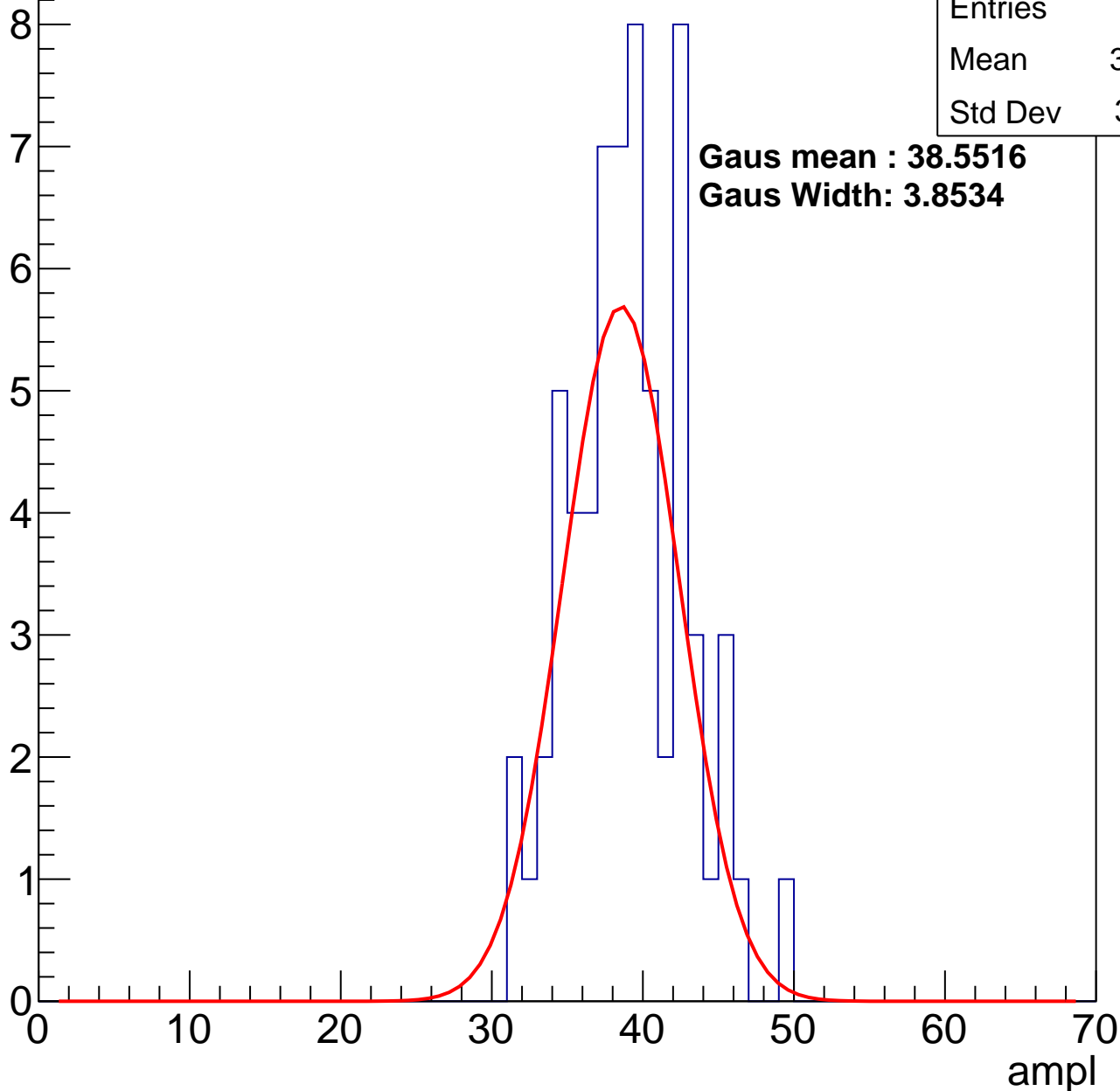
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	38.62
Std Dev	3.781

Gaus mean : 38.5516

Gaus Width: 3.8534



B0L002S, U18-ch119, adc2

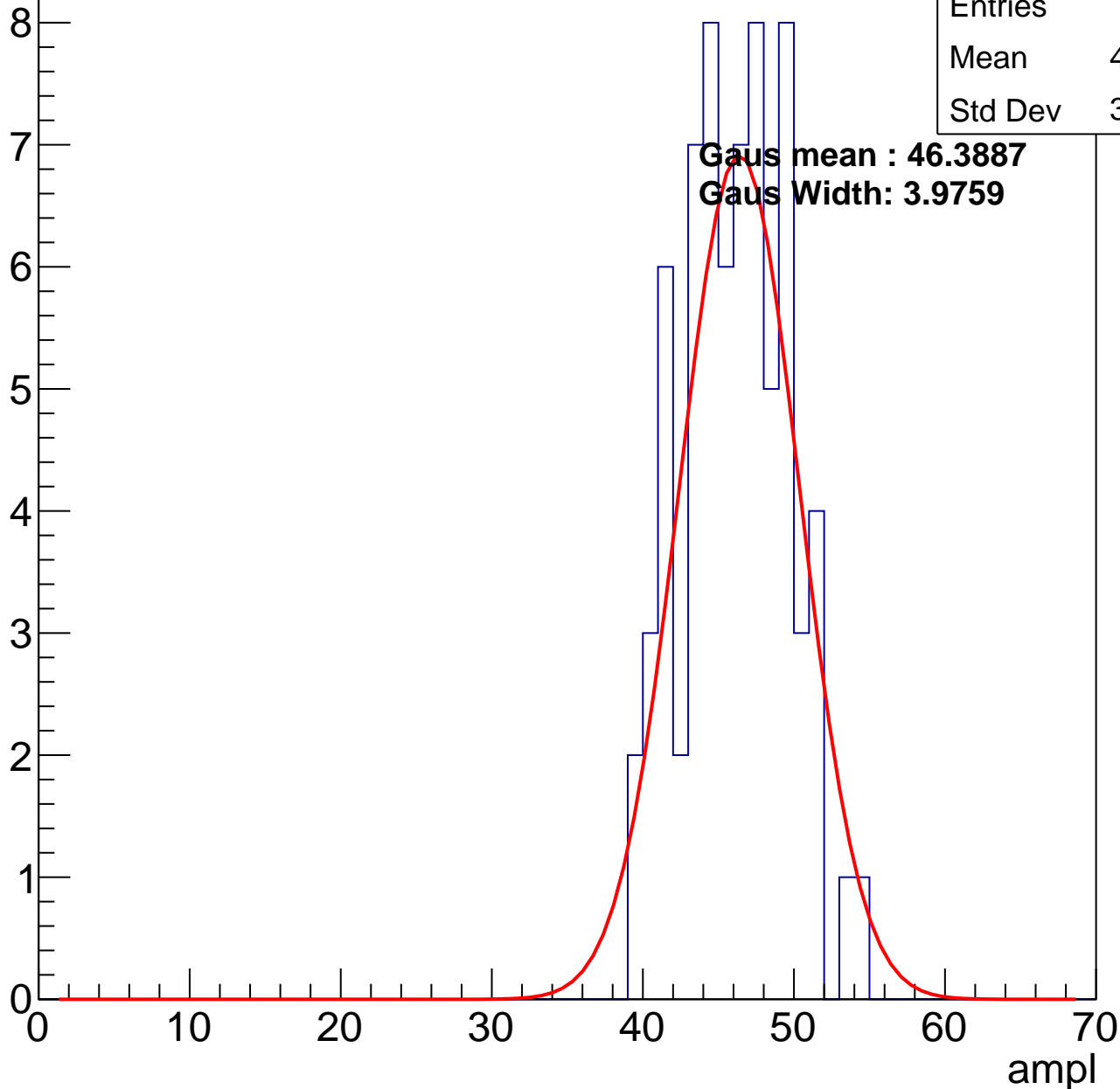
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	71
Mean	45.66
Std Dev	3.443

Gaus mean : 46.3887

Gaus Width: 3.9759

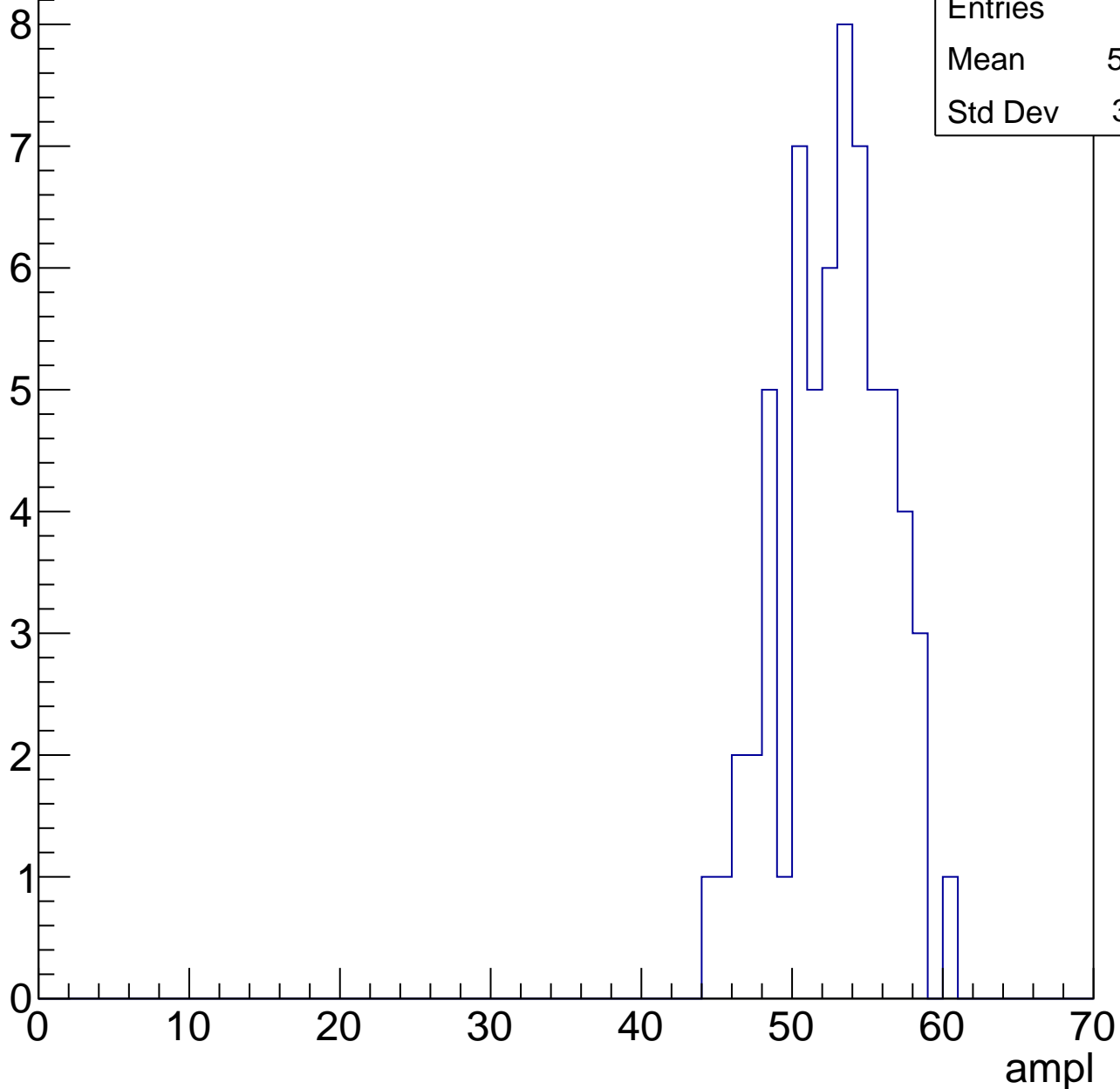


B0L002S, U18-ch119, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	63
Mean	52.38
Std Dev	3.521

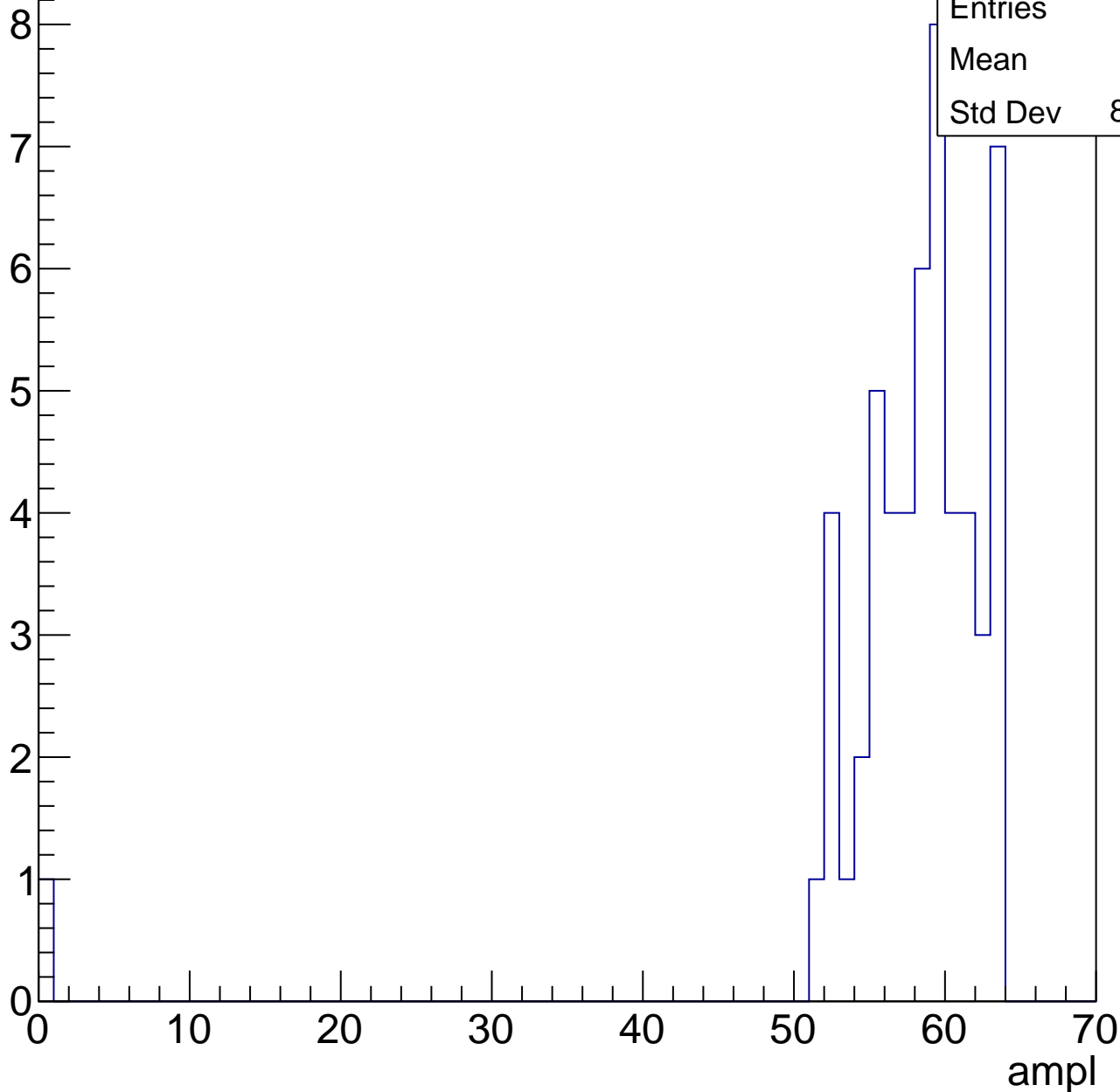


B0L002S, U18-ch119, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	57
Std Dev	8.507

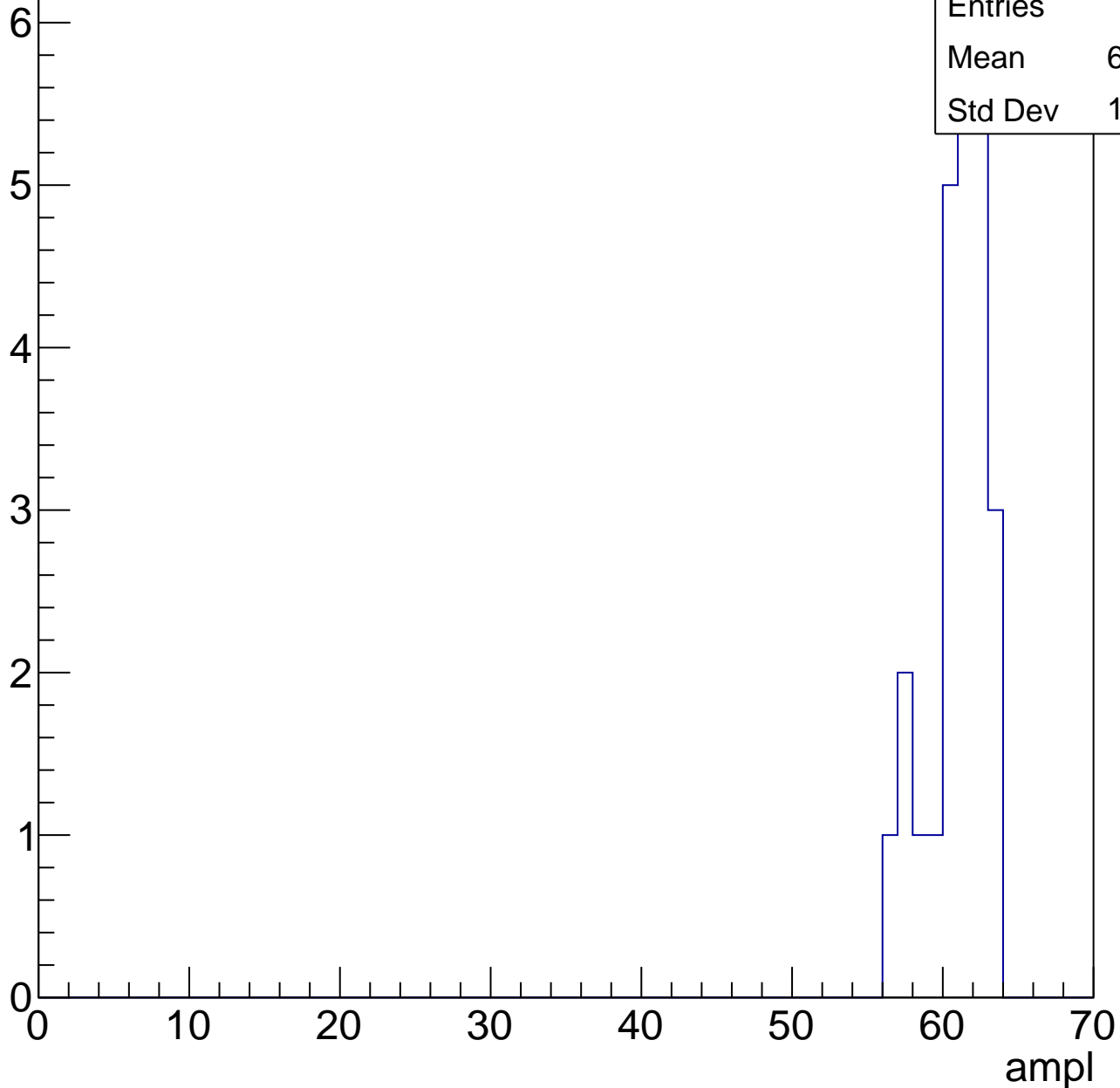


B0L002S, U18-ch119, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

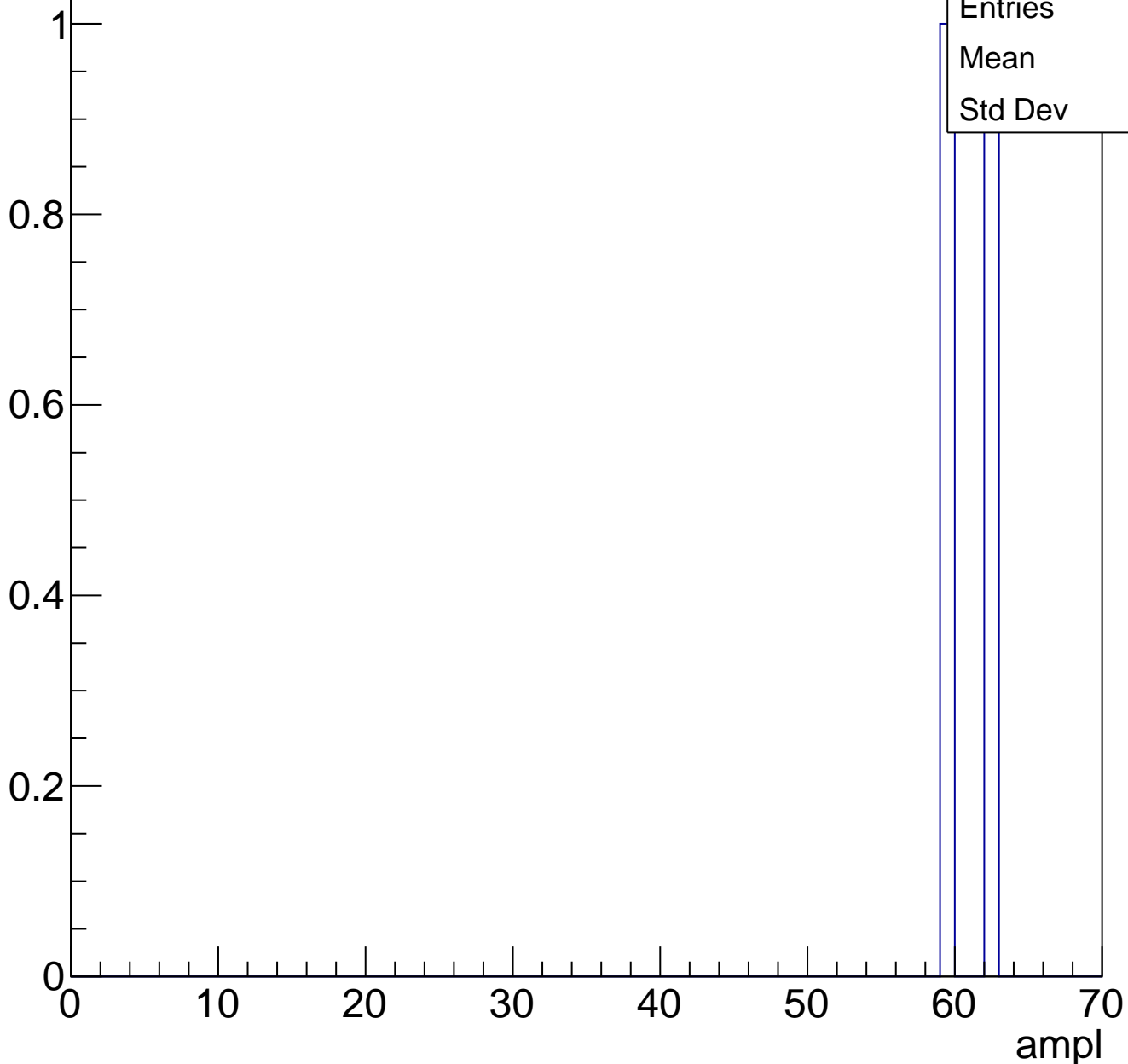
Entries	25
Mean	60.56
Std Dev	1.878



B0L002S, U18-ch119, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch119, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch120, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	66
Mean	29.12
Std Dev	6.026

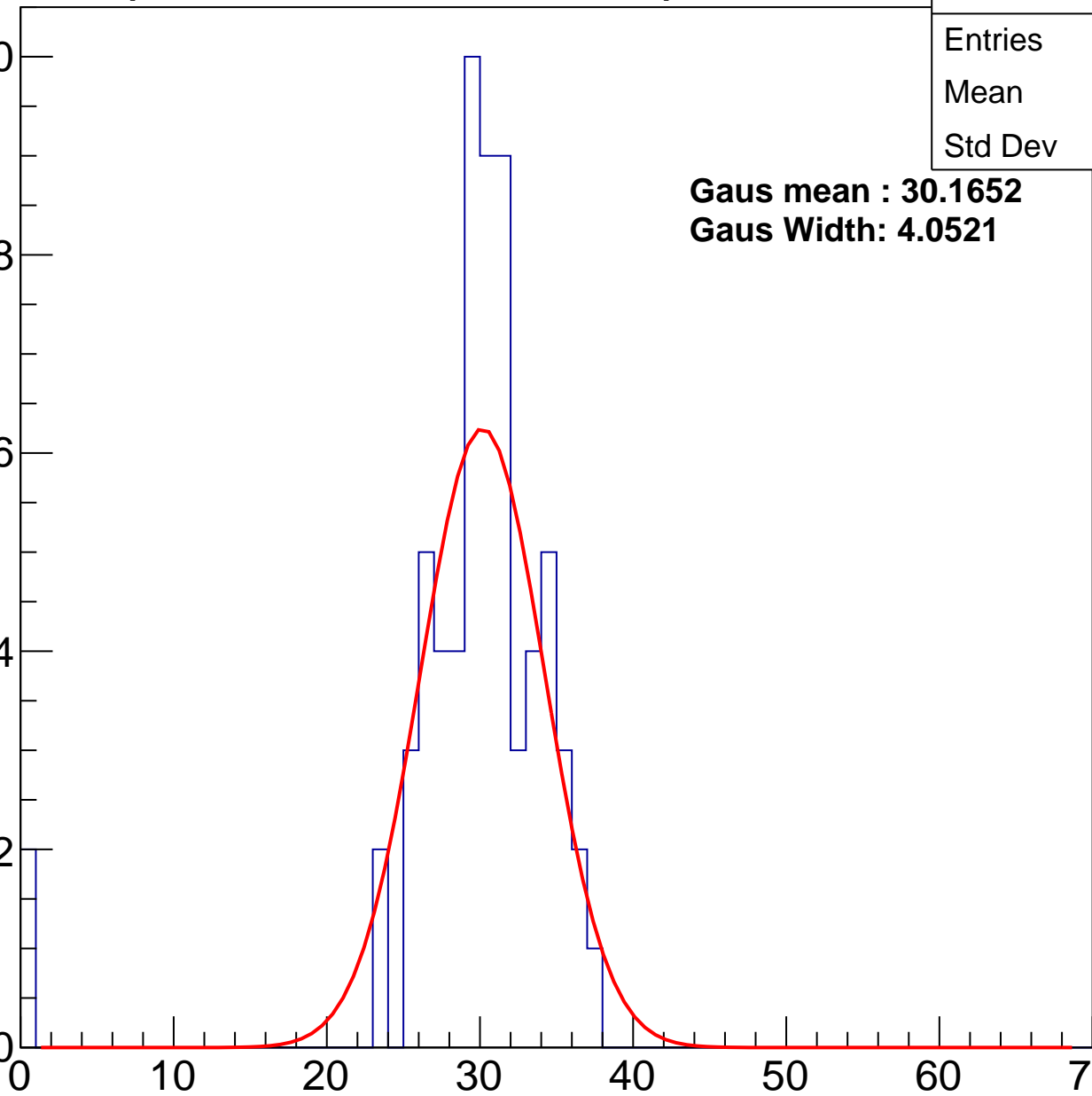
Gaus mean : 30.1652

Gaus Width: 4.0521

Entry

10
8
6
4
2
0

ampl



B0L002S, U18-ch120, adc1

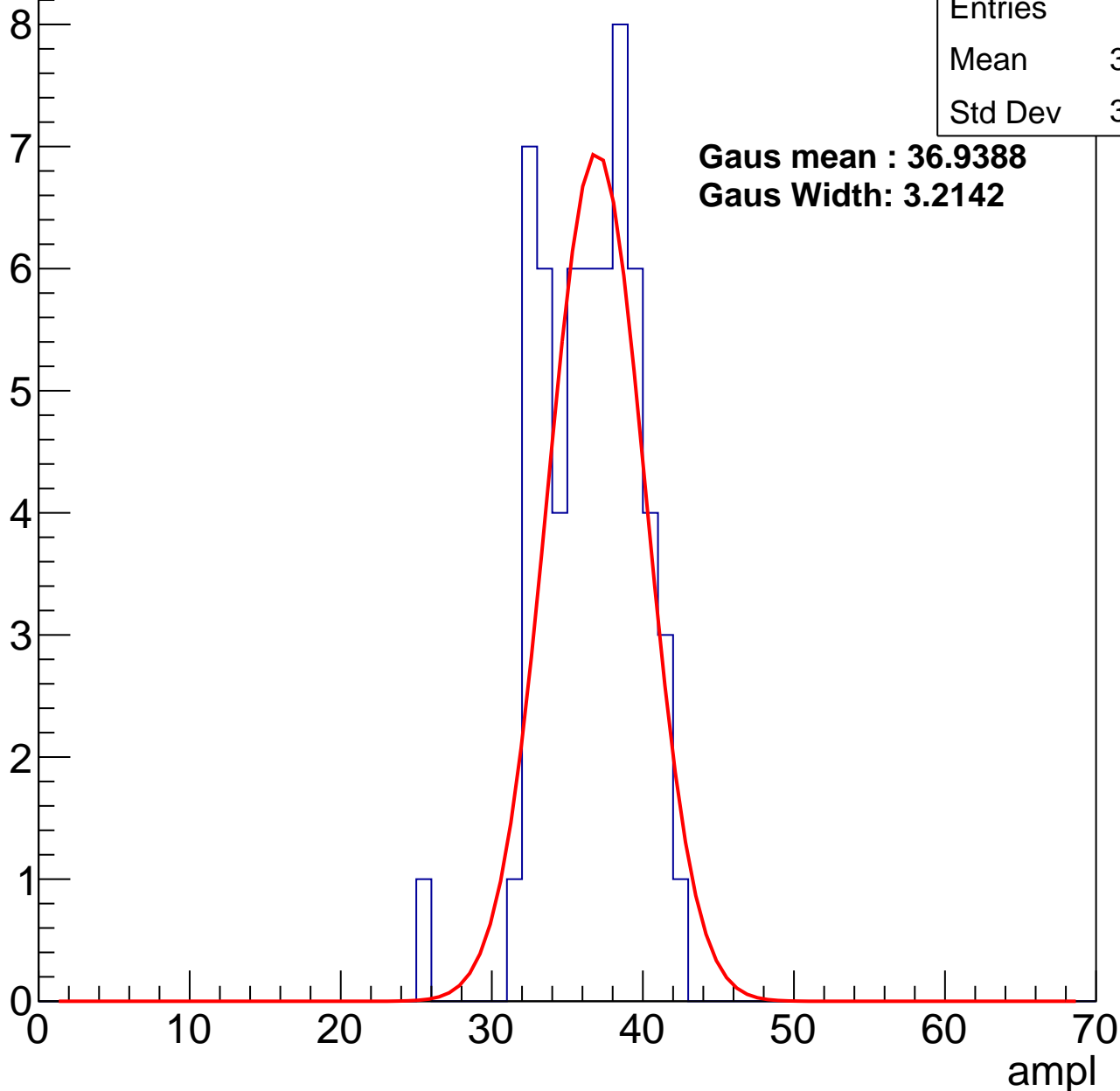
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.02
Std Dev	3.192

Gaus mean : 36.9388

Gaus Width: 3.2142



B0L002S, U18-ch120, adc2

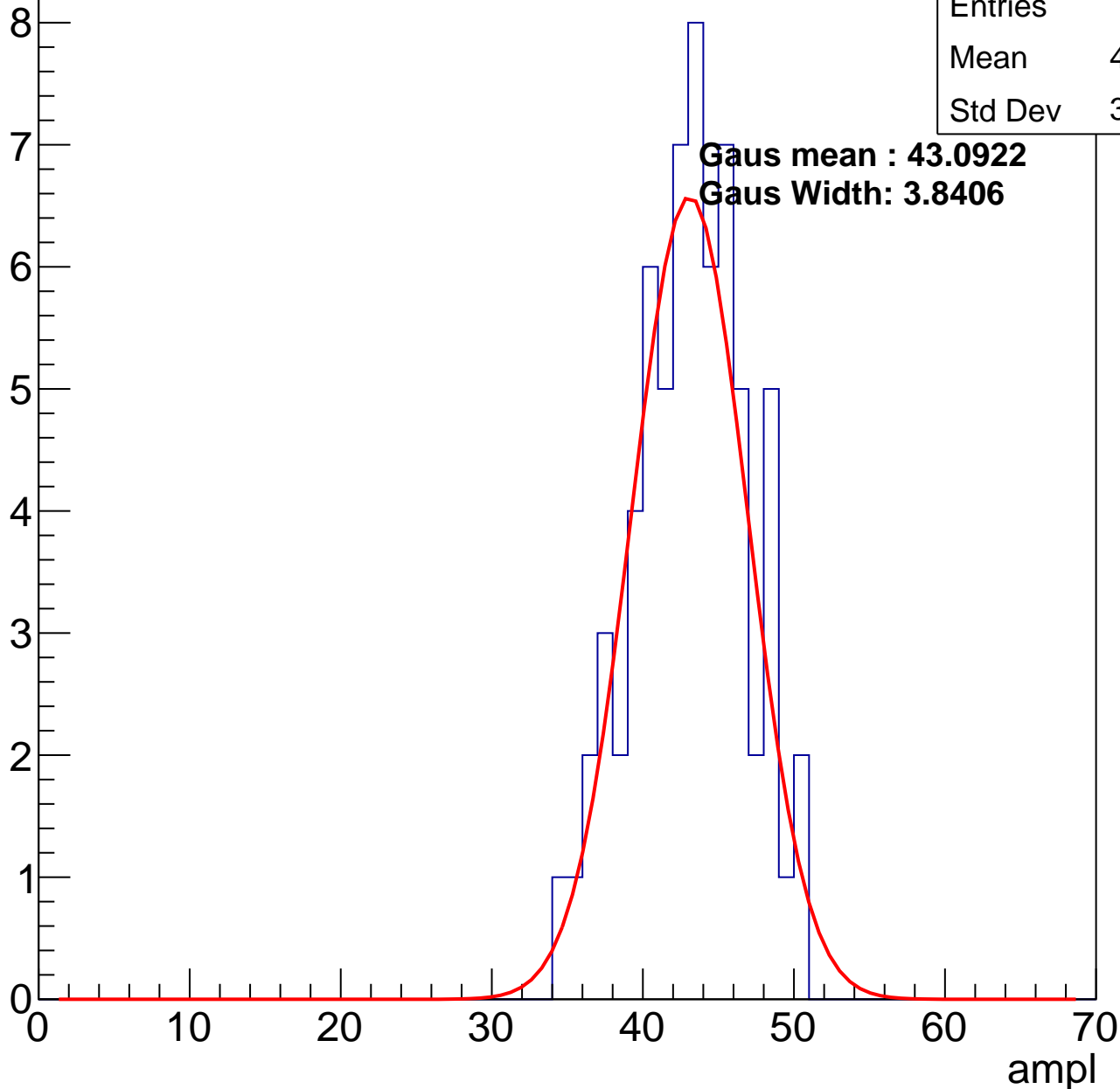
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	42.67
Std Dev	3.683

Gaus mean : 43.0922

Gaus Width: 3.8406

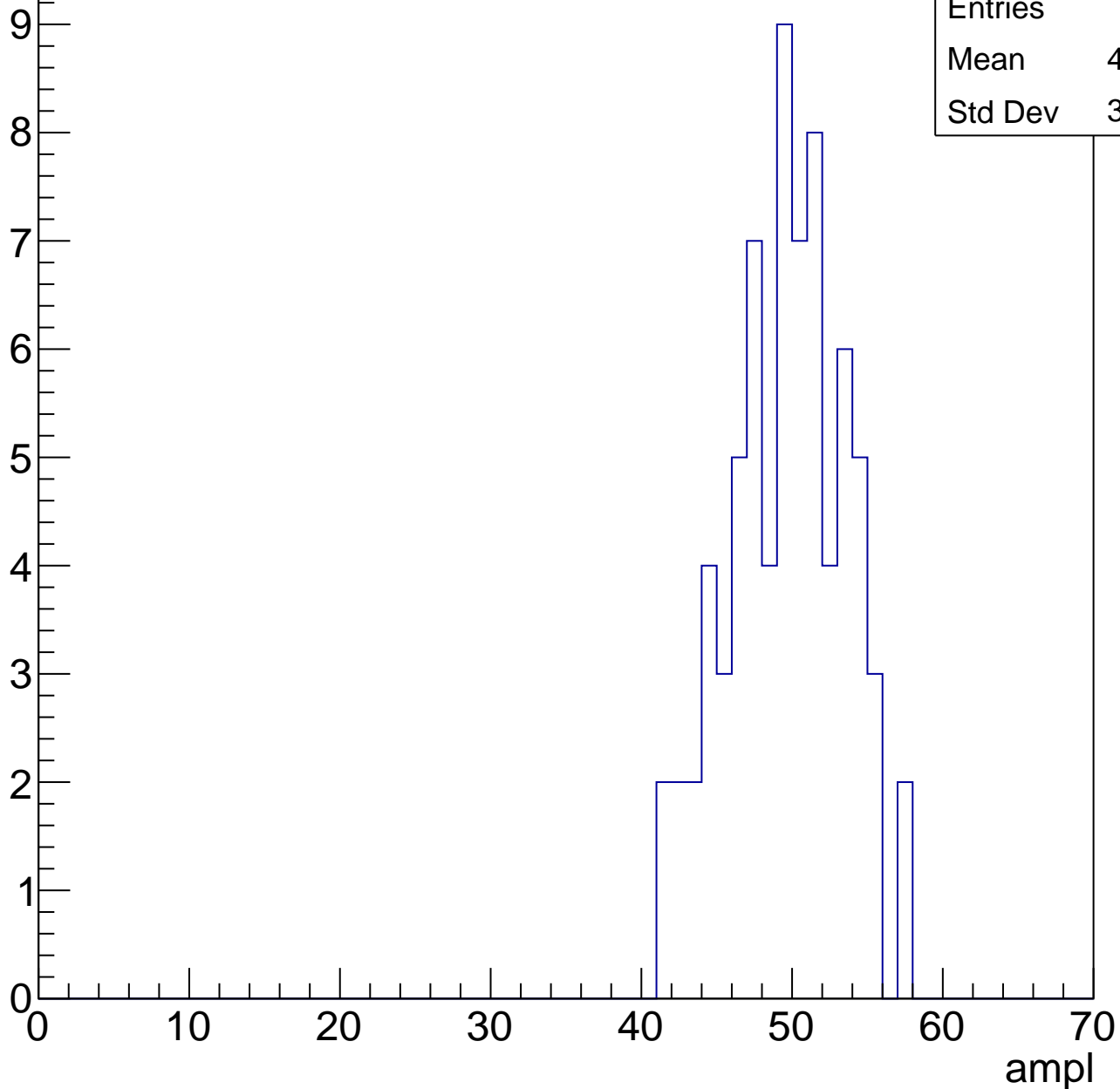


B0L002S, U18-ch120, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	49.15
Std Dev	3.799

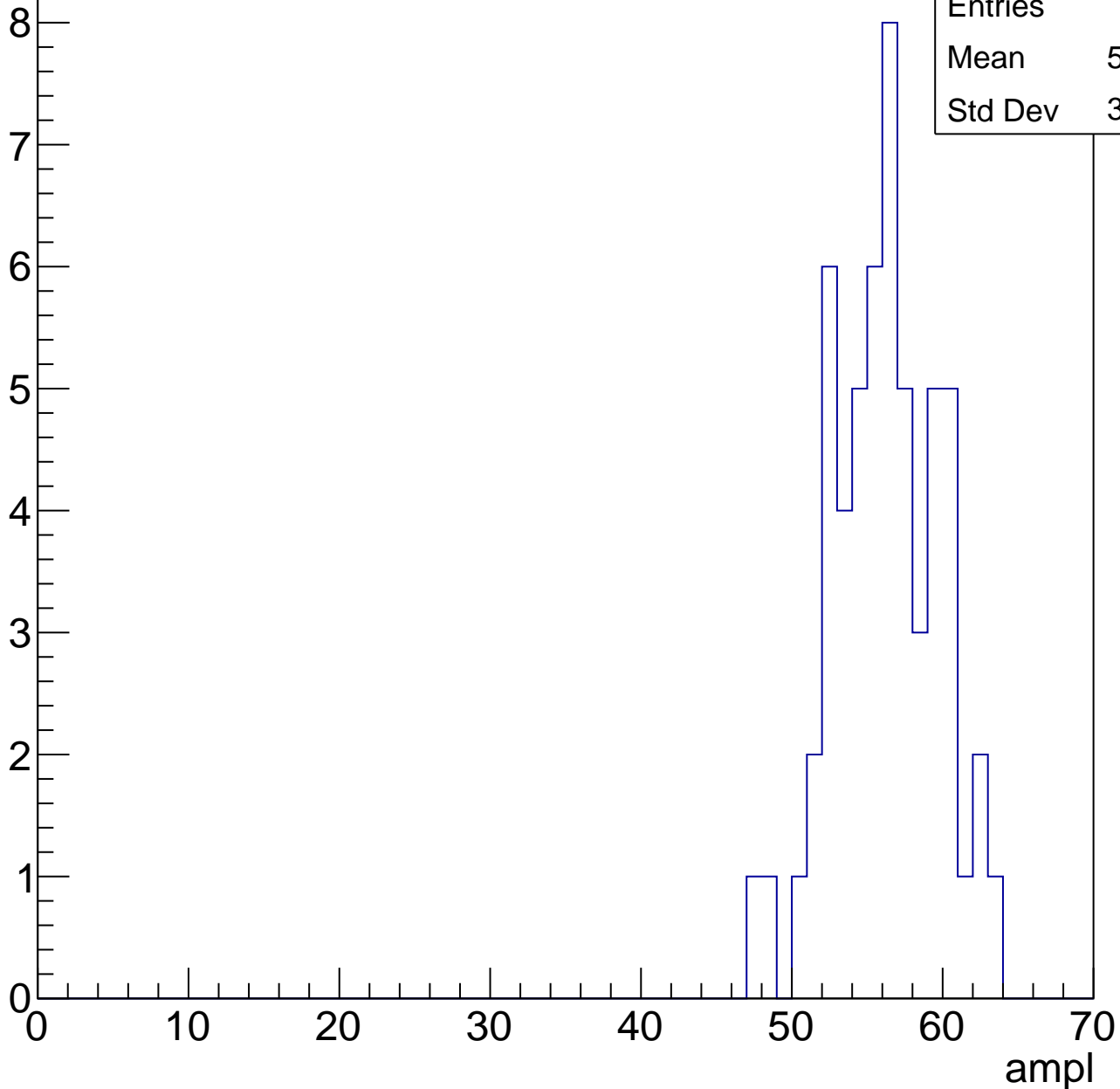


B0L002S, U18-ch120, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.73
Std Dev	3.467

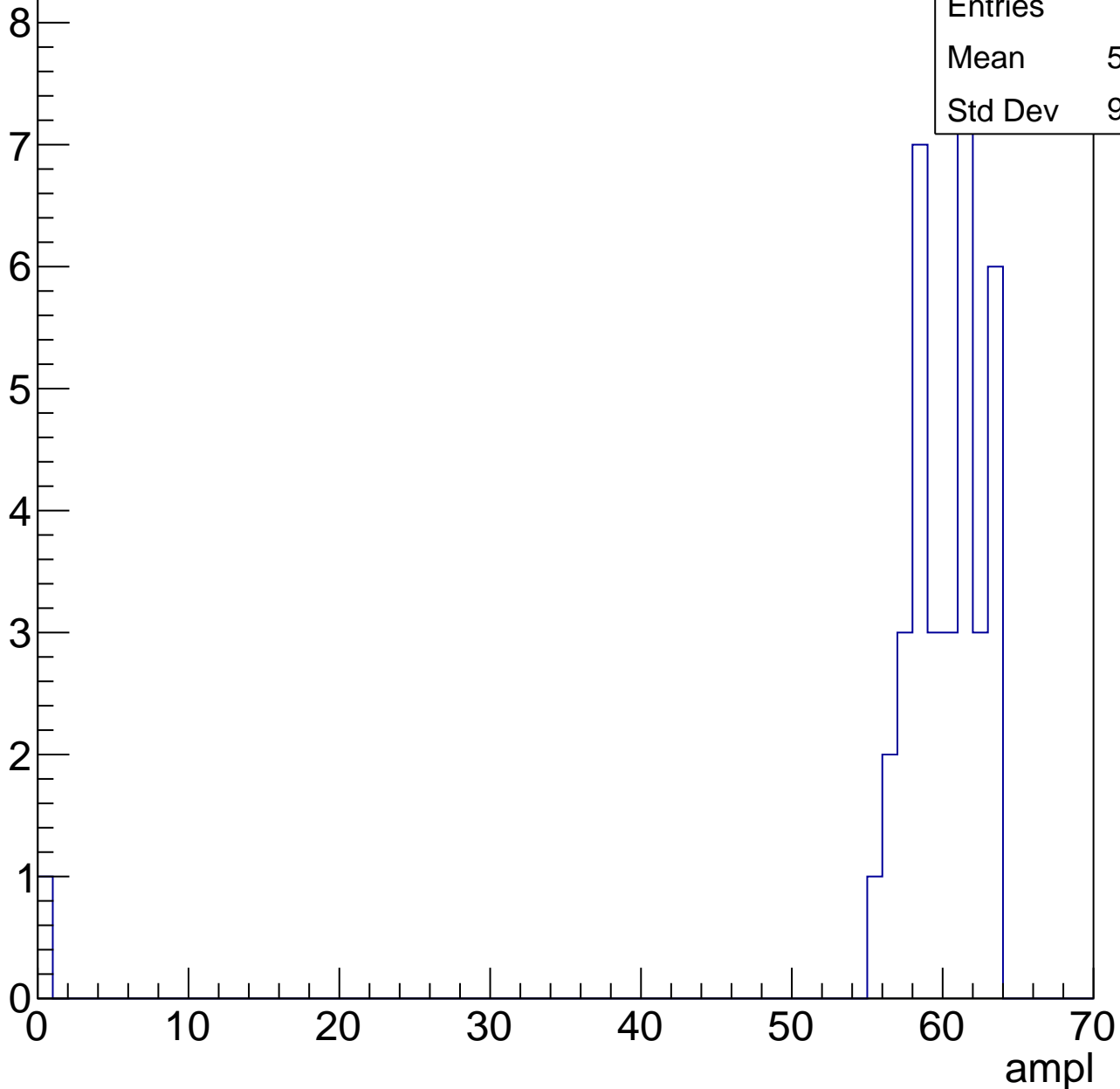


B0L002S, U18-ch120, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	37
Mean	58.19
Std Dev	9.956

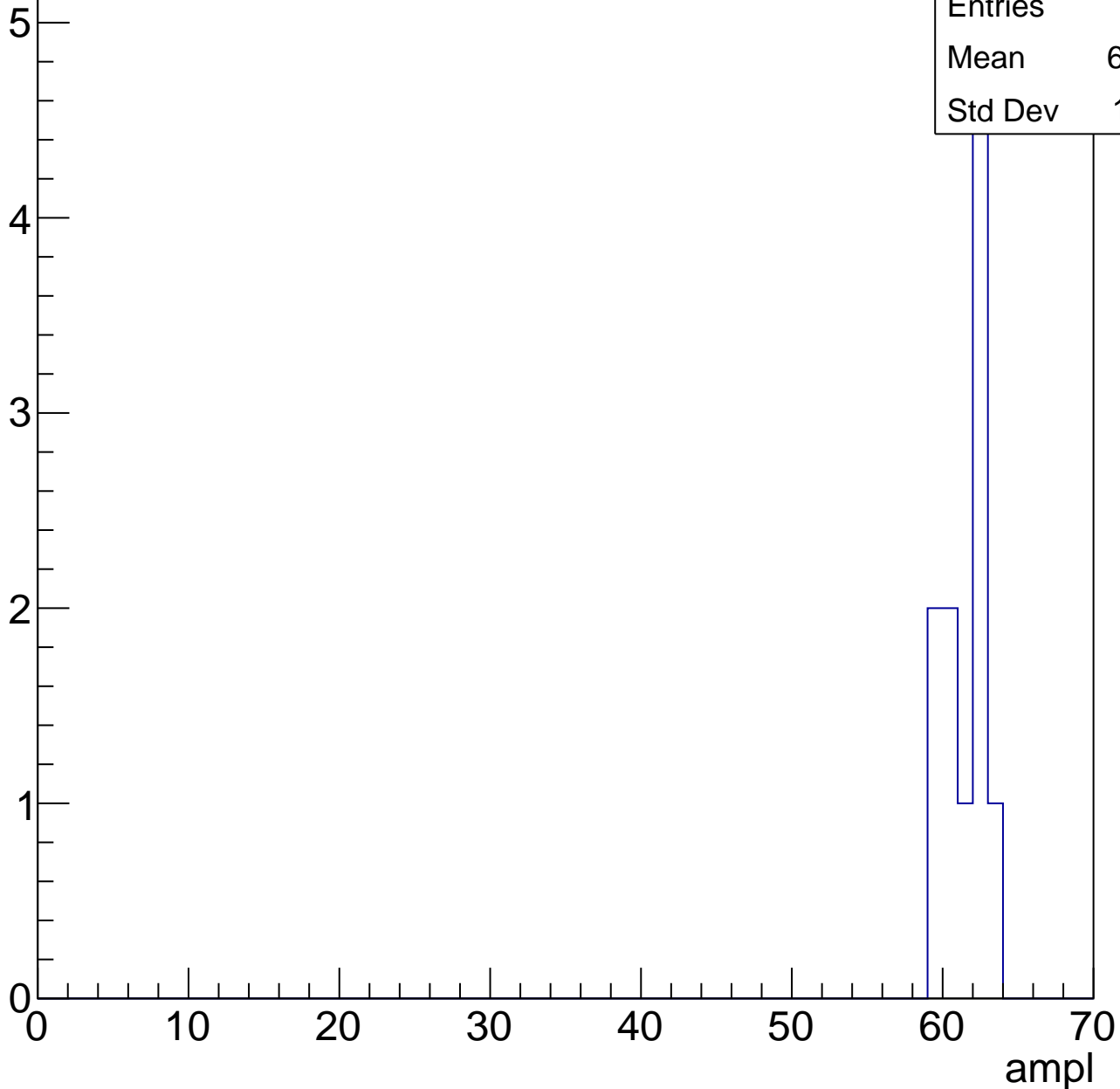


B0L002S, U18-ch120, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	11
Mean	61.09
Std Dev	1.311



B0L002S, U18-ch120, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch121, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	74
Mean	29.78
Std Dev	4.841

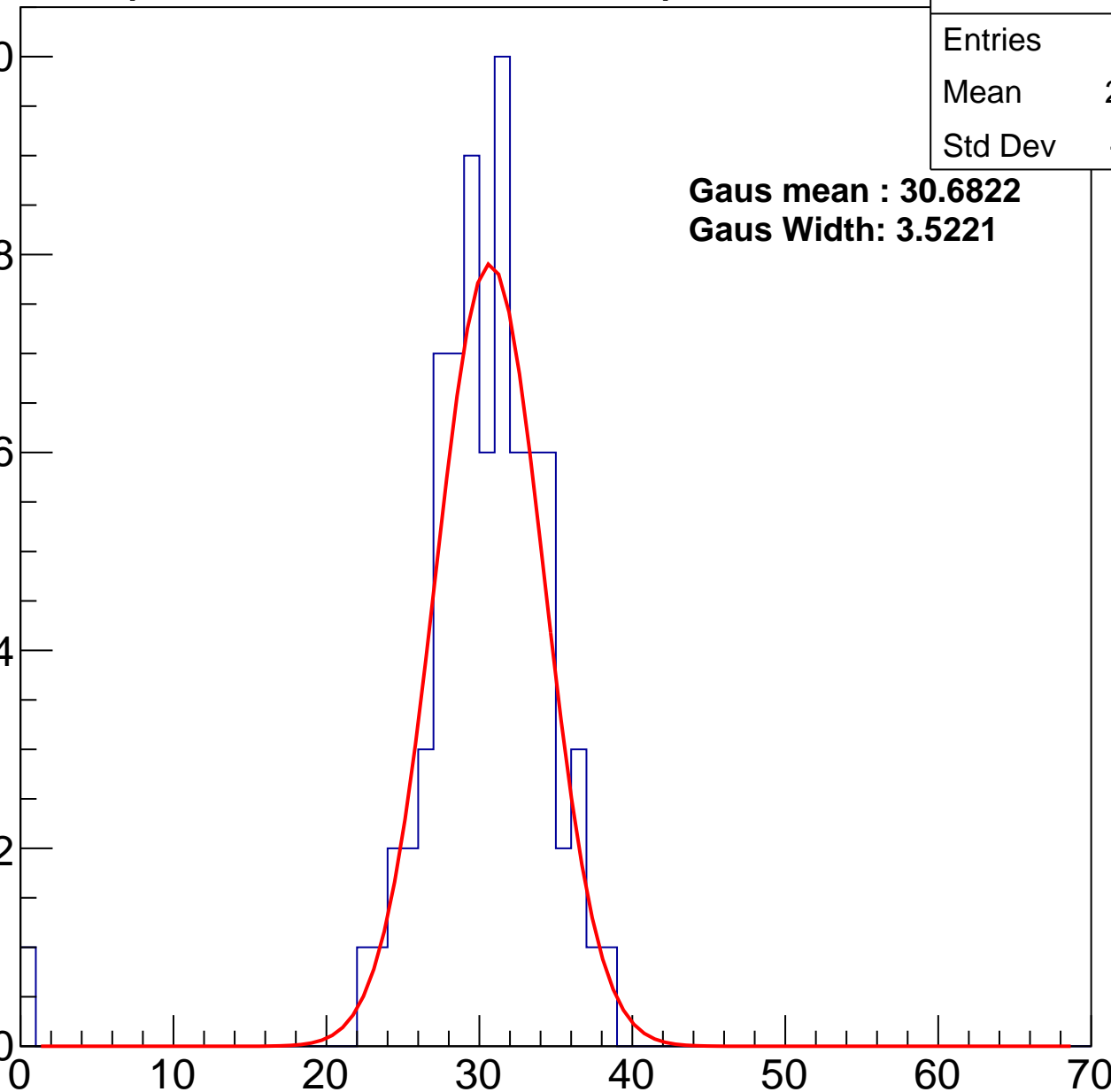
Gaus mean : 30.6822

Gaus Width: 3.5221

Entry

10
8
6
4
2
0

ampl



B0L002S, U18-ch121, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	82
Mean	38.09
Std Dev	3.475

Gaus mean : 38.5808

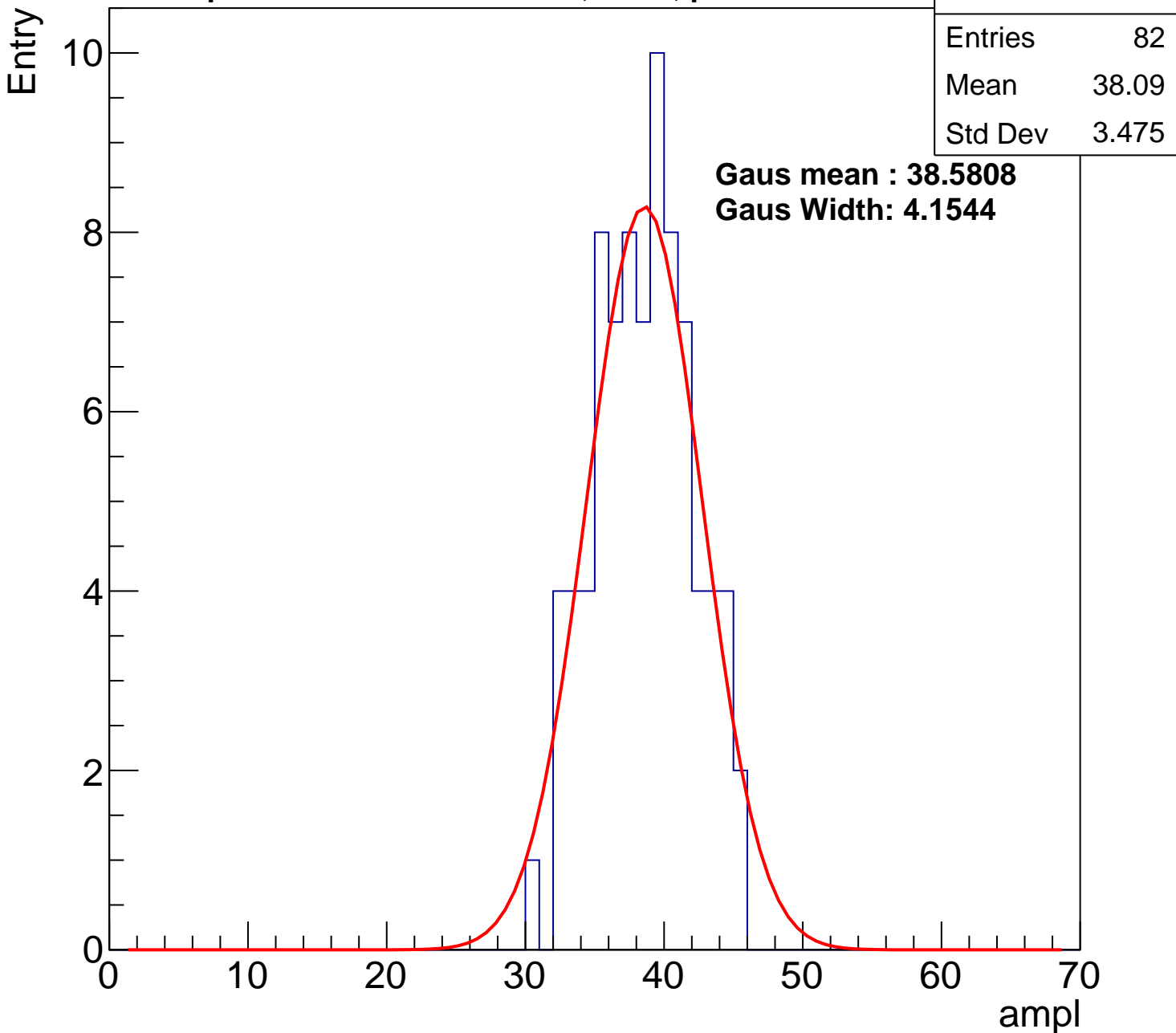
Gaus Width: 4.1544

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B0L002S, U18-ch121, adc2

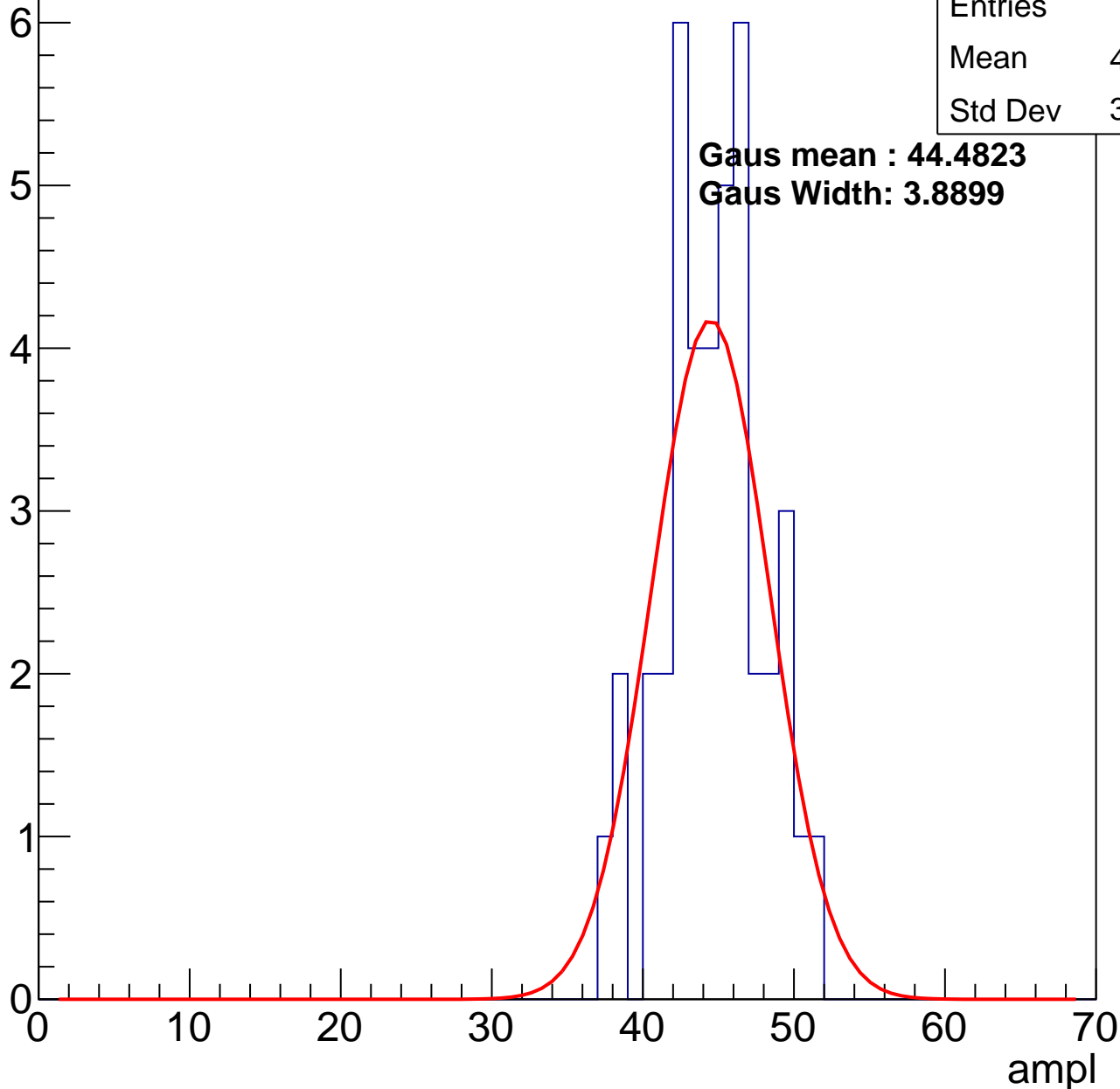
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	41
Mean	44.24
Std Dev	3.259

Gaus mean : 44.4823

Gaus Width: 3.8899

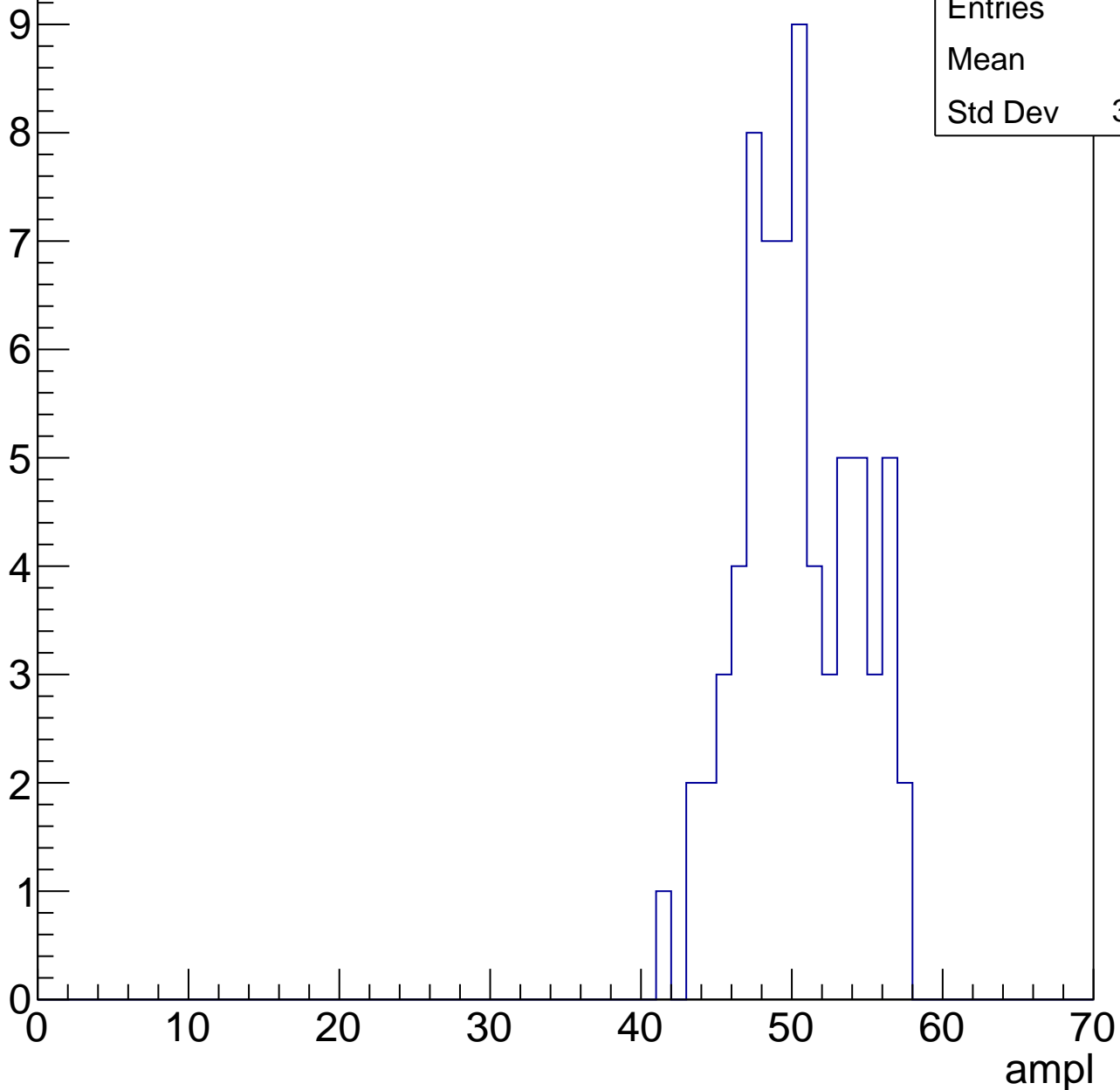


B0L002S, U18-ch121, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	49.9
Std Dev	3.761

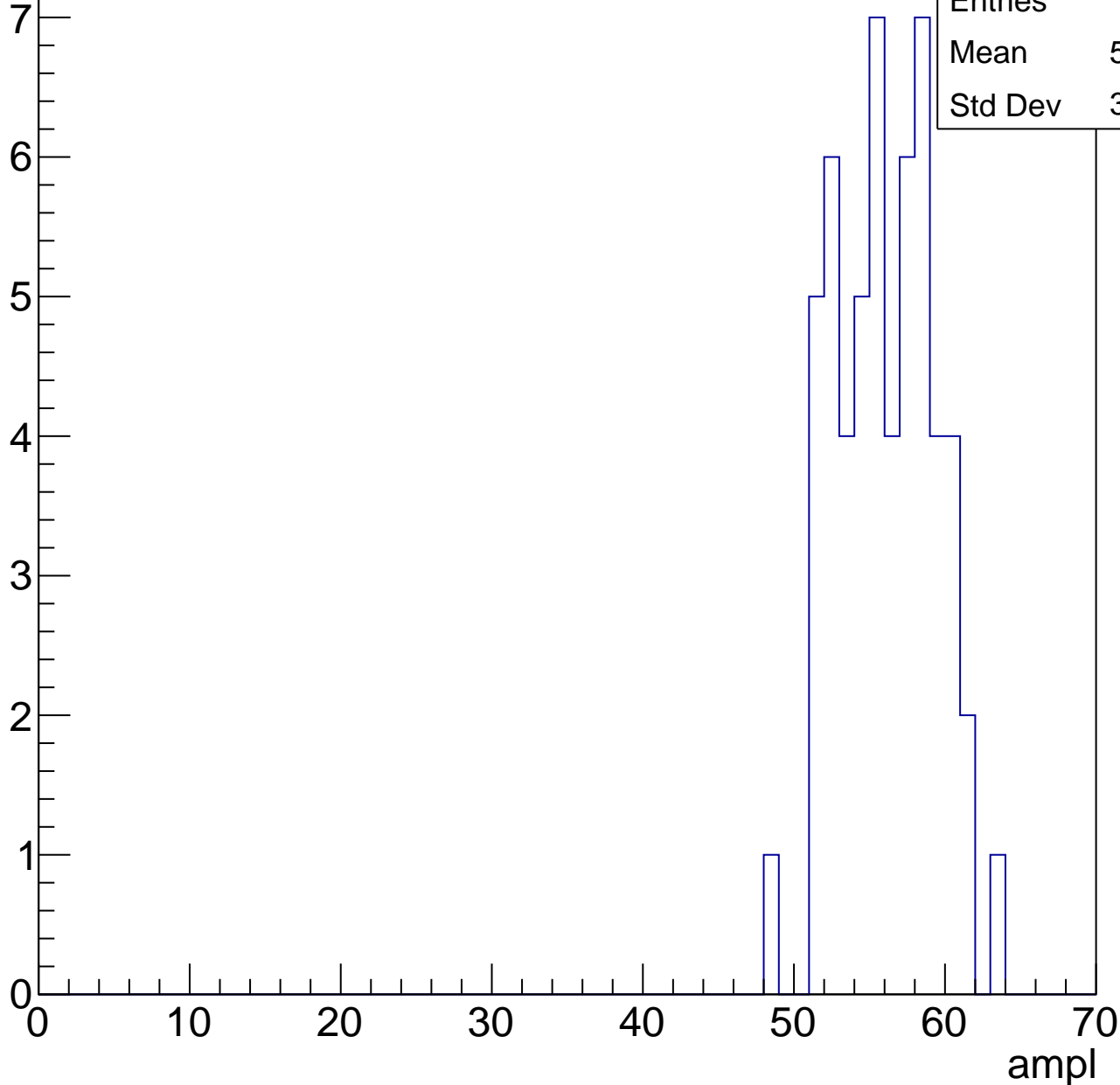


B0L002S, U18-ch121, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	55.62
Std Dev	3.199



B0L002S, U18-ch121, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entries	44
Mean	58.91
Std Dev	9.32

Entry

10

8

6

4

2

0

0

10

20

30

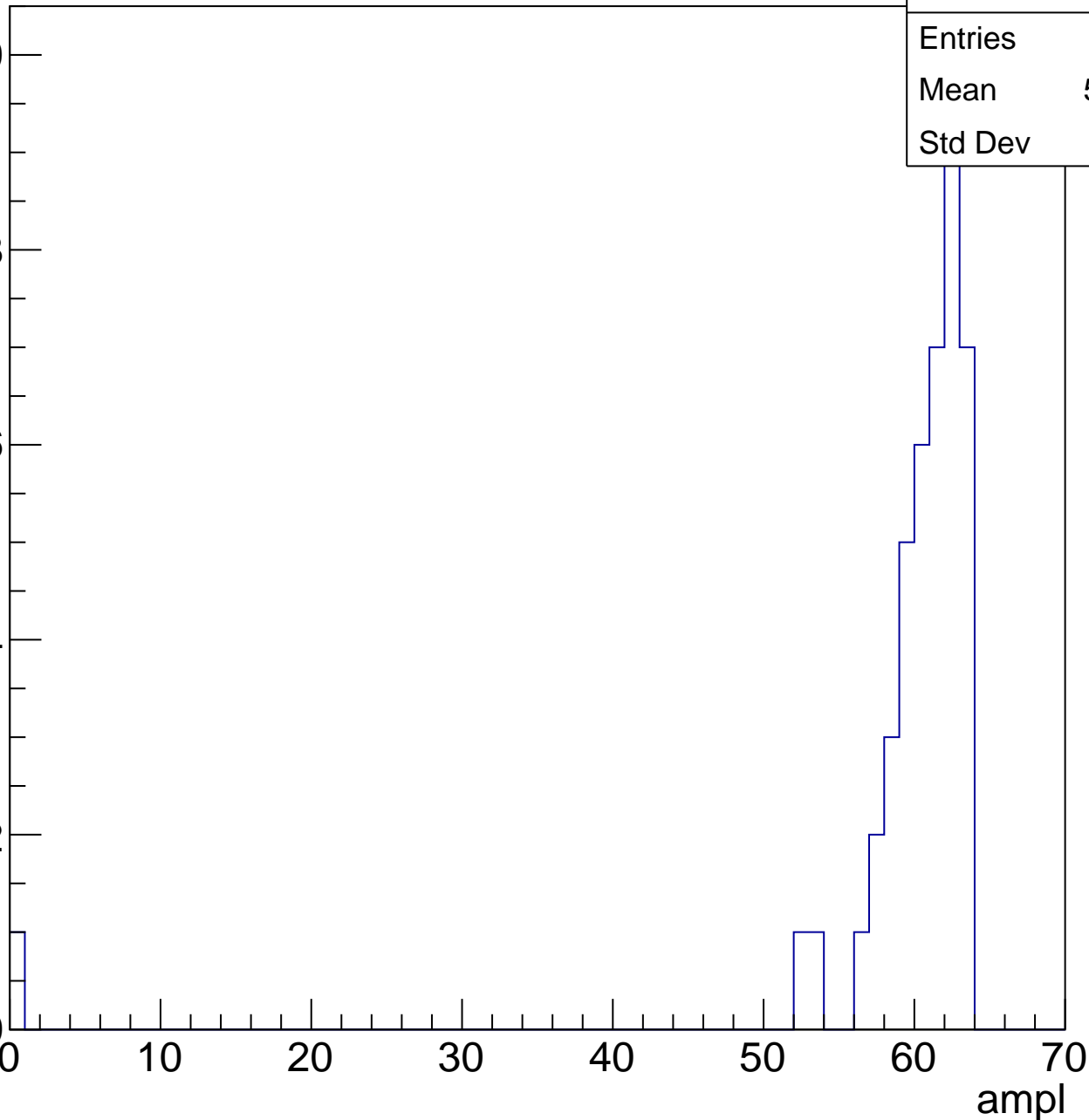
40

50

60

70

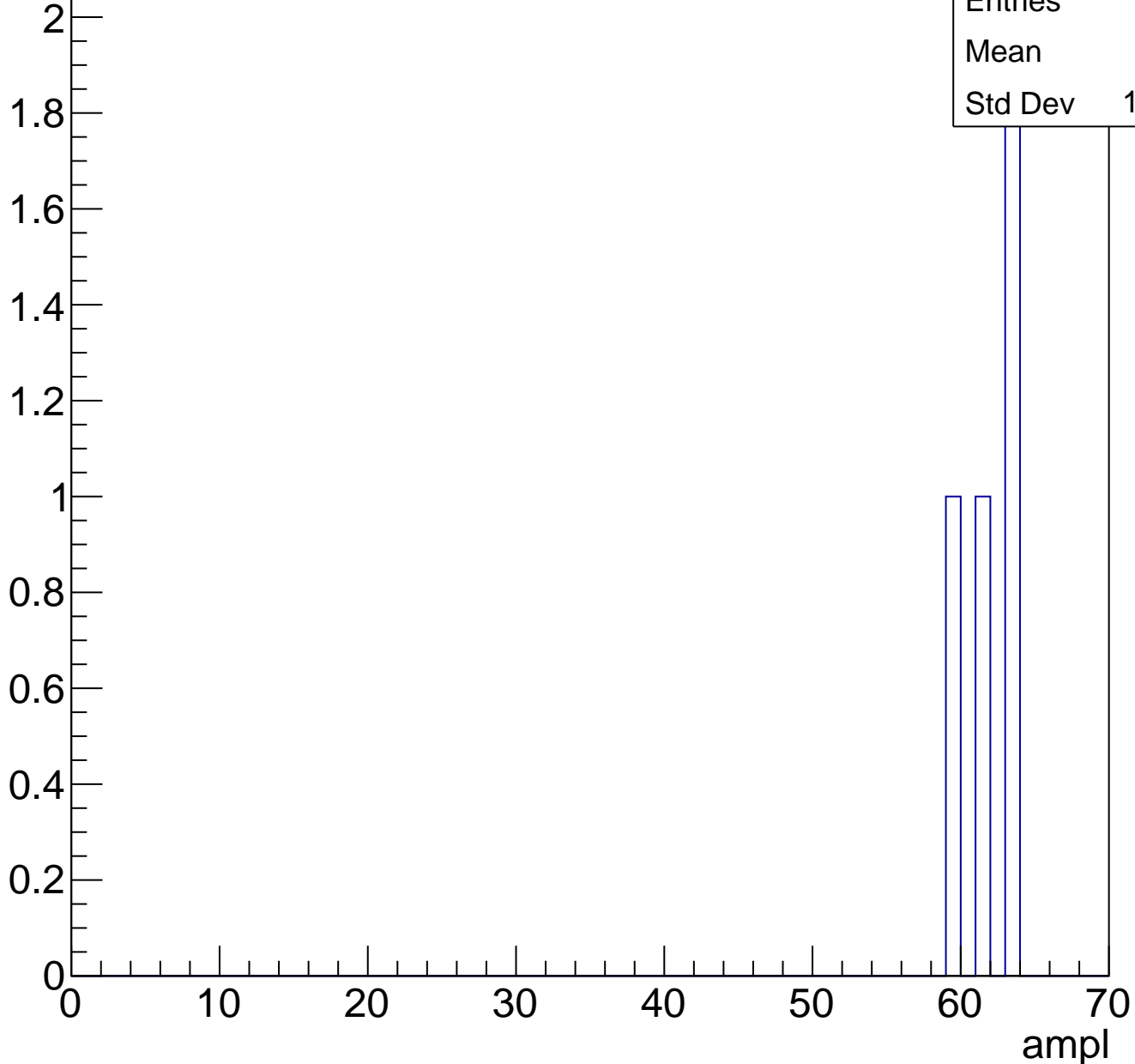
ampl



B0L002S, U18-ch121, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	4
Mean	61.5
Std Dev	1.658

B0L002S, U18-ch121, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch122, adc0

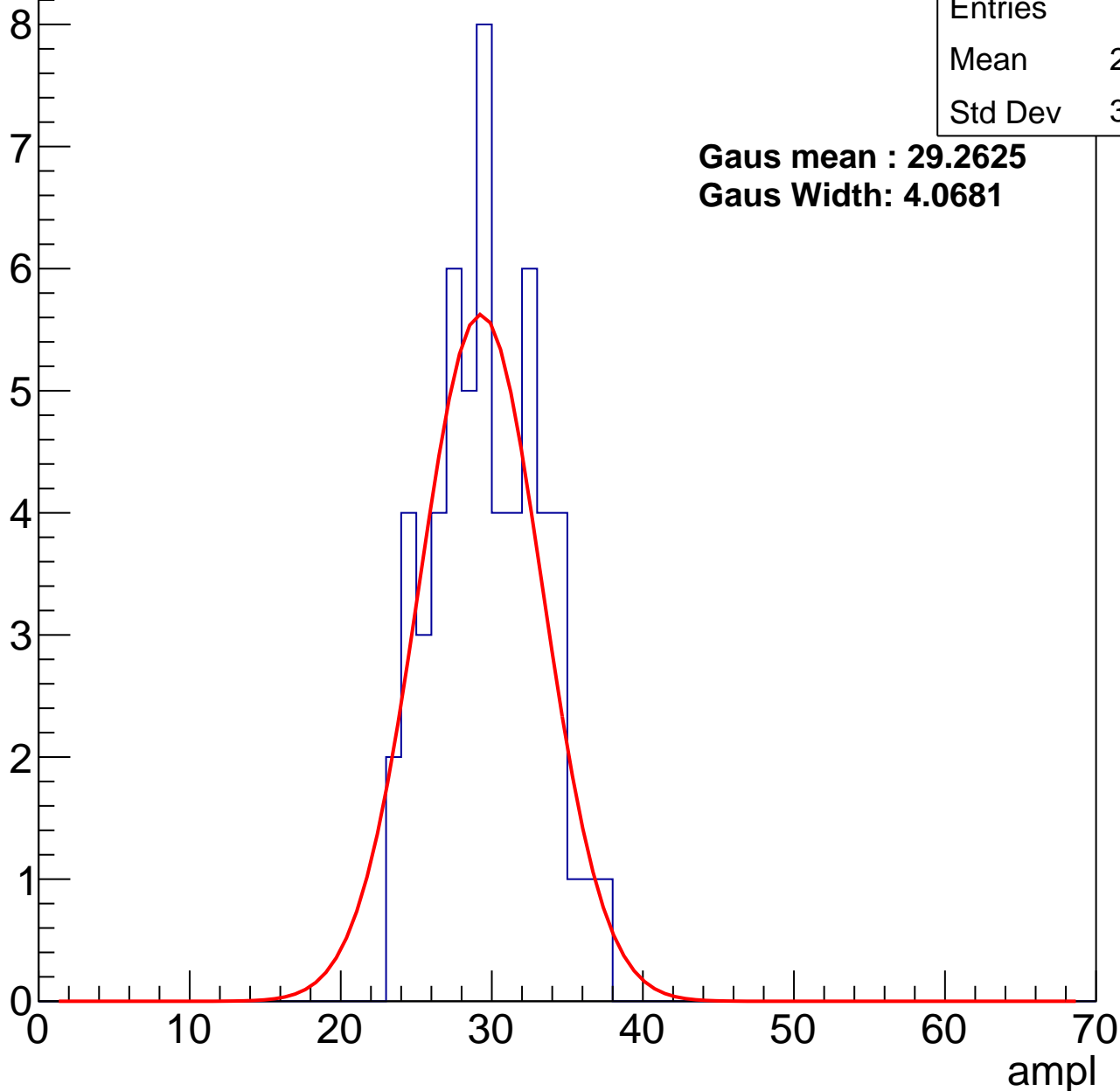
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	57
Mean	29.25
Std Dev	3.425

Gaus mean : 29.2625

Gaus Width: 4.0681



B0L002S, U18-ch122, adc1

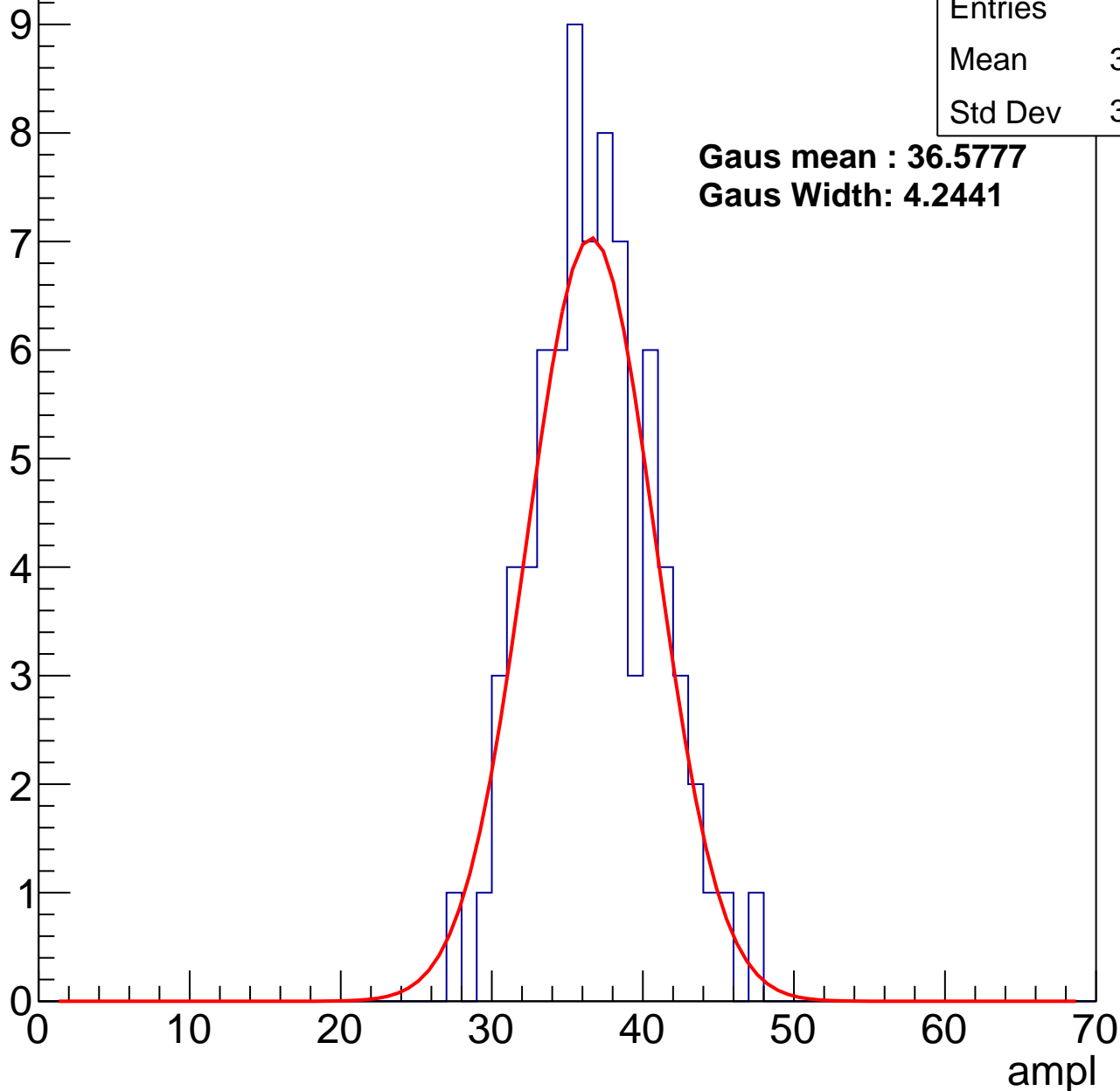
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	36.34
Std Dev	3.966

Gaus mean : 36.5777

Gaus Width: 4.2441



B0L002S, U18-ch122, adc2

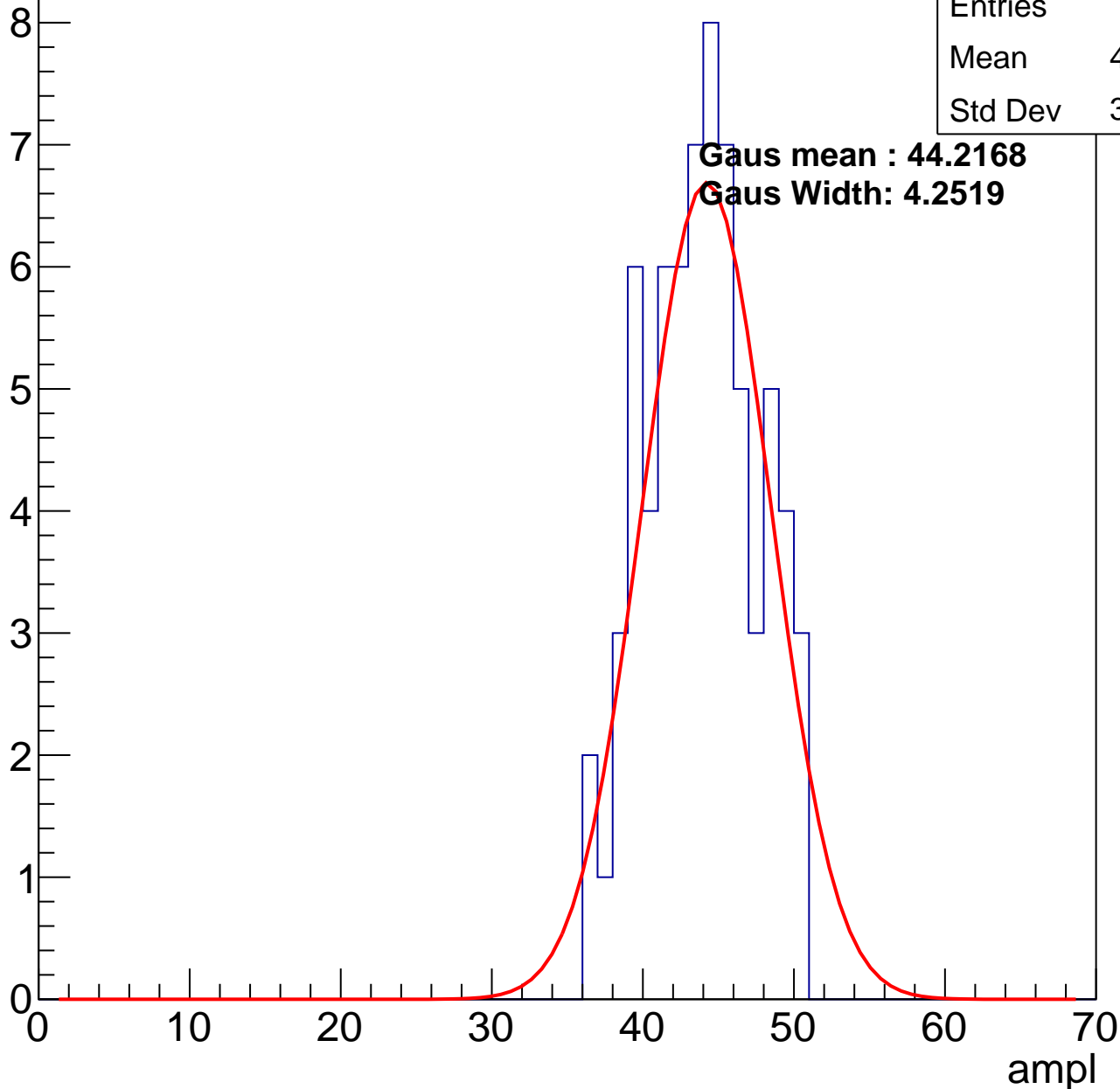
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	70
Mean	43.43
Std Dev	3.592

Gaus mean : 44.2168

Gaus Width: 4.2519

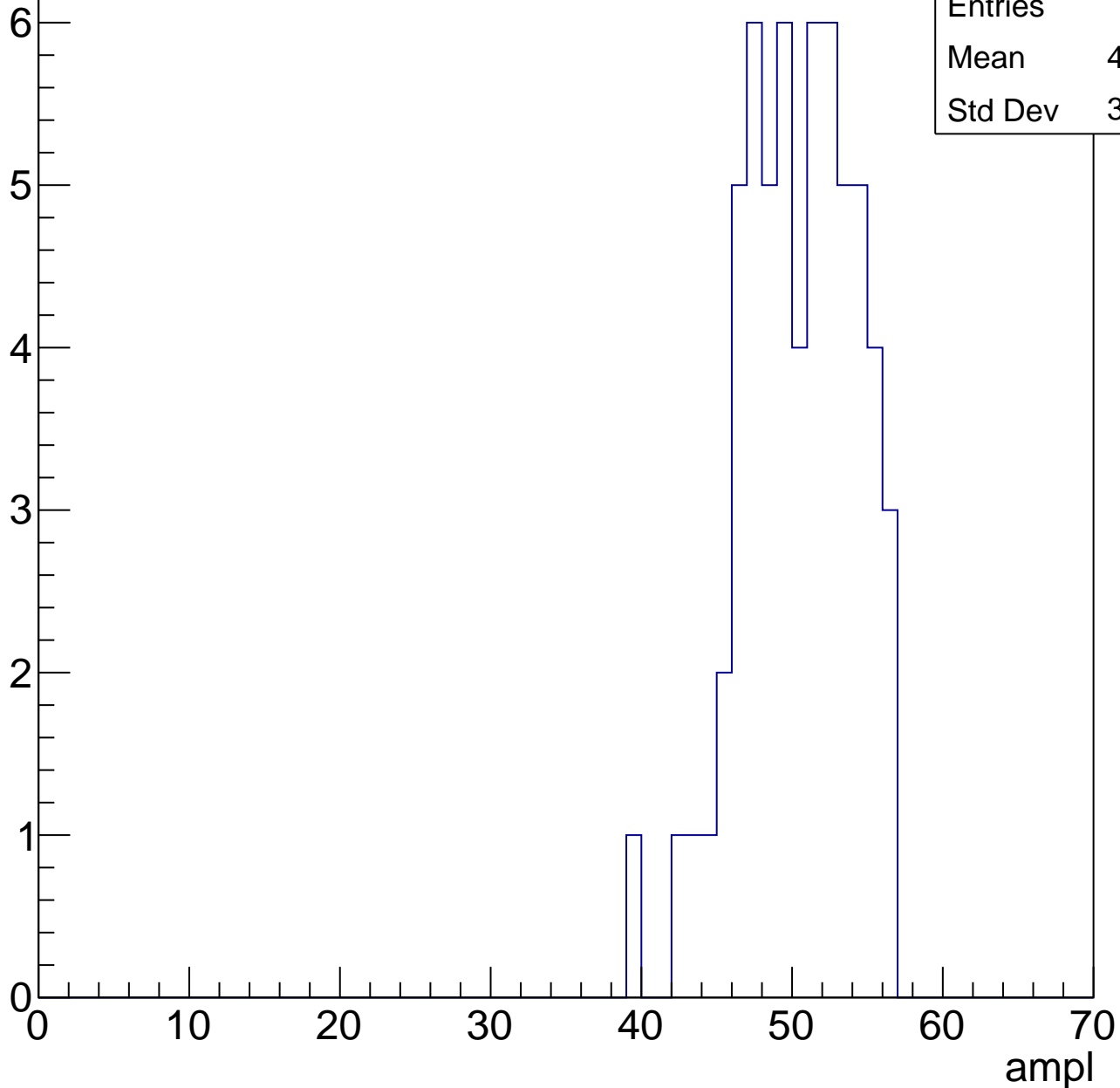


B0L002S, U18-ch122, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	61
Mean	49.92
Std Dev	3.717

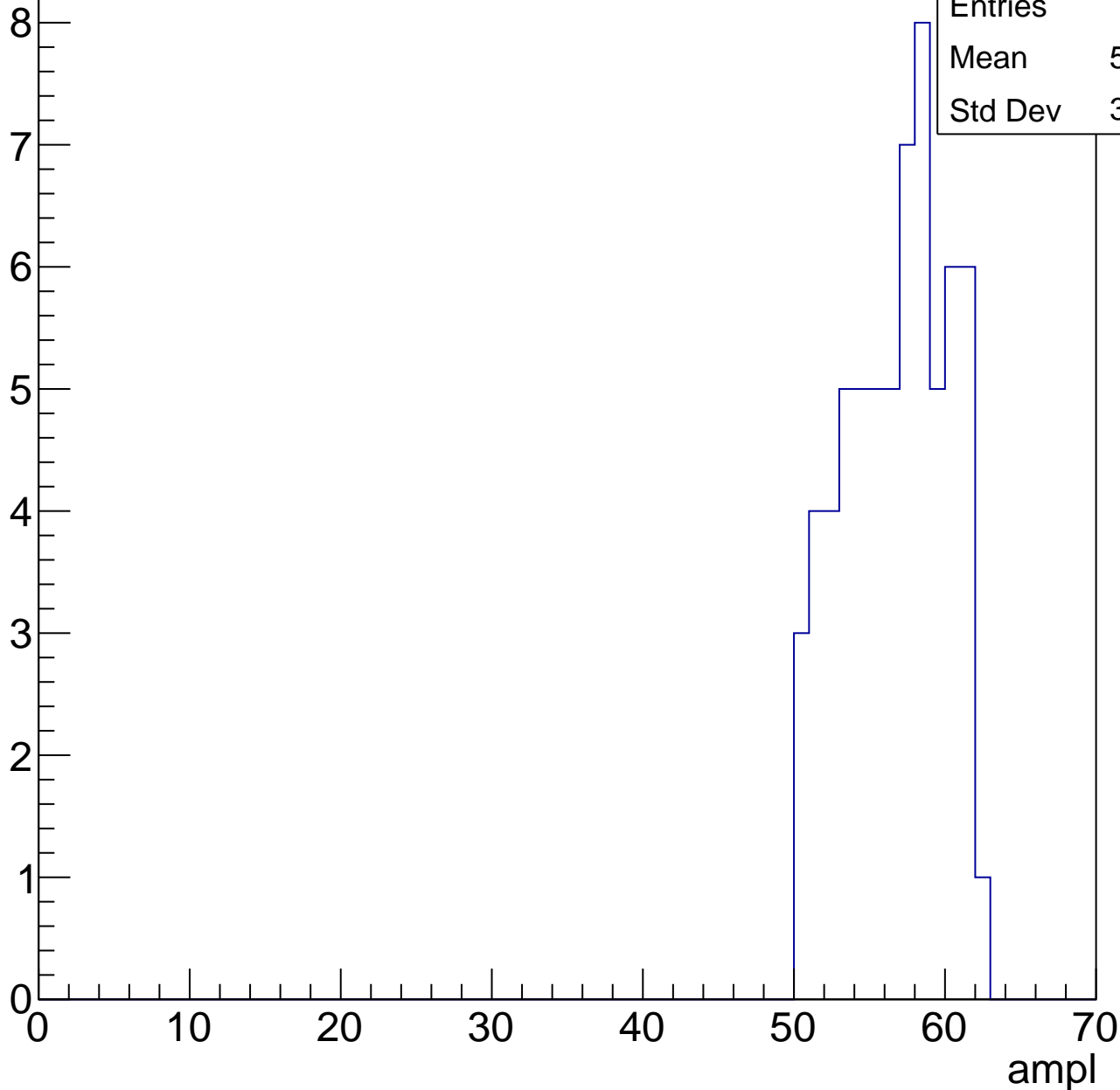


B0L002S, U18-ch122, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	56.22
Std Dev	3.319

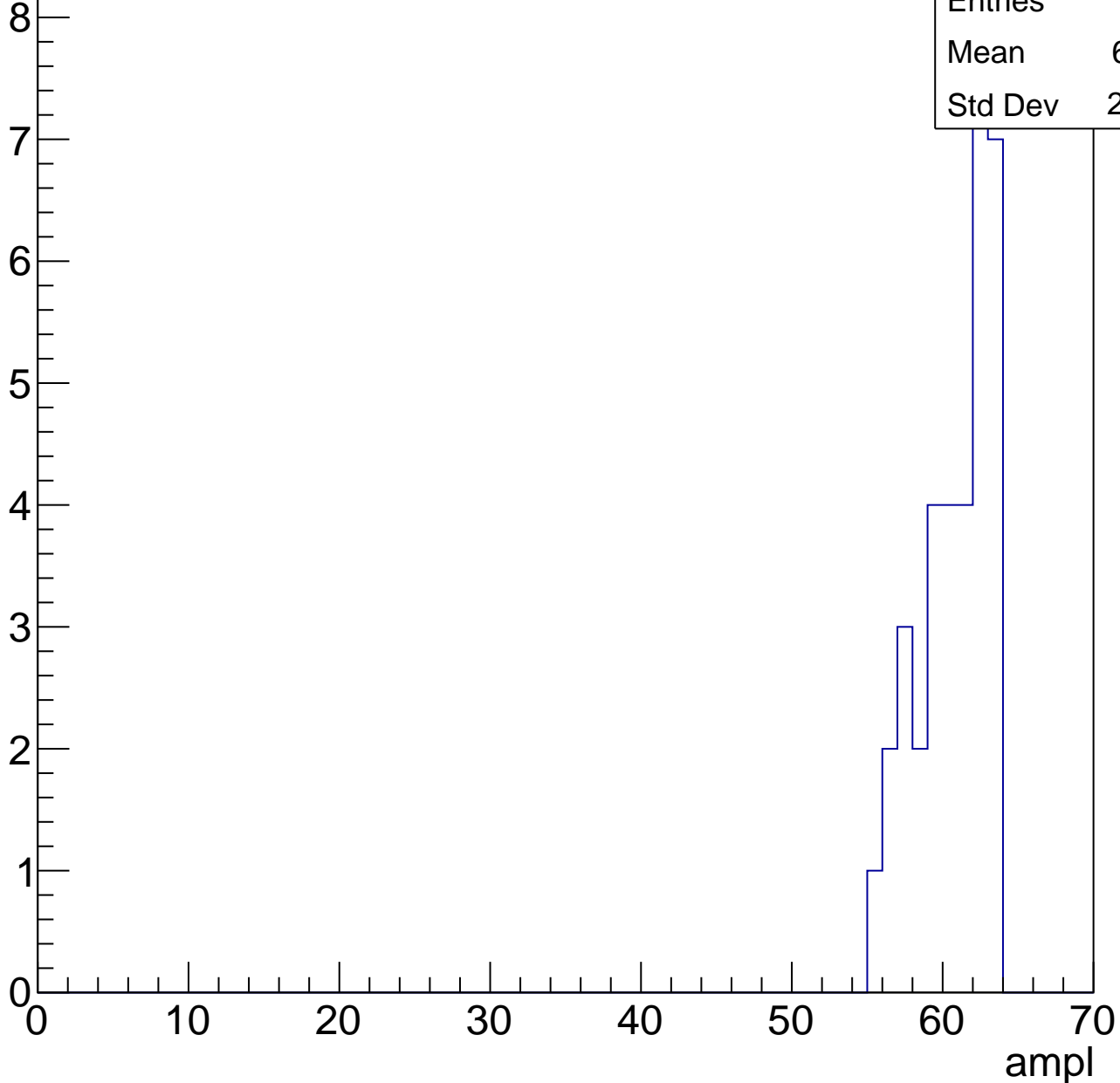


B0L002S, U18-ch122, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

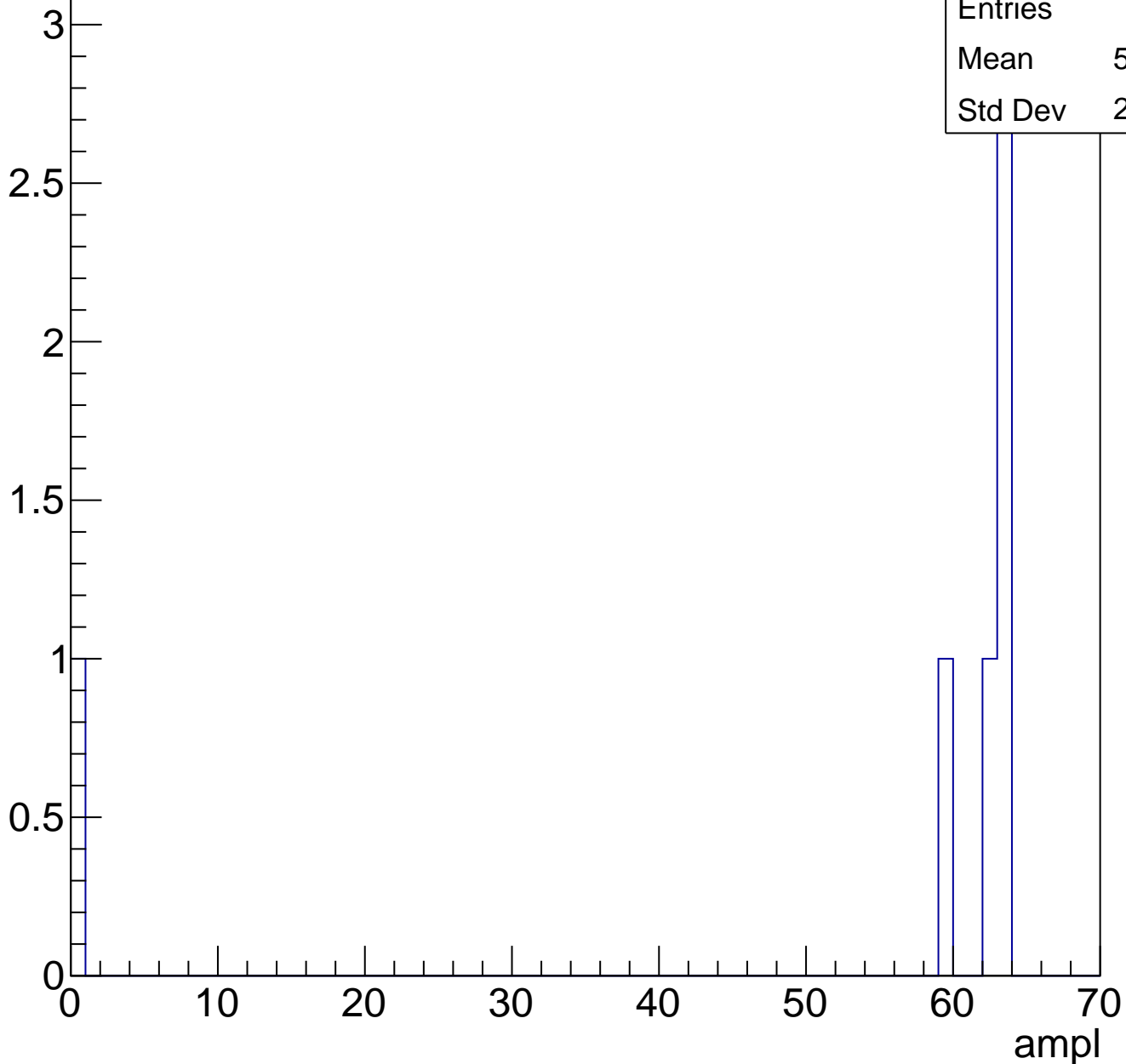
Entries	35
Mean	60.31
Std Dev	2.339



B0L002S, U18-ch122, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch122, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	1
Mean	0
Std Dev	0

B0L002S, U18-ch123, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	94
Mean	31.09
Std Dev	3.783

Gaus mean : 31.0341

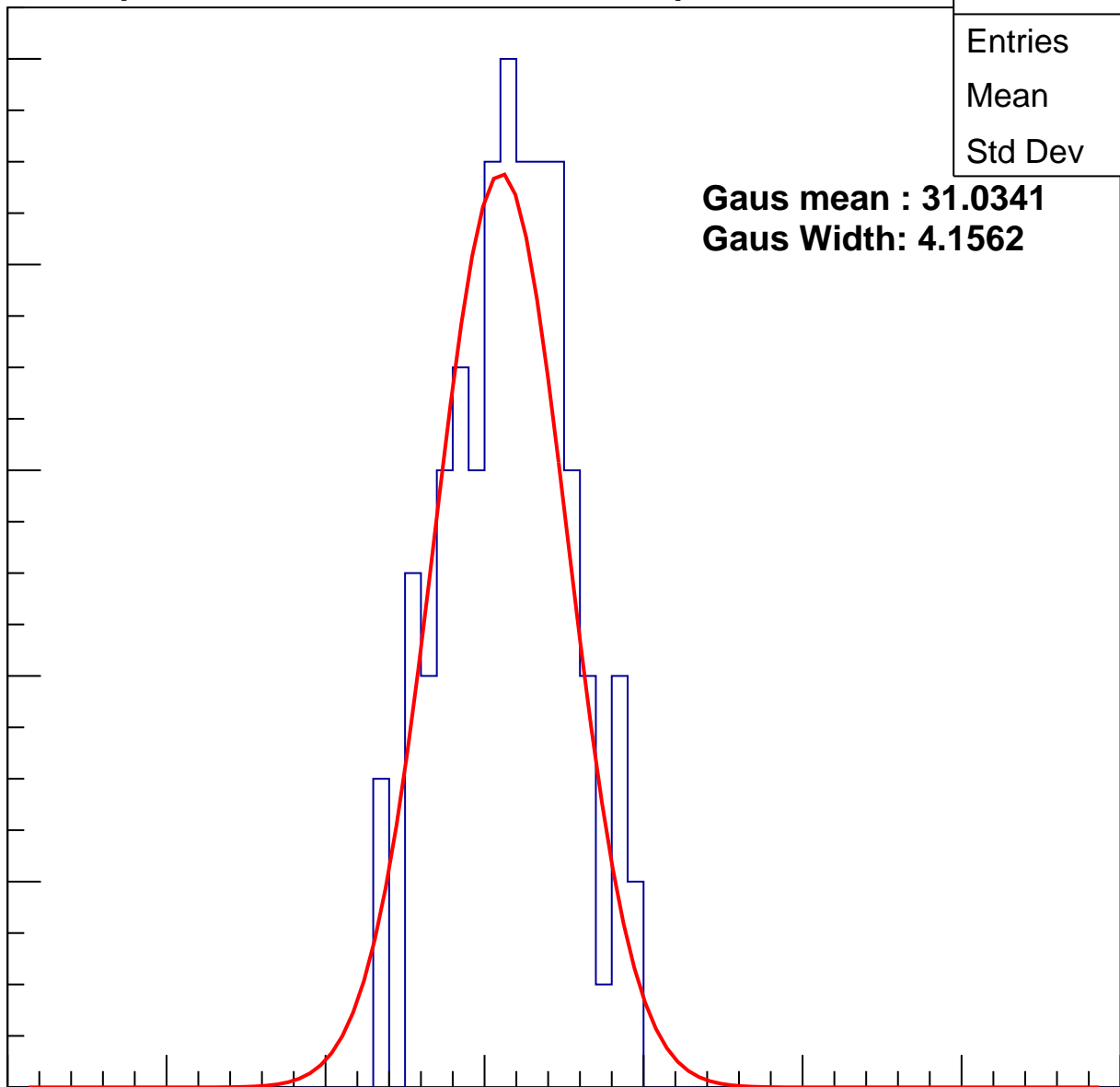
Gaus Width: 4.1562

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch123, adc1

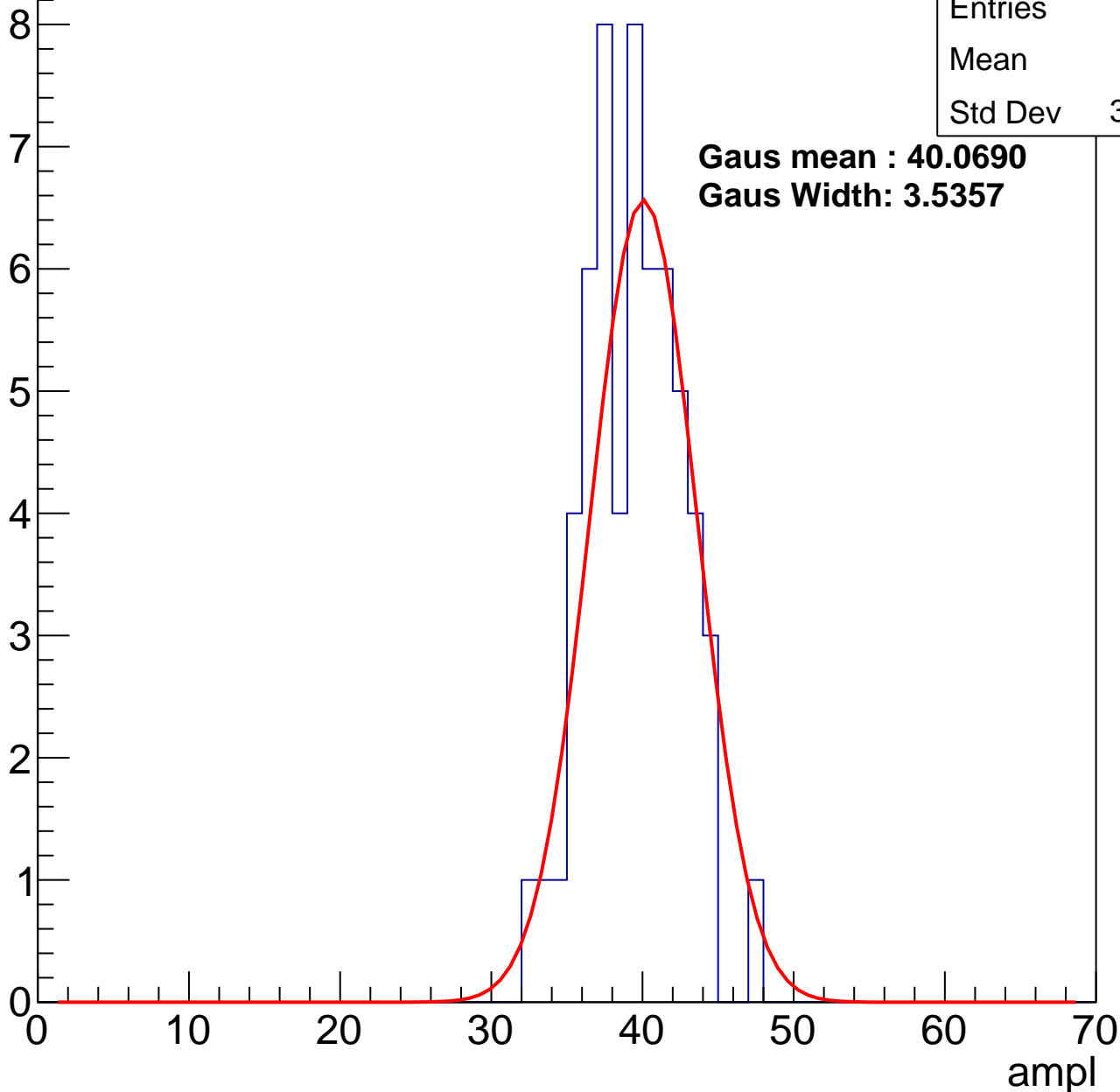
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	58
Mean	39
Std Dev	3.057

Gaus mean : 40.0690

Gaus Width: 3.5357



B0L002S, U18-ch123, adc2

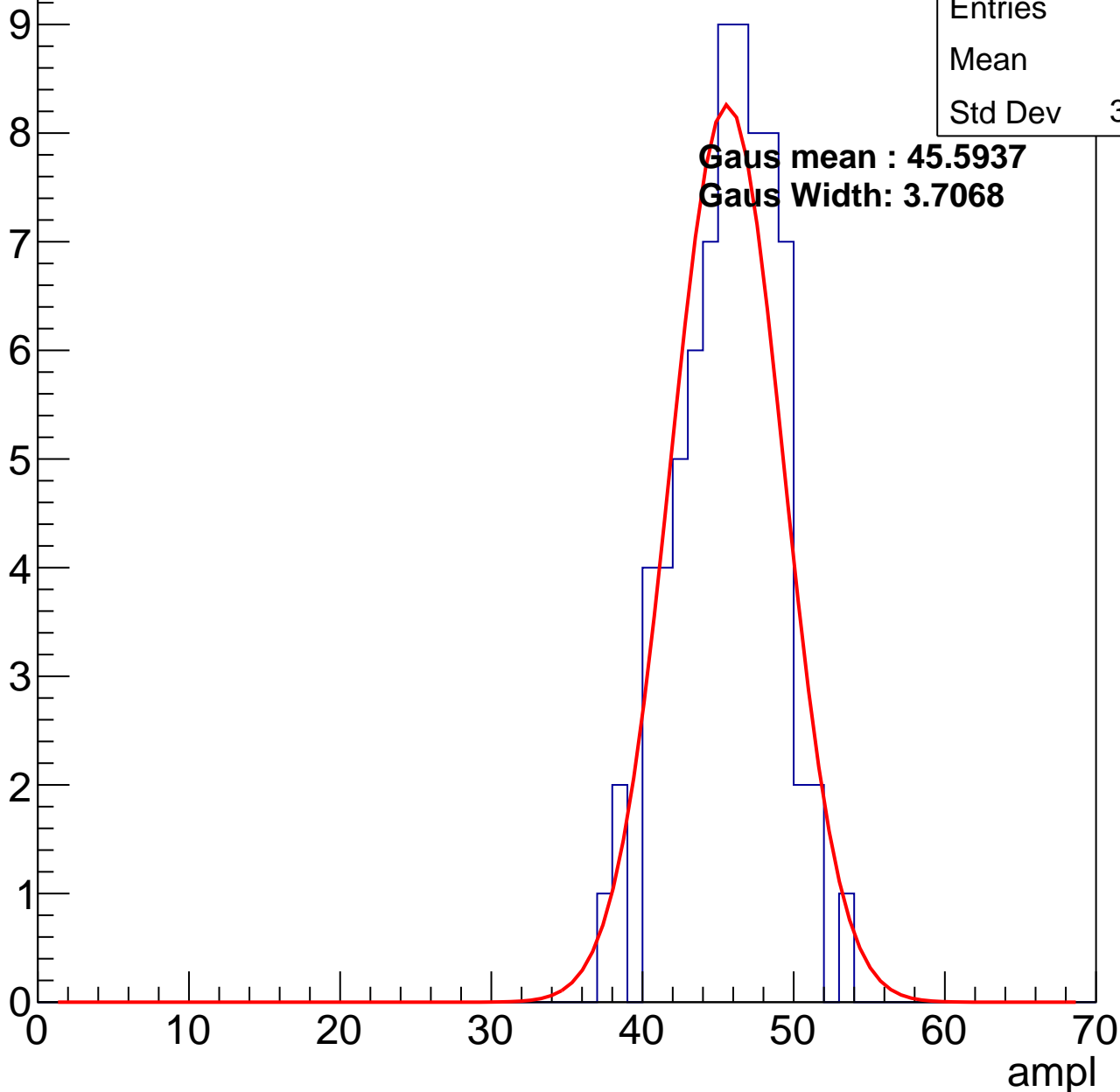
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	45.2
Std Dev	3.286

Gaus mean : 45.5937

Gaus Width: 3.7068

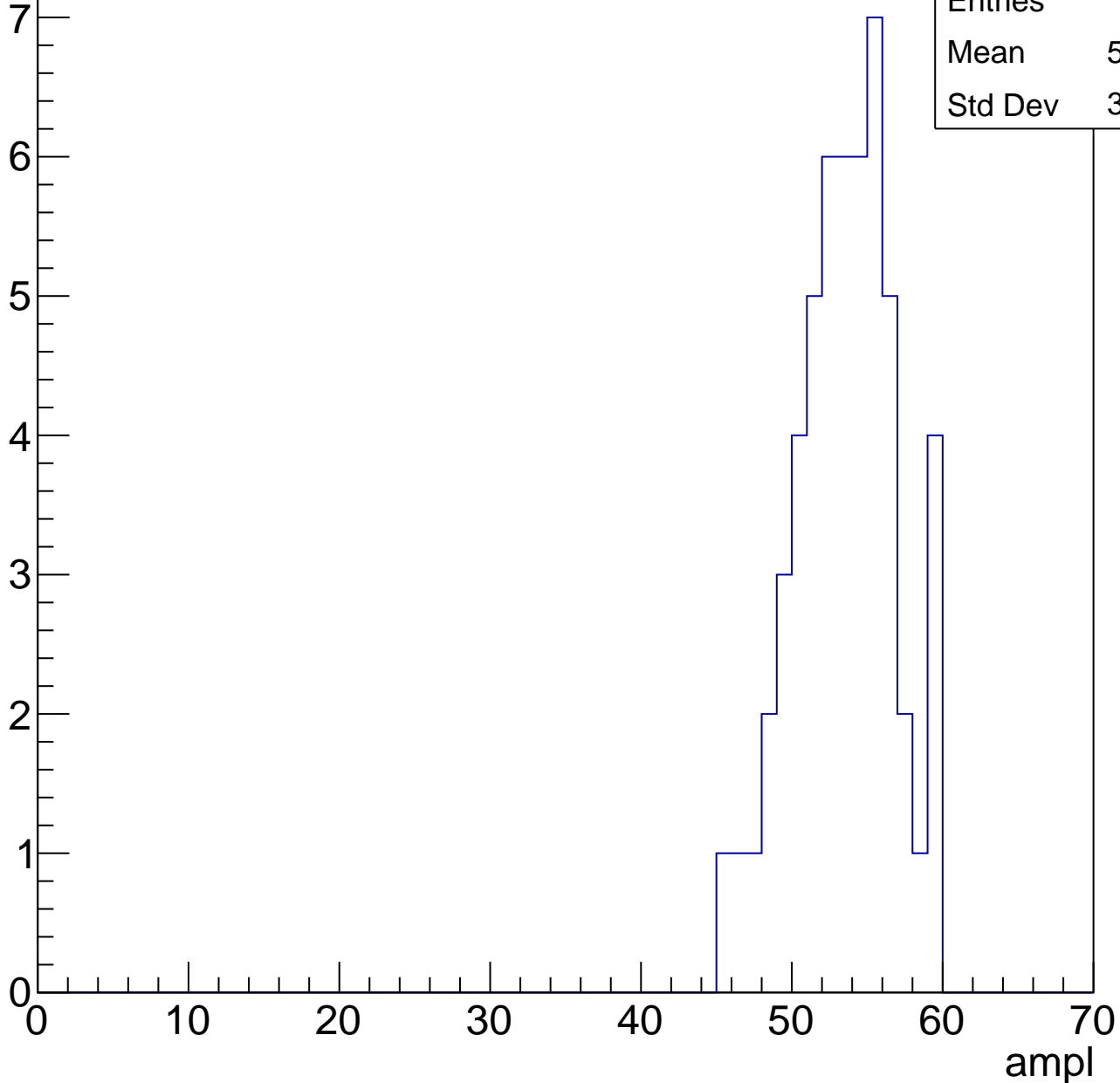


B0L002S, U18-ch123, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	54
Mean	53.02
Std Dev	3.303

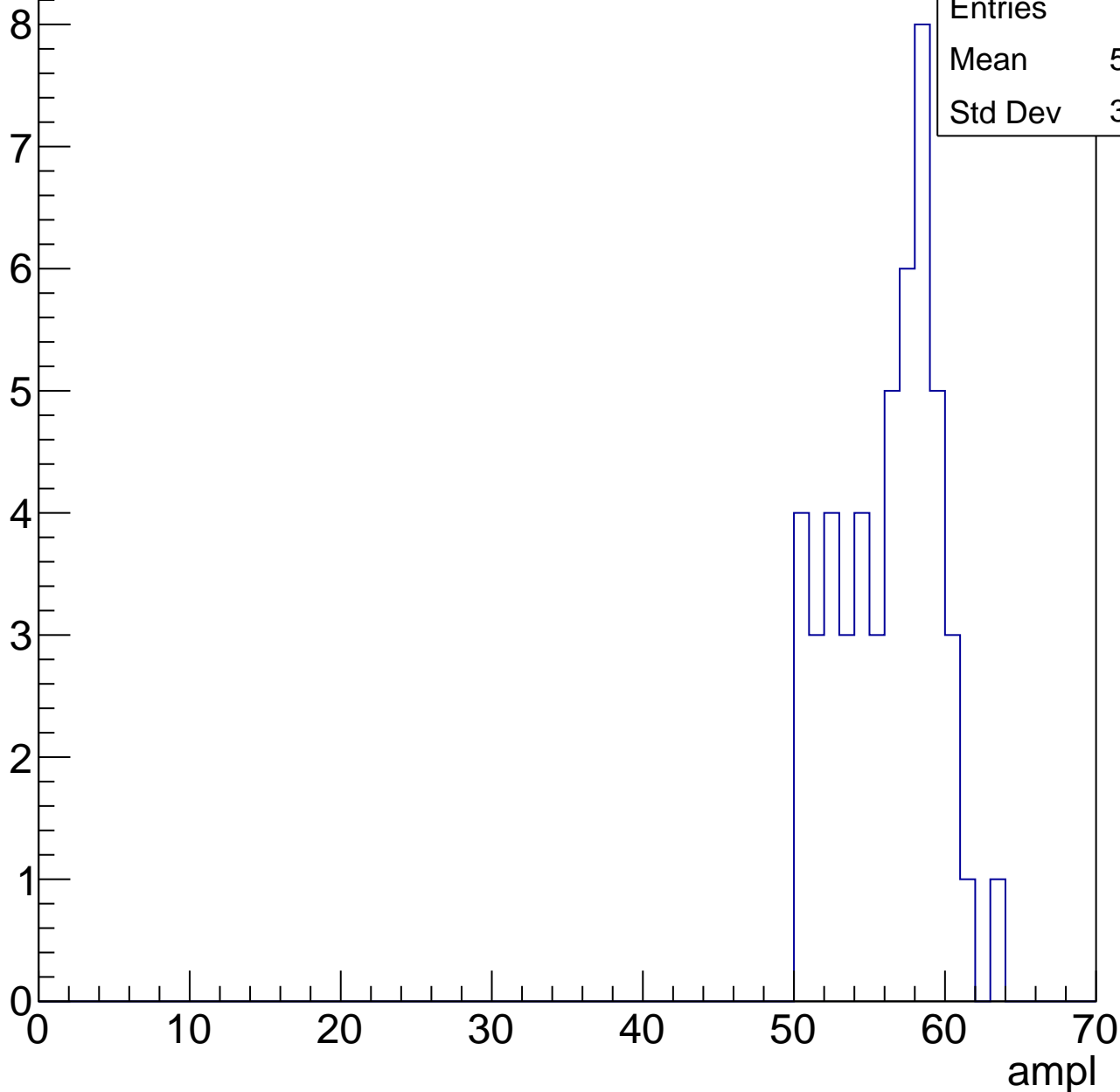


B0L002S, U18-ch123, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	55.72
Std Dev	3.256

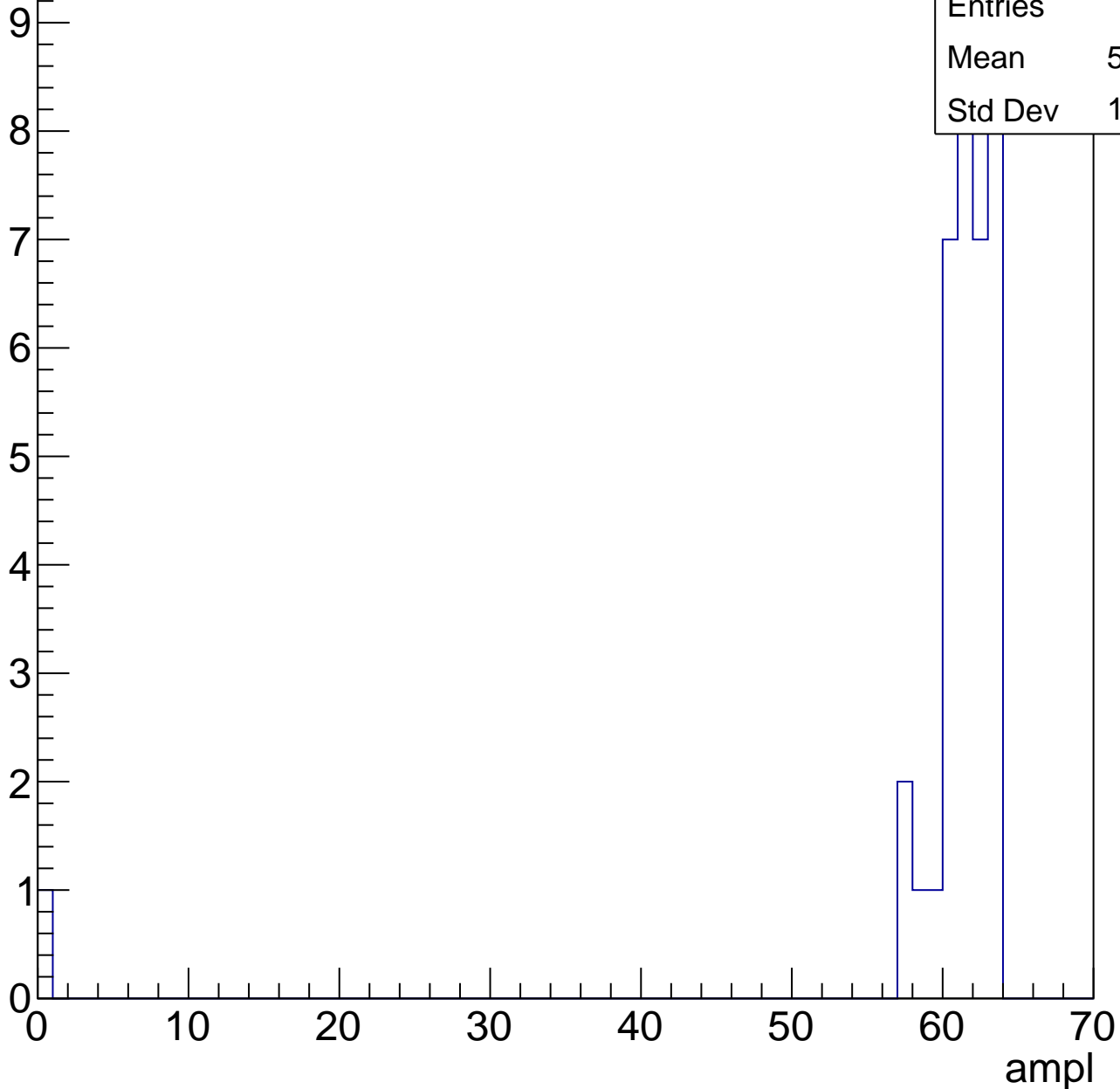


B0L002S, U18-ch123, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	36
Mean	59.44
Std Dev	10.18



B0L002S, U18-ch123, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch123, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch124, adc0

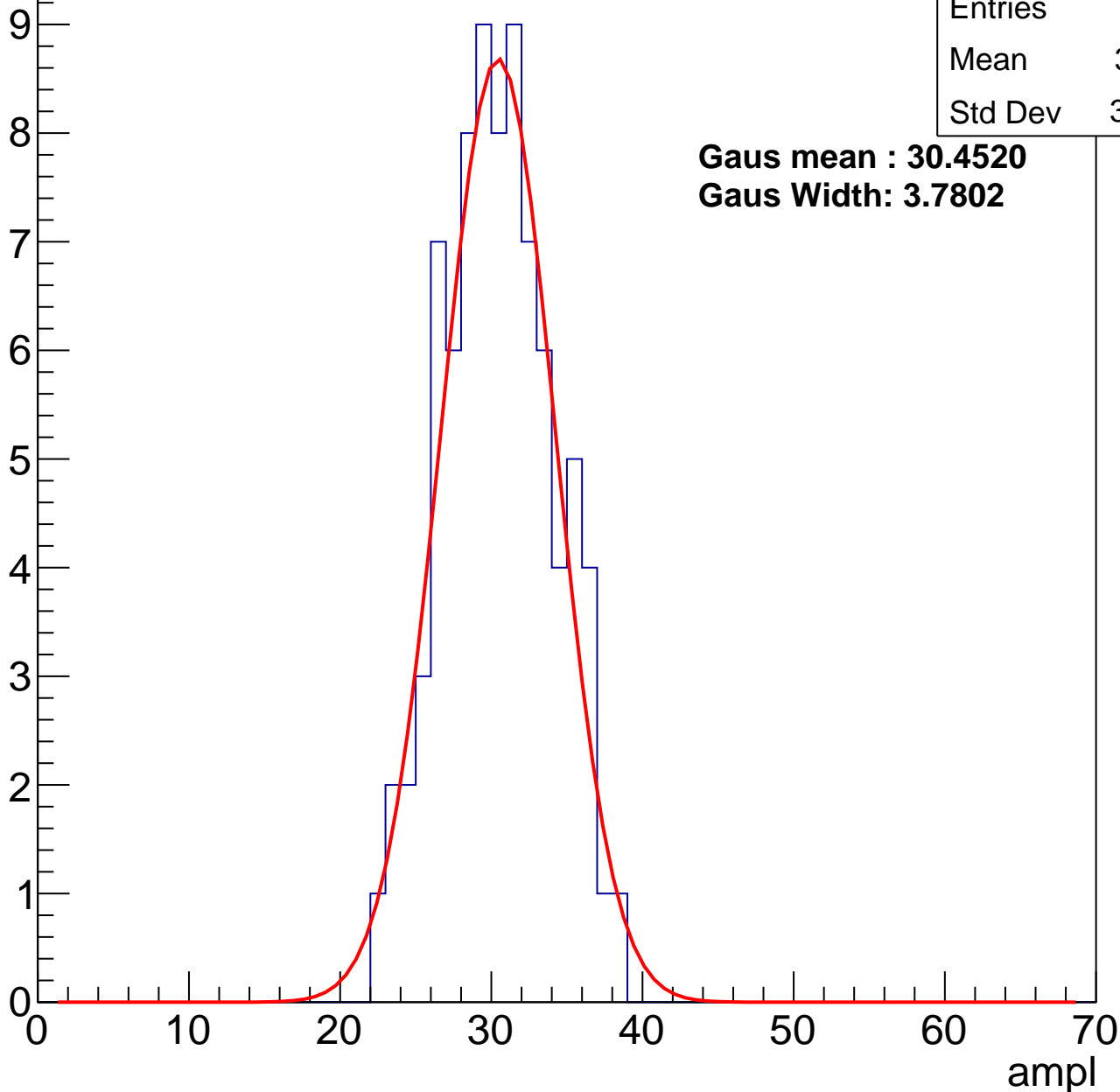
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	83
Mean	30.01
Std Dev	3.562

Gaus mean : 30.4520

Gaus Width: 3.7802



B0L002S, U18-ch124, adc1

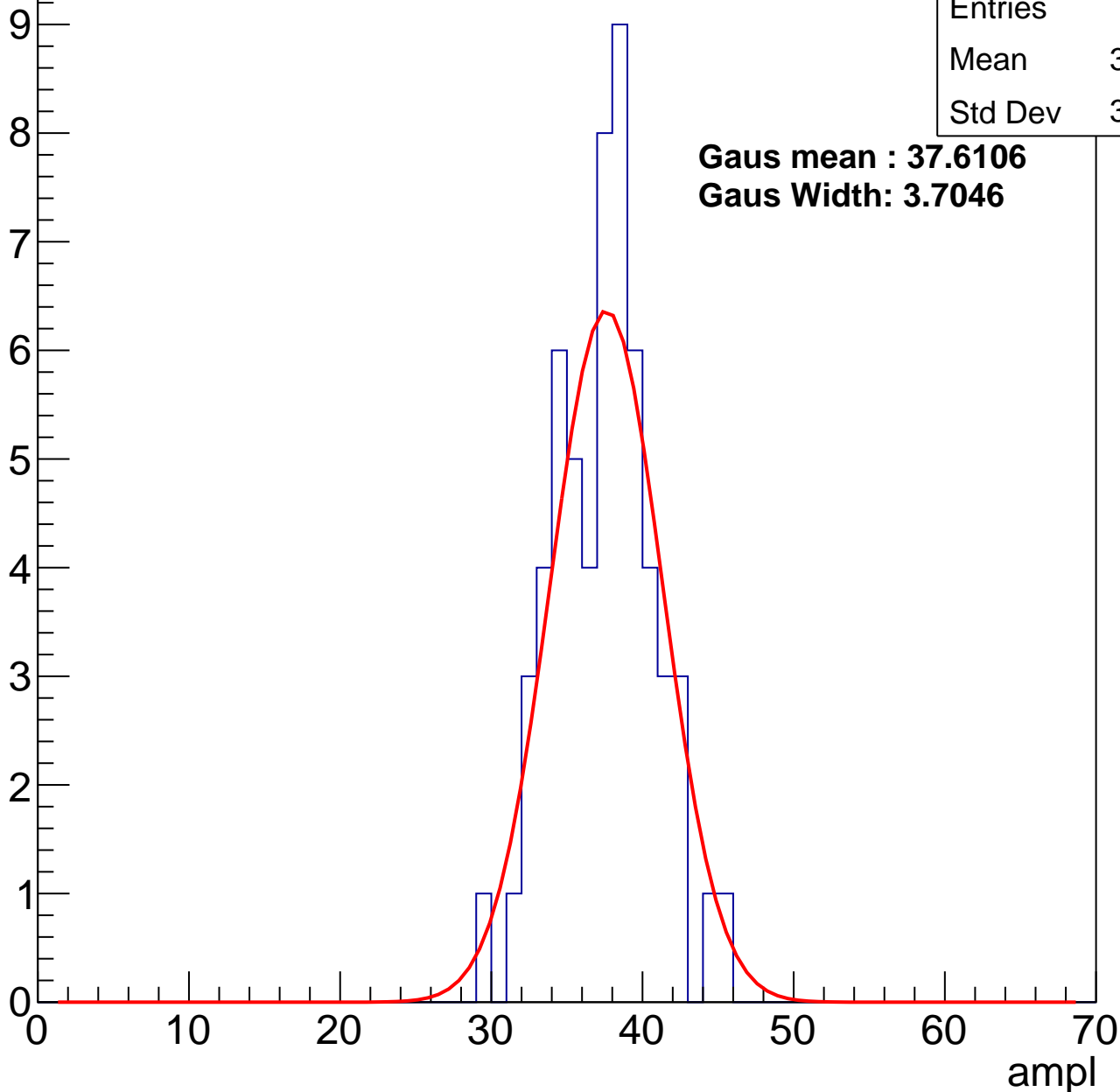
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	36.97
Std Dev	3.247

Gaus mean : 37.6106

Gaus Width: 3.7046



B0L002S, U18-ch124, adc2

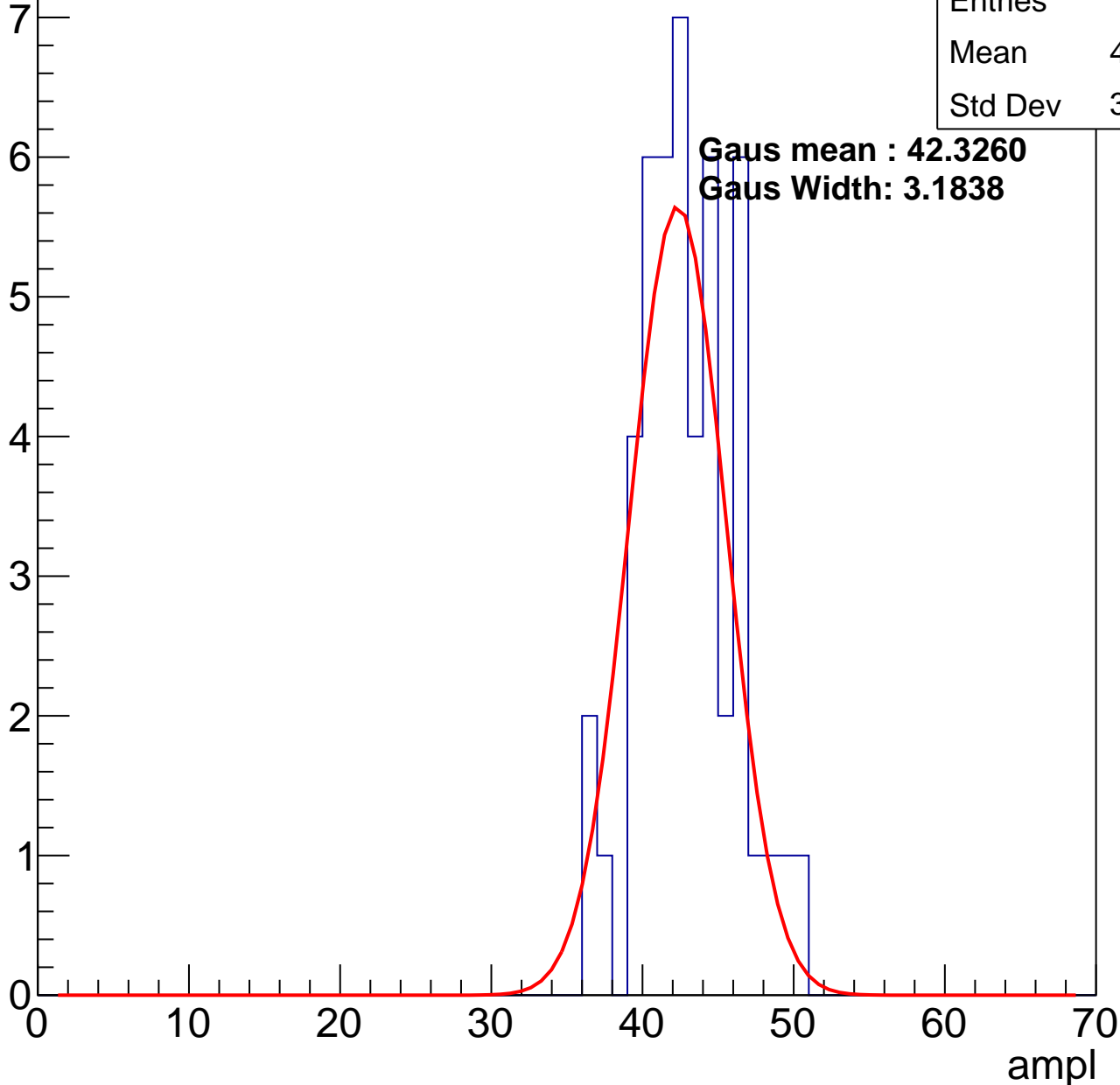
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	48
Mean	42.52
Std Dev	3.116

Gaus mean : 42.3260

Gaus Width: 3.1838

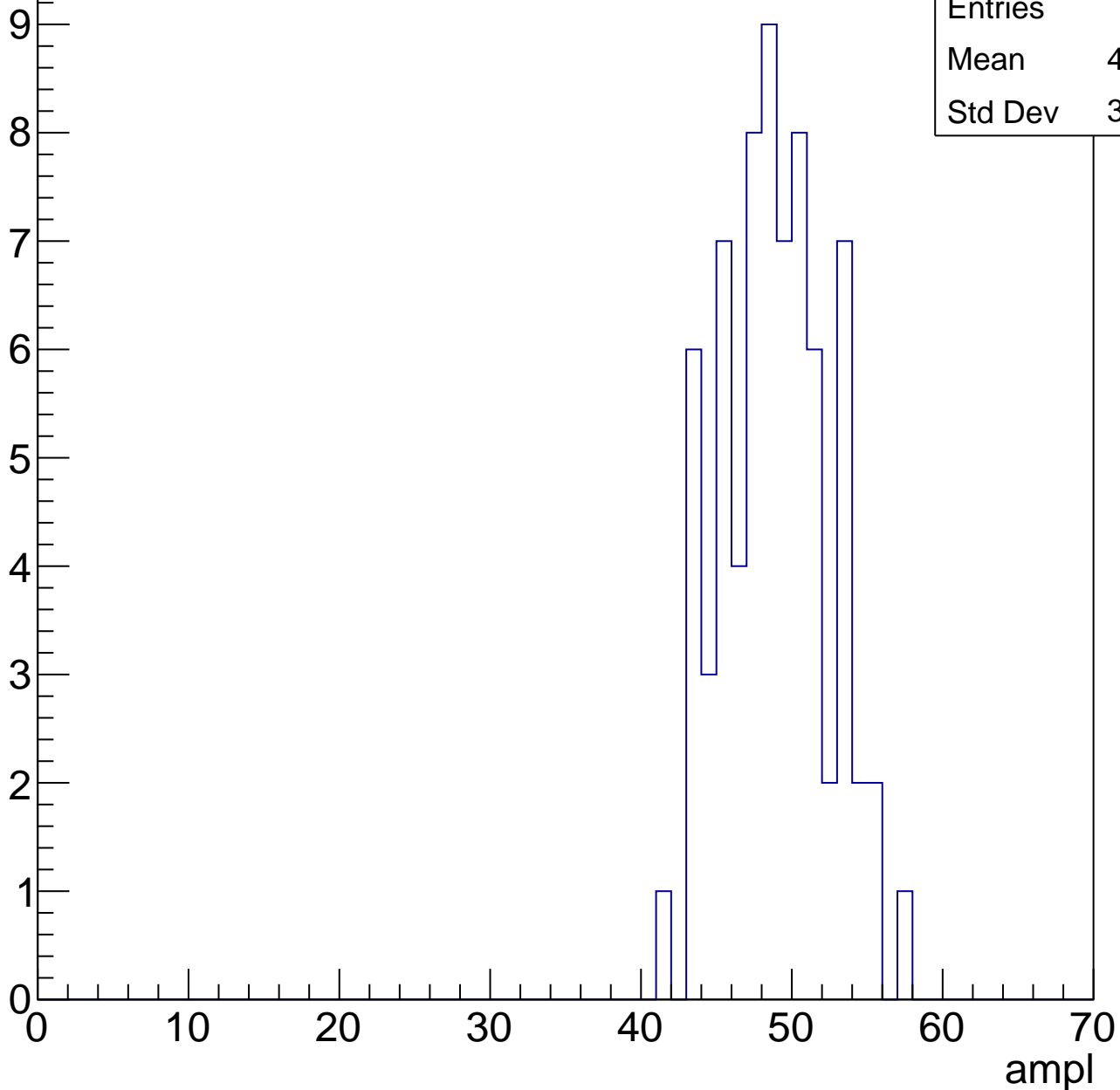


B0L002S, U18-ch124, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	48.45
Std Dev	3.452

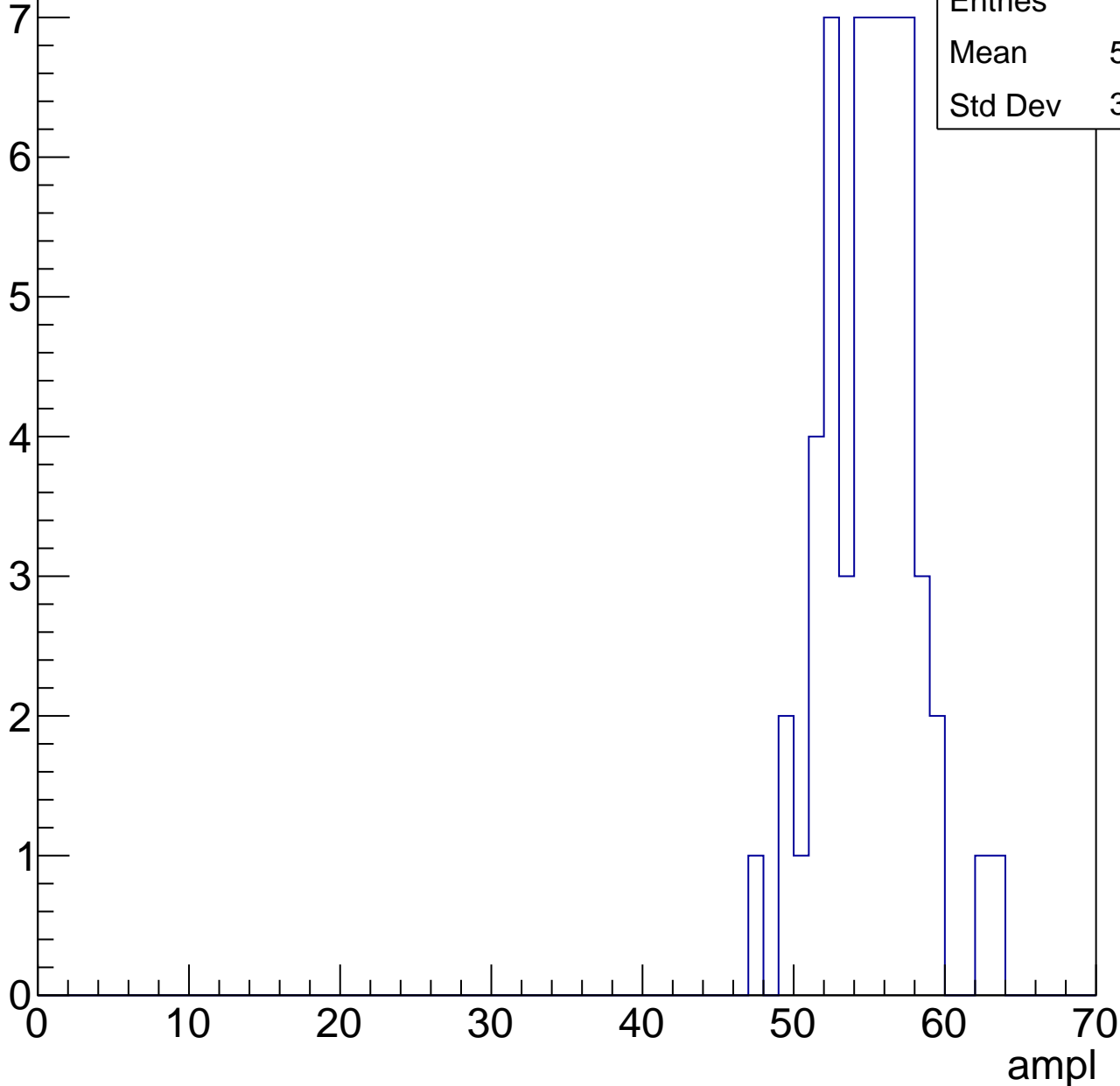


B0L002S, U18-ch124, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	53
Mean	54.58
Std Dev	3.086



B0L002S, U18-ch124, adc5

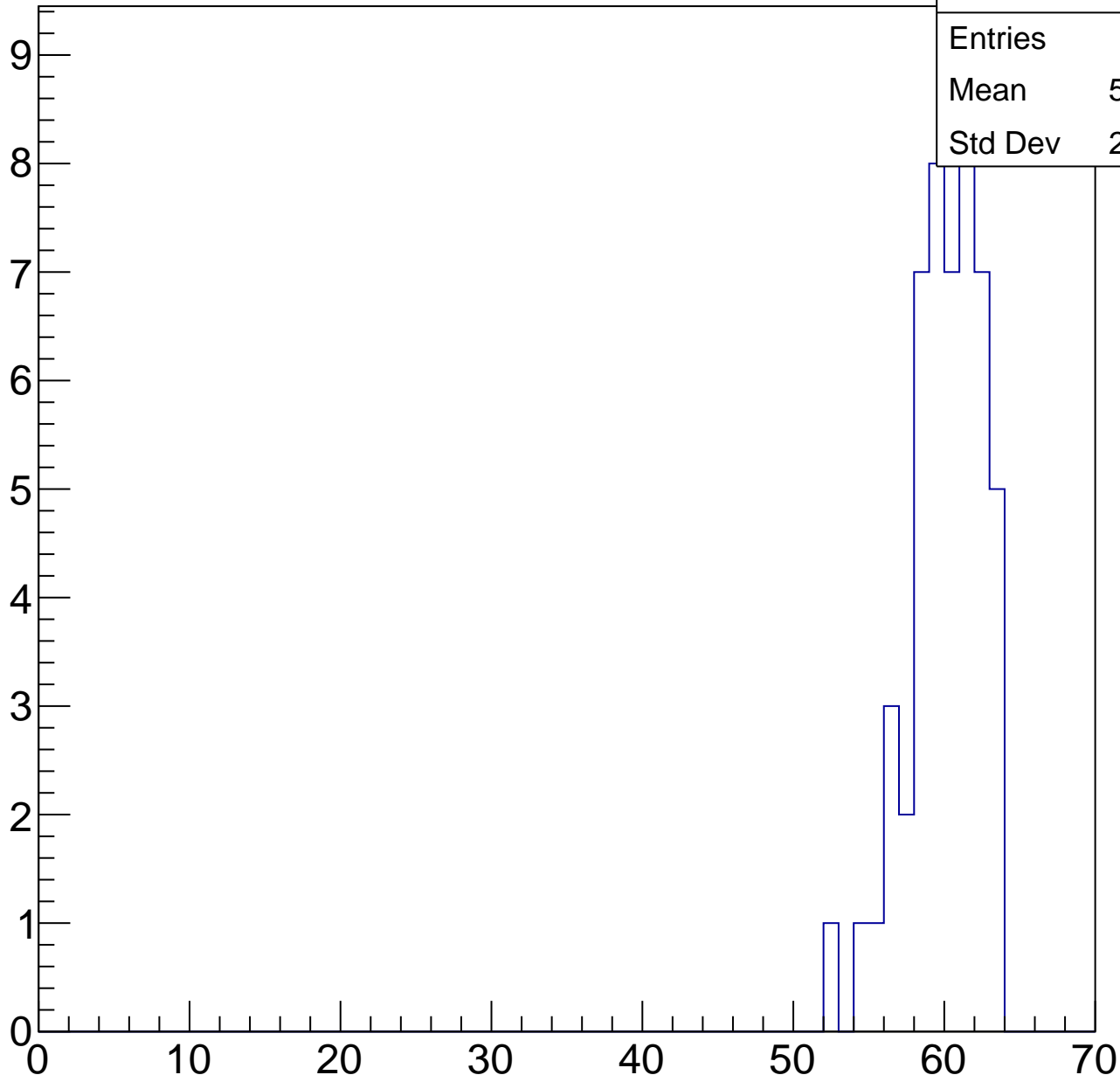
calib_packv5_042523_0143.root, FC#8, port C1

Entry

9
8
7
6
5
4
3
2
1
0

Entries	51
Mean	59.59
Std Dev	2.427

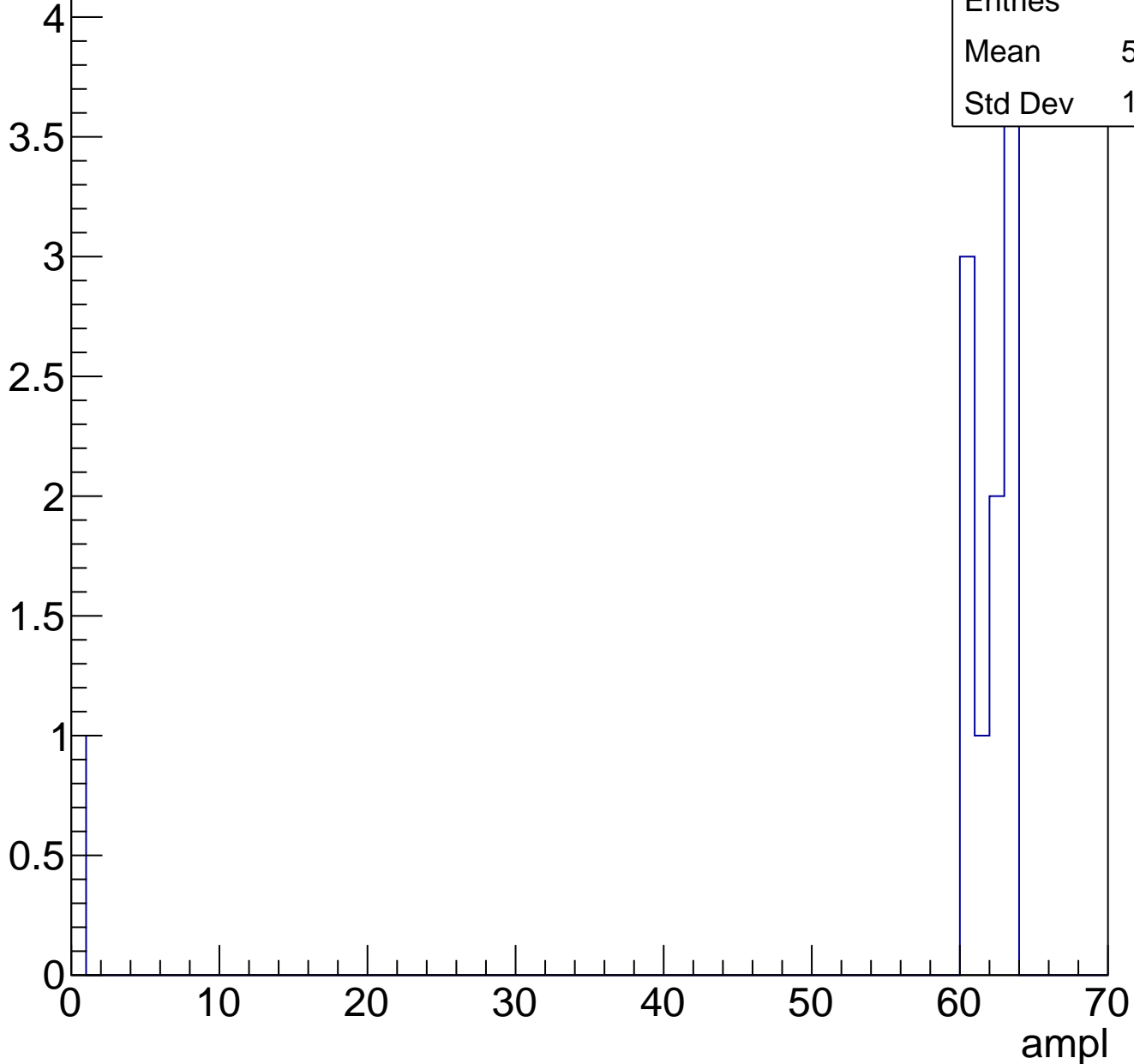
ampl



B0L002S, U18-ch124, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch124, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch125, adc0

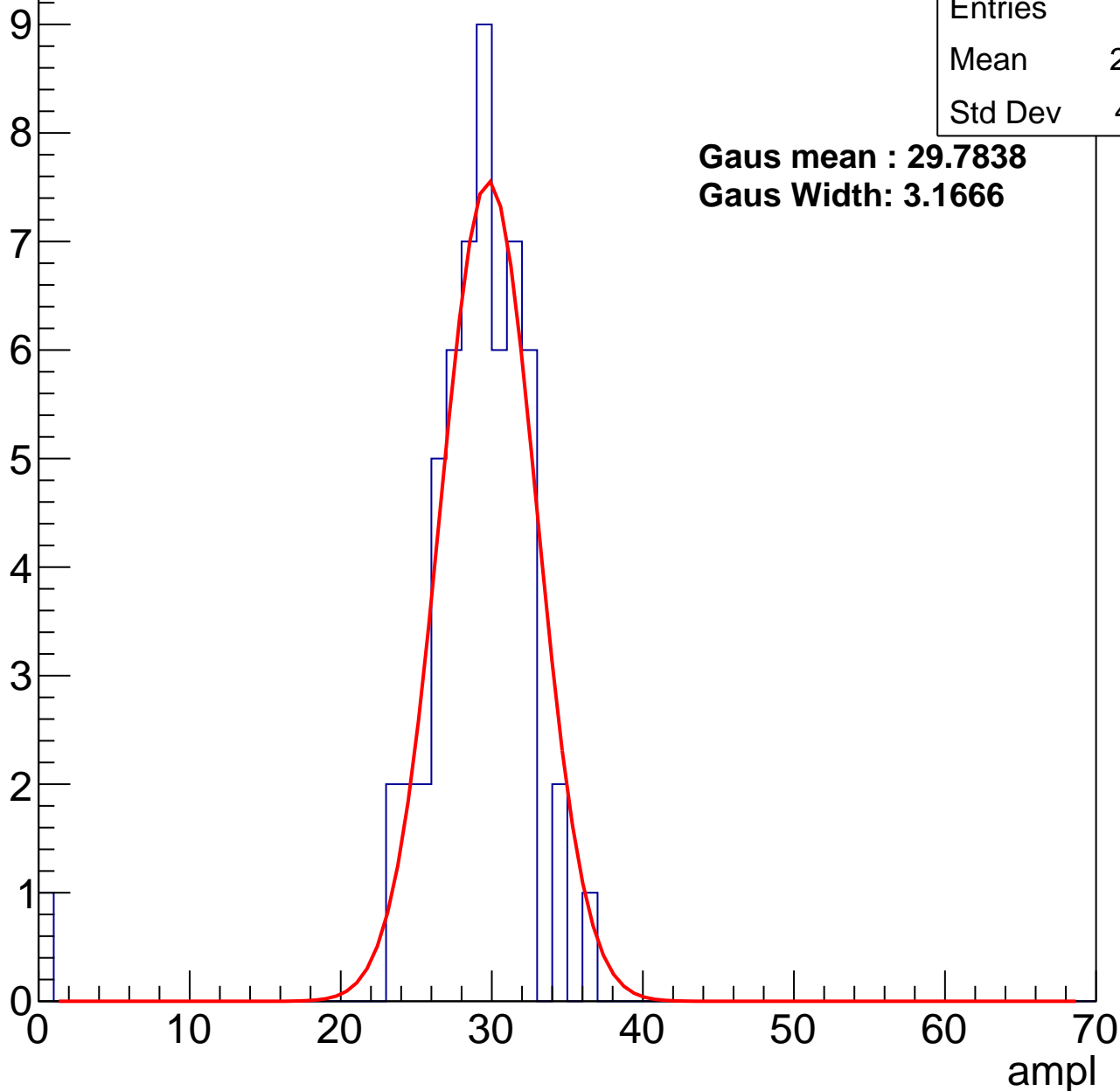
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	28.32
Std Dev	4.691

Gaus mean : 29.7838

Gaus Width: 3.1666



B0L002S, U18-ch125, adc1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	87
Mean	36.03
Std Dev	3.334

Gaus mean : 36.4132

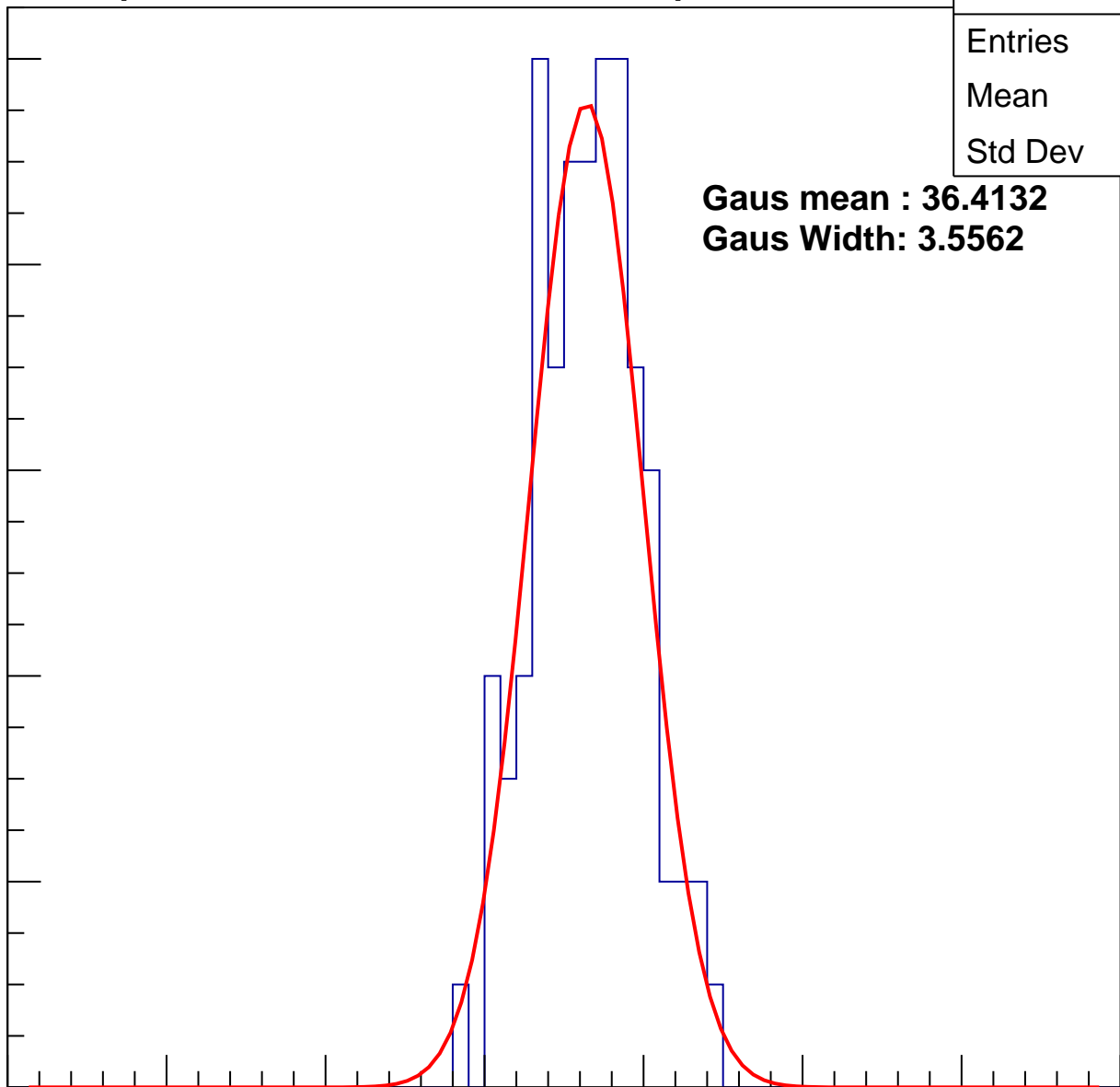
Gaus Width: 3.5562

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch125, adc2

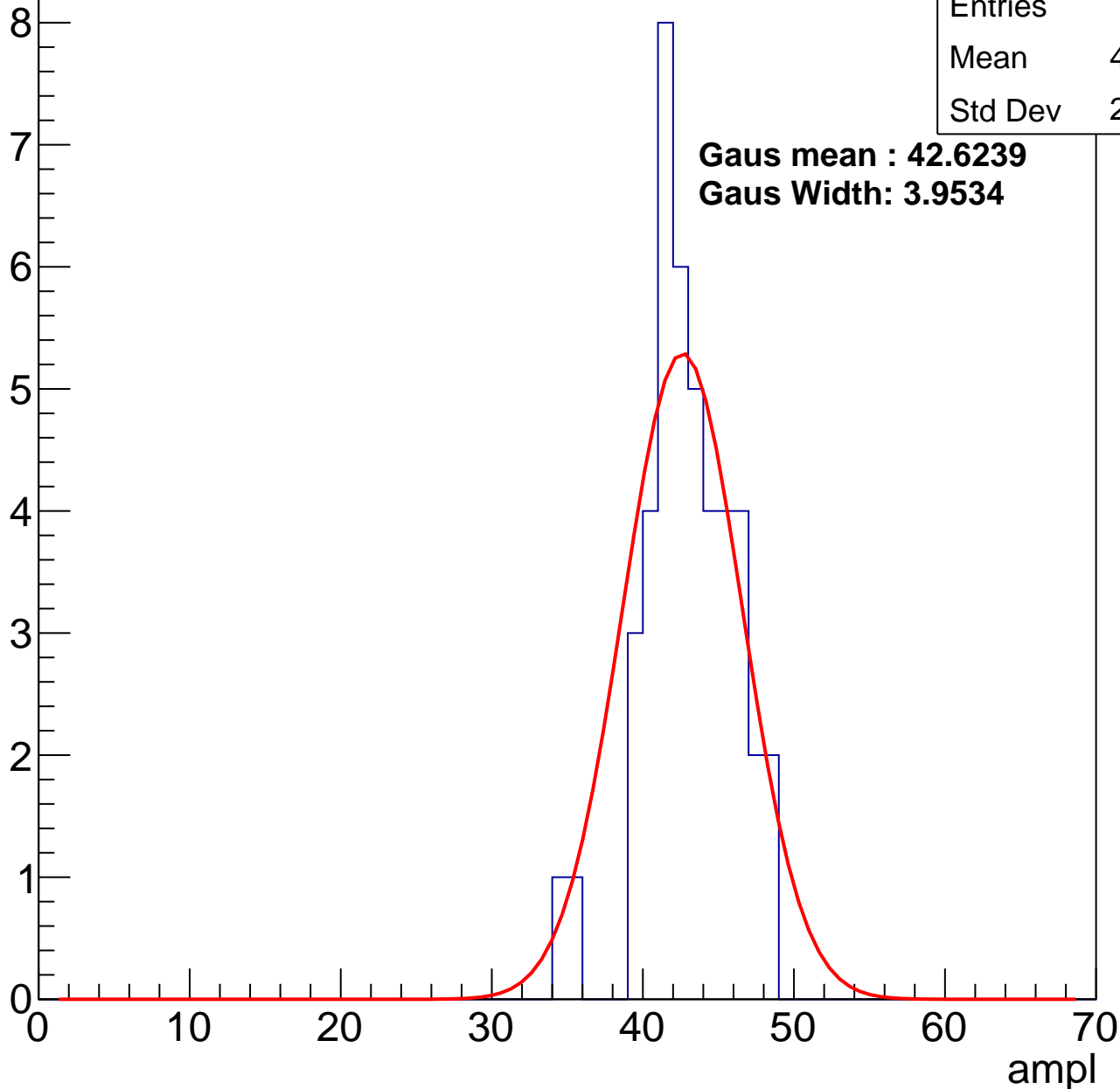
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	44
Mean	42.52
Std Dev	2.996

Gaus mean : 42.6239

Gaus Width: 3.9534



B0L002S, U18-ch125, adc3

calib_packv5_042523_0143.root, FC#8, port C1

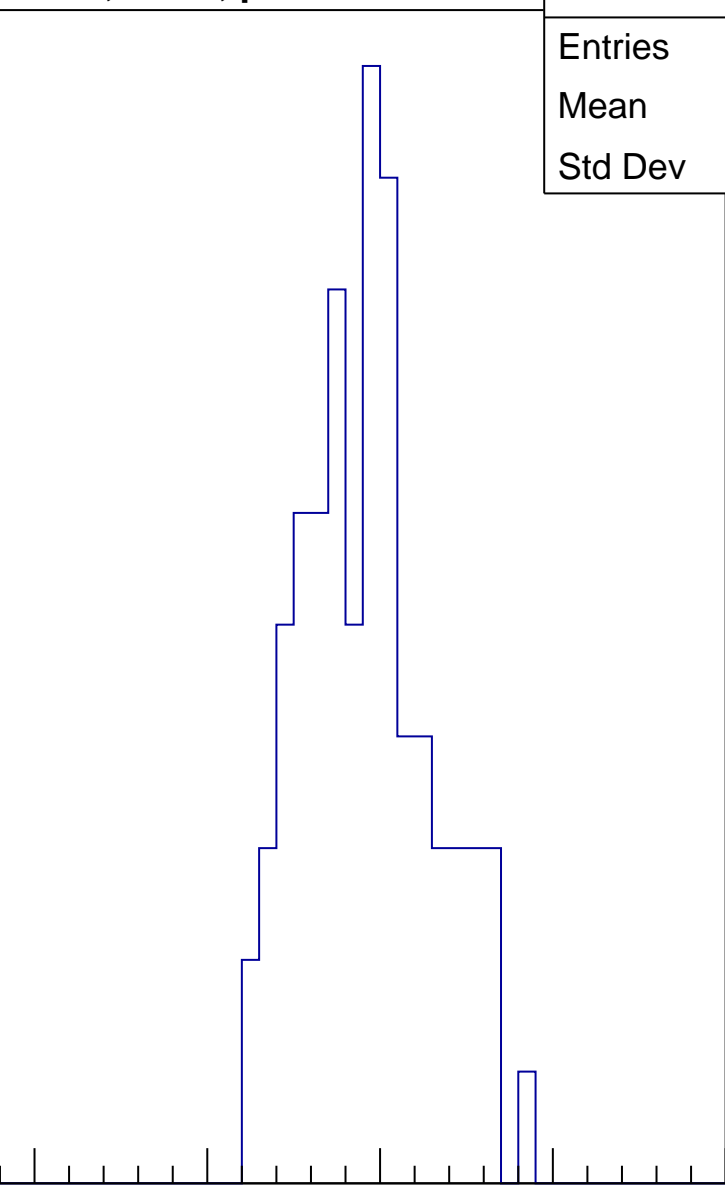
Entries	75
Mean	48.79
Std Dev	3.714

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

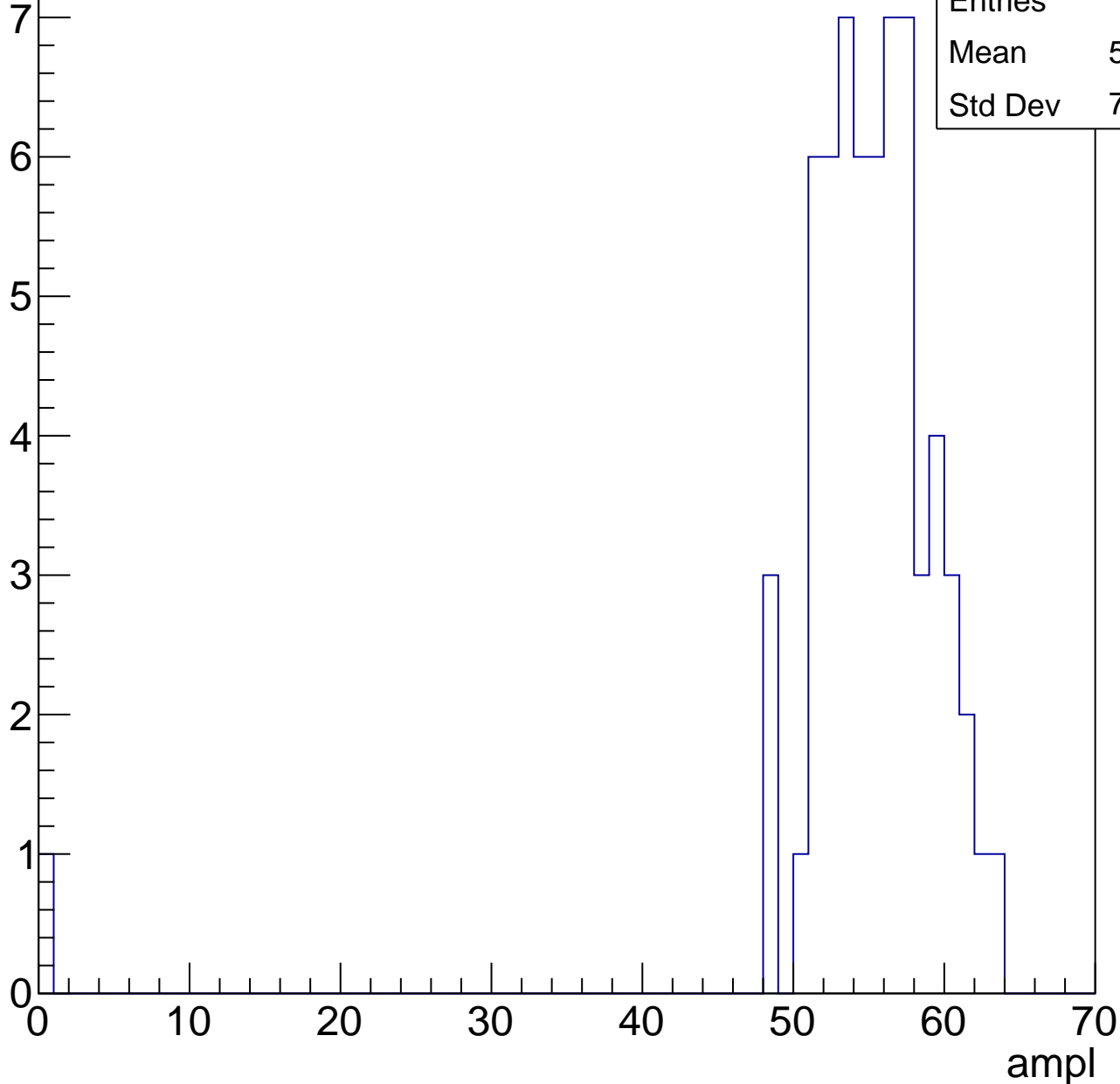


B0L002S, U18-ch125, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	64
Mean	54.14
Std Dev	7.624

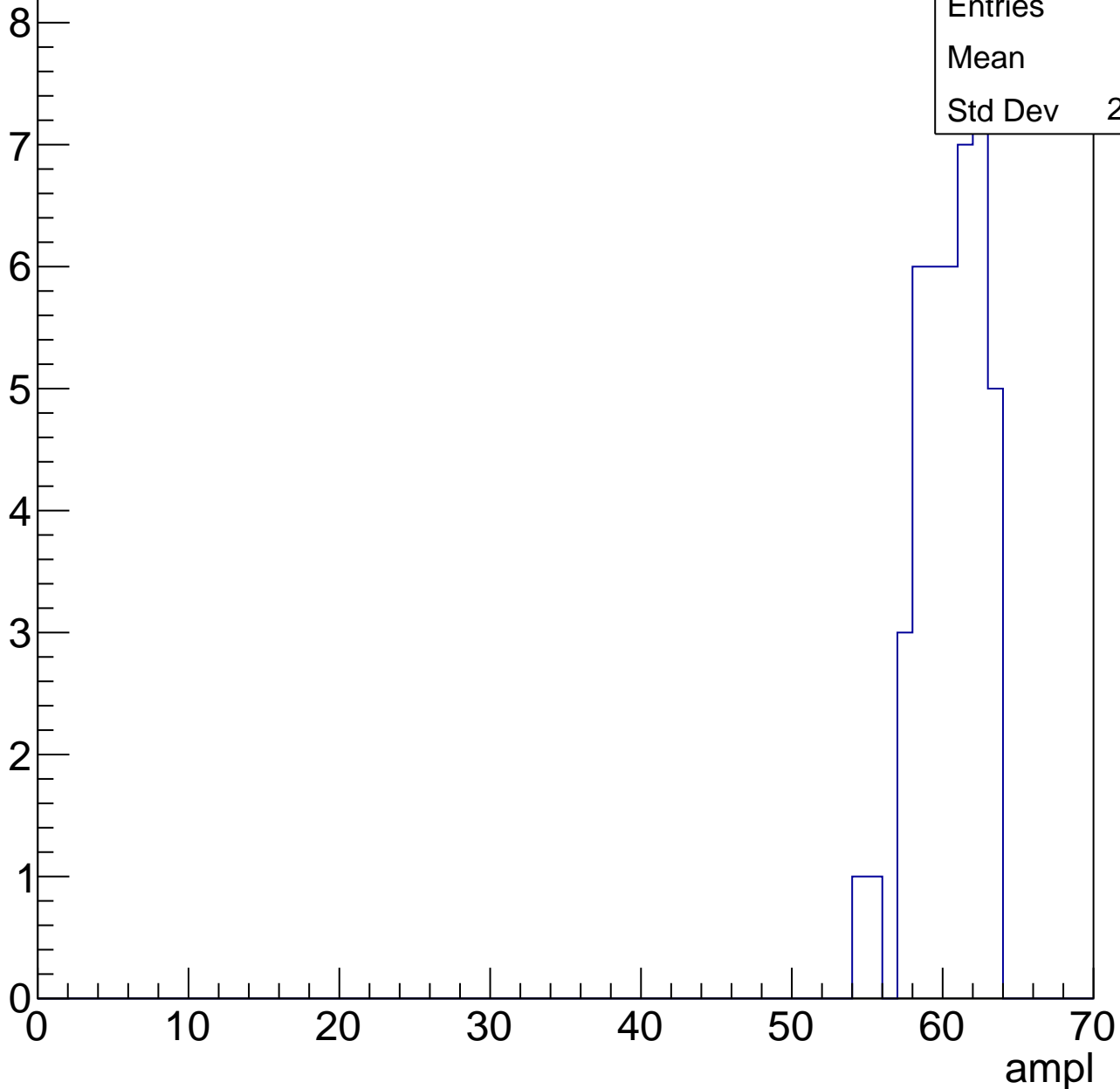


B0L002S, U18-ch125, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	43
Mean	60
Std Dev	2.167



B0L002S, U18-ch125, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch125, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	2
Mean	0
Std Dev	0

B0L002S, U18-ch126, adc0

calib_packv5_042523_0143.root, FC#8, port C1

Entries	85
Mean	29.74
Std Dev	5.981

Gaus mean : 31.1613

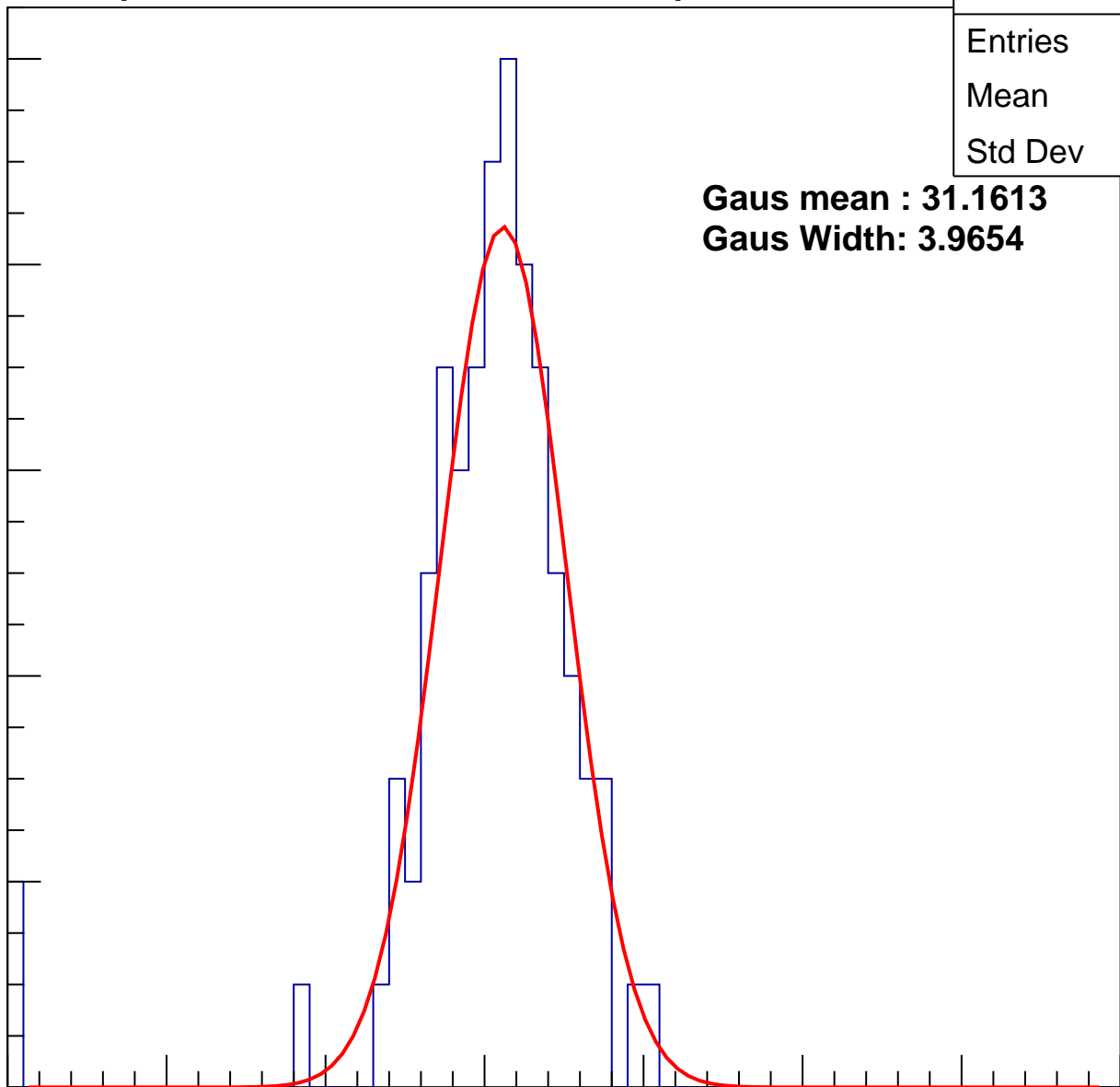
Gaus Width: 3.9654

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B0L002S, U18-ch126, adc1

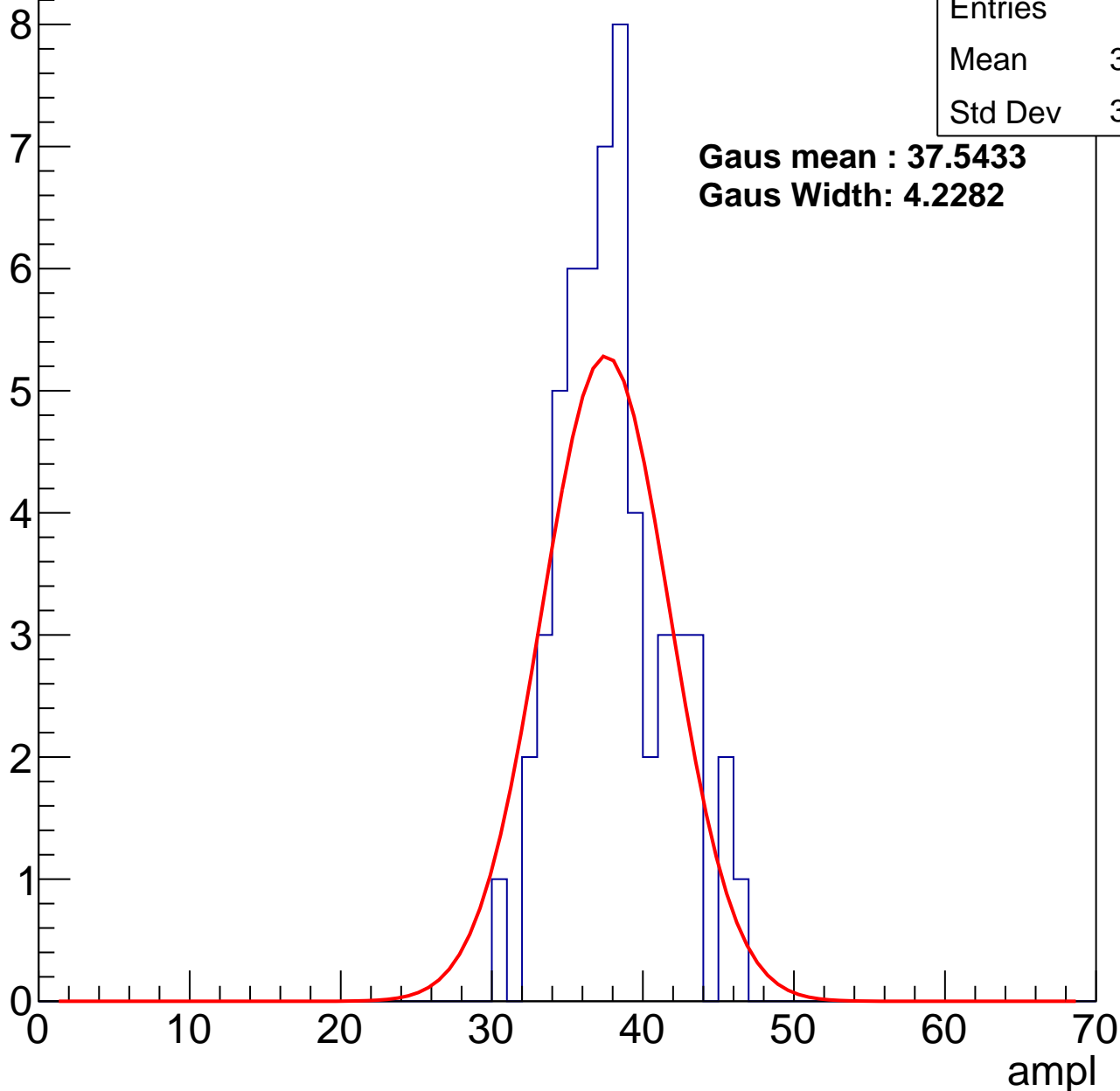
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	56
Mean	37.54
Std Dev	3.505

Gaus mean : 37.5433

Gaus Width: 4.2282



B0L002S, U18-ch126, adc2

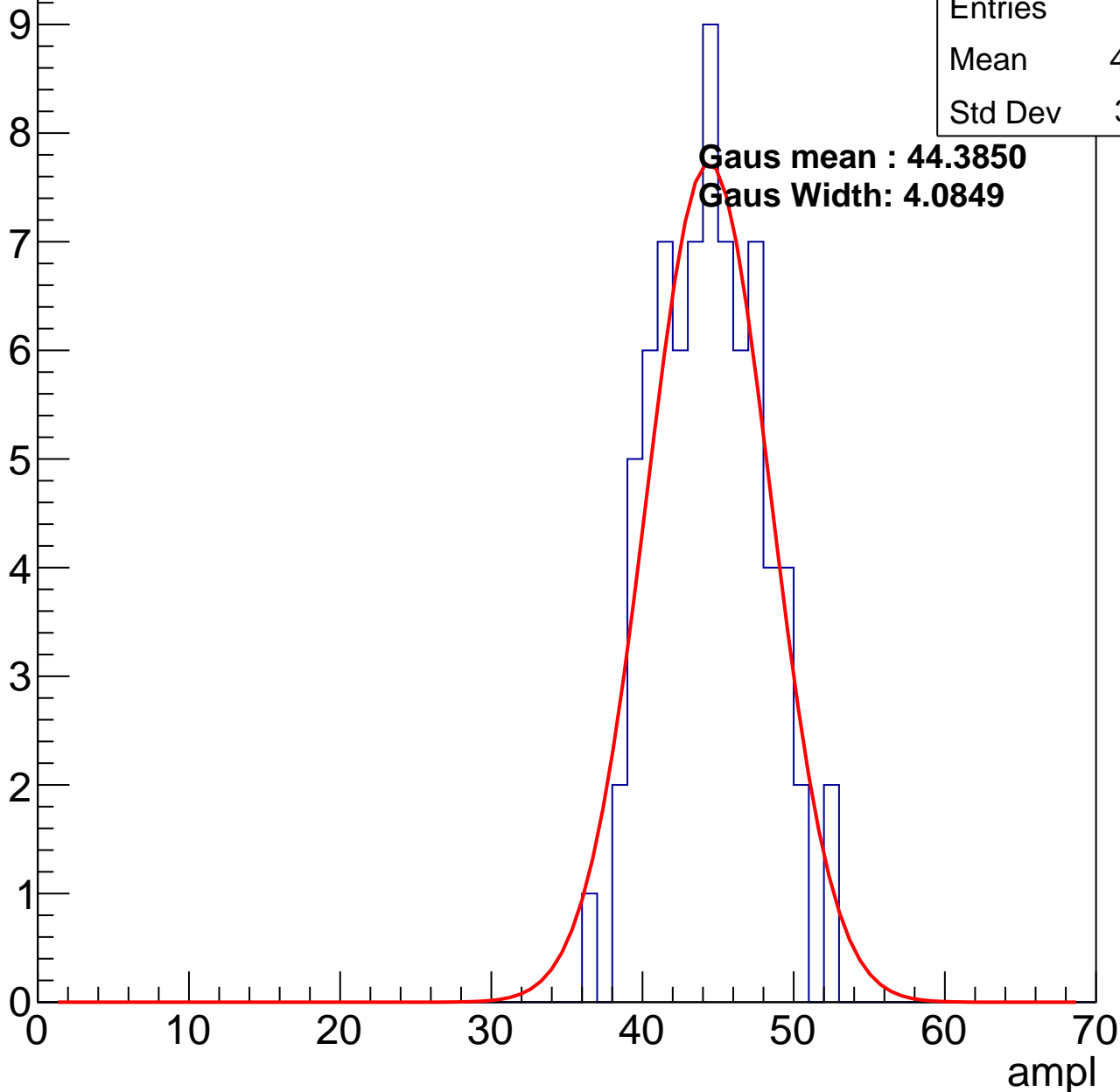
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	75
Mean	43.93
Std Dev	3.481

Gaus mean : 44.3850

Gaus Width: 4.0849

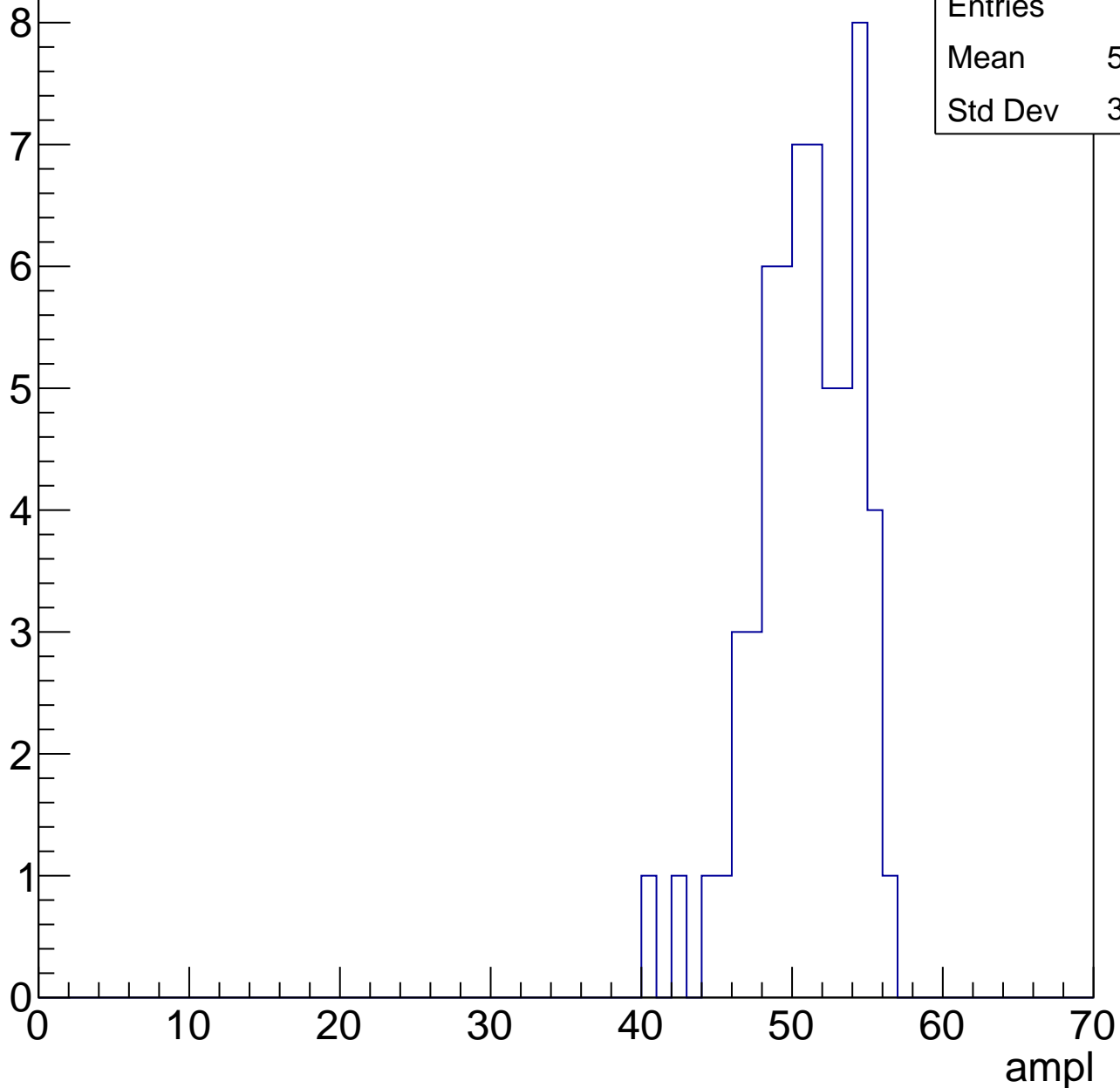


B0L002S, U18-ch126, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	59
Mean	50.37
Std Dev	3.339

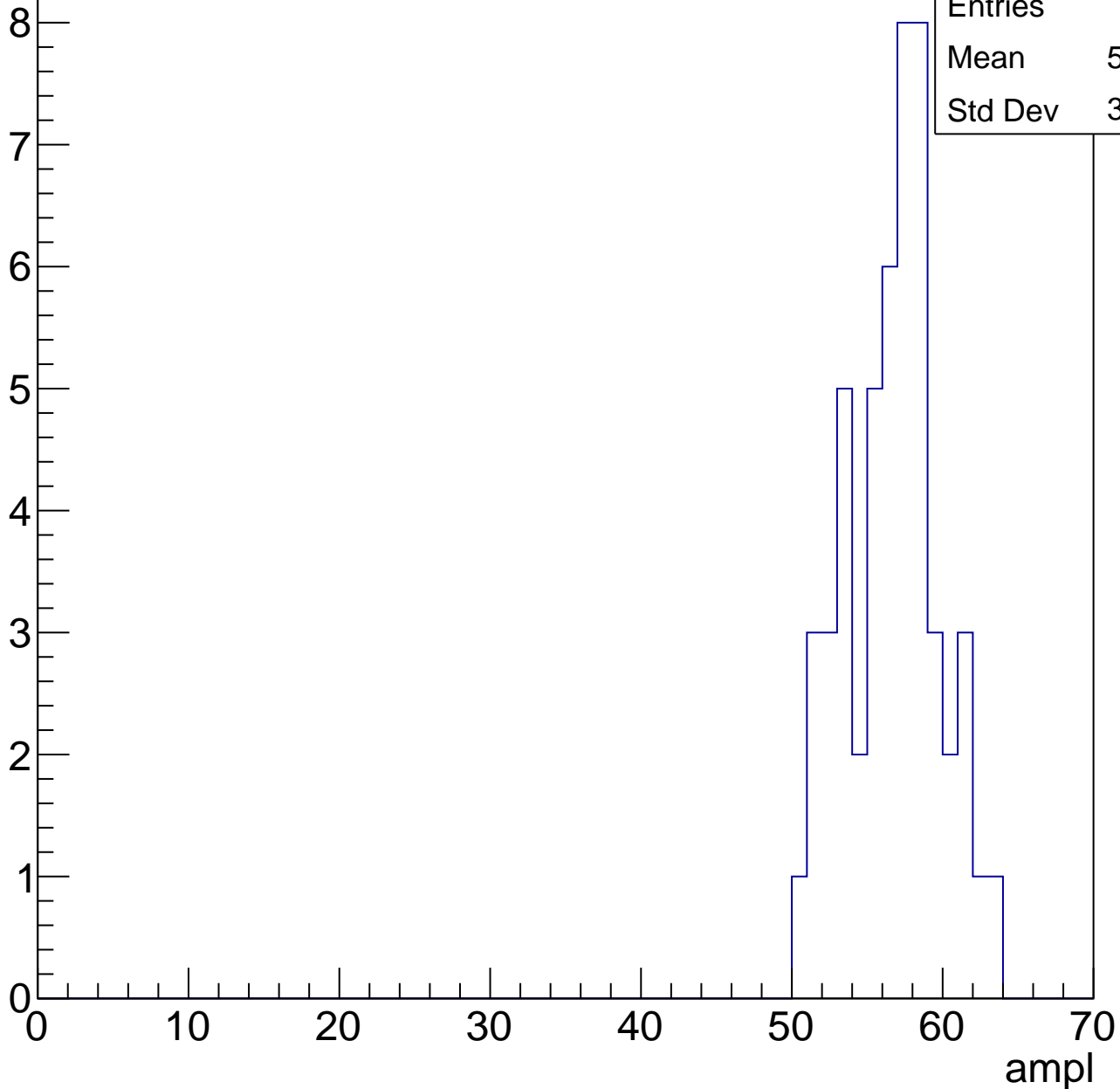


B0L002S, U18-ch126, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	51
Mean	56.24
Std Dev	3.046

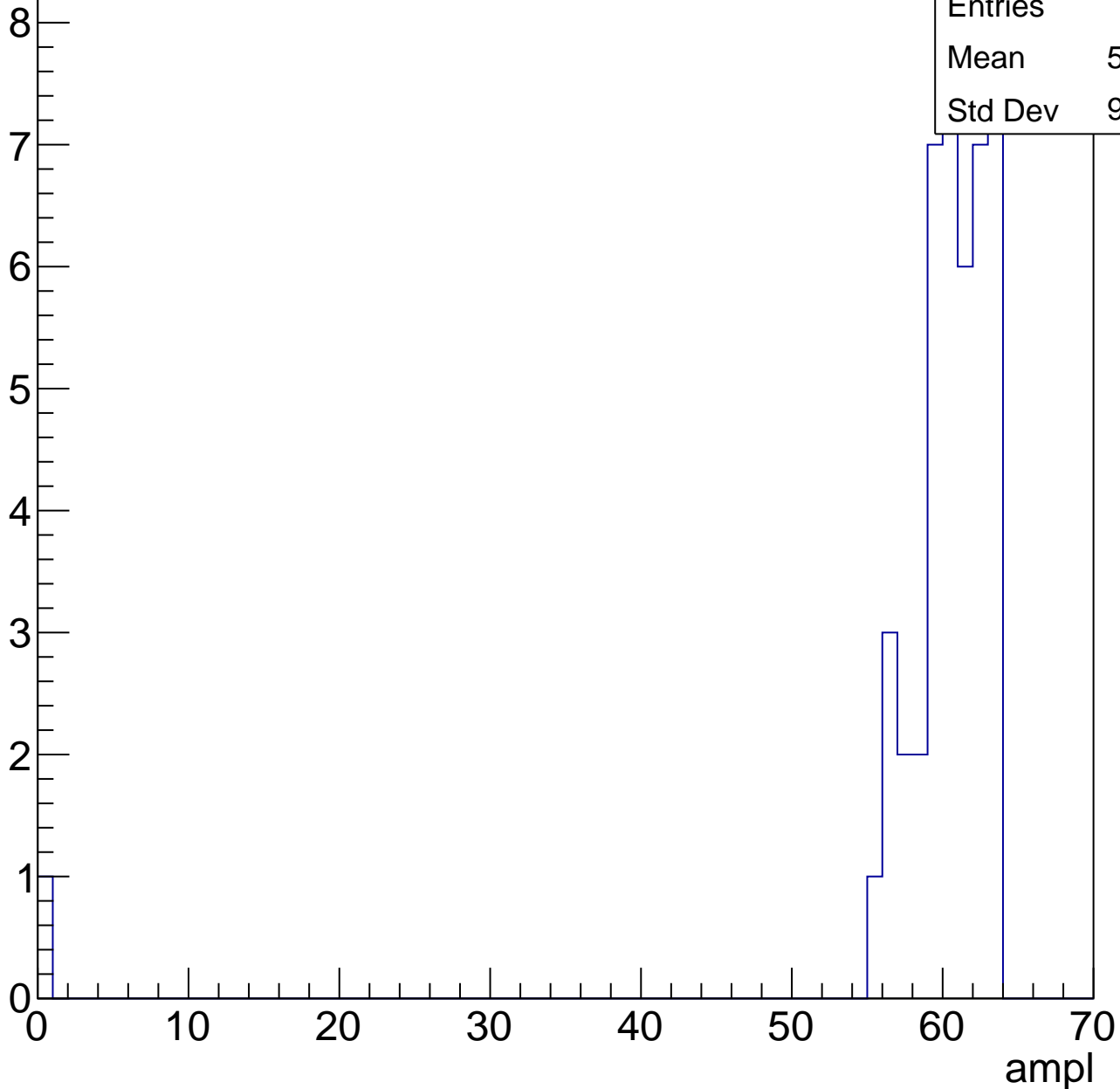


B0L002S, U18-ch126, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	45
Mean	58.89
Std Dev	9.137



B0L002S, U18-ch126, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch126, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch127, adc0

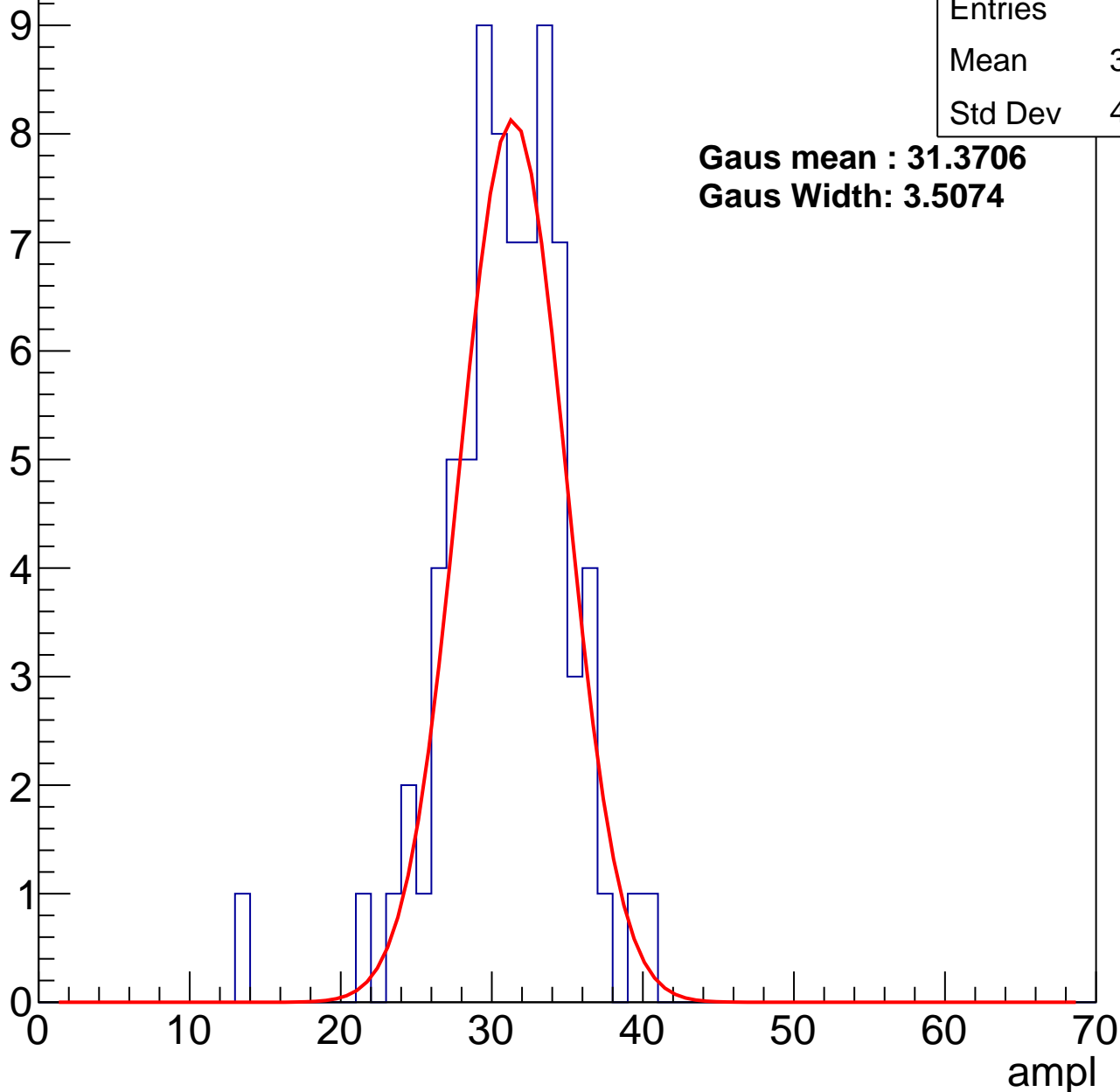
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	77
Mean	30.53
Std Dev	4.123

Gaus mean : 31.3706

Gaus Width: 3.5074



B0L002S, U18-ch127, adc1

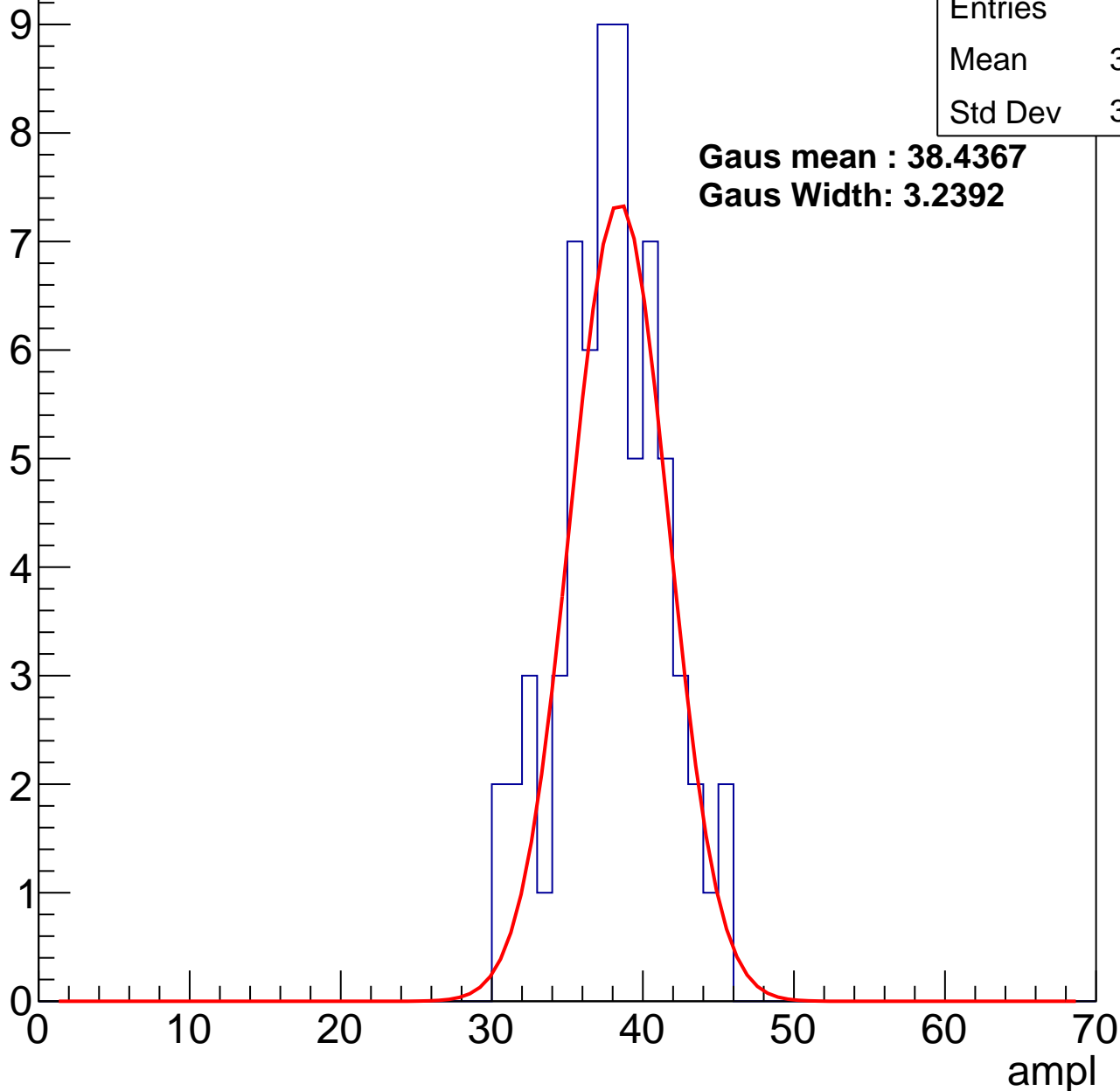
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	67
Mean	37.54
Std Dev	3.453

Gaus mean : 38.4367

Gaus Width: 3.2392



B0L002S, U18-ch127, adc2

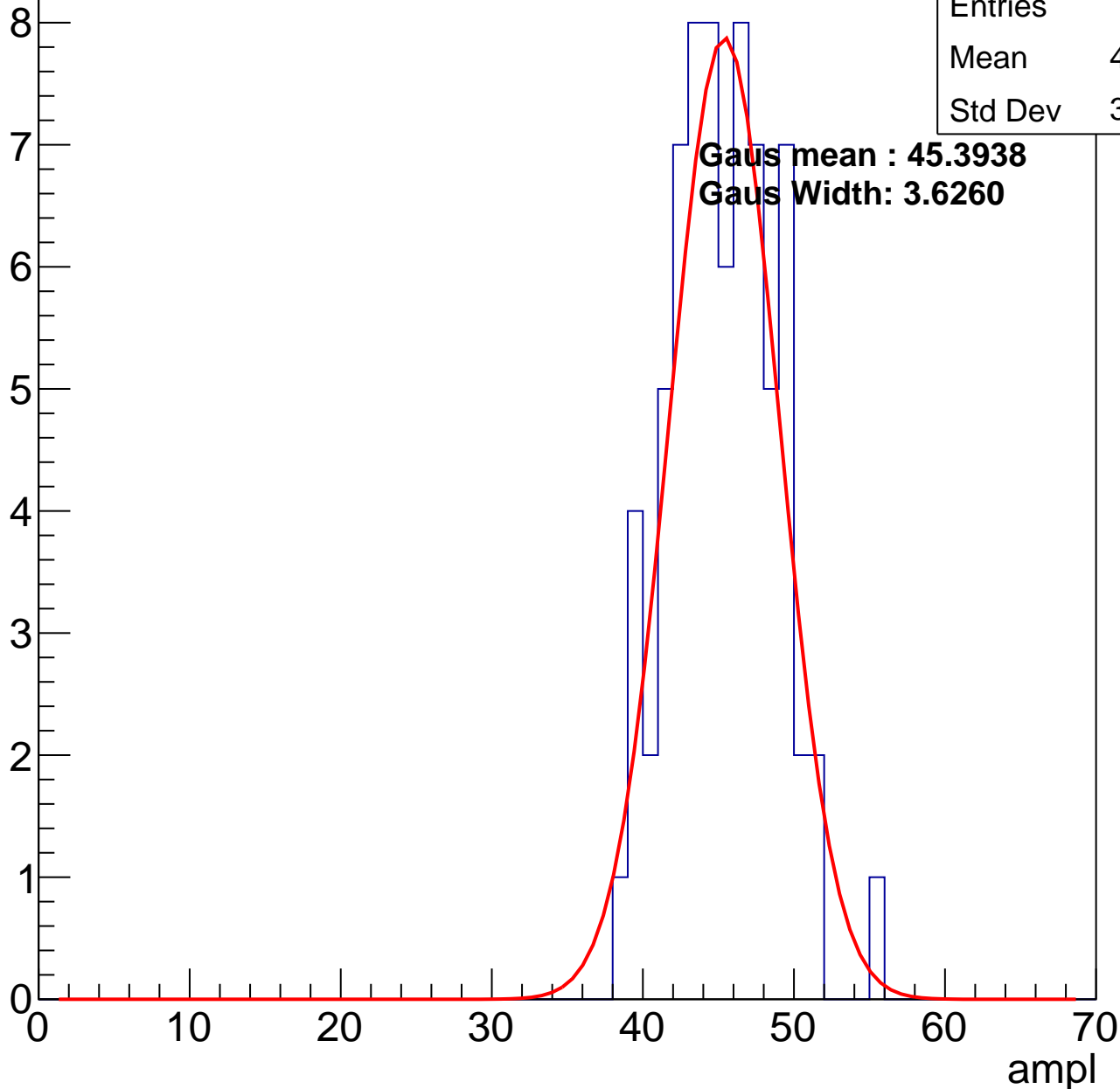
calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	73
Mean	44.88
Std Dev	3.392

Gaus mean : 45.3938

Gaus Width: 3.6260

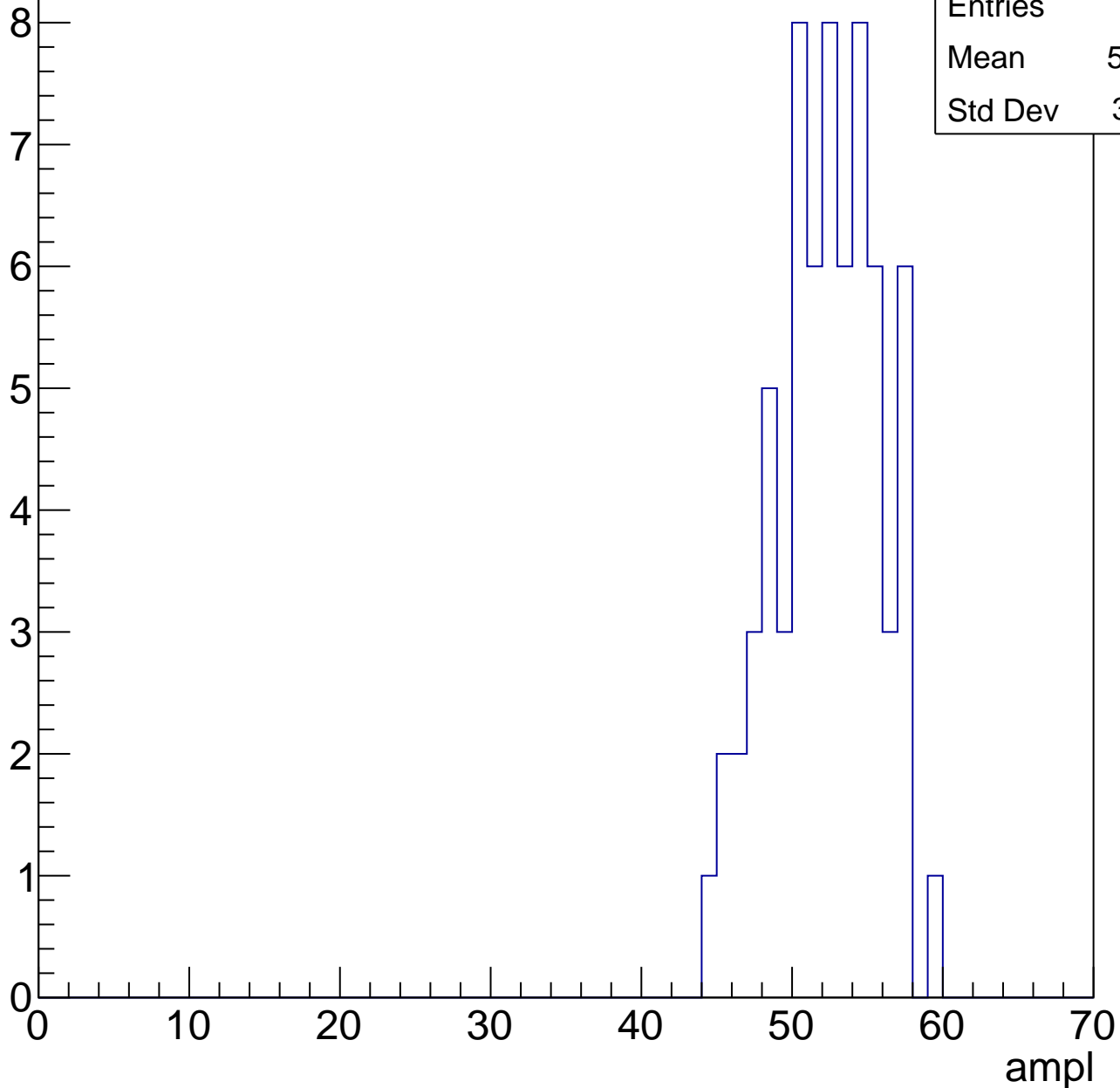


B0L002S, U18-ch127, adc3

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	68
Mean	51.84
Std Dev	3.411

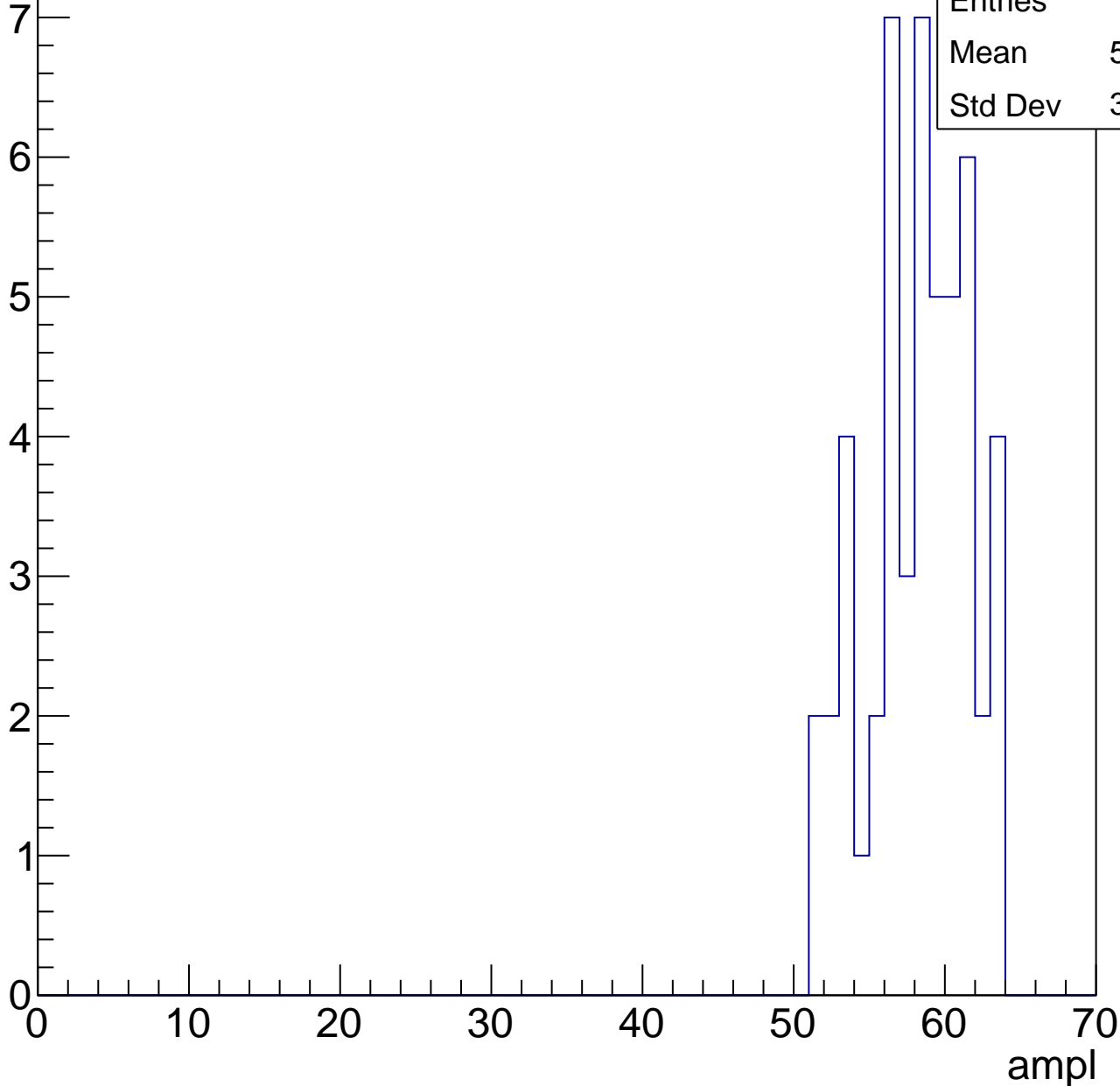


B0L002S, U18-ch127, adc4

calib_packv5_042523_0143.root, FC#8, port C1

Entry

Entries	50
Mean	57.76
Std Dev	3.296

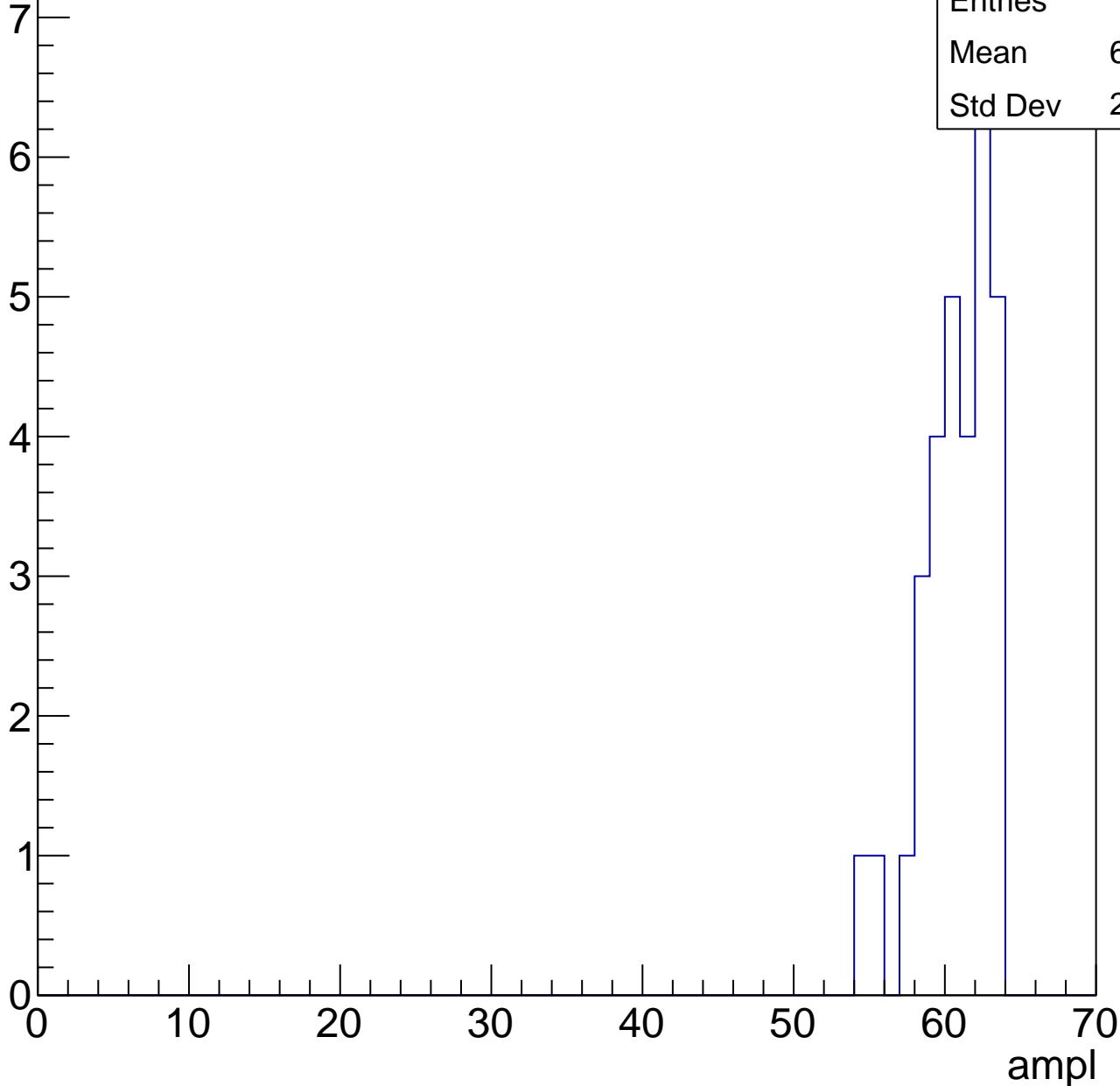


B0L002S, U18-ch127, adc5

calib_packv5_042523_0143.root, FC#8, port C1

Entry

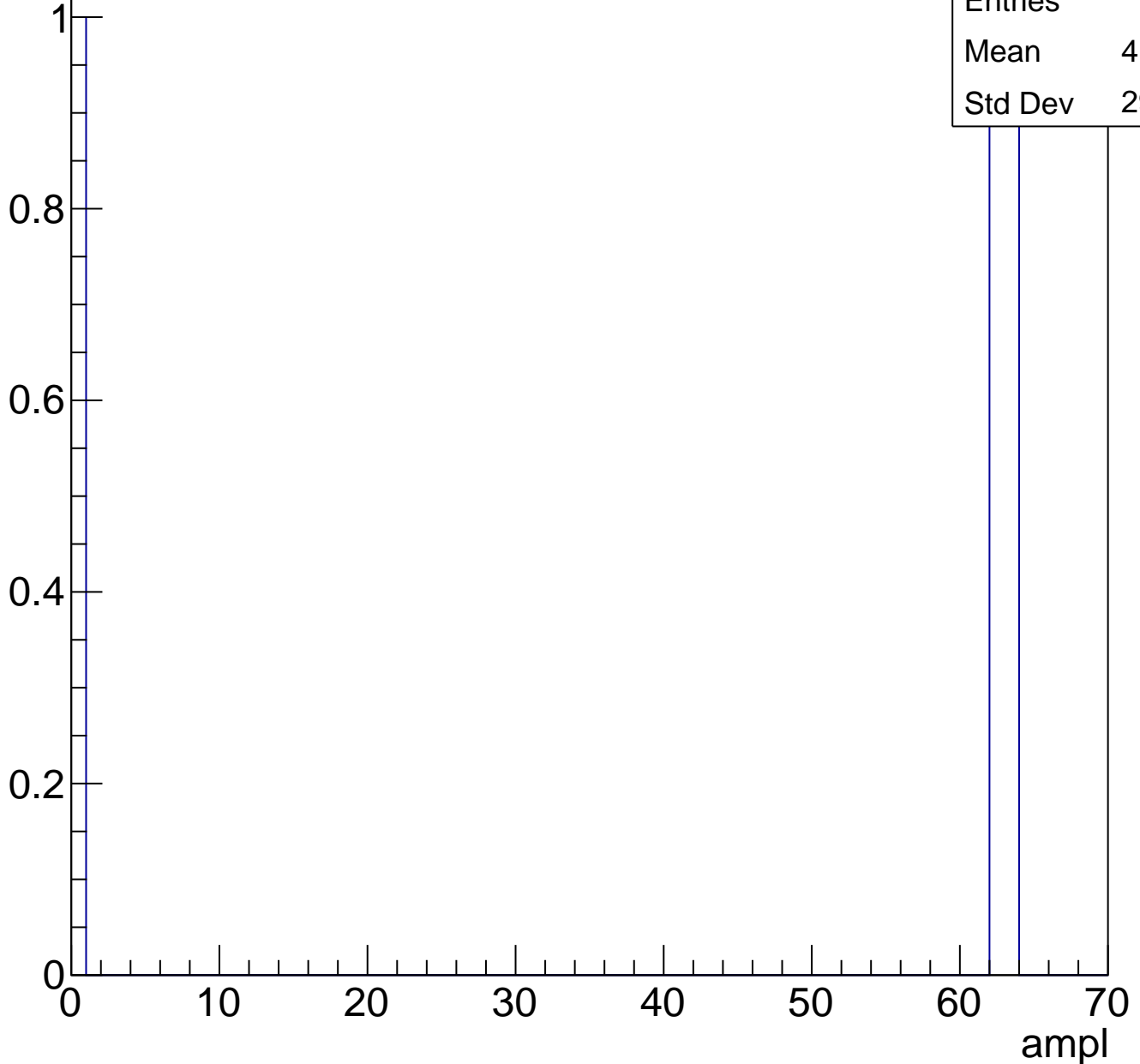
Entries	31
Mean	60.29
Std Dev	2.275



B0L002S, U18-ch127, adc6

calib_packv5_042523_0143.root, FC#8, port C1

Entry



B0L002S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0

B0L002S, U18-ch127, adc7

calib_packv5_042523_0143.root, FC#8, port C1

Entry



Entries	0
Mean	0
Std Dev	0