

B1L104S, U9-ch0

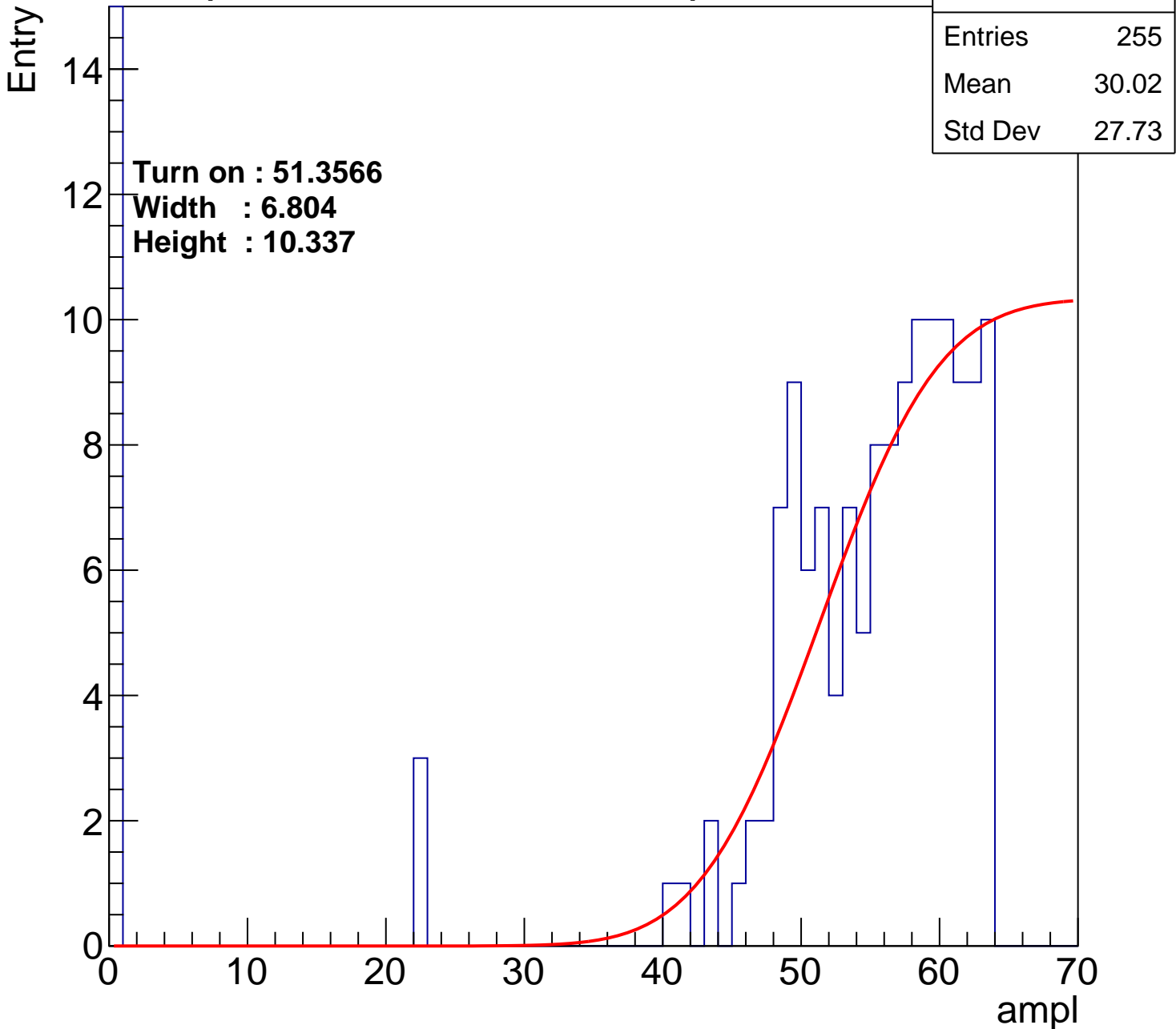
calib_packv5_033123_0516.root, FC#4, port A1

Entries	255
Mean	30.02
Std Dev	27.73

Turn on : 51.3566

Width : 6.804

Height : 10.337



B1L104S, U9-ch1

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	33.85
Std Dev	28.38

Turn on : 53.9053

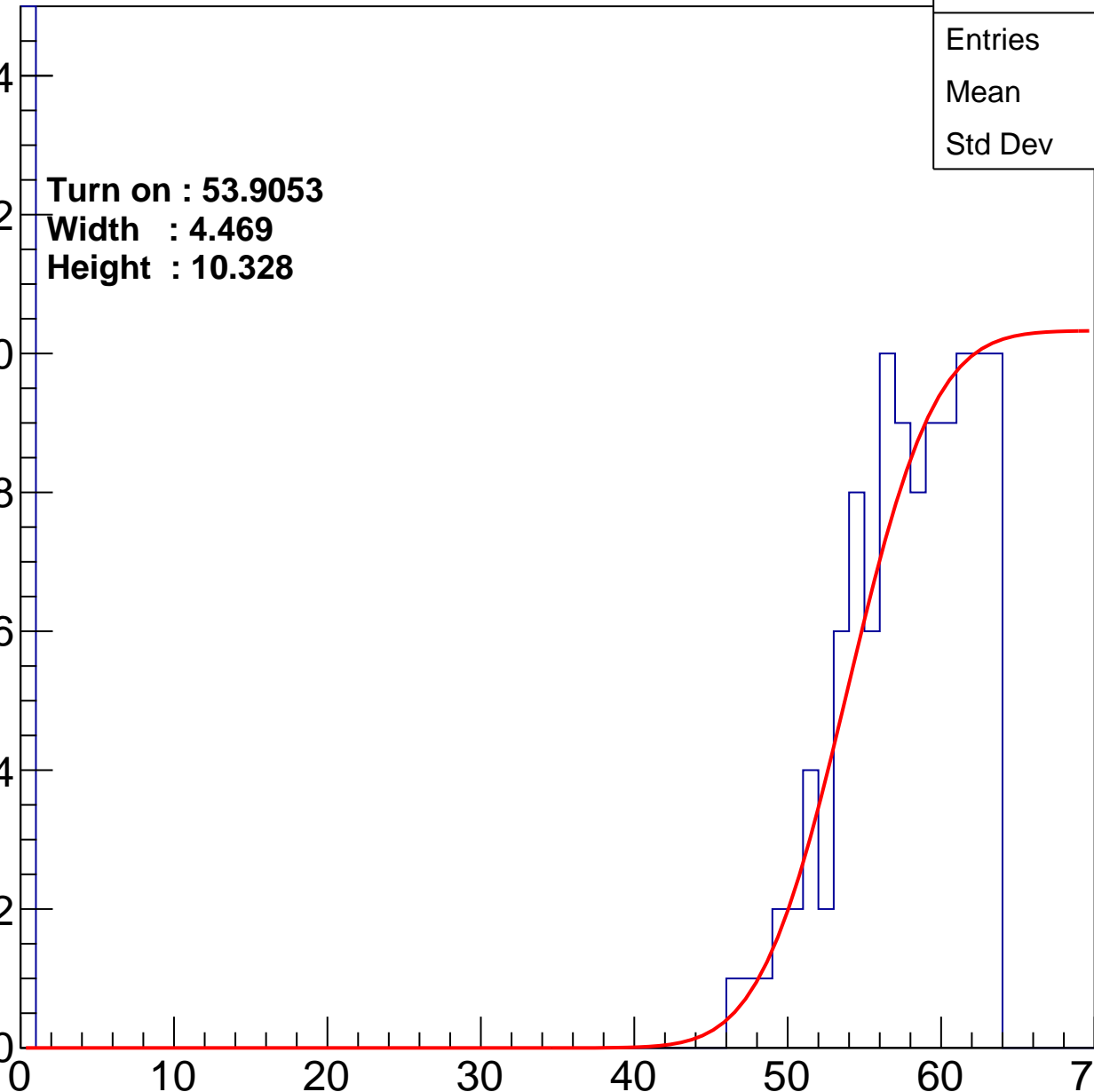
Width : 4.469

Height : 10.328

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch2

calib_packv5_033123_0516.root, FC#4, port A1

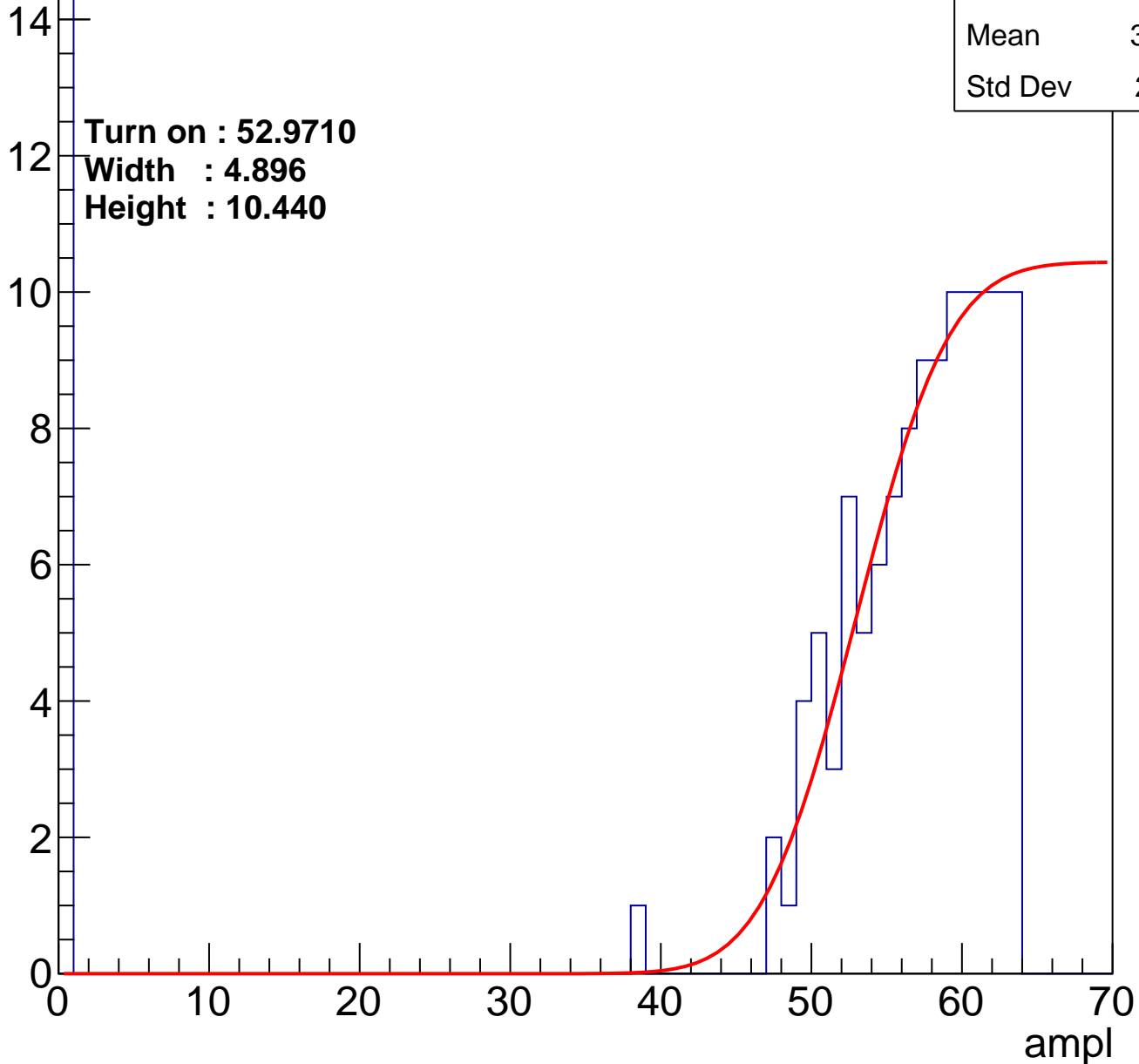
Entries	215
Mean	30.93
Std Dev	28.51

Turn on : 52.9710

Width : 4.896

Height : 10.440

Entry



B1L104S, U9-ch3

calib_packv5_033123_0516.root, FC#4, port A1

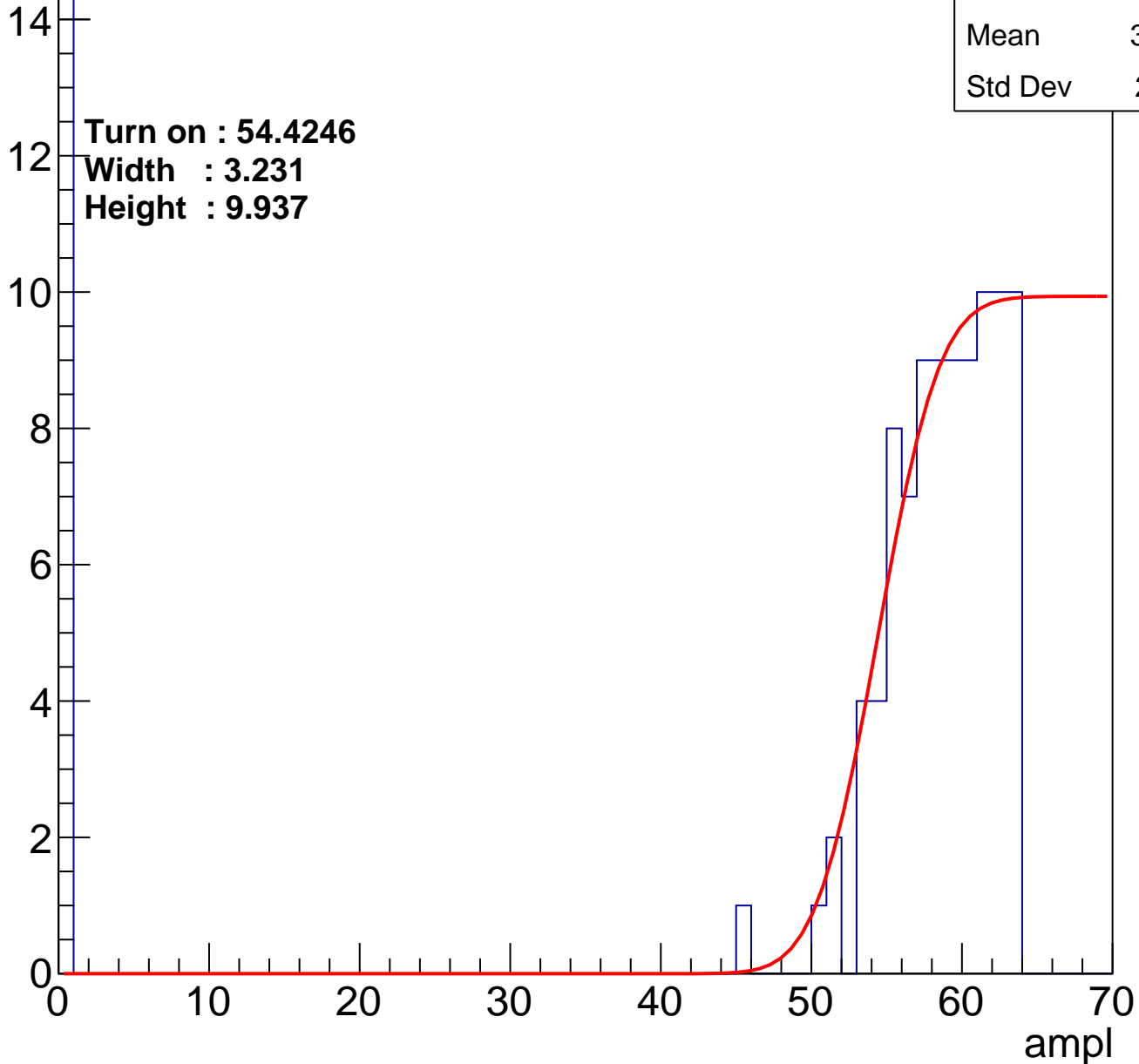
Entry

Entries	175
Mean	30.99
Std Dev	29.21

Turn on : 54.4246

Width : 3.231

Height : 9.937



B1L104S, U9-ch4

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	29.09
Std Dev	28.95

Turn on : 53.5606

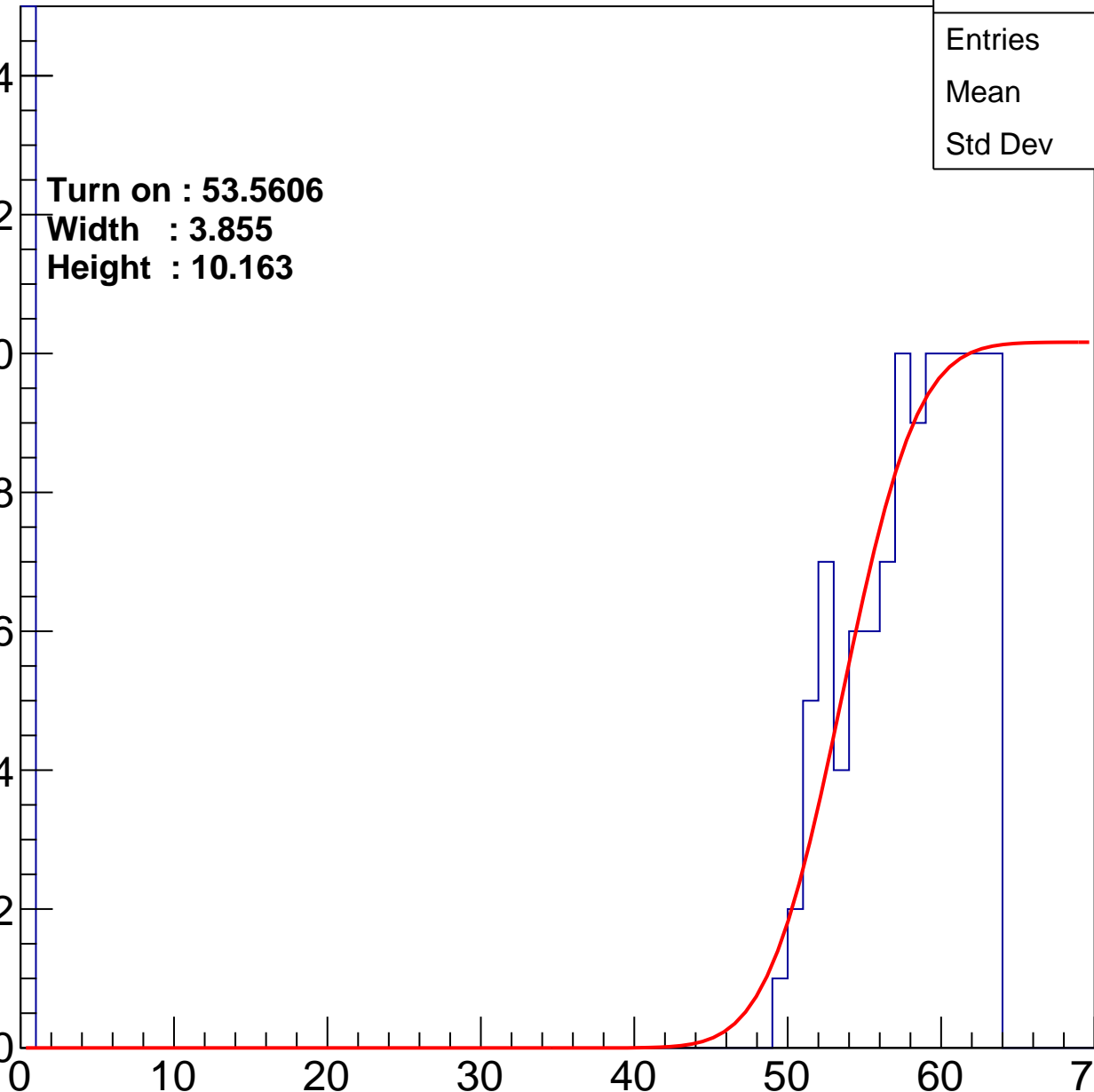
Width : 3.855

Height : 10.163

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch5

calib_packv5_033123_0516.root, FC#4, port A1

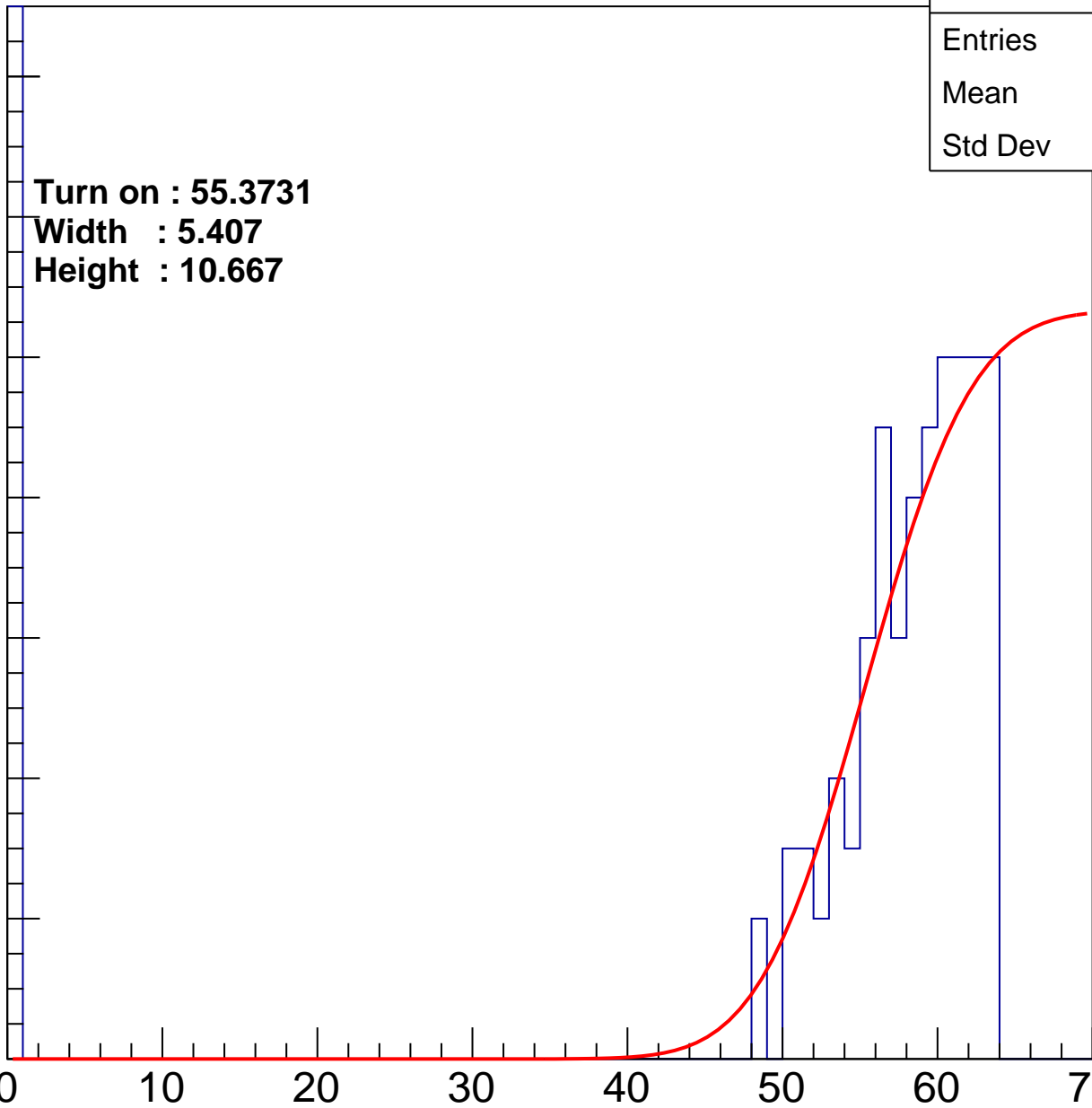
Entry

14
12
10
8
6
4
2
0

Turn on : 55.3731
Width : 5.407
Height : 10.667

Entries	179
Mean	30.77
Std Dev	29.07

ampl



B1L104S, U9-ch6

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	34.85
Std Dev	28.35

Turn on : 54.8119

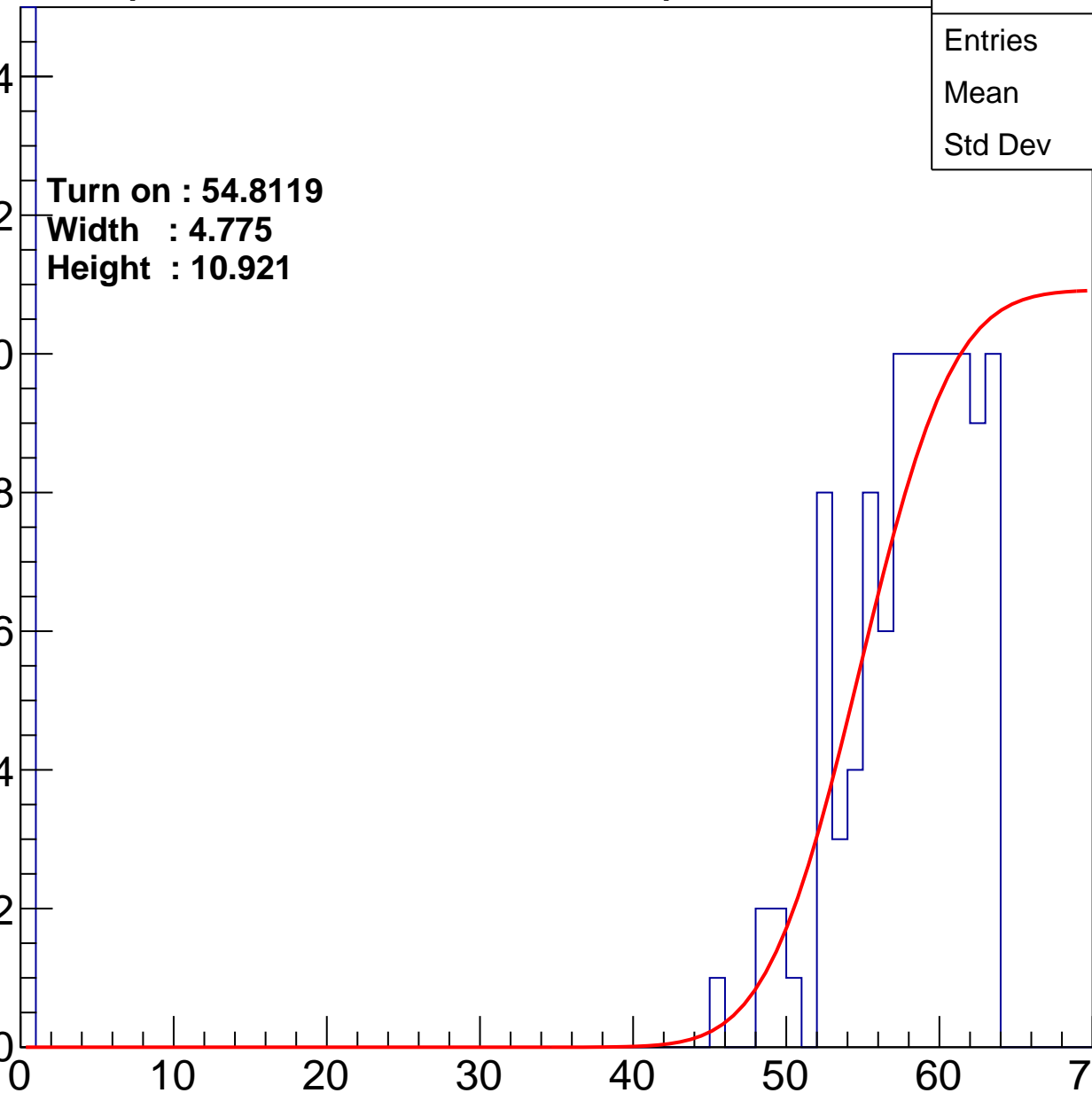
Width : 4.775

Height : 10.921

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch7

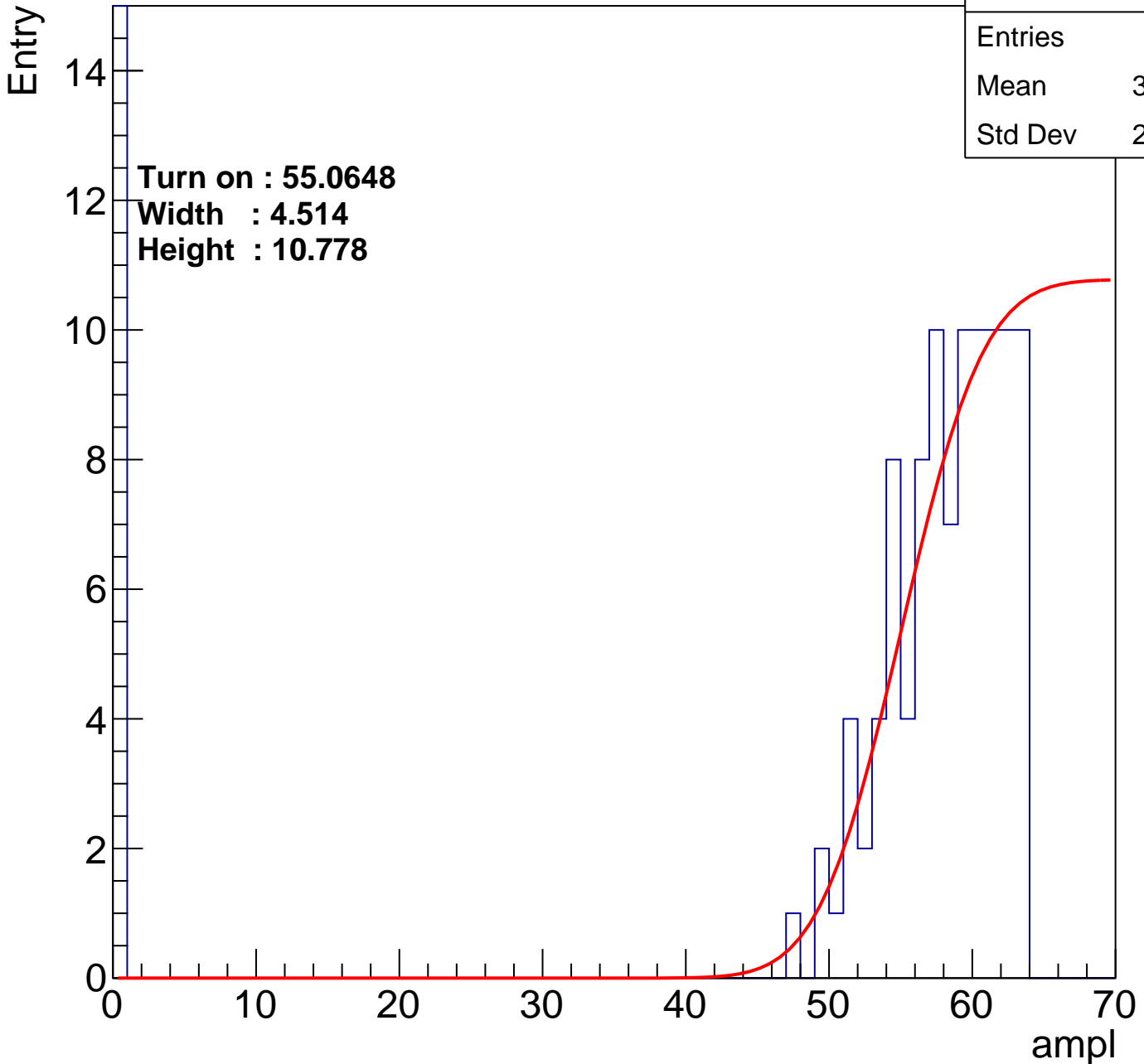
calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	31.24
Std Dev	28.96

Turn on : 55.0648

Width : 4.514

Height : 10.778



B1L104S, U9-ch8

calib_packv5_033123_0516.root, FC#4, port A1

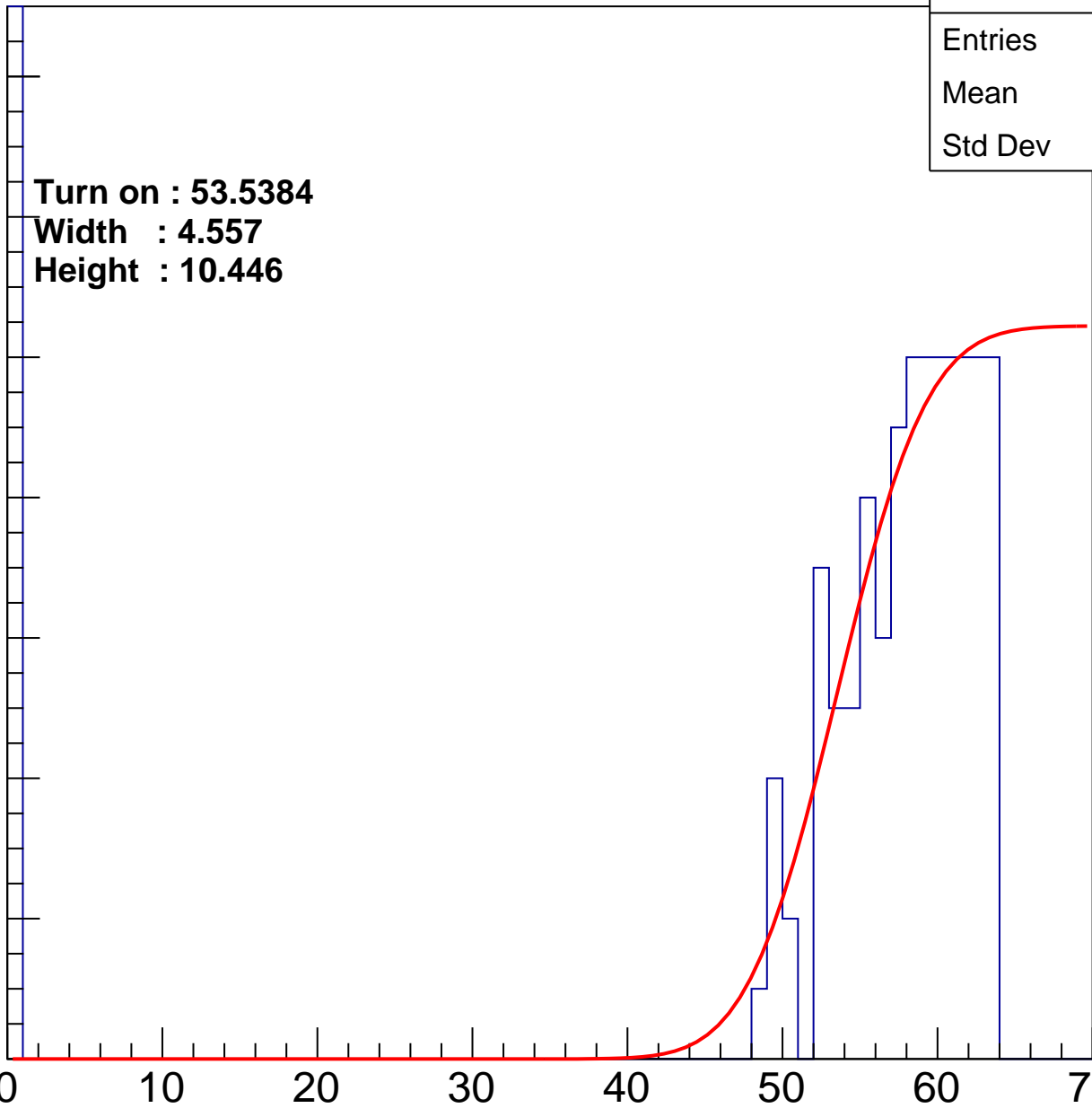
Entry

14
12
10
8
6
4
2
0

Turn on : 53.5384
Width : 4.557
Height : 10.446

Entries	195
Mean	31.6
Std Dev	28.8

ampl



B1L104S, U9-ch9

calib_packv5_033123_0516.root, FC#4, port A1

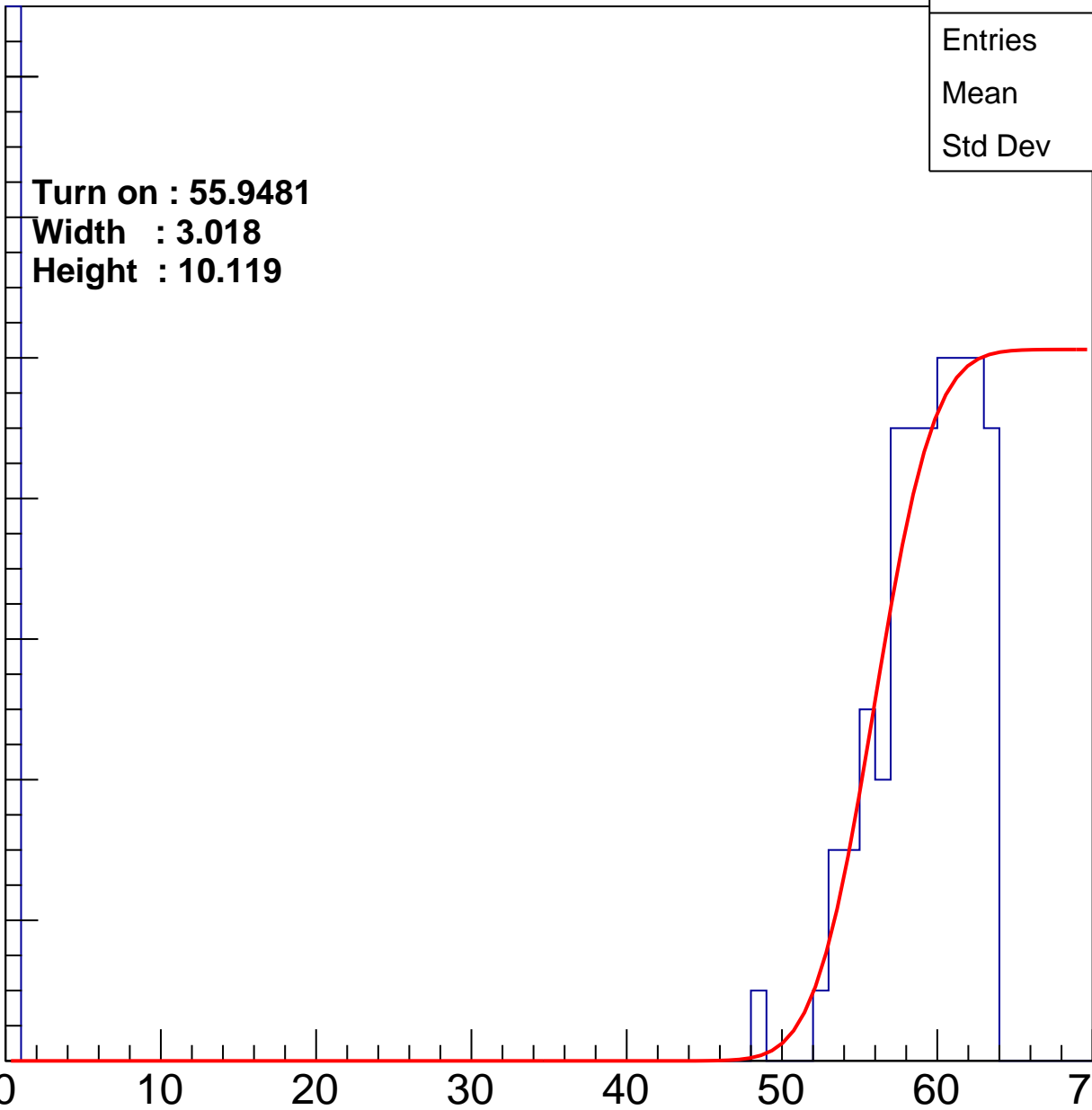
Entry

14
12
10
8
6
4
2
0

Turn on : 55.9481
Width : 3.018
Height : 10.119

Entries	160
Mean	30.52
Std Dev	29.48

ampl



B1L104S, U9-ch10

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	32.88
Std Dev	28.75

Turn on : 53.5605

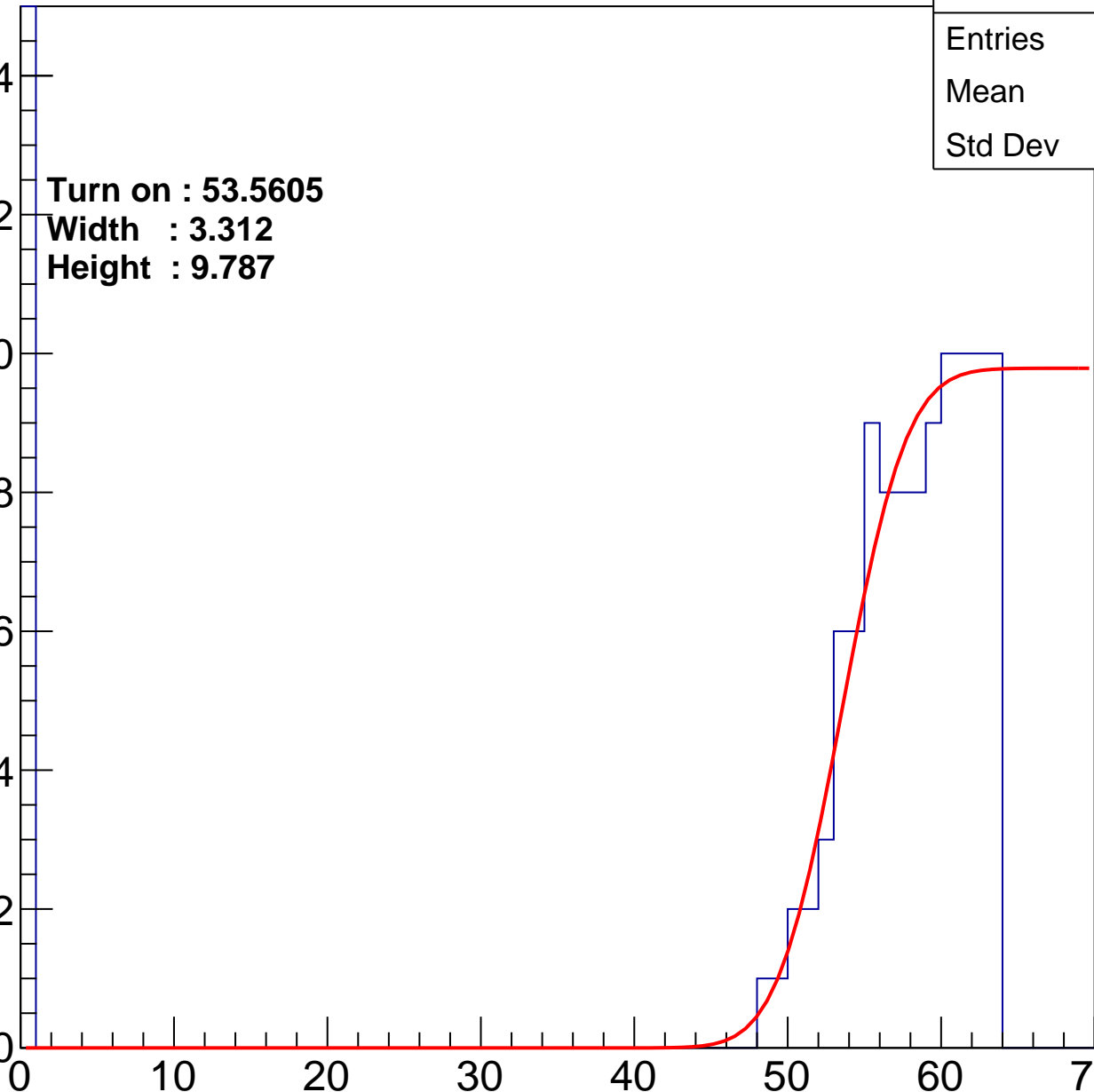
Width : 3.312

Height : 9.787

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch11

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	29.05
Std Dev	29.15

Turn on : 53.7322

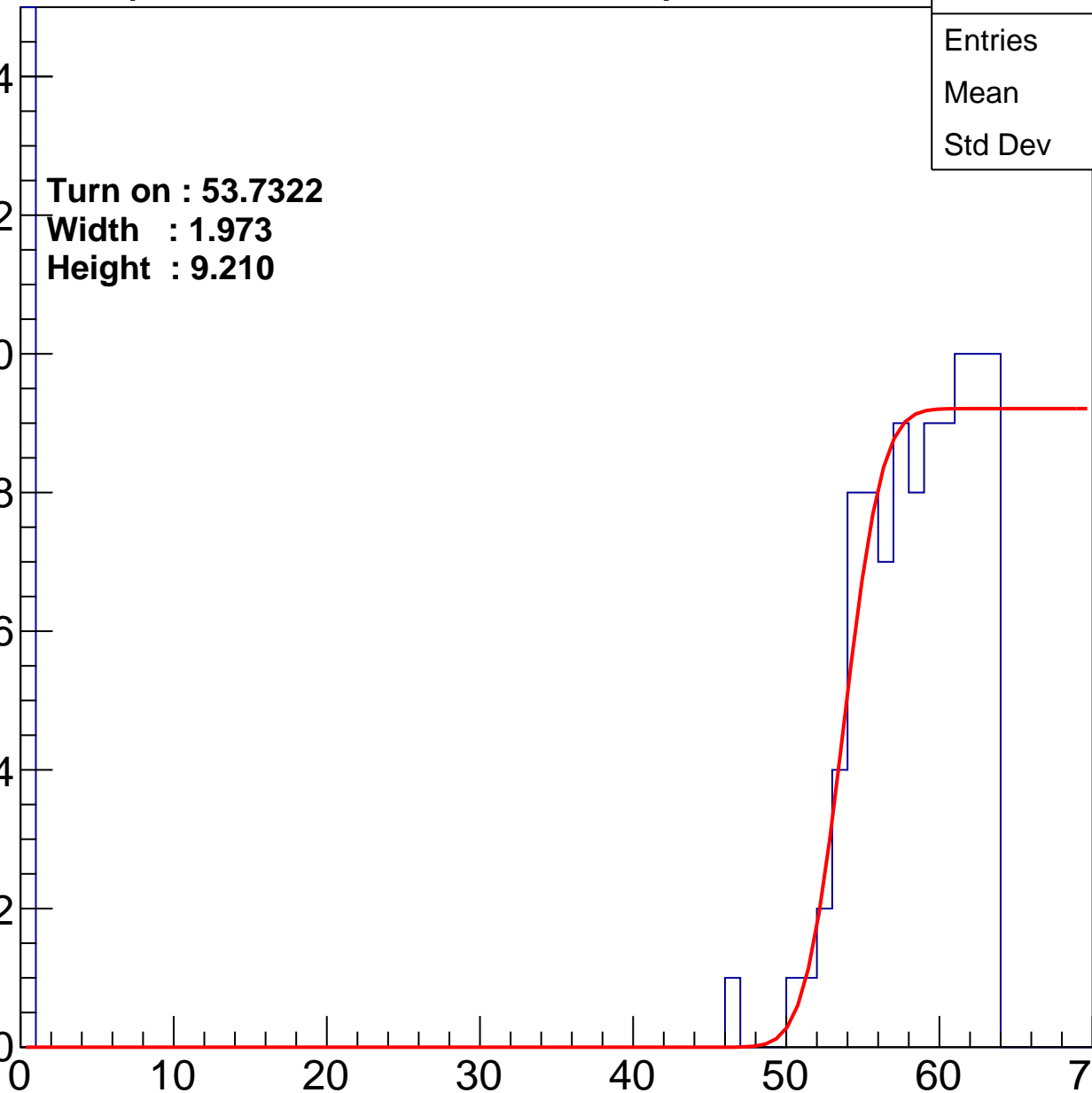
Width : 1.973

Height : 9.210

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch12

calib_packv5_033123_0516.root, FC#4, port A1

Entries	177
Mean	37.34
Std Dev	27.3

Turn on : 52.4576

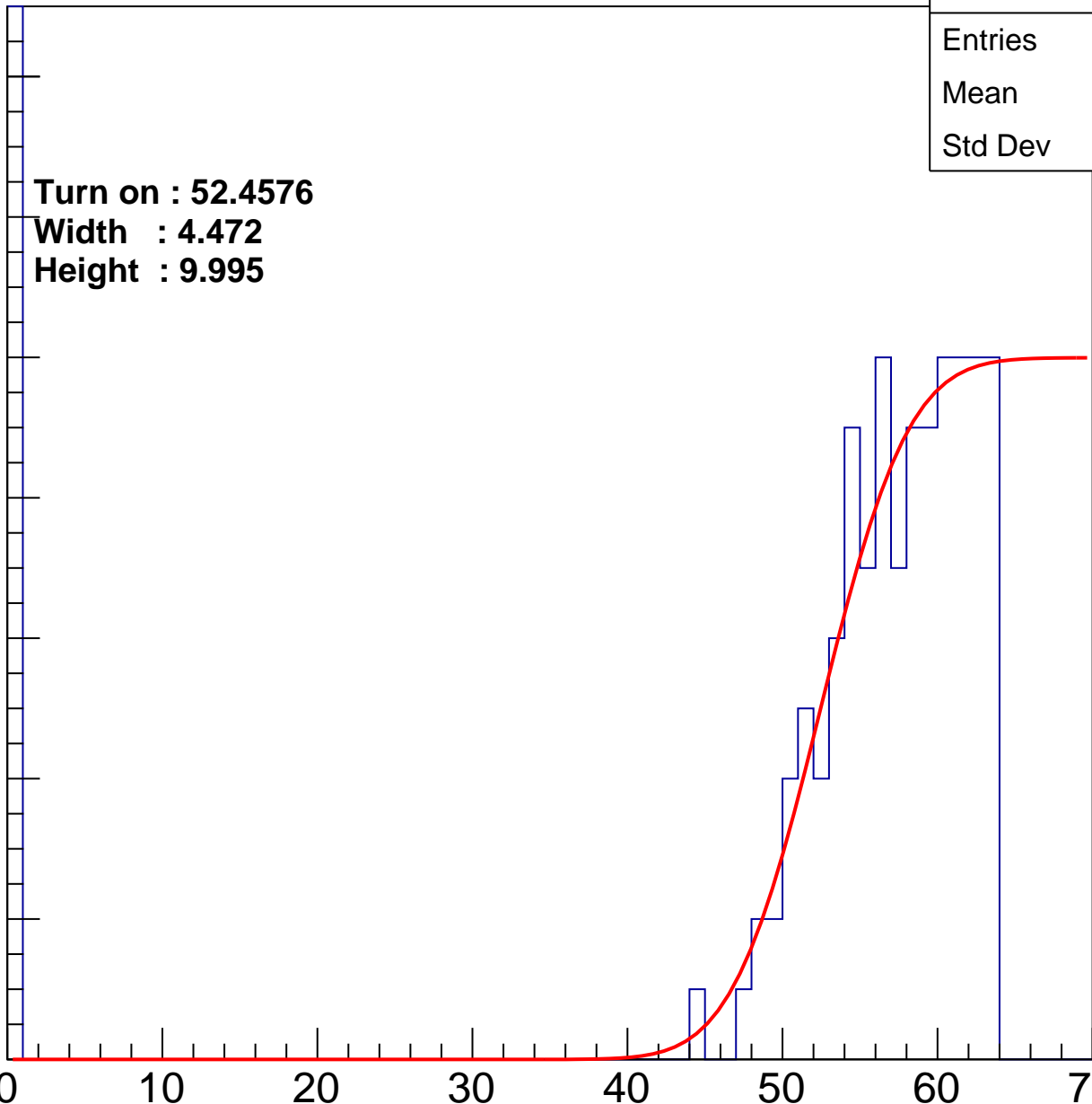
Width : 4.472

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch13

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	34.27
Std Dev	28.11

Turn on : 51.9738

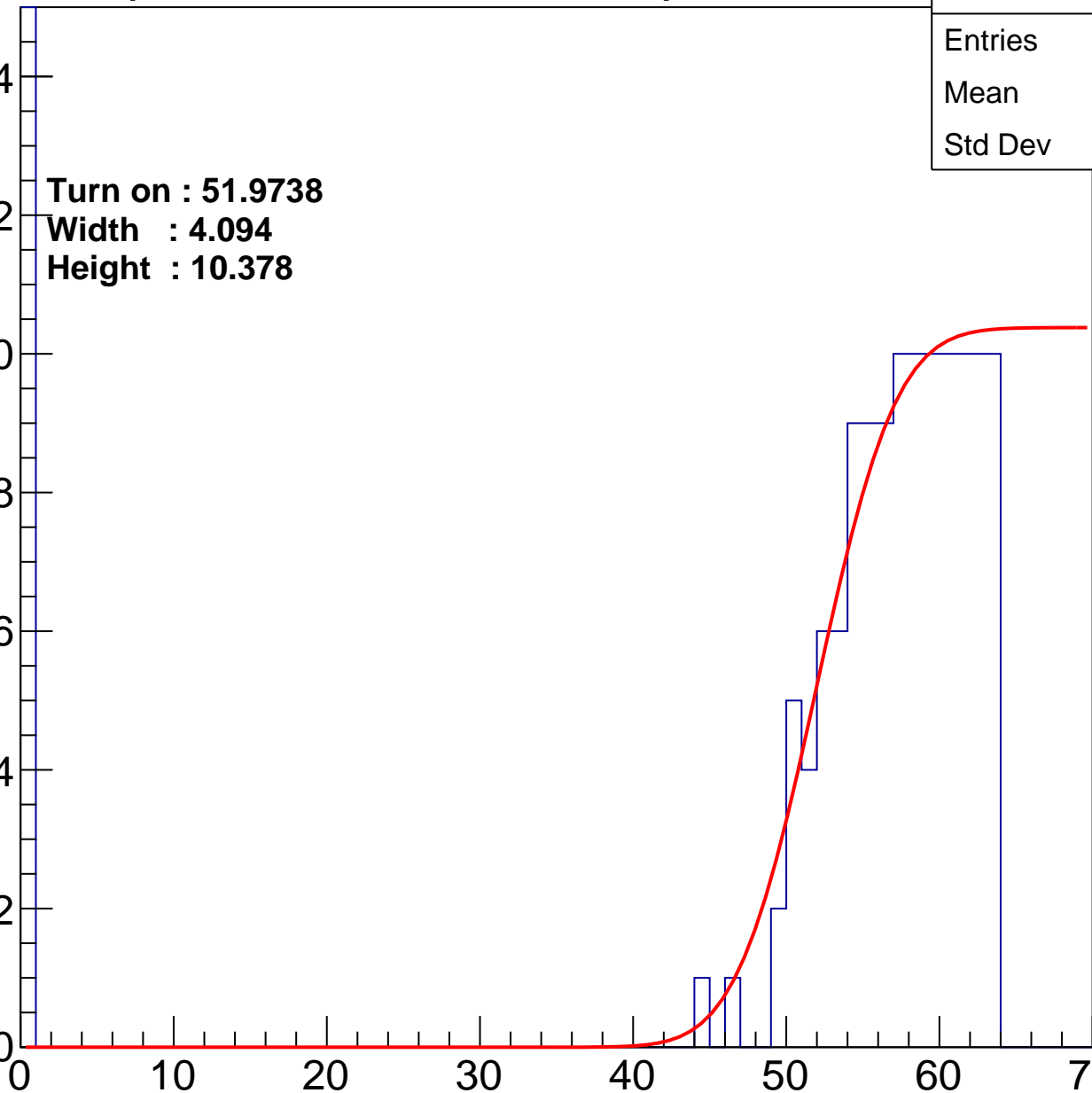
Width : 4.094

Height : 10.378

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch14

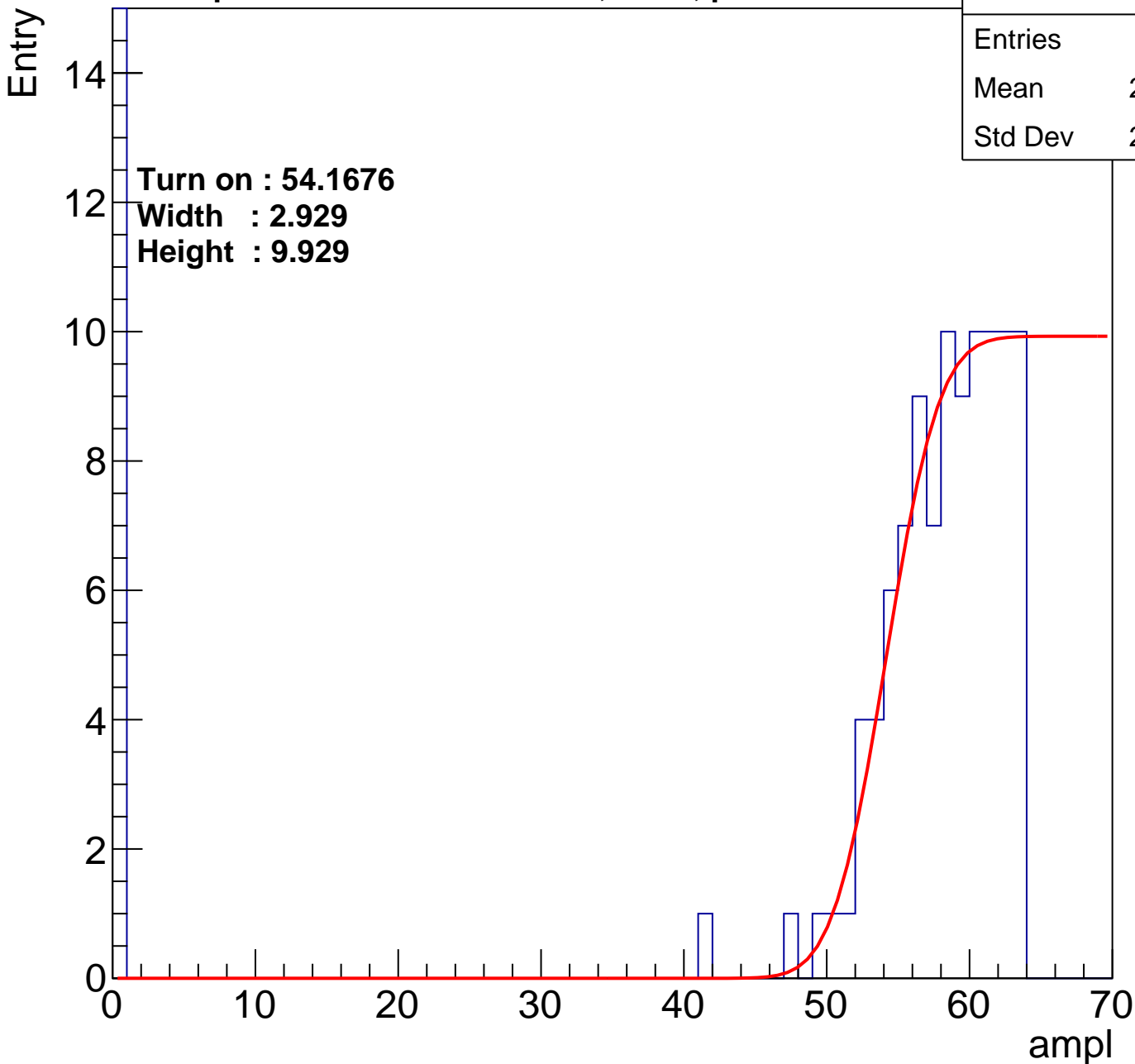
calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	29.35
Std Dev	29.05

Turn on : 54.1676

Width : 2.929

Height : 9.929



B1L104S, U9-ch15

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	37.76
Std Dev	27.78

Turn on : 54.7310

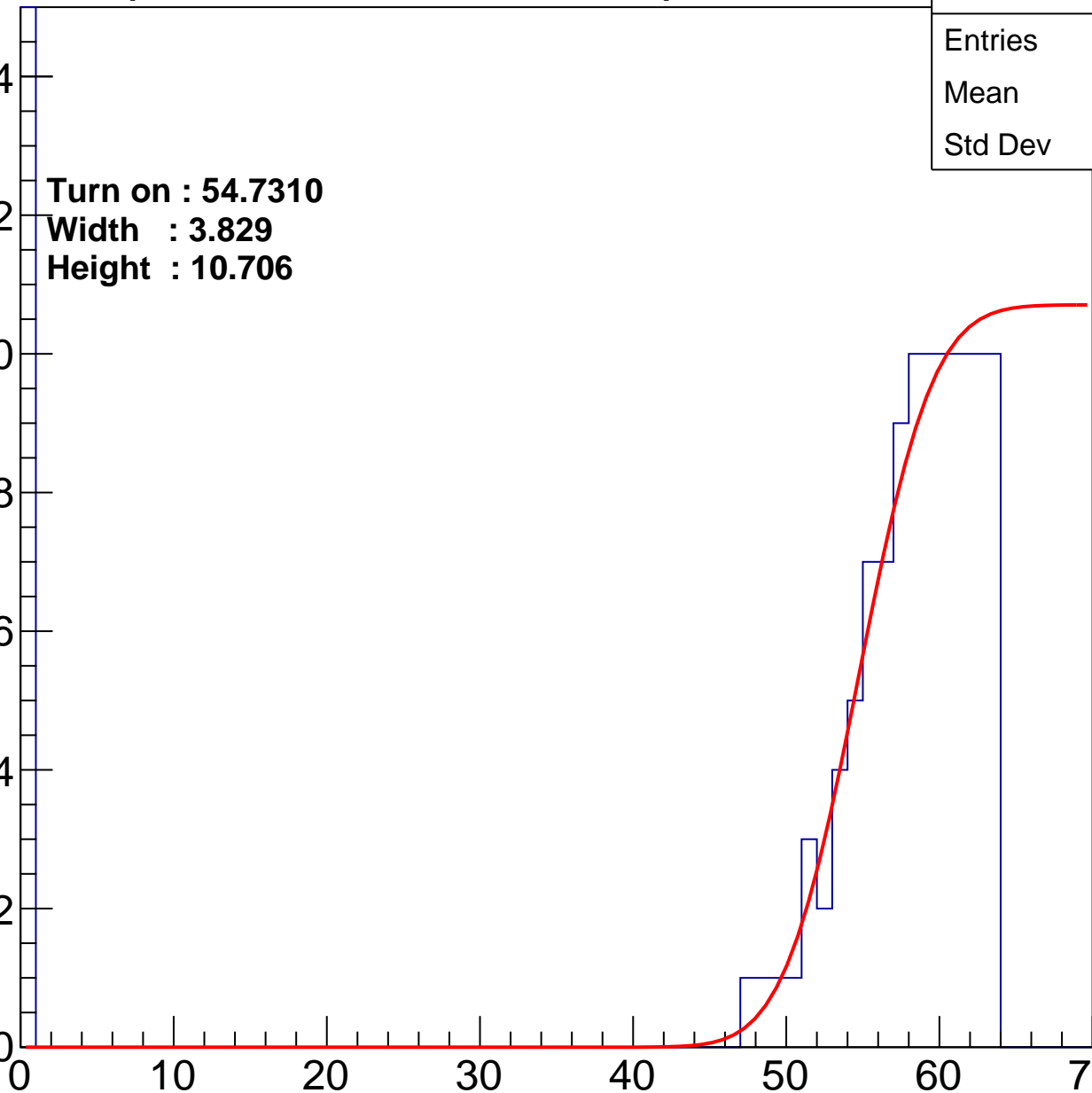
Width : 3.829

Height : 10.706

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch16

calib_packv5_033123_0516.root, FC#4, port A1

Entries	247
Mean	37.22
Std Dev	25.29

Turn on : 51.7473

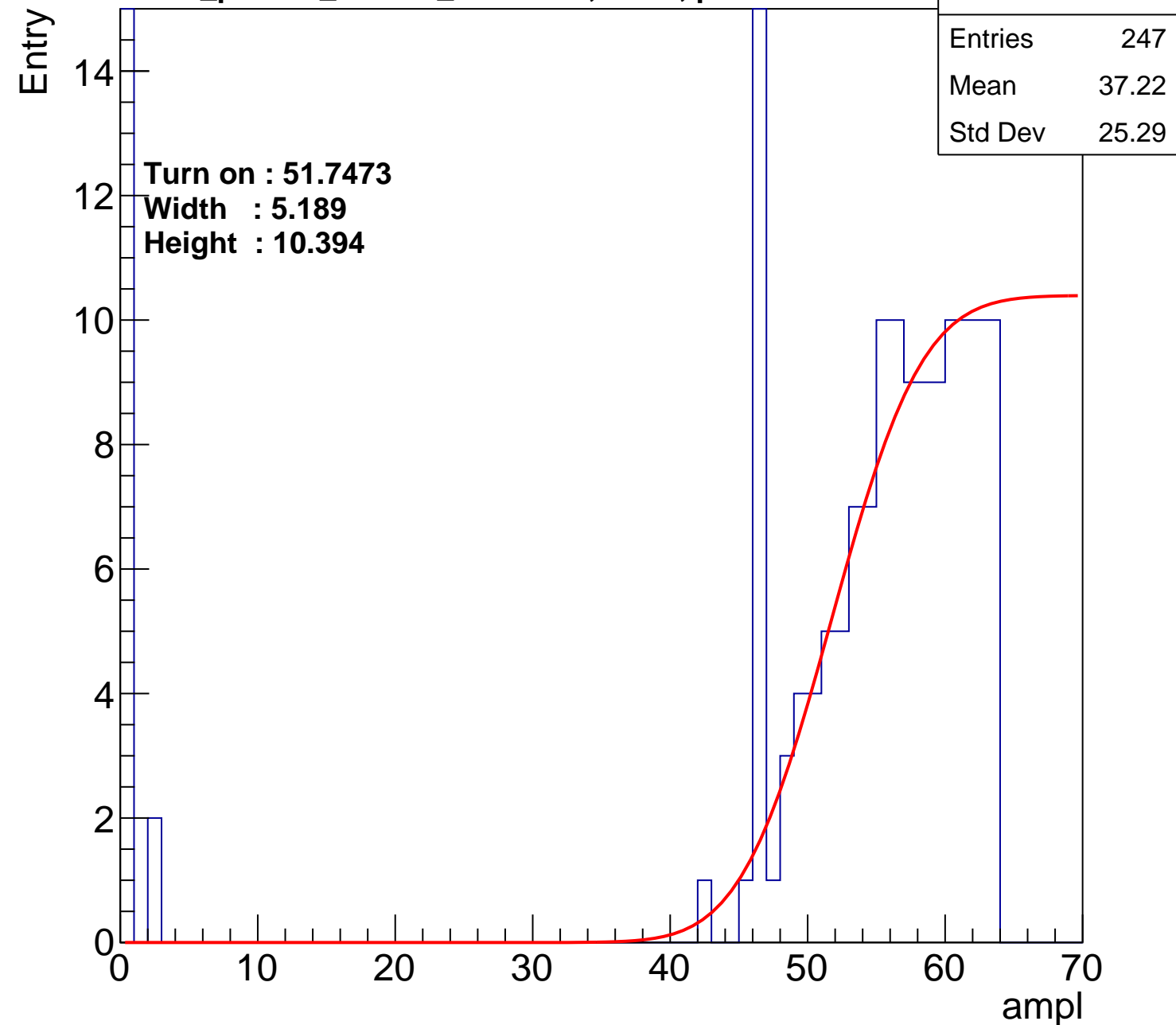
Width : 5.189

Height : 10.394

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch17

calib_packv5_033123_0516.root, FC#4, port A1

Entries	187
Mean	33.33
Std Dev	28.65

Turn on : 53.6625

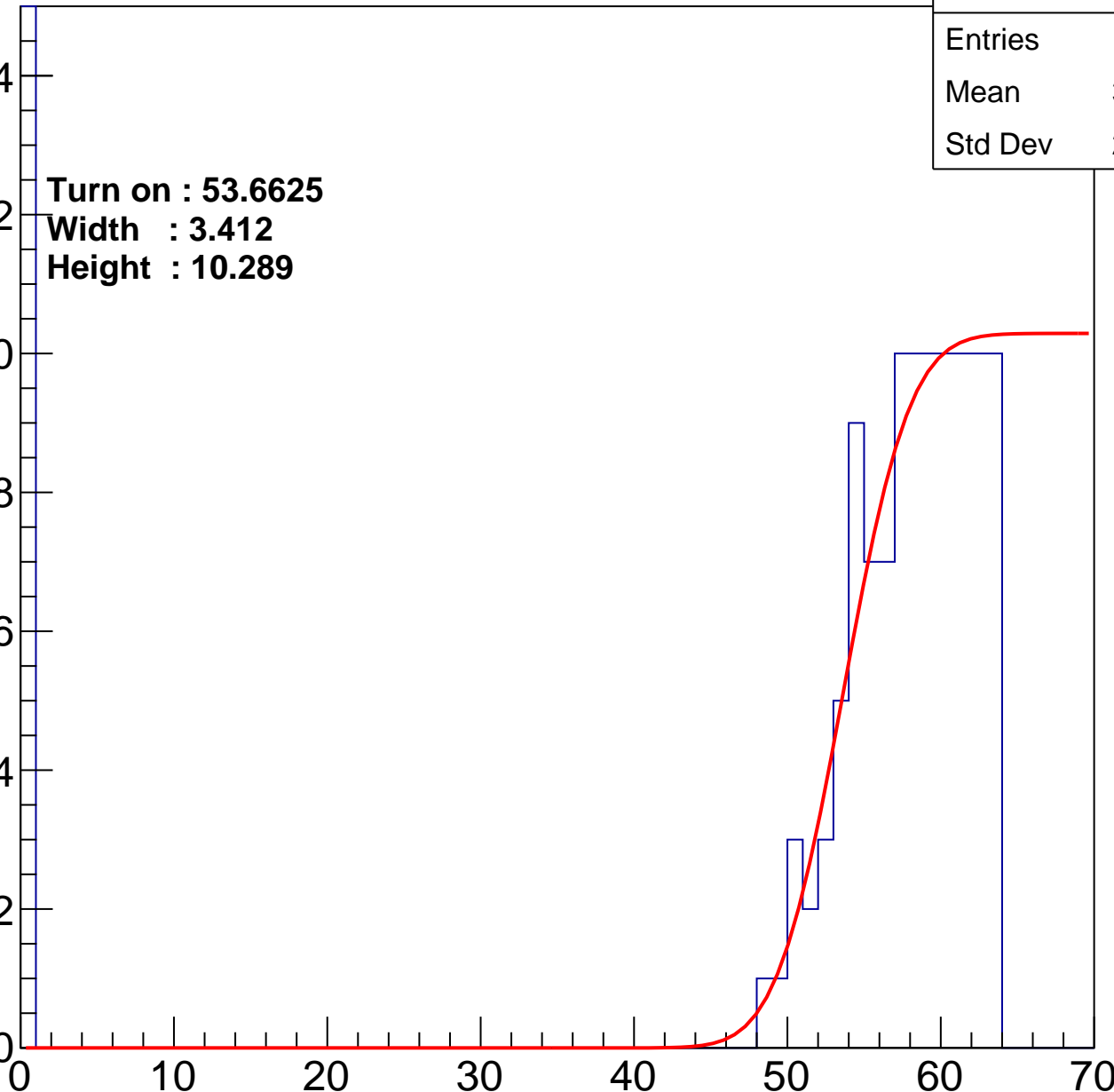
Width : 3.412

Height : 10.289

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch18

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	36.8
Std Dev	27.28

Turn on : 51.1689

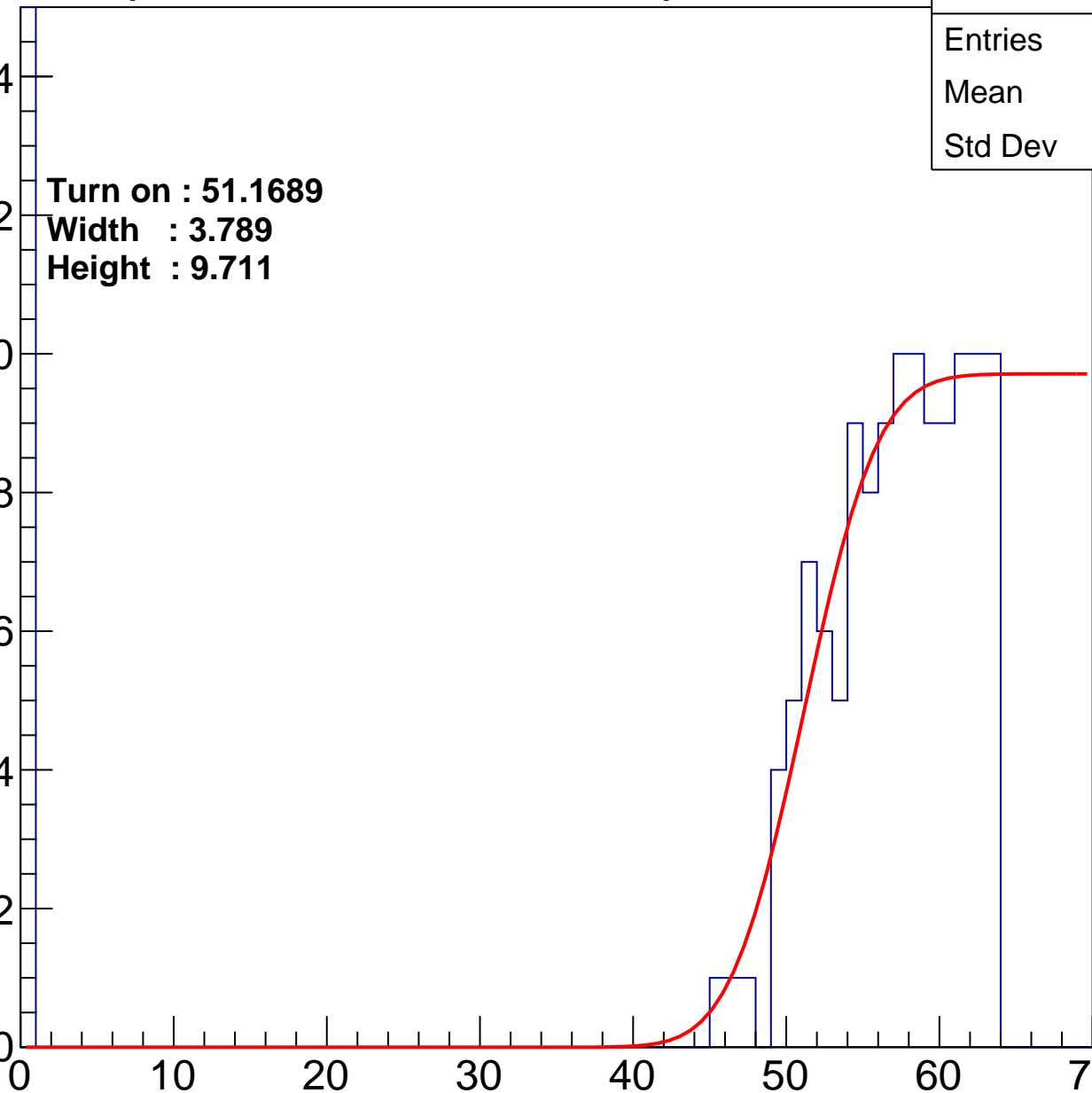
Width : 3.789

Height : 9.711

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch19

calib_packv5_033123_0516.root, FC#4, port A1

Entries	170
Mean	34.72
Std Dev	28.5

Turn on : 54.2832

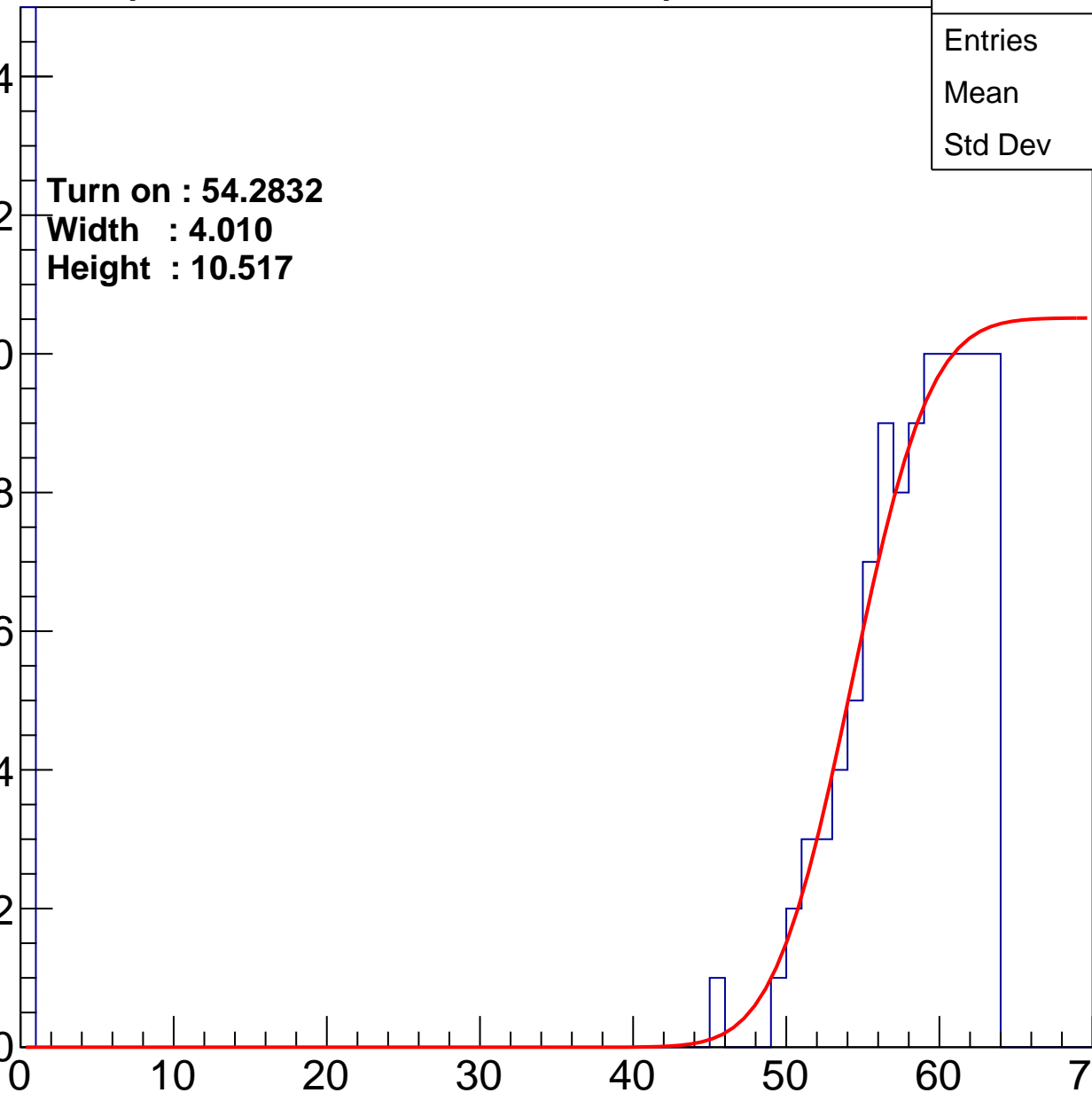
Width : 4.010

Height : 10.517

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch20

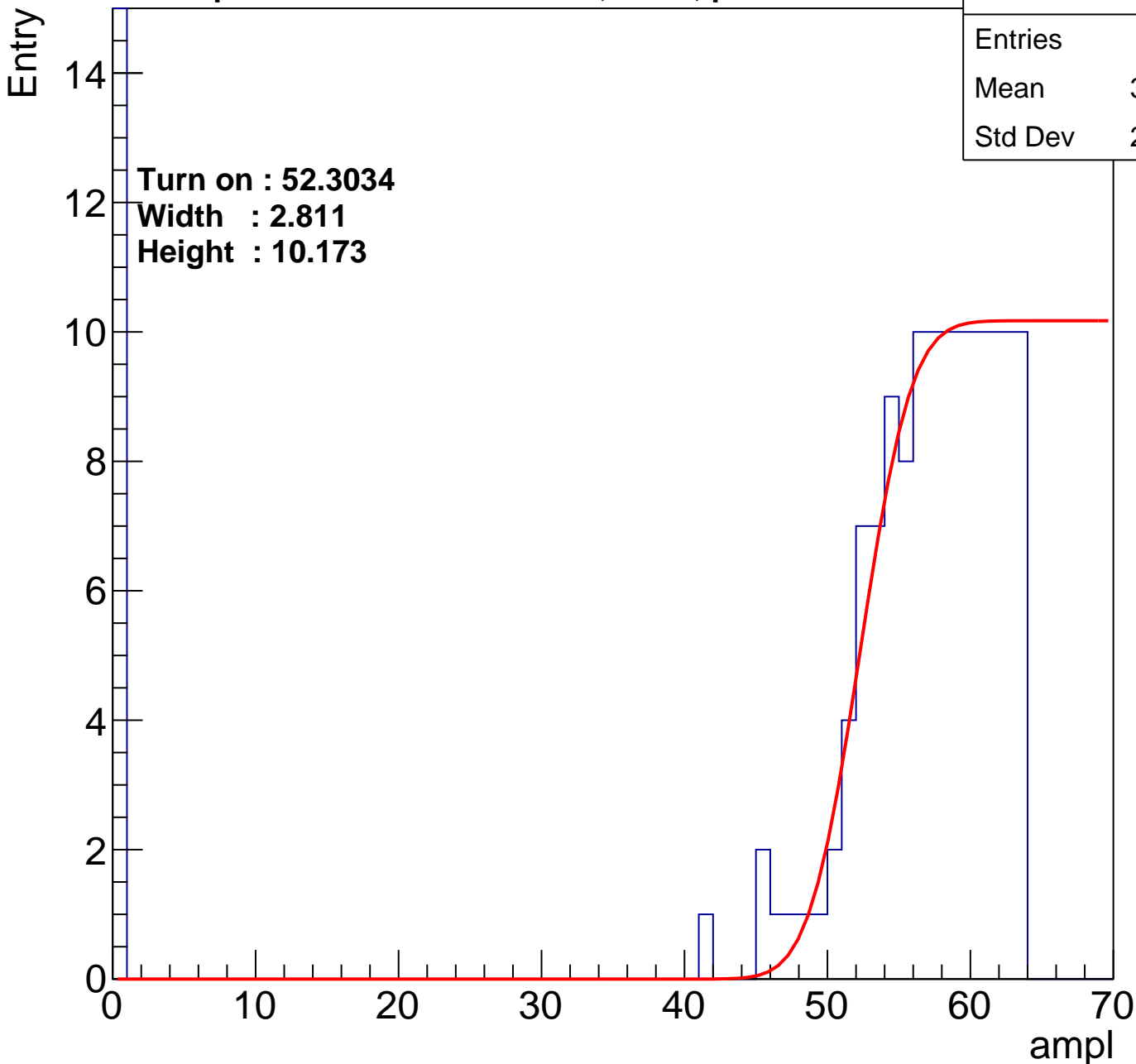
calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	37.28
Std Dev	27.23

Turn on : 52.3034

Width : 2.811

Height : 10.173



B1L104S, U9-ch21

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	31.13
Std Dev	29.11

Turn on : 54.1305

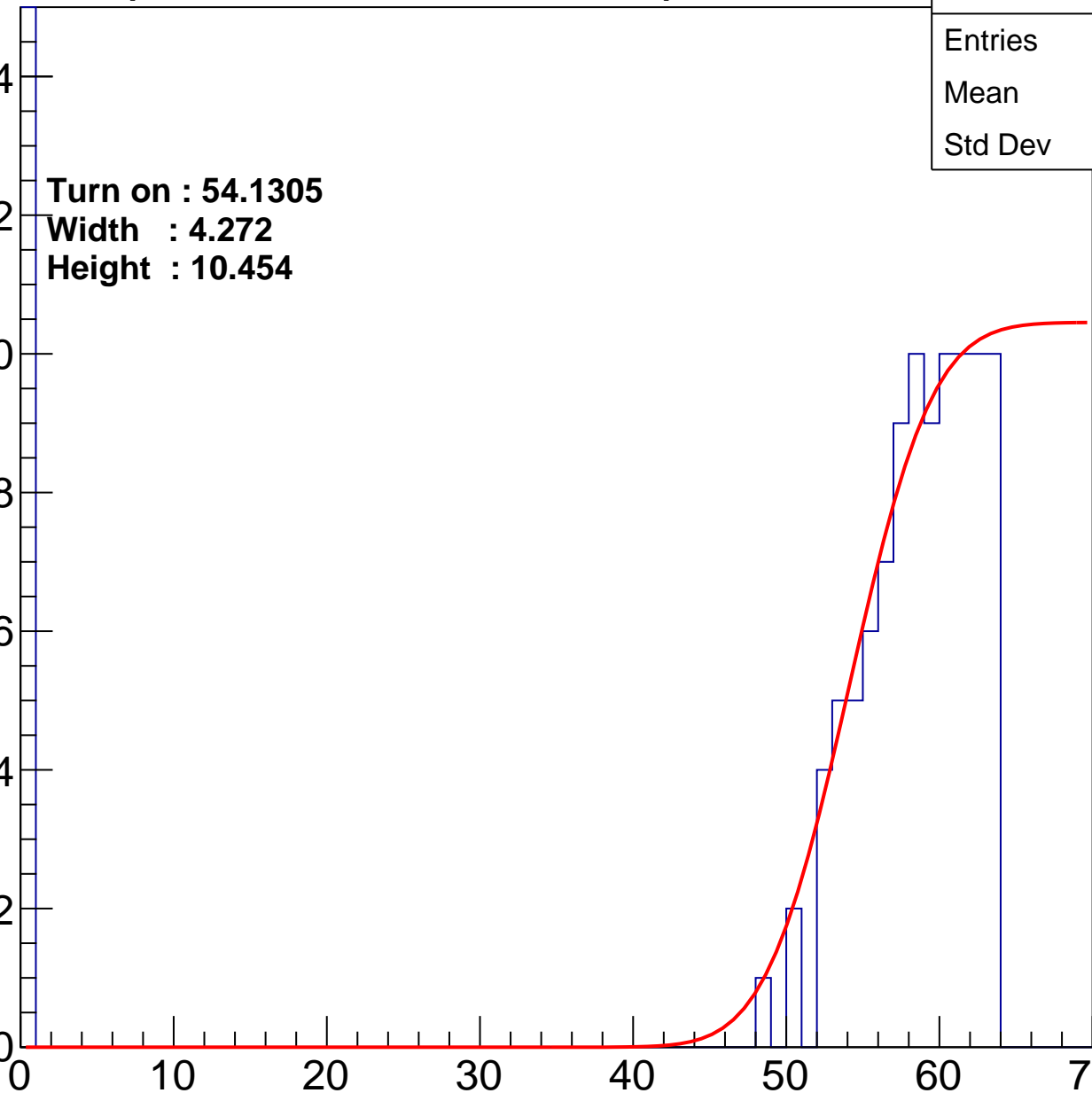
Width : 4.272

Height : 10.454

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch22

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	34.59
Std Dev	28.4

Turn on : 55.1834

Width : 4.995

Height : 11.111

Entry

14

12

10

8

6

4

2

0

0

10

20

30

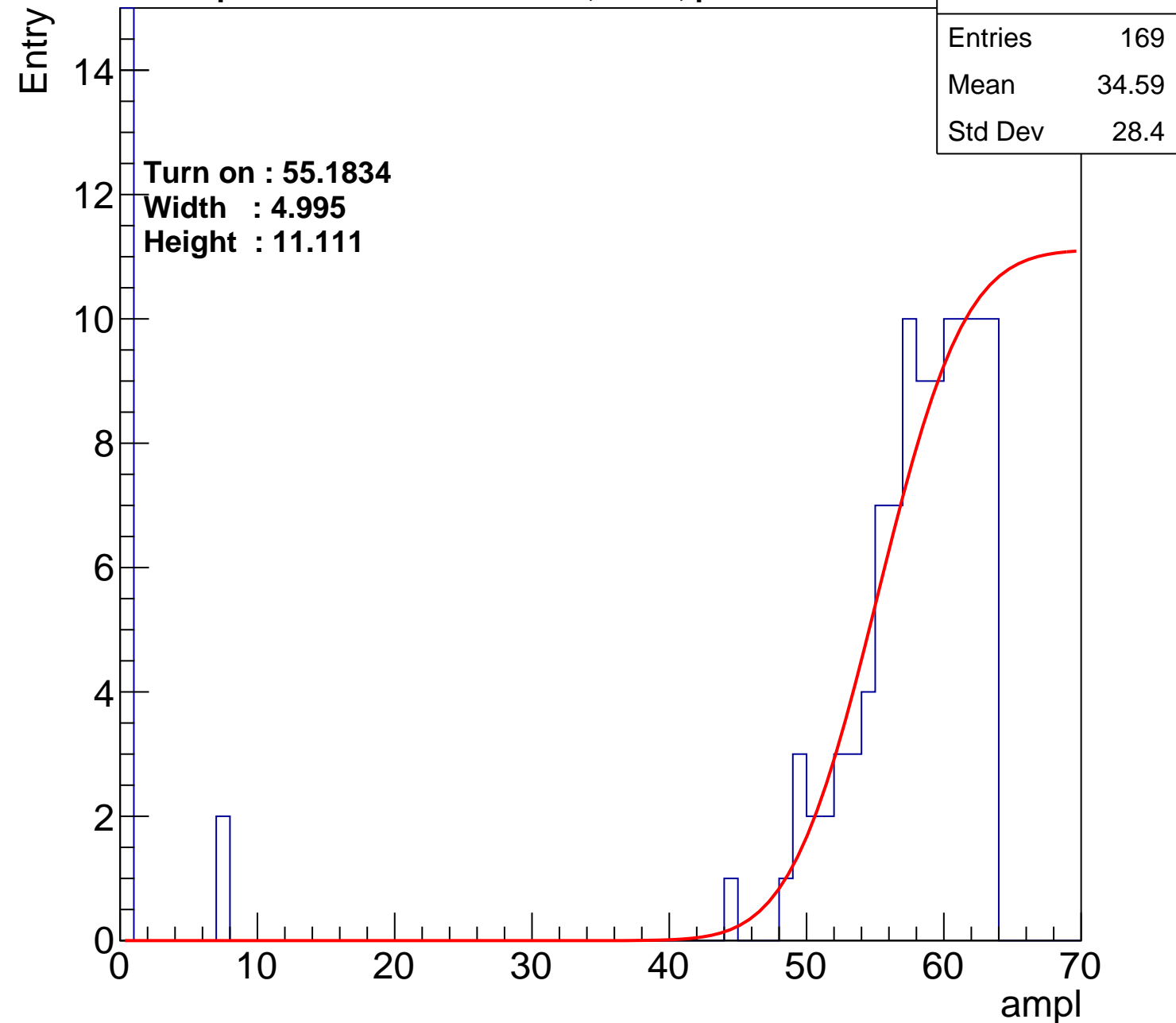
40

50

60

70

ampl



B1L104S, U9-ch23

calib_packv5_033123_0516.root, FC#4, port A1

Entries	184
Mean	29.72
Std Dev	29.2

Turn on : 55.2700

Width : 4.641

Height : 10.556

Entry

14

12

10

8

6

4

2

0

0

10

20

30

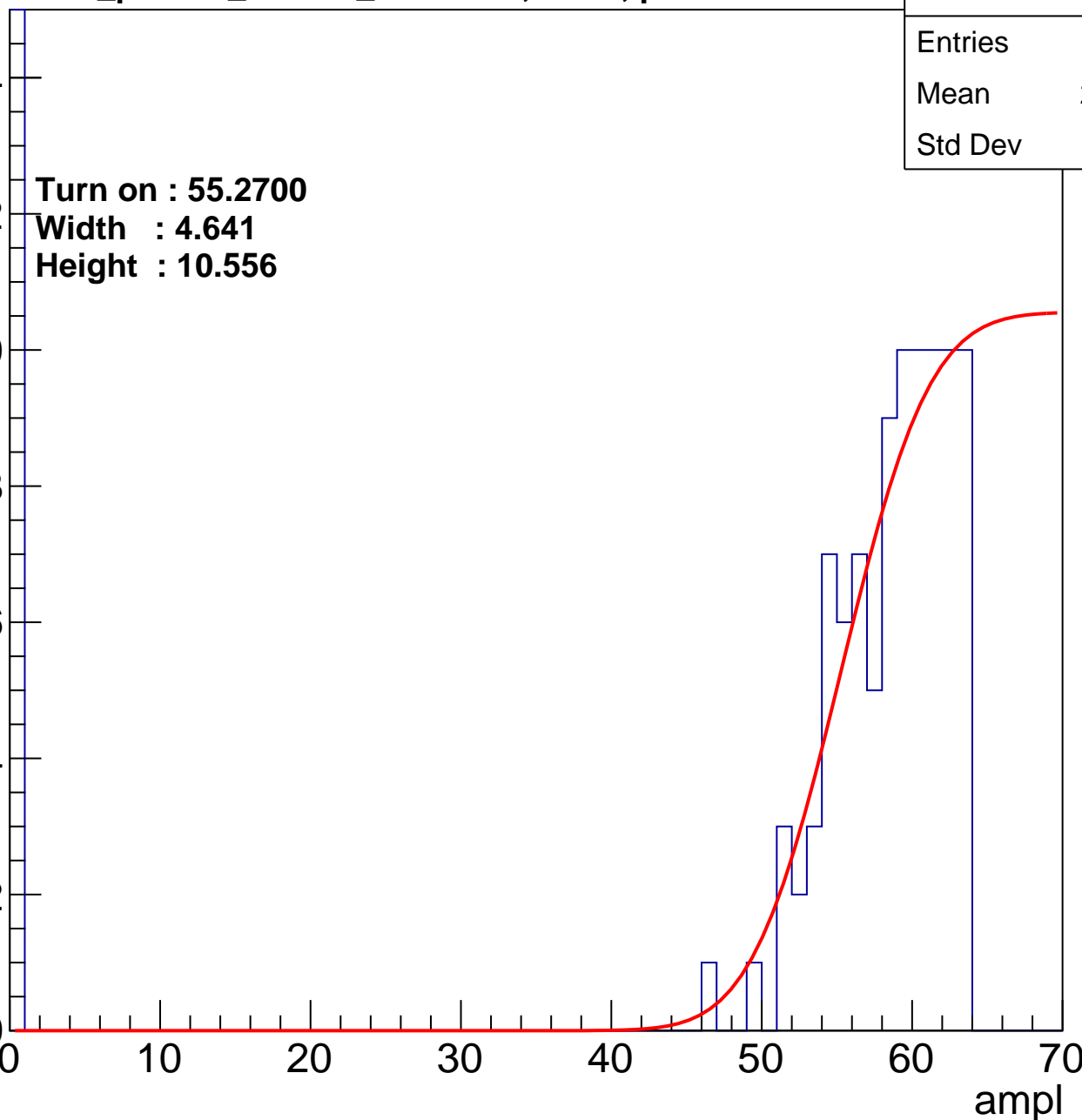
40

50

60

70

ampl



B1L104S, U9-ch24

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	30.7
Std Dev	28.81

Turn on : 54.5229

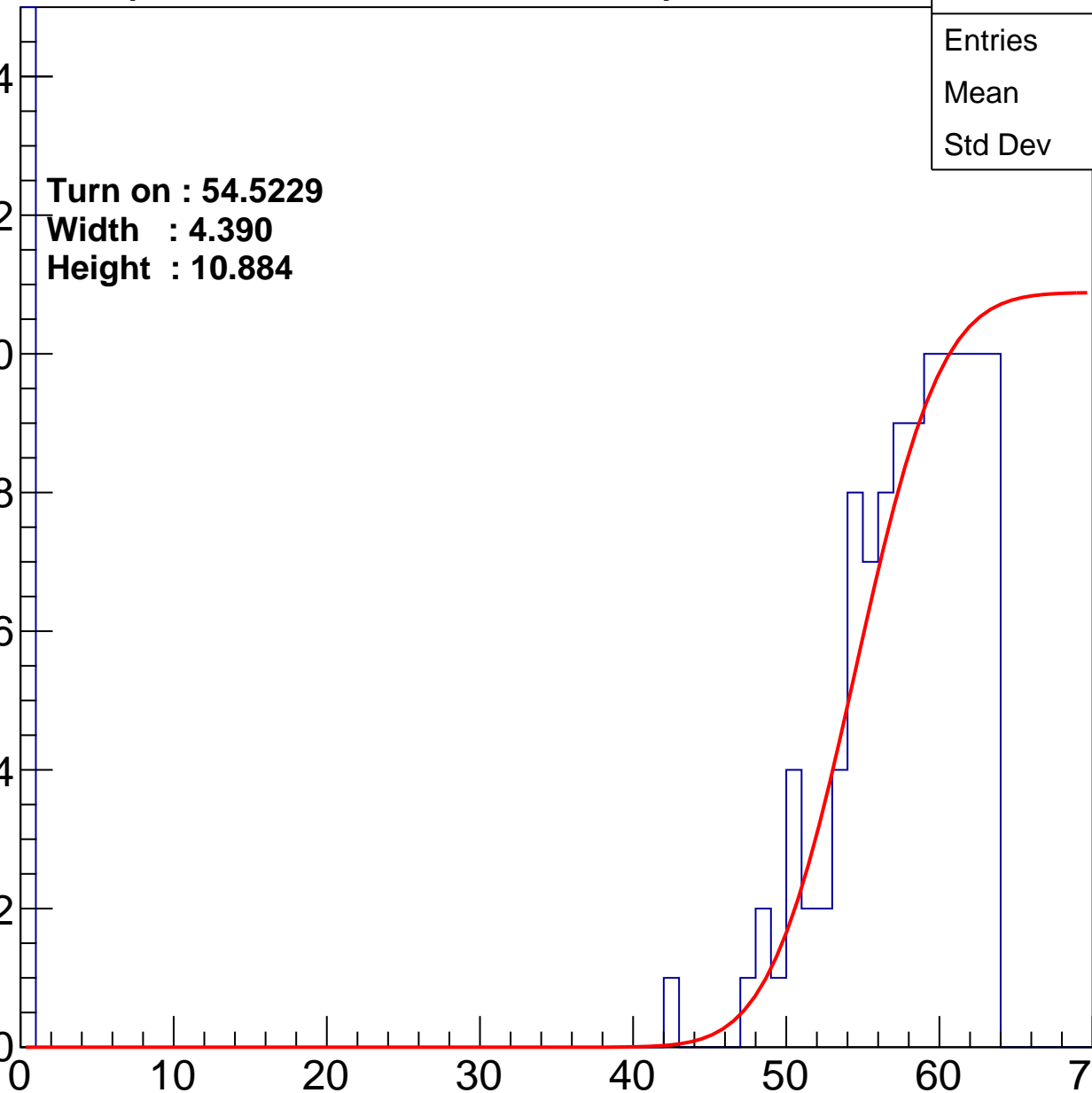
Width : 4.390

Height : 10.884

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch25

calib_packv5_033123_0516.root, FC#4, port A1

Entries	150
Mean	35.52
Std Dev	28.71

Turn on : 55.5708

Width : 3.096

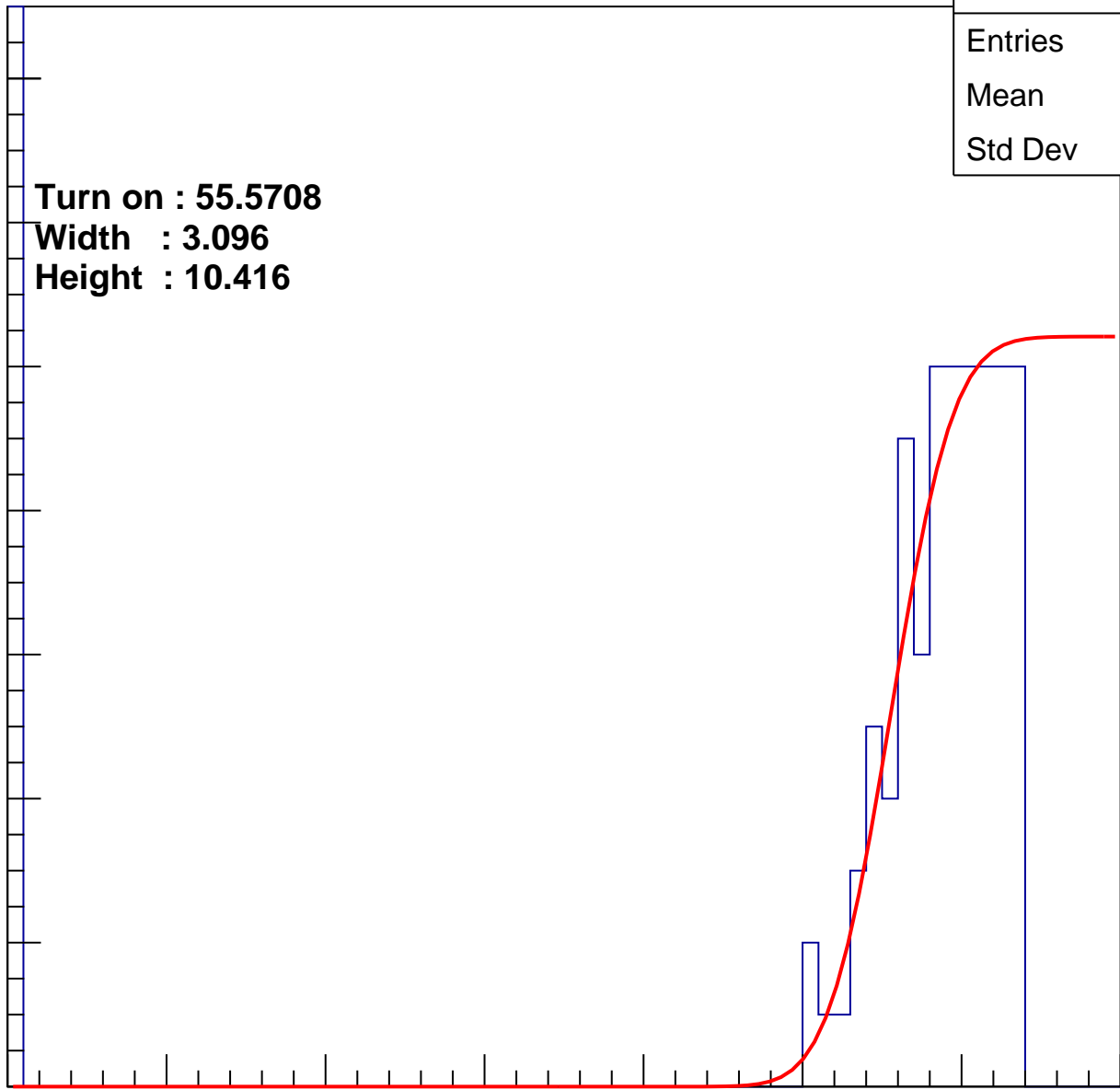
Height : 10.416

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch26

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	28.36
Std Dev	28.93

Turn on : 54.3232

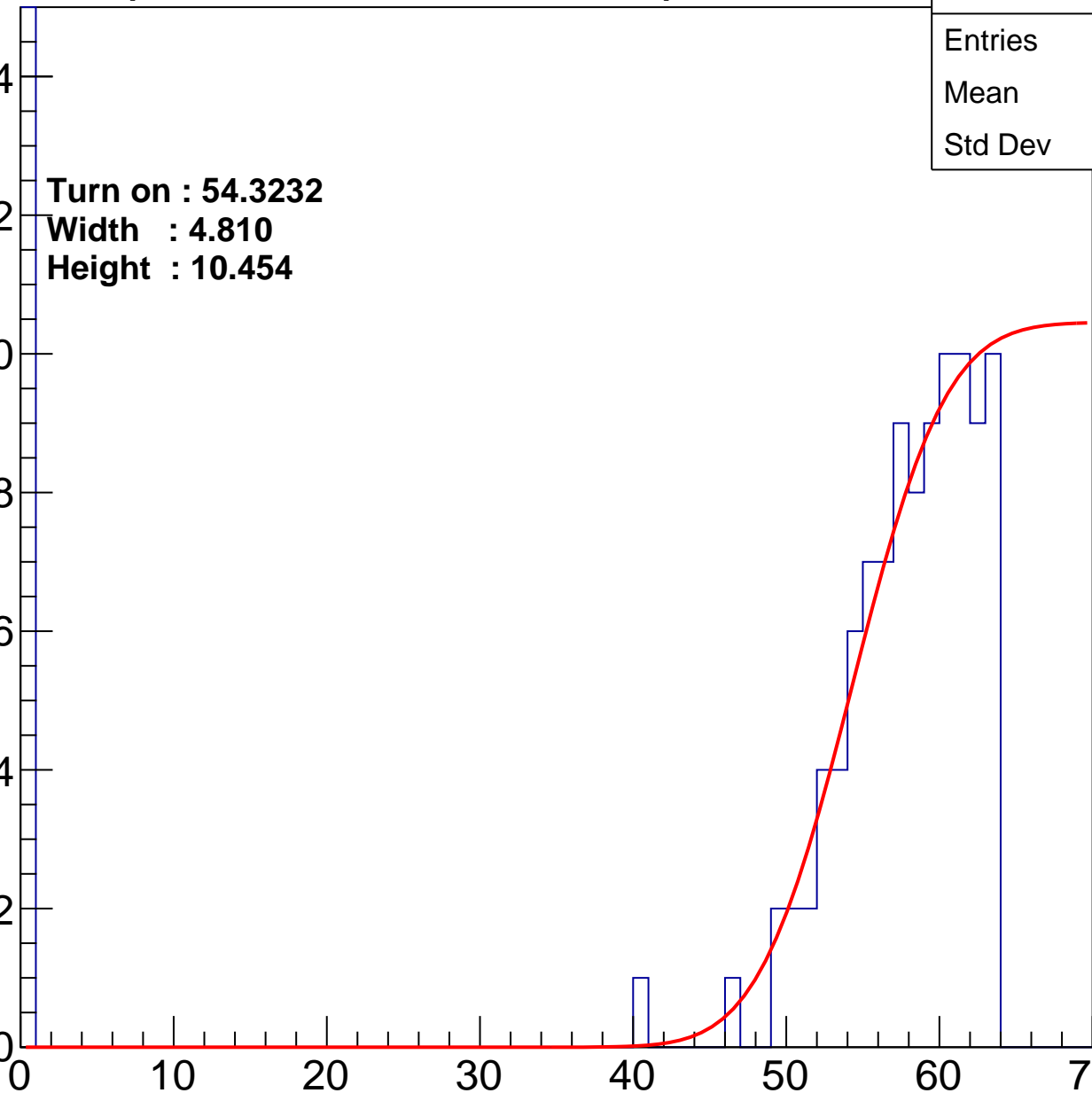
Width : 4.810

Height : 10.454

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch27

calib_packv5_033123_0516.root, FC#4, port A1

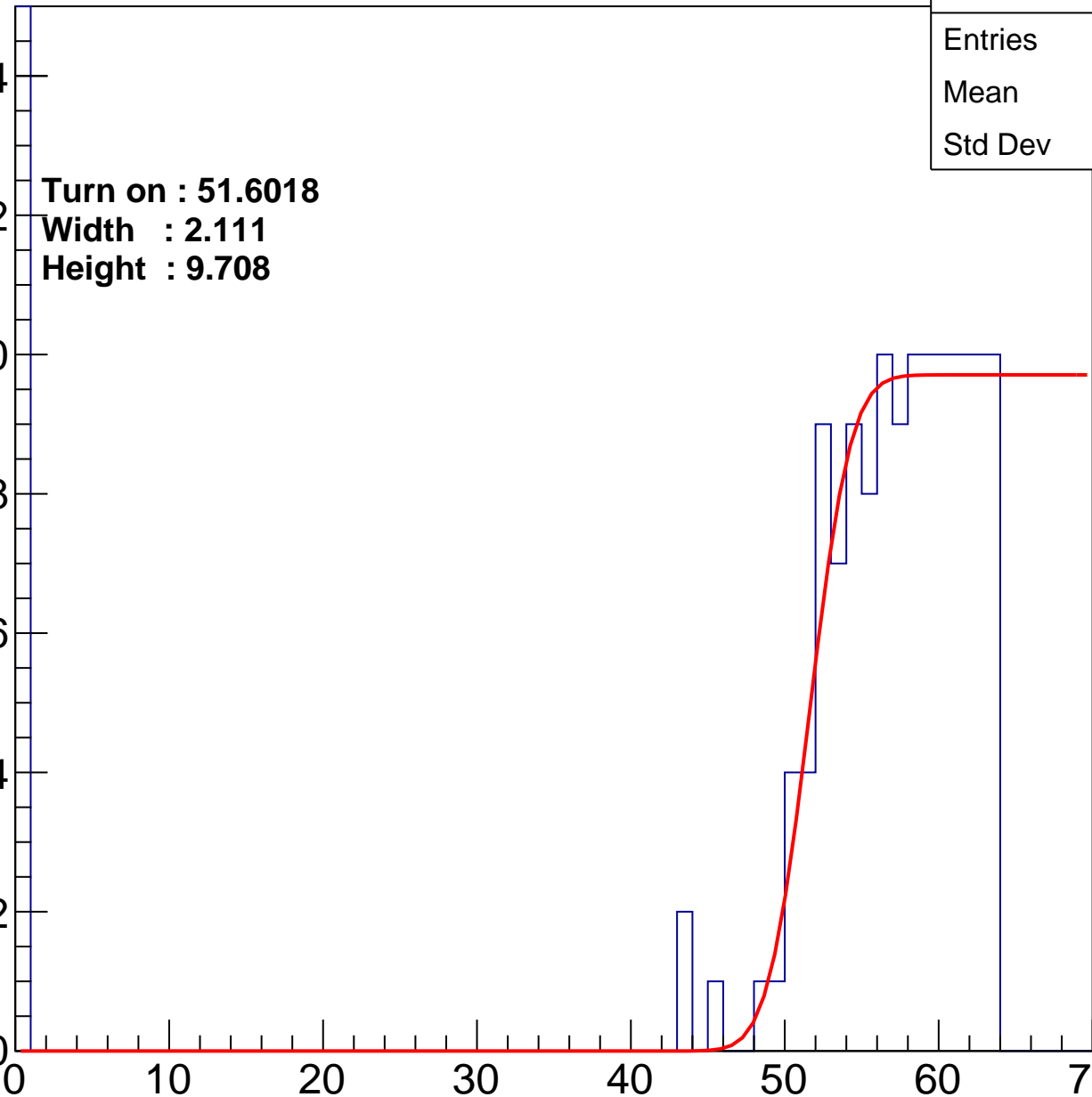
Entry

14
12
10
8
6
4
2
0

Turn on : 51.6018
Width : 2.111
Height : 9.708

Entries	174
Mean	40.8
Std Dev	25.81

ampl



B1L104S, U9-ch28

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	34.16
Std Dev	28

Turn on : 53.2505

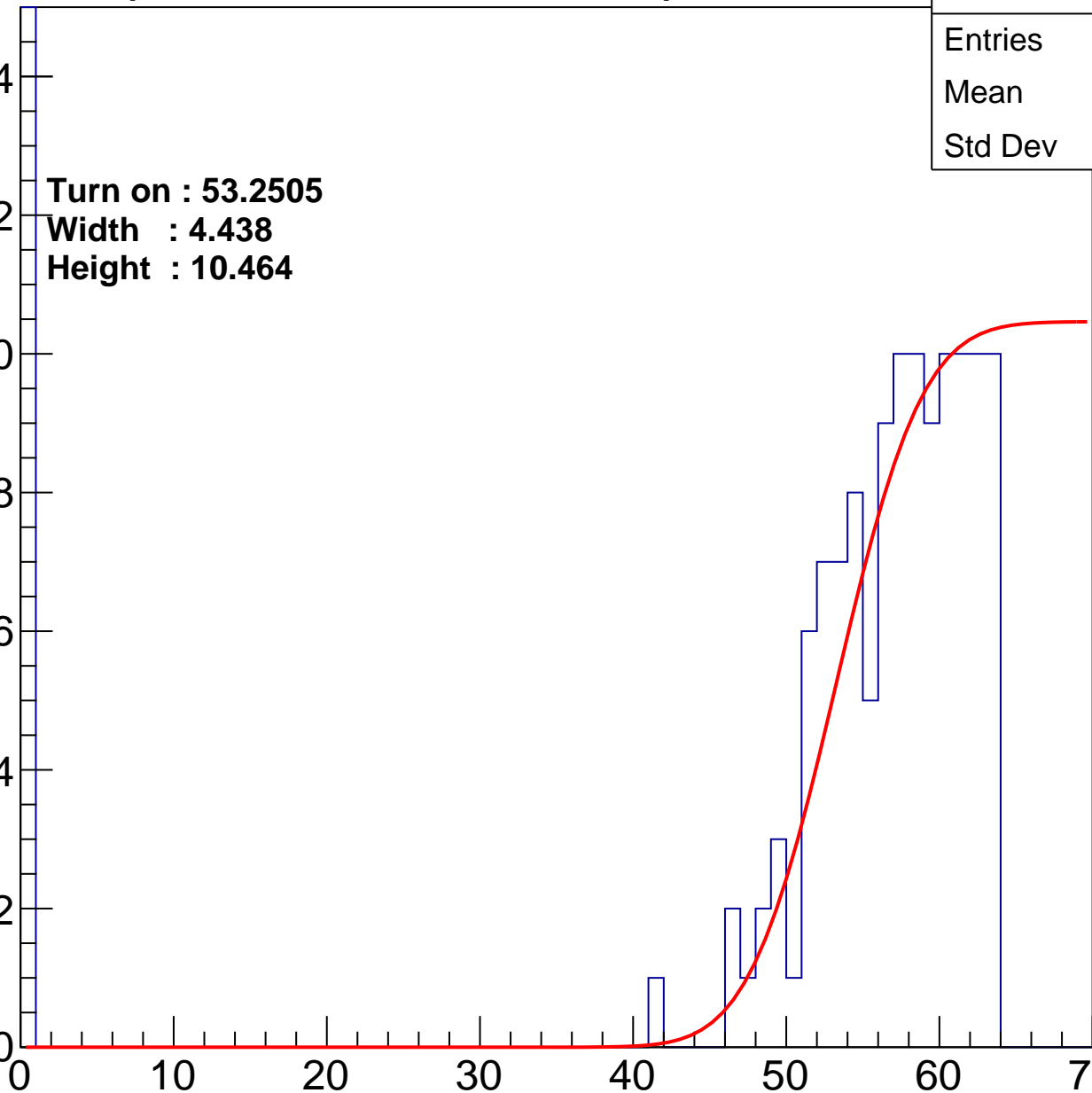
Width : 4.438

Height : 10.464

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch29

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	33.91
Std Dev	28.53

Turn on : 53.2345

Width : 2.949

Height : 9.851

Entry

14

12

10

8

6

4

2

0

0

10

20

30

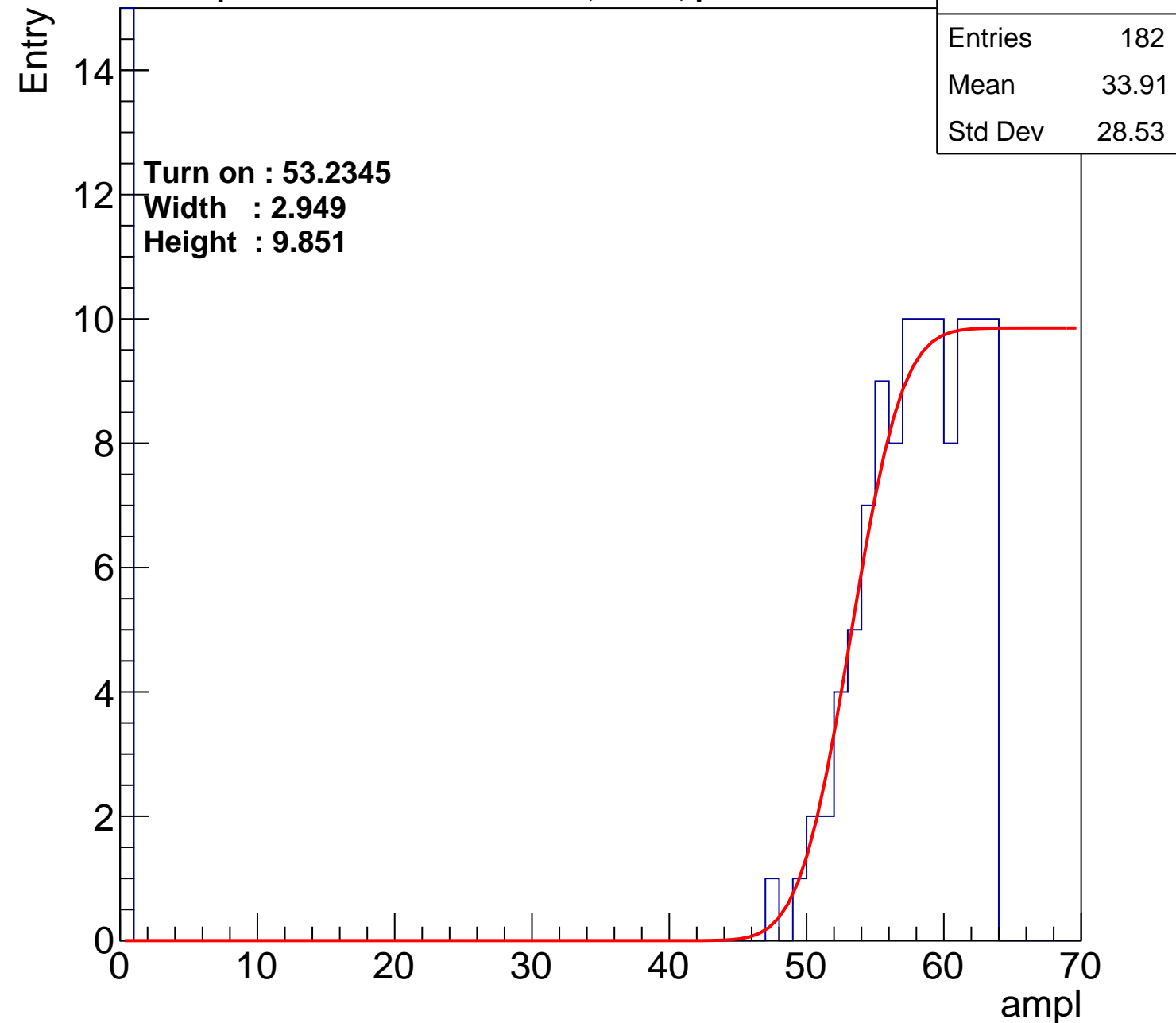
40

50

60

70

ampl



B1L104S, U9-ch30

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.84
Std Dev	28.85

Turn on : 54.0391

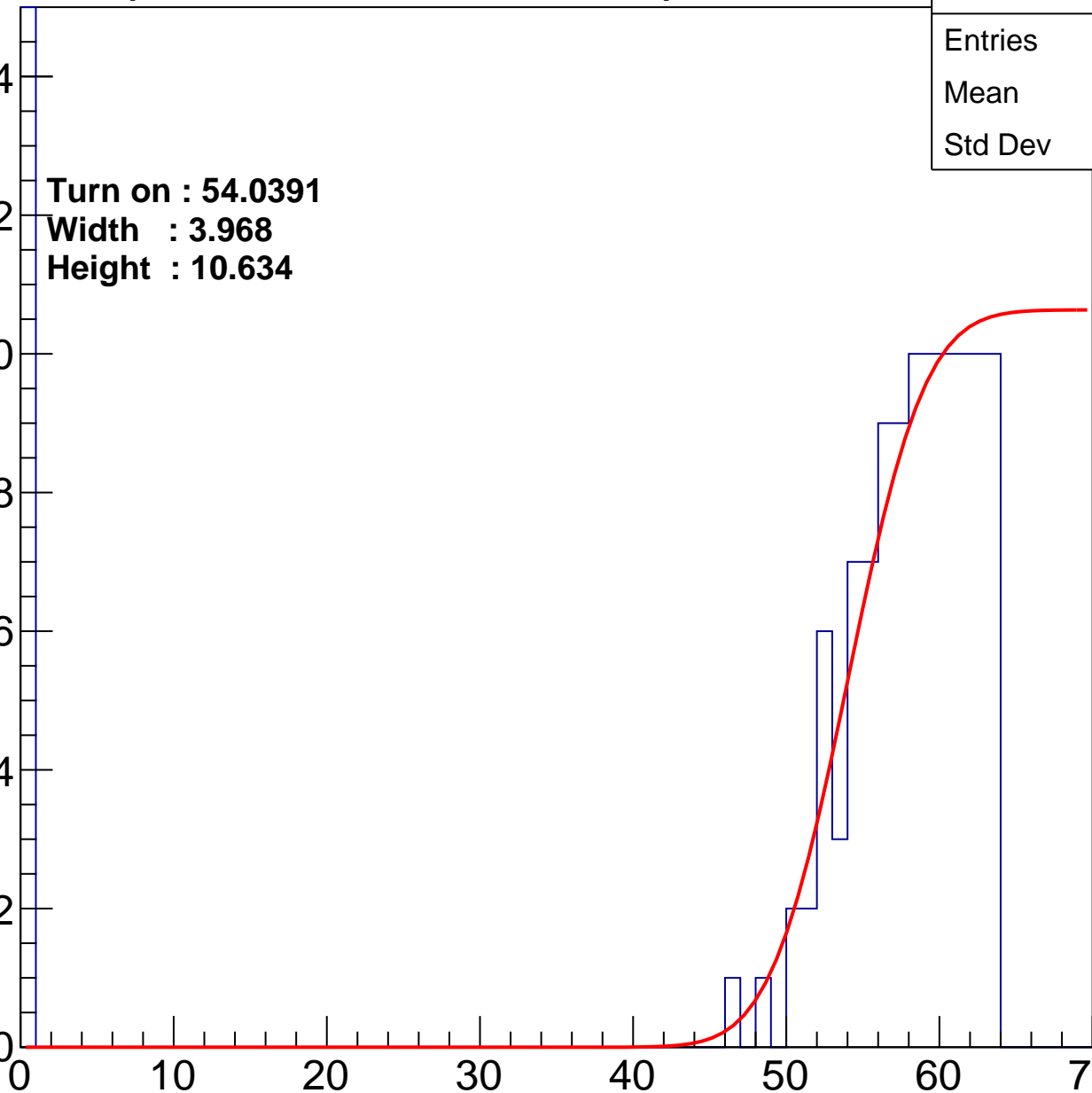
Width : 3.968

Height : 10.634

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch31

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	30.99
Std Dev	28.97

Turn on : 54.7487

Width : 3.019

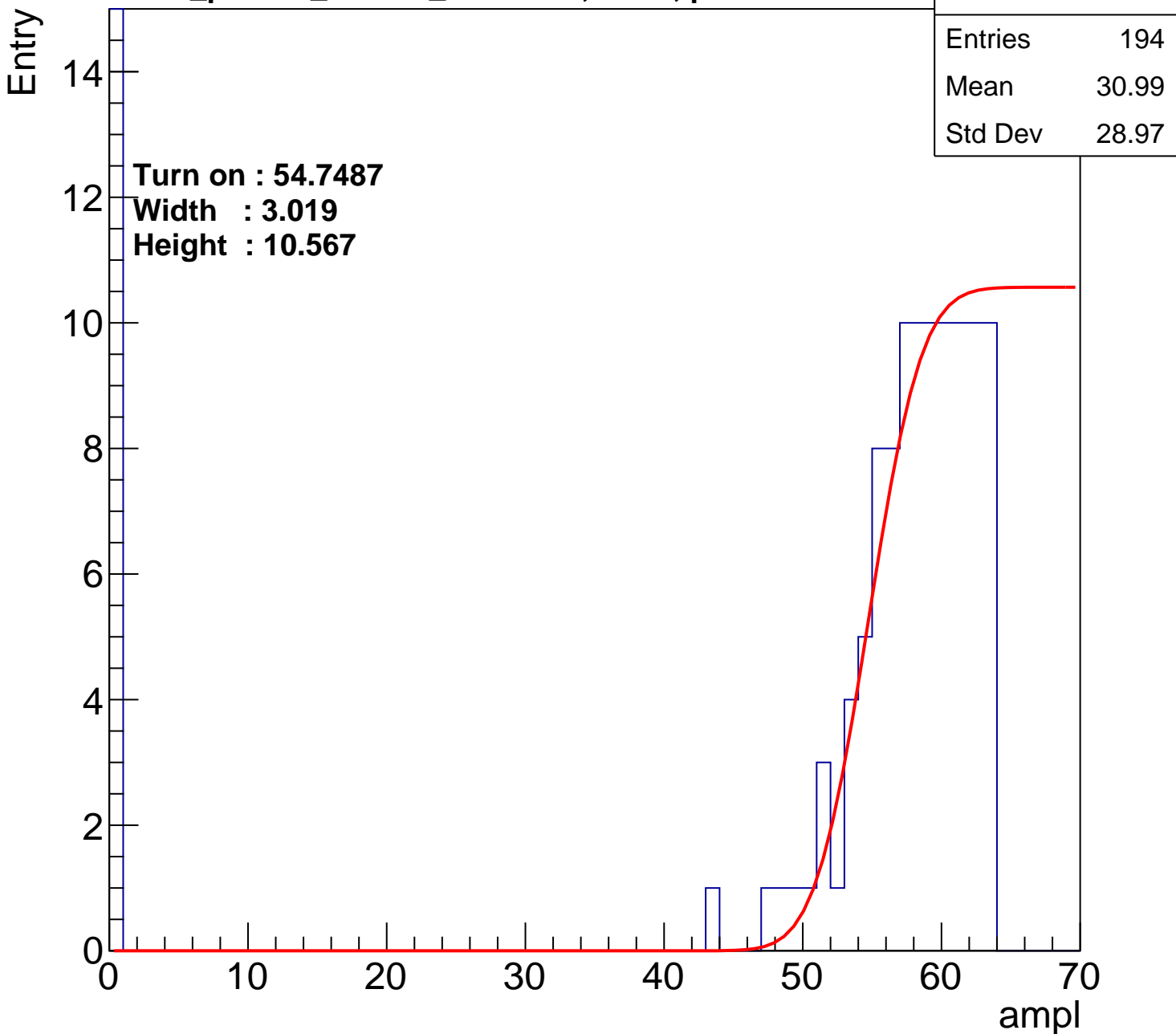
Height : 10.567

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch32

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	36.08
Std Dev	27.39

Turn on : 51.3977

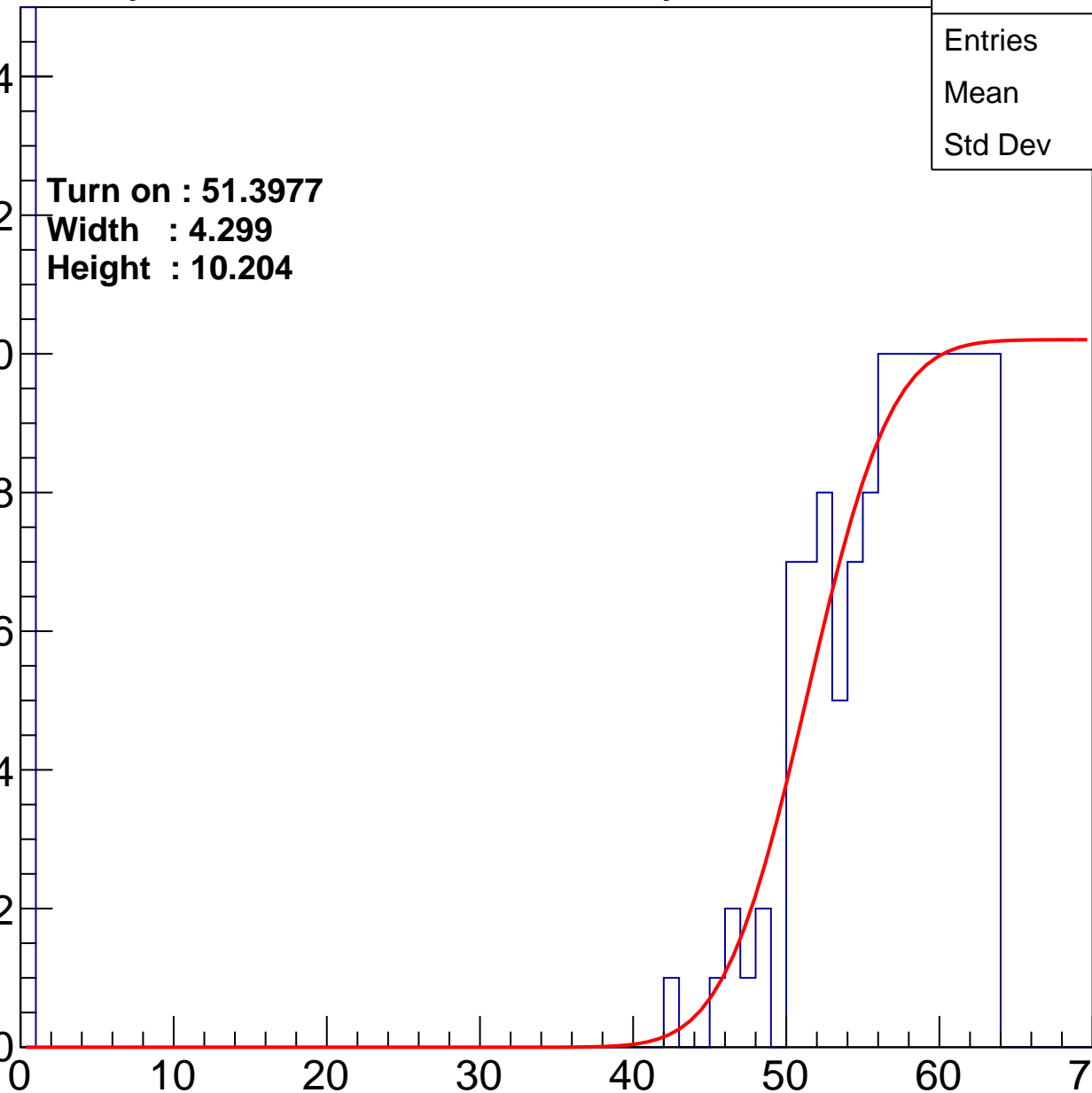
Width : 4.299

Height : 10.204

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch33

calib_packv5_033123_0516.root, FC#4, port A1

Entries	179
Mean	38.35
Std Dev	27.08

Turn on : 51.9147

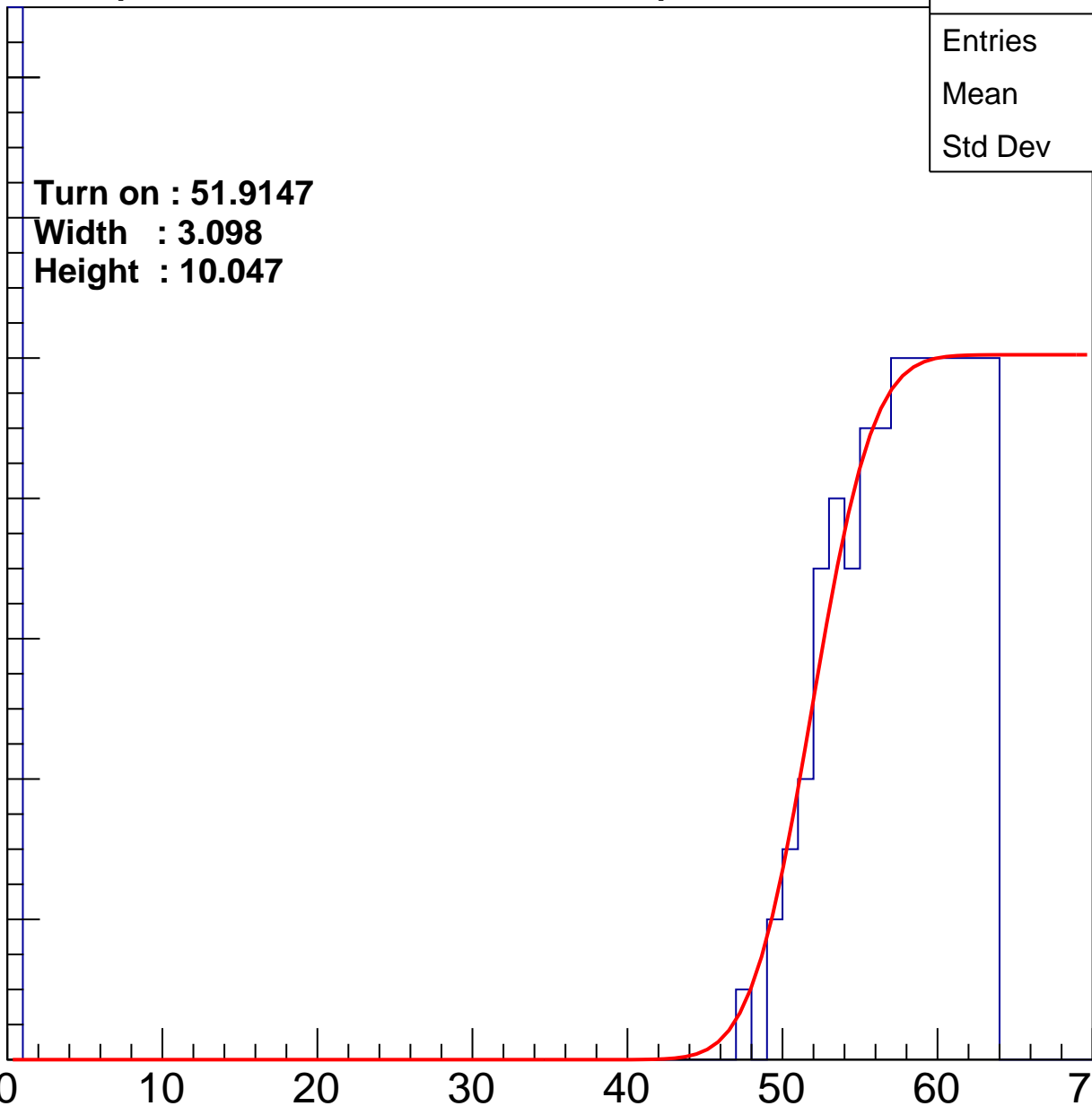
Width : 3.098

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch34

calib_packv5_033123_0516.root, FC#4, port A1

Entries	181
Mean	35.51
Std Dev	27.89

Turn on : 53.3303

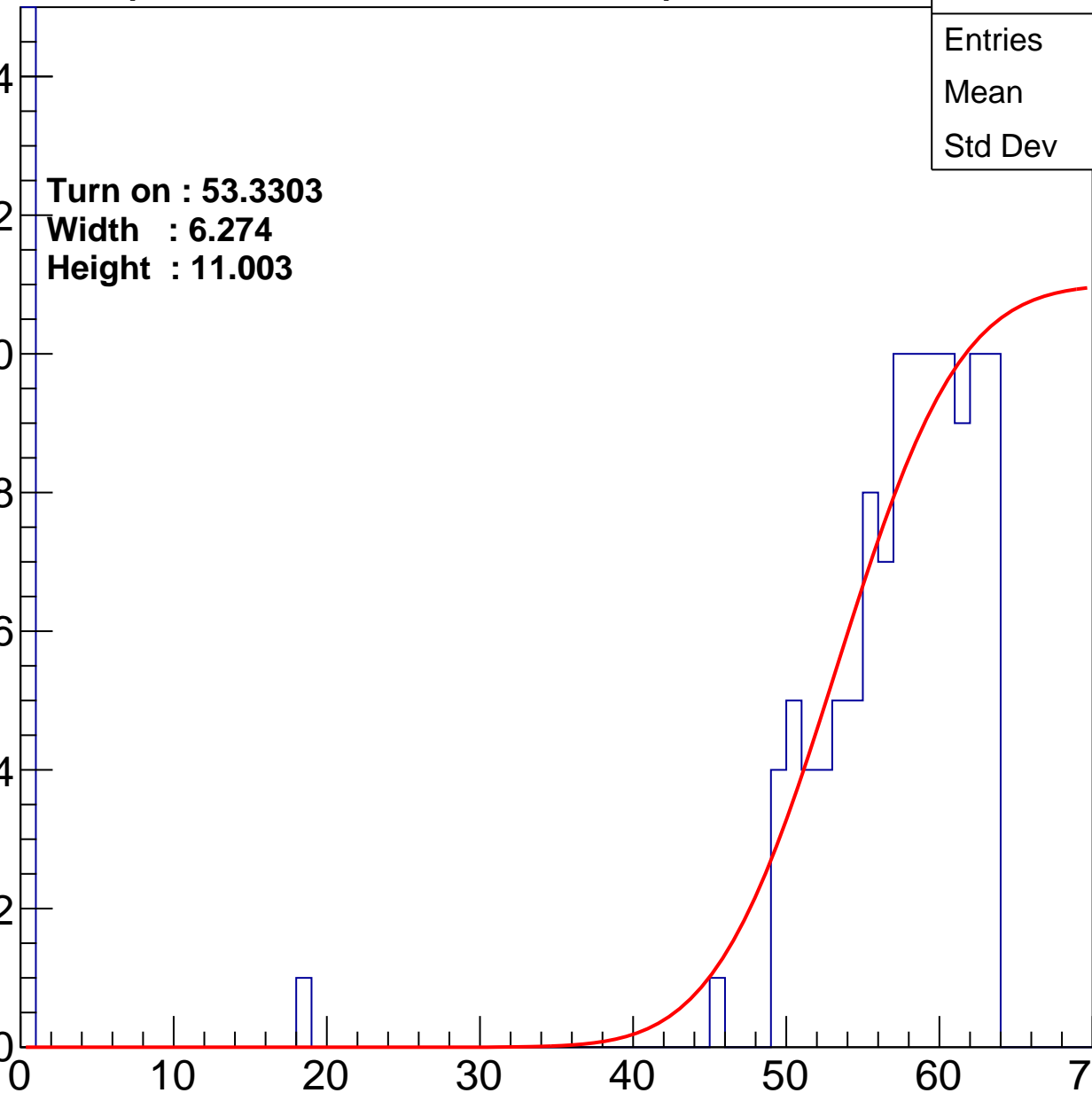
Width : 6.274

Height : 11.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch35

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	34.95
Std Dev	28.29

Turn on : 55.0153

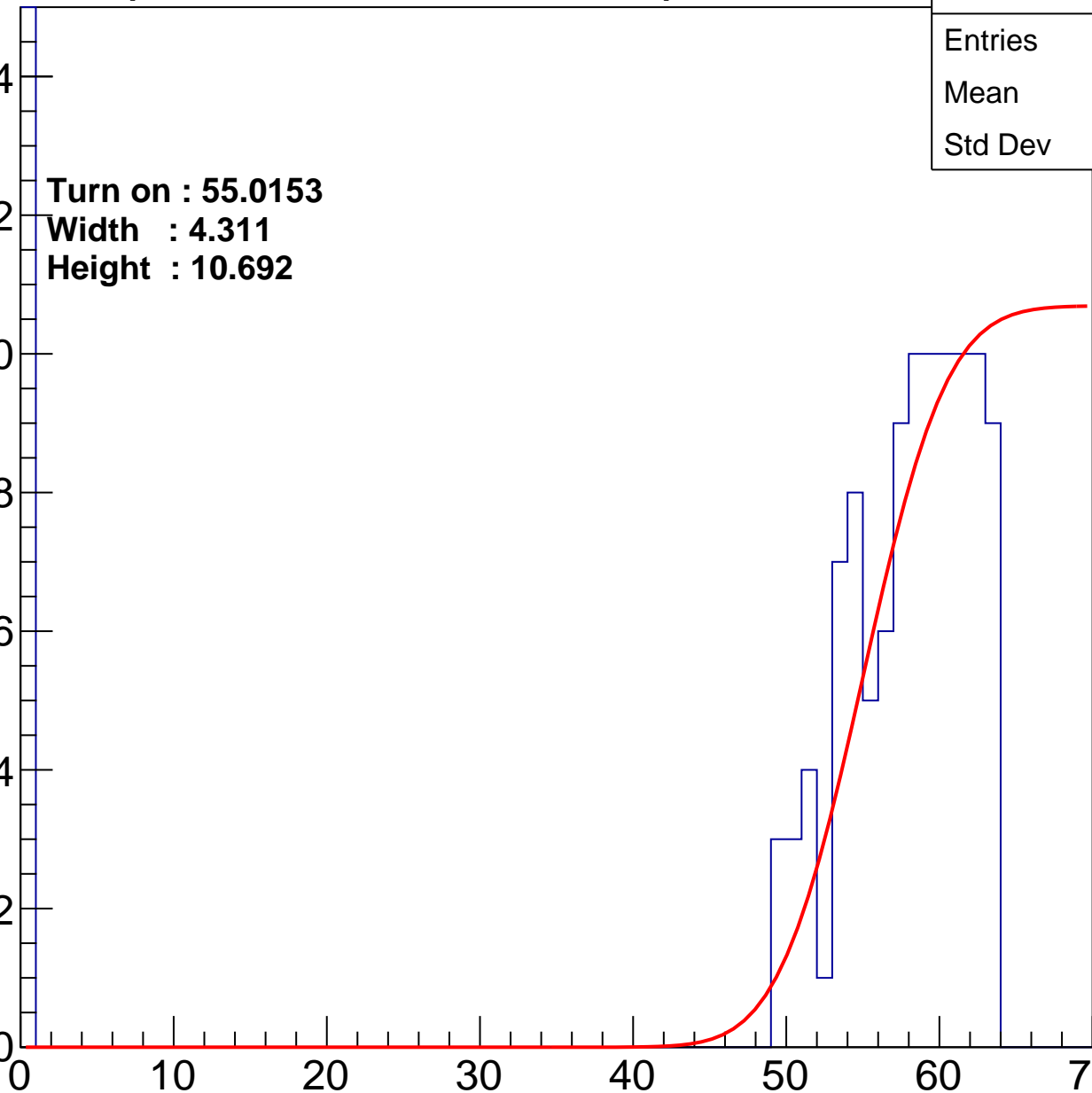
Width : 4.311

Height : 10.692

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch36

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	31.33
Std Dev	28.68

Turn on : 54.9957

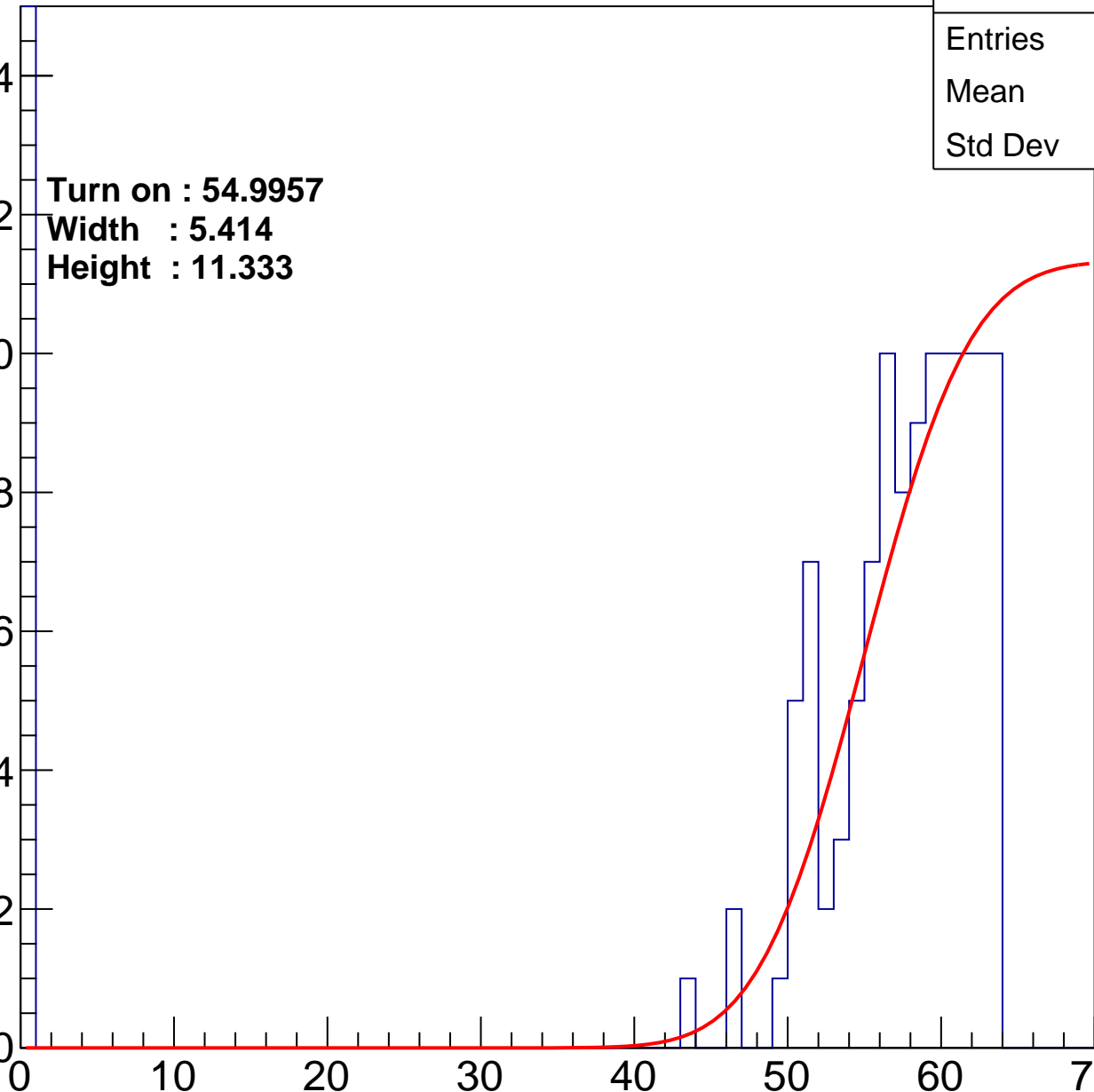
Width : 5.414

Height : 11.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch37

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	34.68
Std Dev	28.28

Turn on : 52.5943

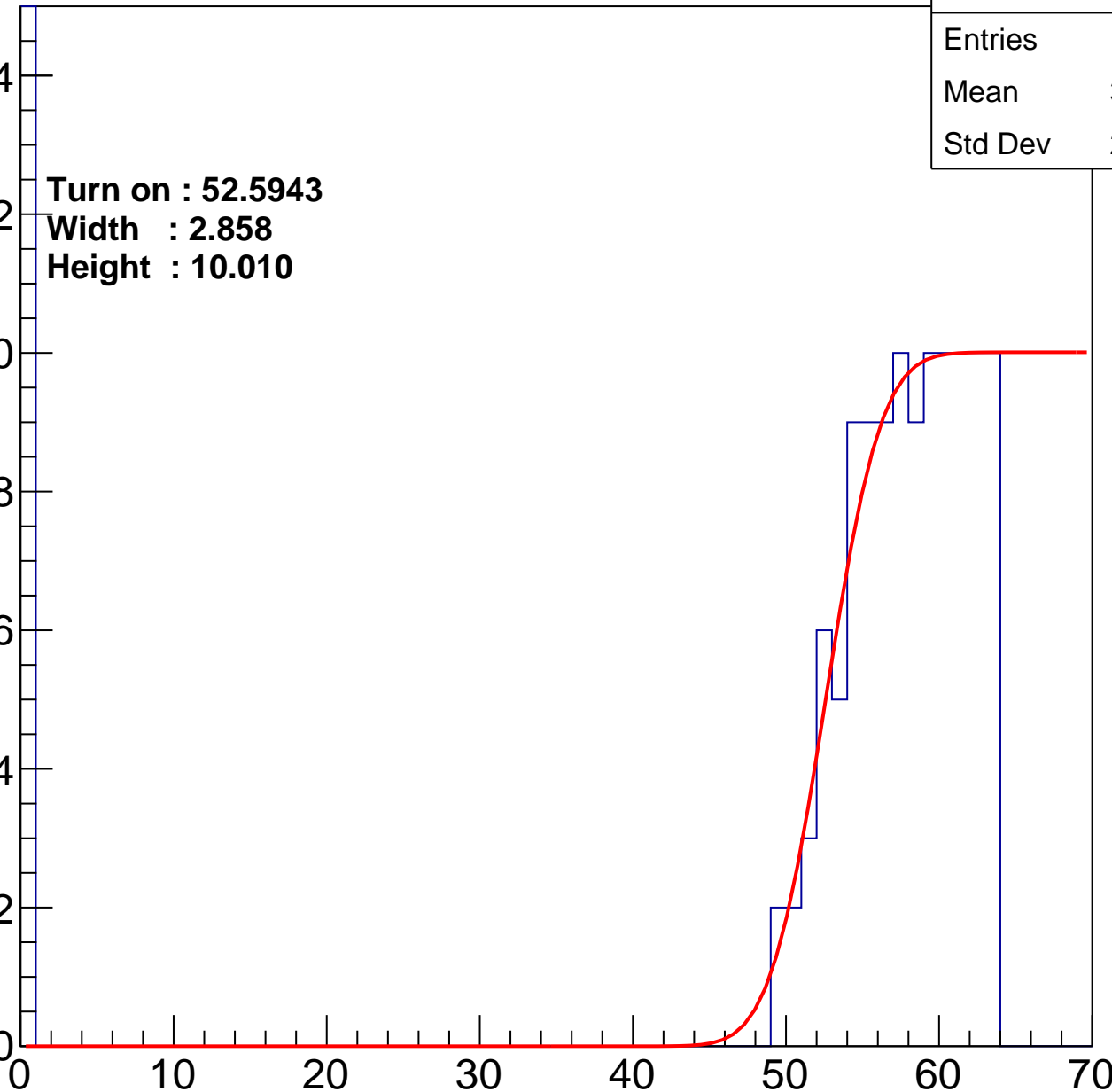
Width : 2.858

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch38

calib_packv5_033123_0516.root, FC#4, port A1

Entries	172
Mean	33.5
Std Dev	28.58

Turn on : 53.9801

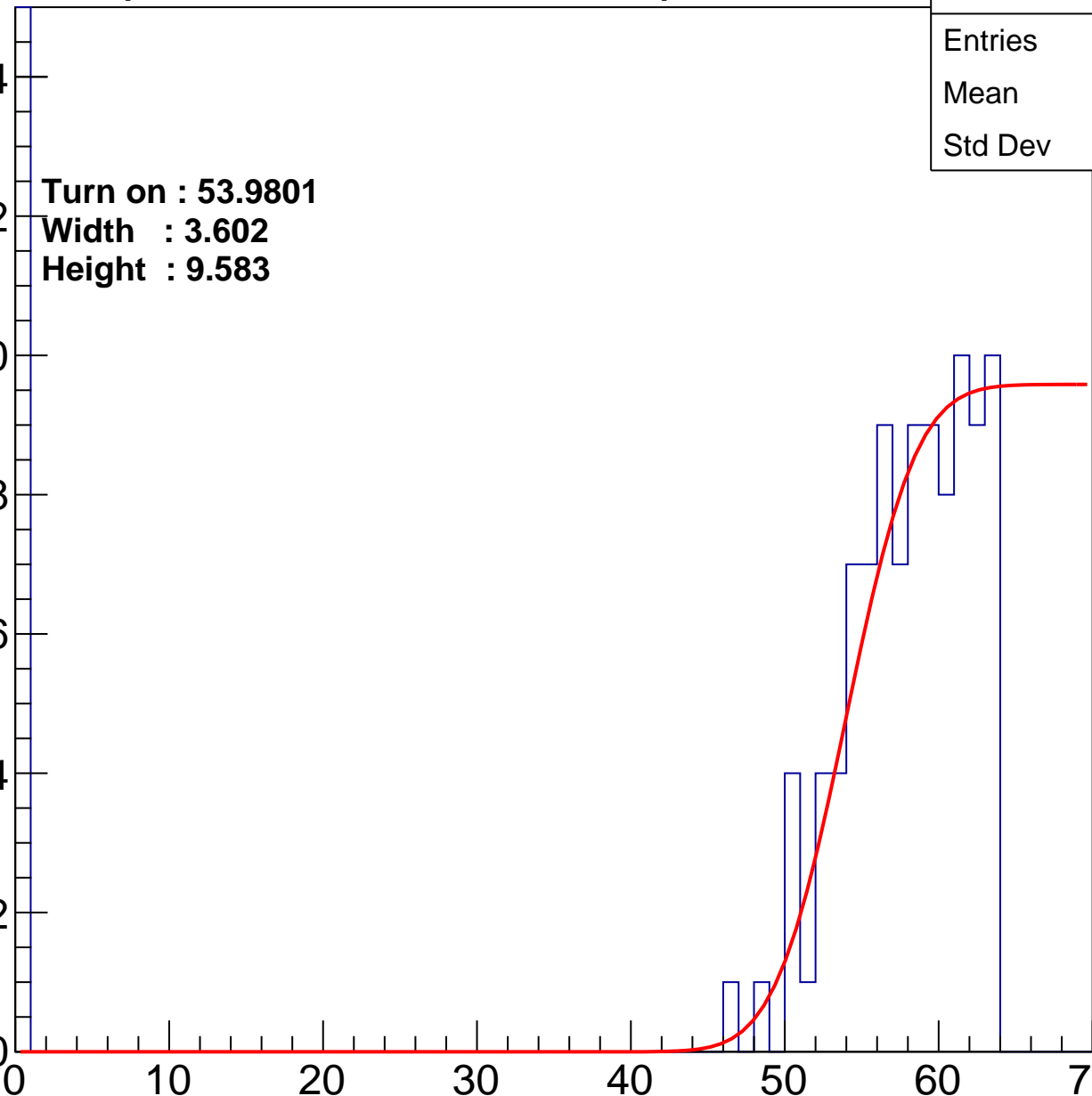
Width : 3.602

Height : 9.583

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch39

calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	34.66
Std Dev	28.38

Turn on : 53.6002

Width : 3.329

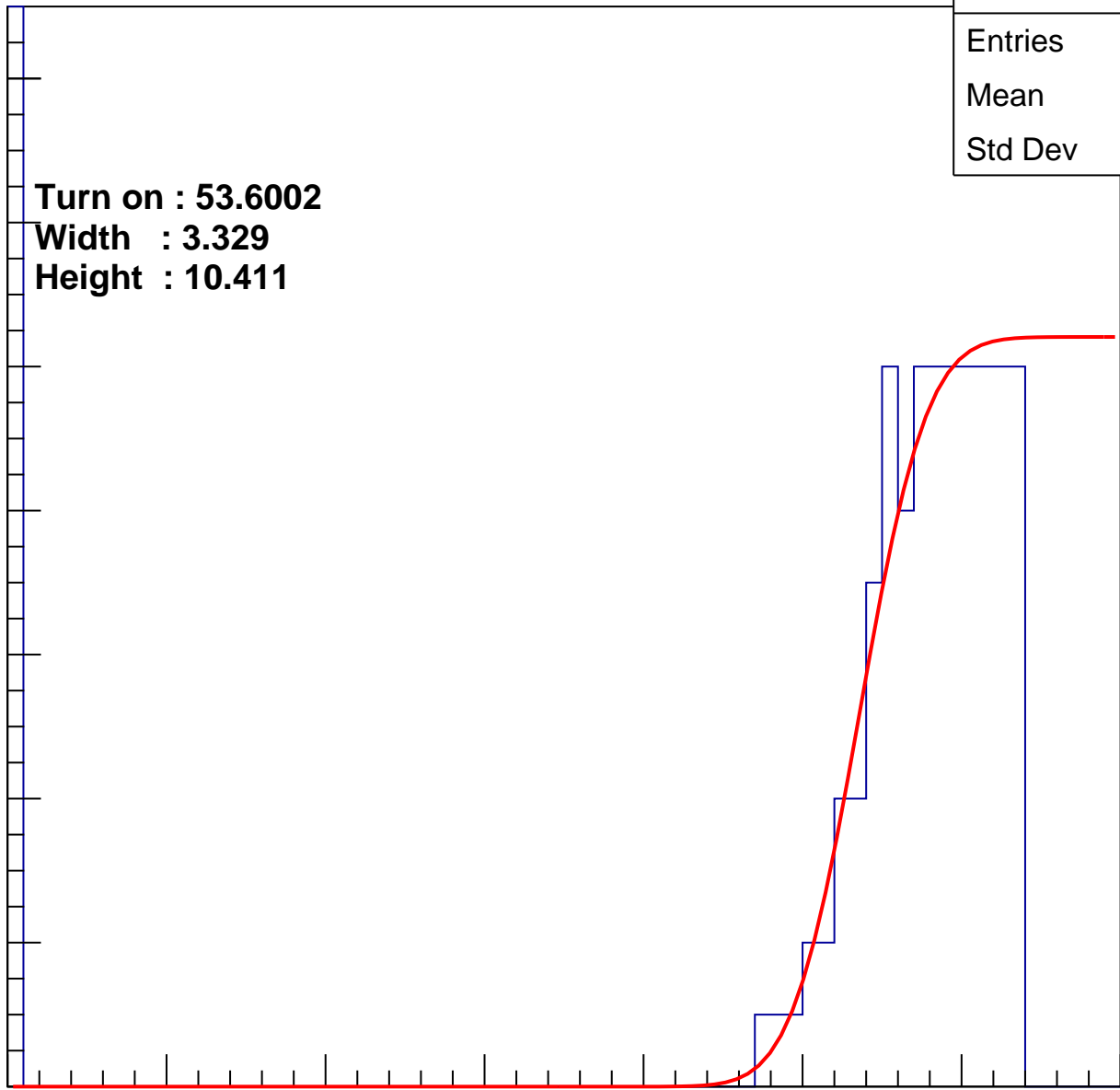
Height : 10.411

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch40

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	29.13
Std Dev	28.56

Turn on : 52.8897

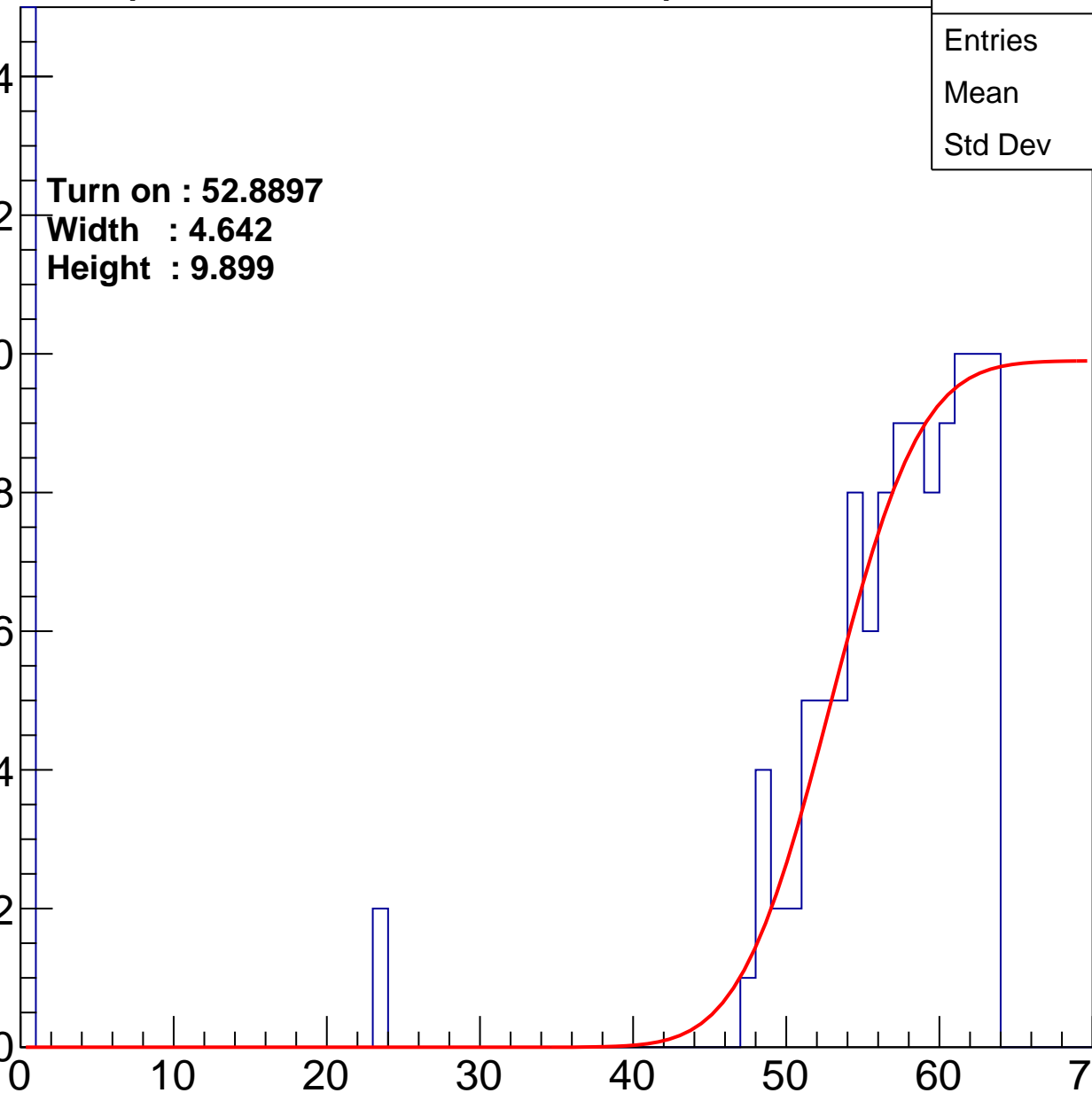
Width : 4.642

Height : 9.899

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch41

calib_packv5_033123_0516.root, FC#4, port A1

Entries	168
Mean	34.54
Std Dev	28.62

Turn on : 53.6477

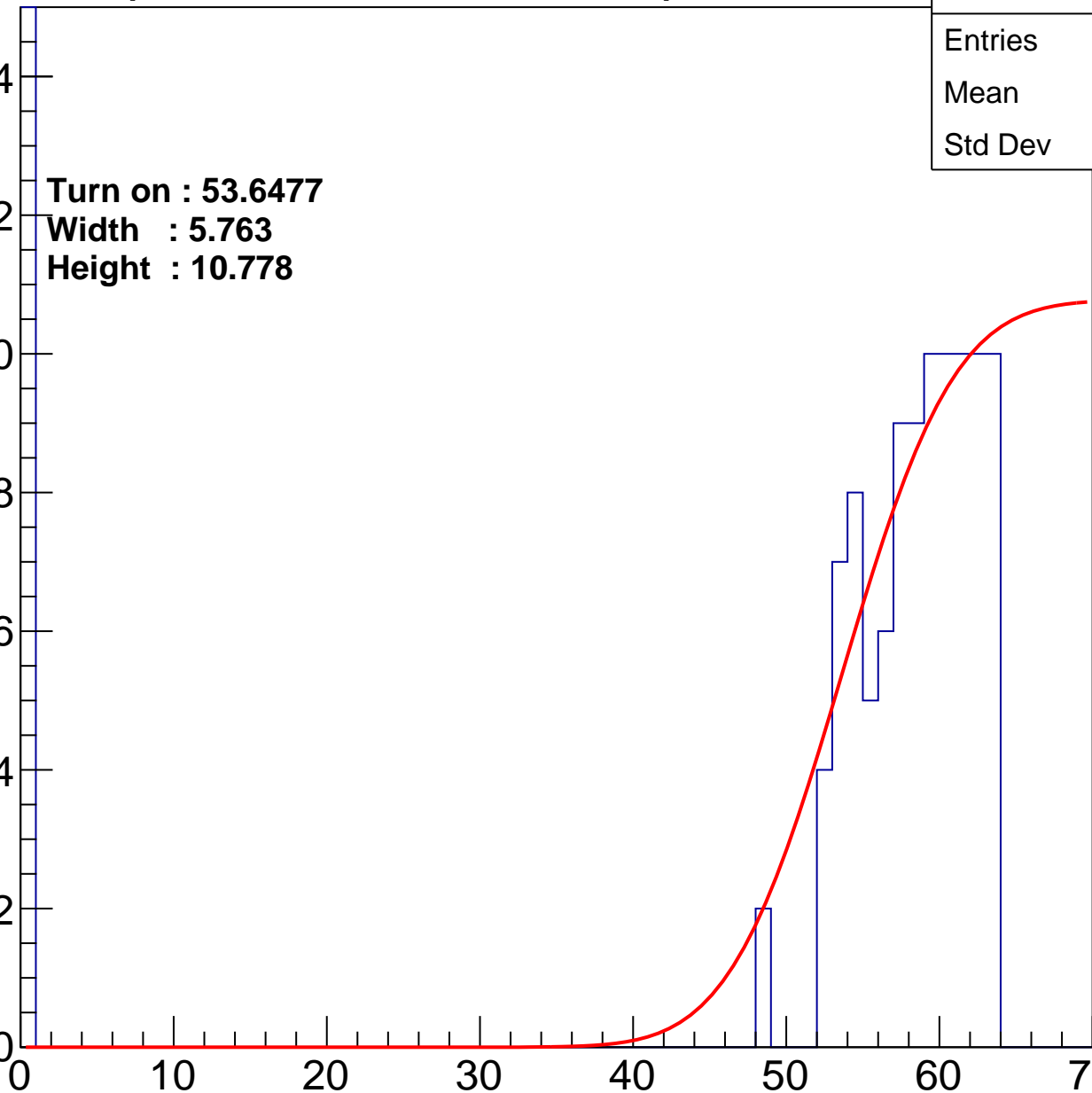
Width : 5.763

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch42

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.45
Std Dev	28.81

Turn on : 53.7507

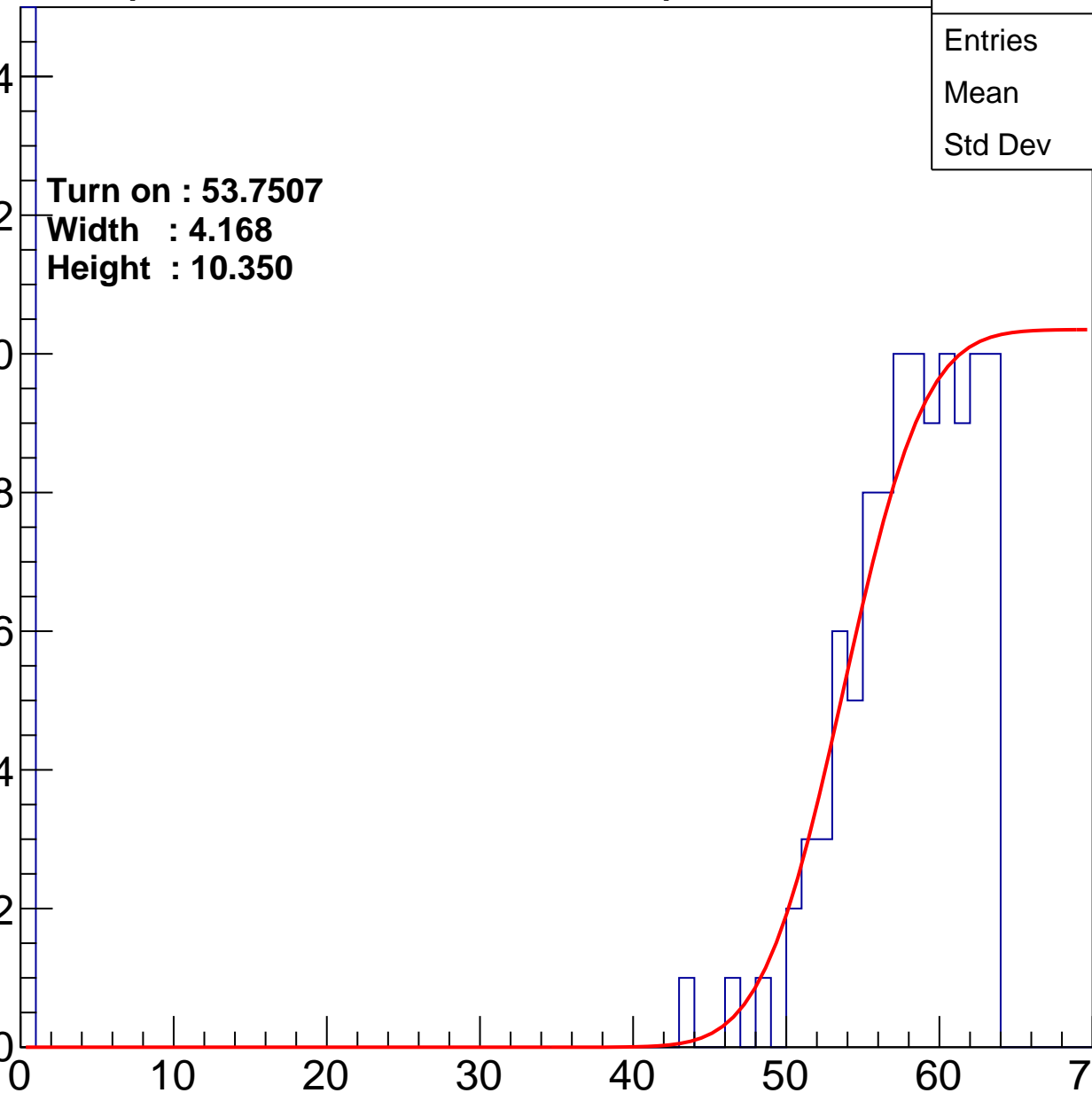
Width : 4.168

Height : 10.350

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch43

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	37.88
Std Dev	27.11

Turn on : 51.9972

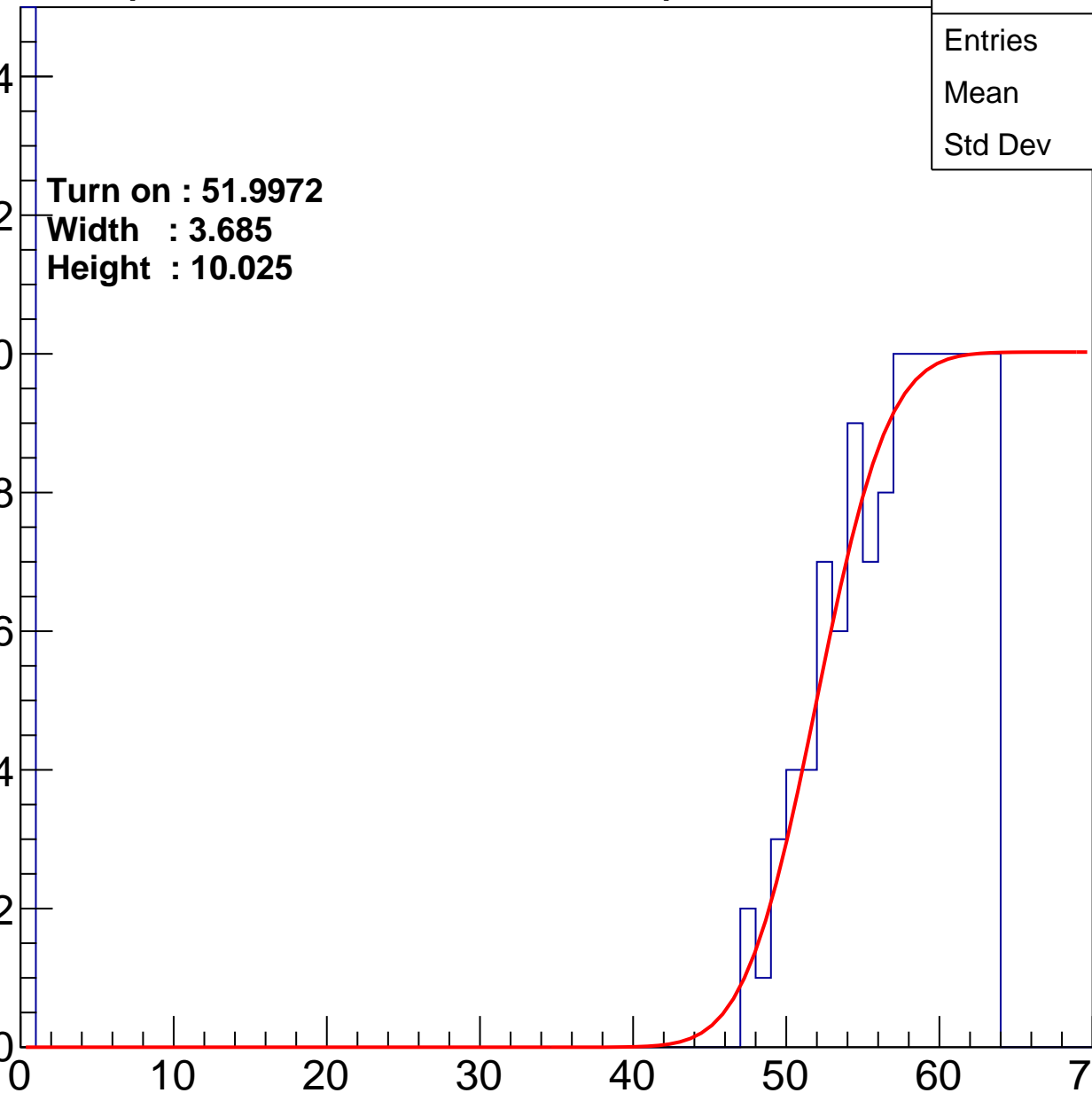
Width : 3.685

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch44

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	32.95
Std Dev	28.11

Turn on : 52.6142

Width : 4.529

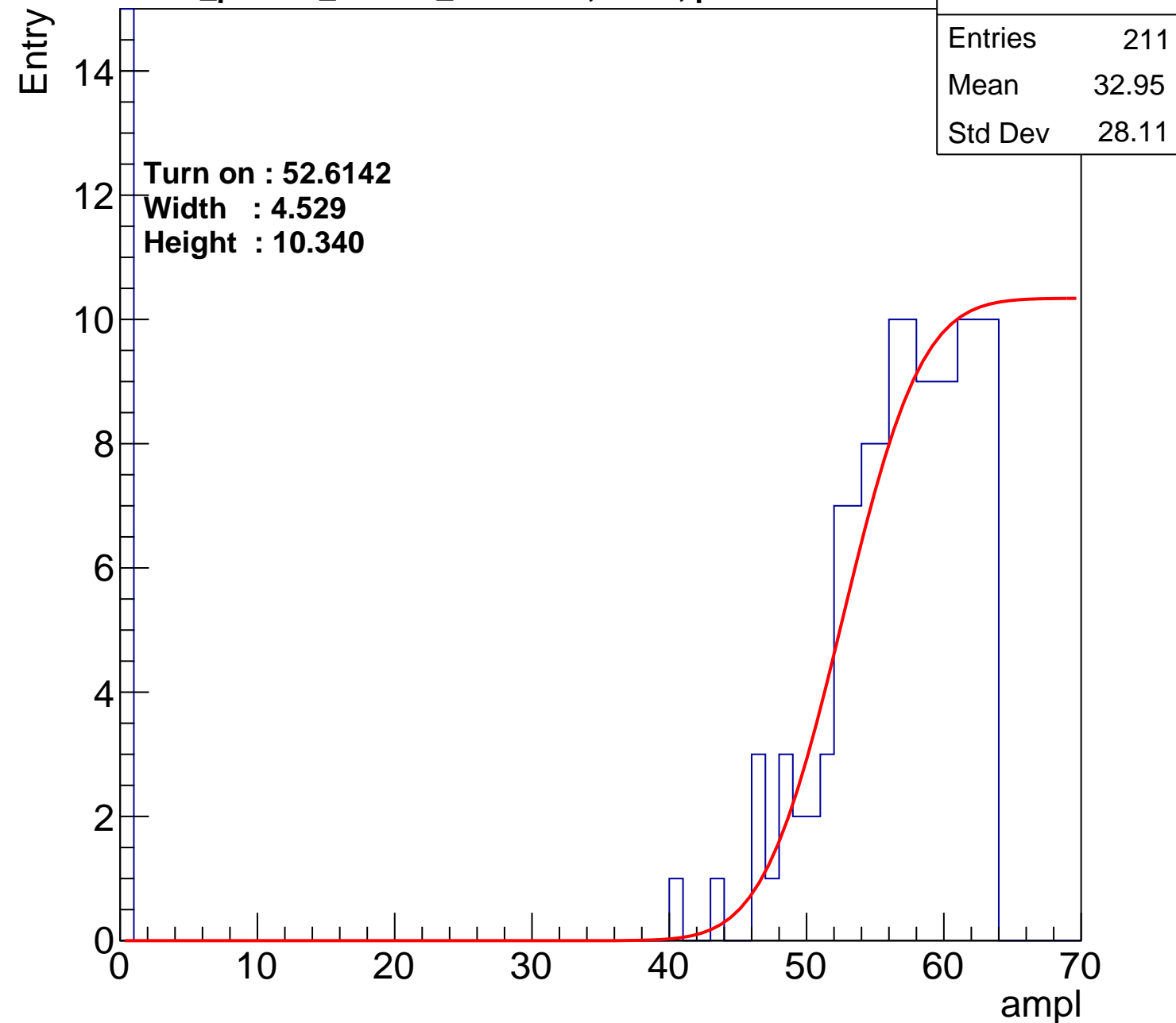
Height : 10.340

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch45

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	35.15
Std Dev	27.73

Turn on : 51.7886

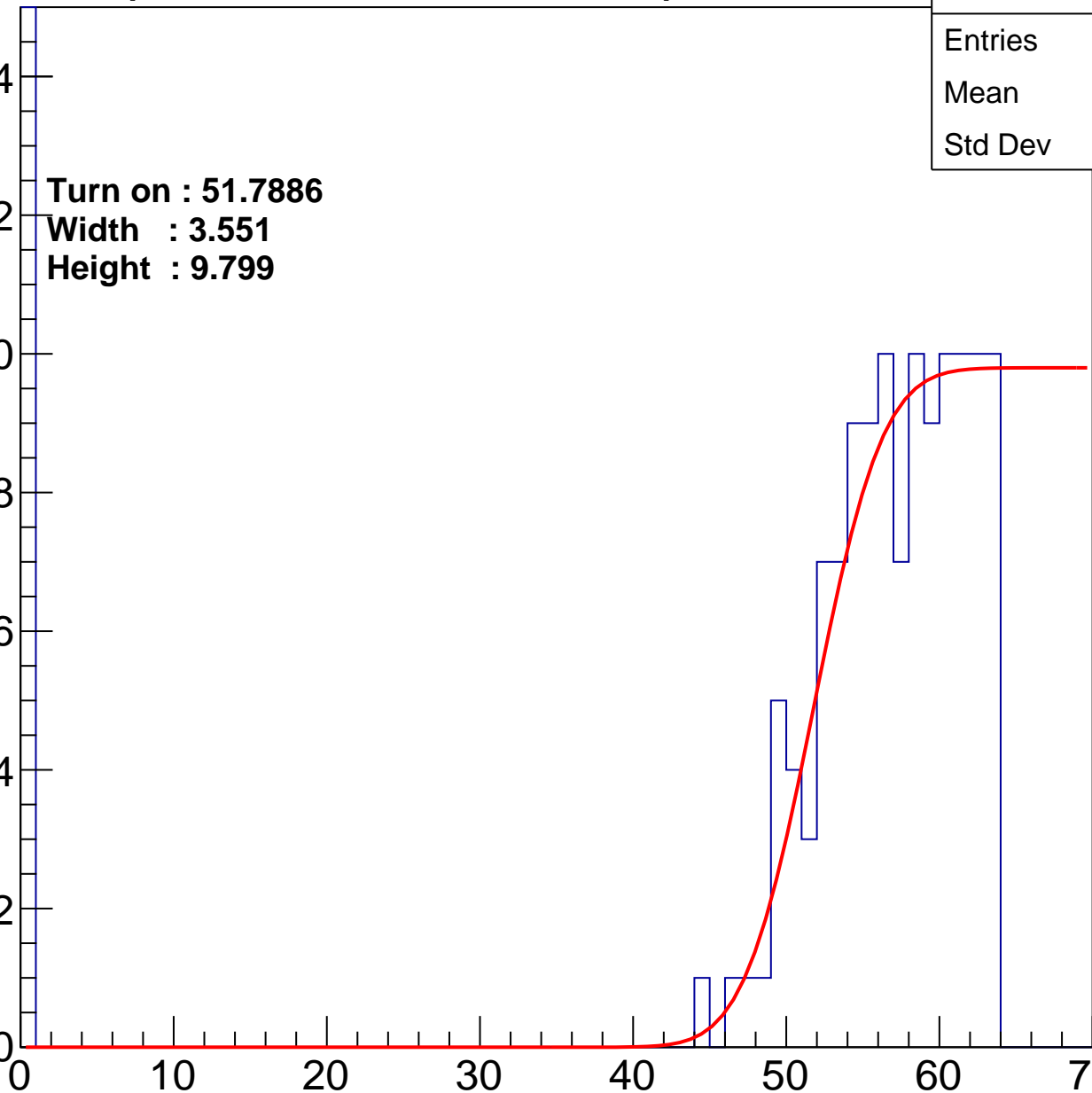
Width : 3.551

Height : 9.799

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch46

calib_packv5_033123_0516.root, FC#4, port A1

Entries	203
Mean	30.32
Std Dev	28.72

Turn on : 54.5470

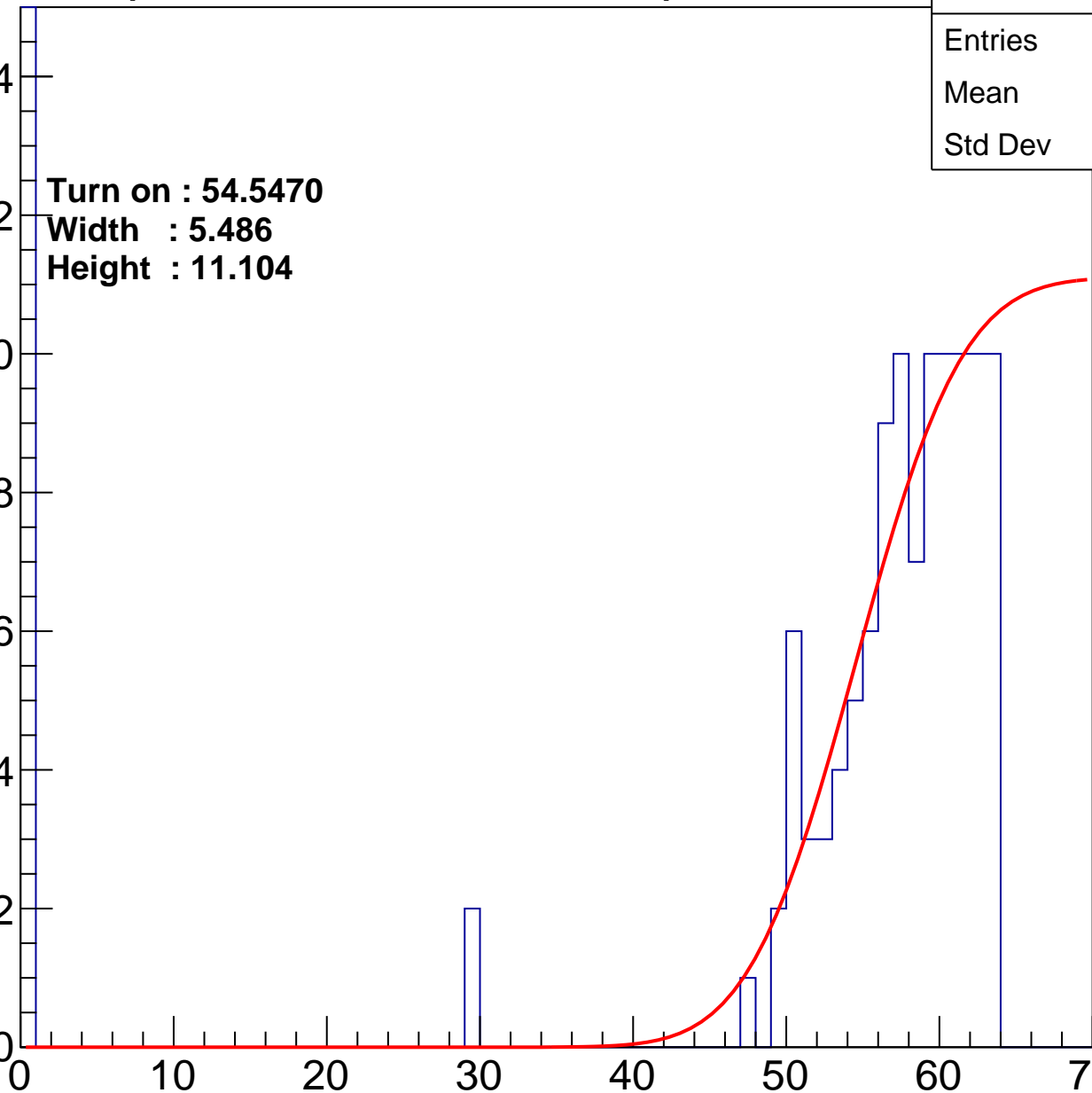
Width : 5.486

Height : 11.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch47

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	35.51
Std Dev	28.13

Turn on : 52.7796

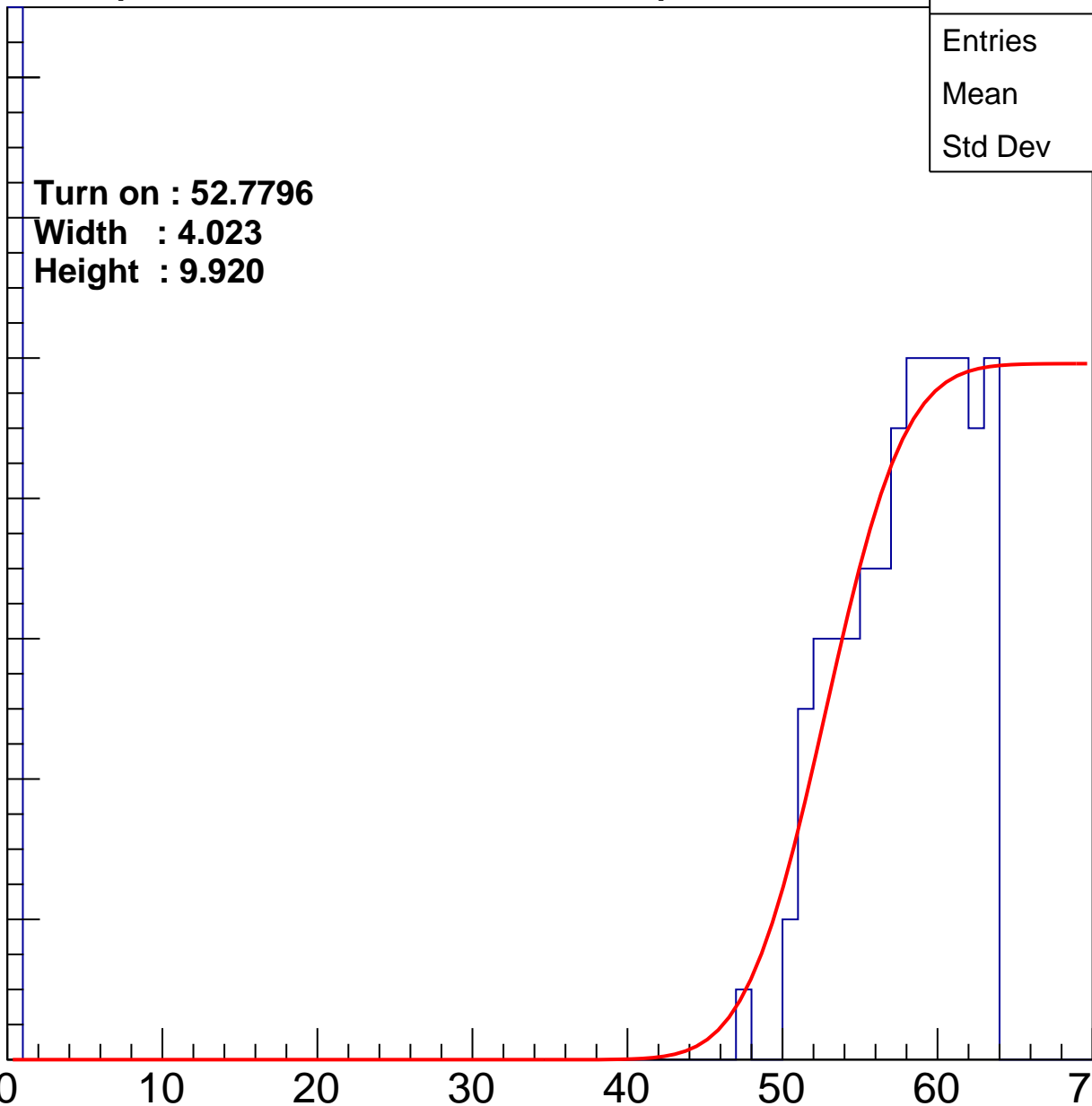
Width : 4.023

Height : 9.920

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch48

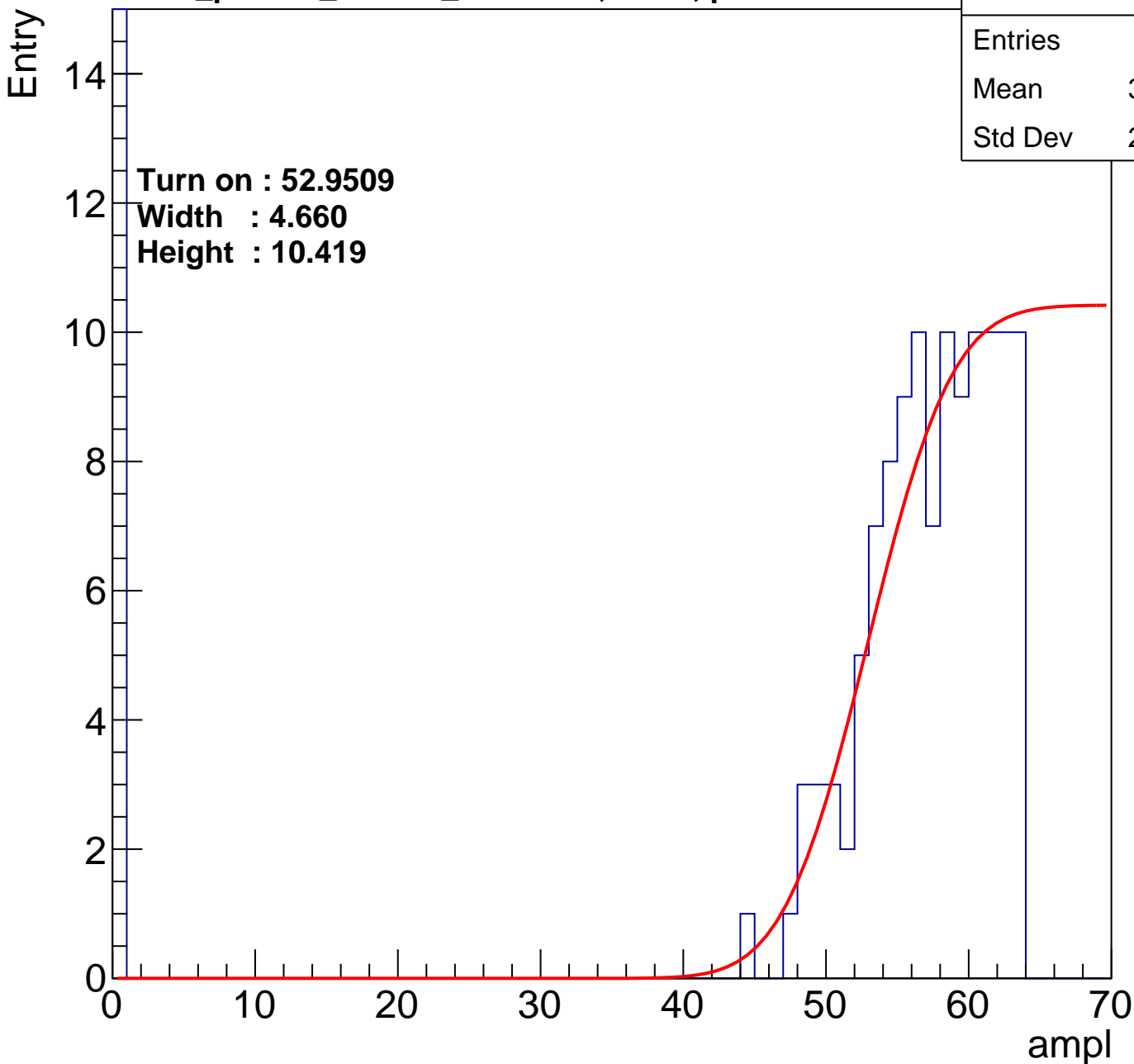
calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	32.16
Std Dev	28.43

Turn on : 52.9509

Width : 4.660

Height : 10.419



B1L104S, U9-ch49

calib_packv5_033123_0516.root, FC#4, port A1

Entries	167
Mean	37.2
Std Dev	27.69

Turn on : 54.1970

Width : 3.606

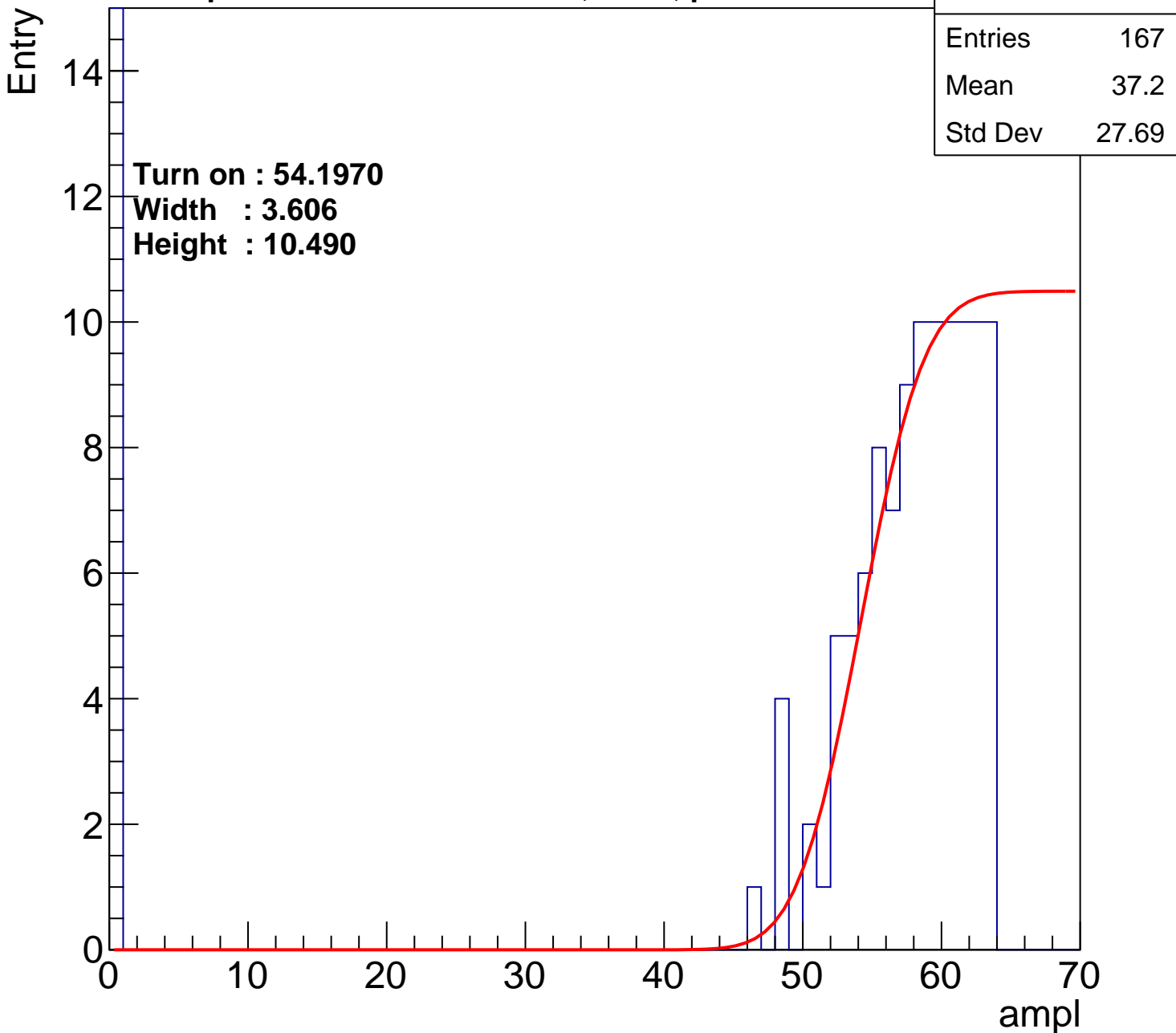
Height : 10.490

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch50

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	33.31
Std Dev	27.94

Turn on : 51.2093

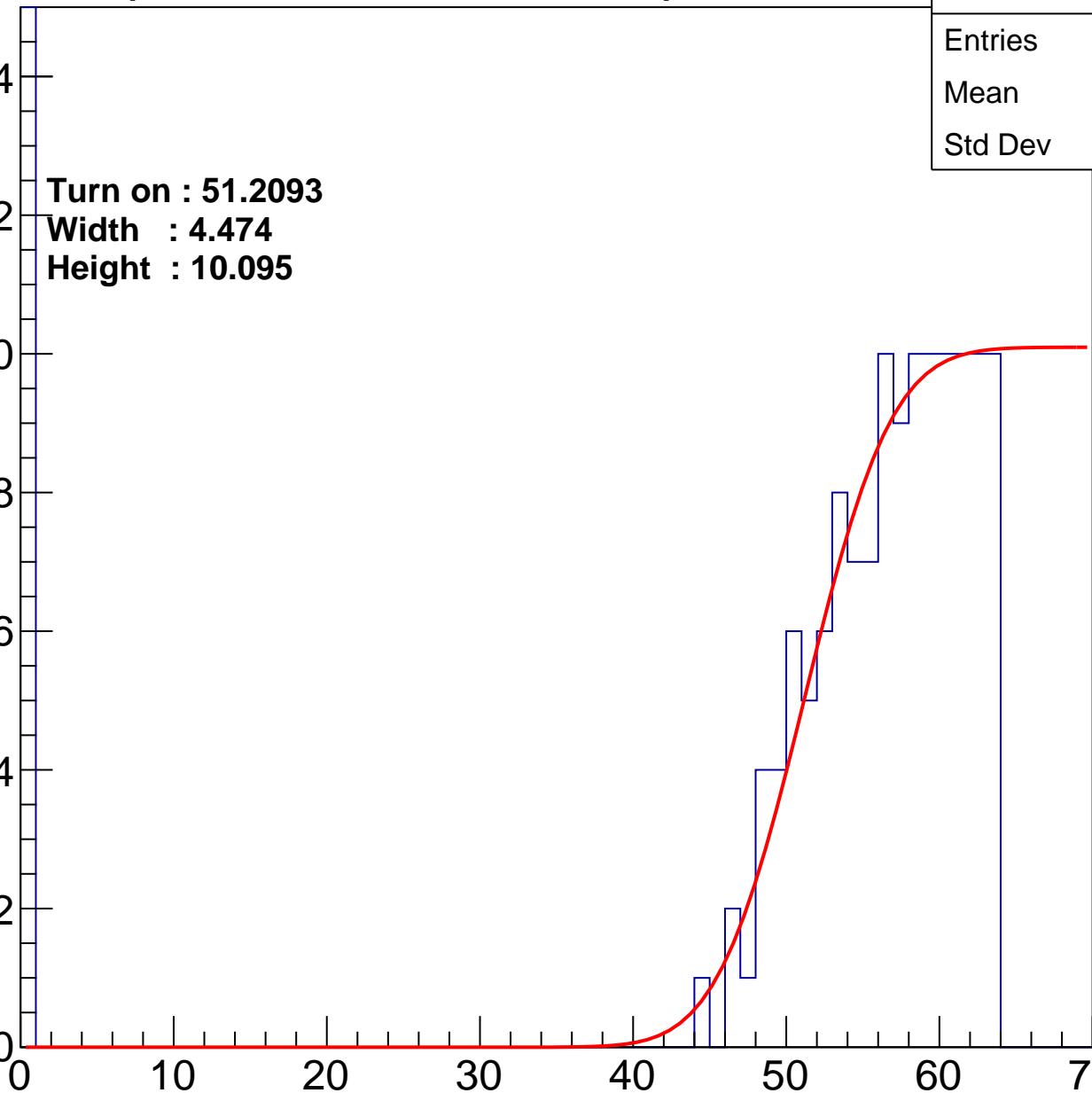
Width : 4.474

Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch51

calib_packv5_033123_0516.root, FC#4, port A1

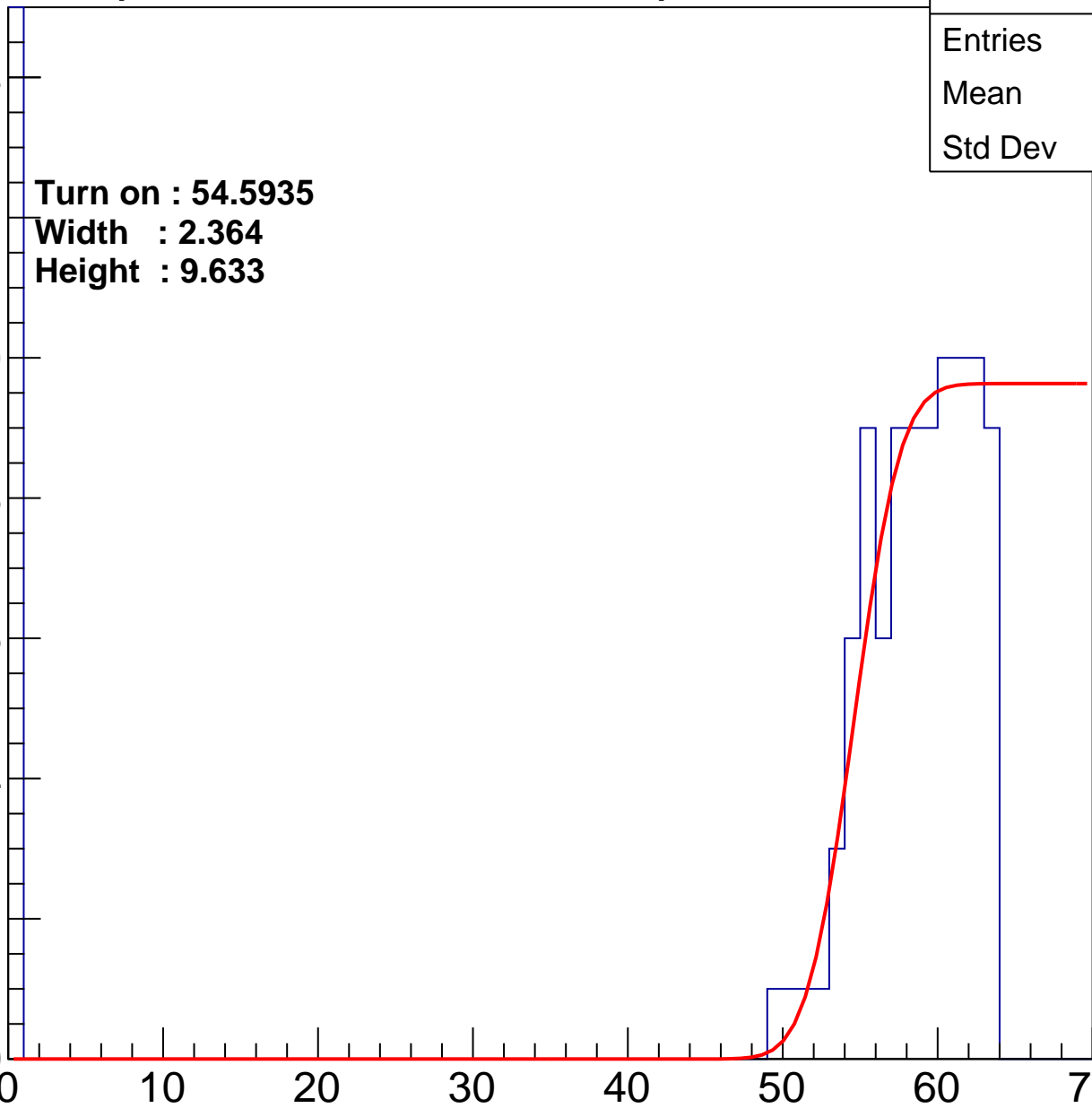
Entry

14
12
10
8
6
4
2
0

Turn on : 54.5935
Width : 2.364
Height : 9.633

Entries	191
Mean	28.69
Std Dev	29.23

ampl



B1L104S, U9-ch52

calib_packv5_033123_0516.root, FC#4, port A1

Entries	233
Mean	31.82
Std Dev	28.07

Turn on : 51.1066

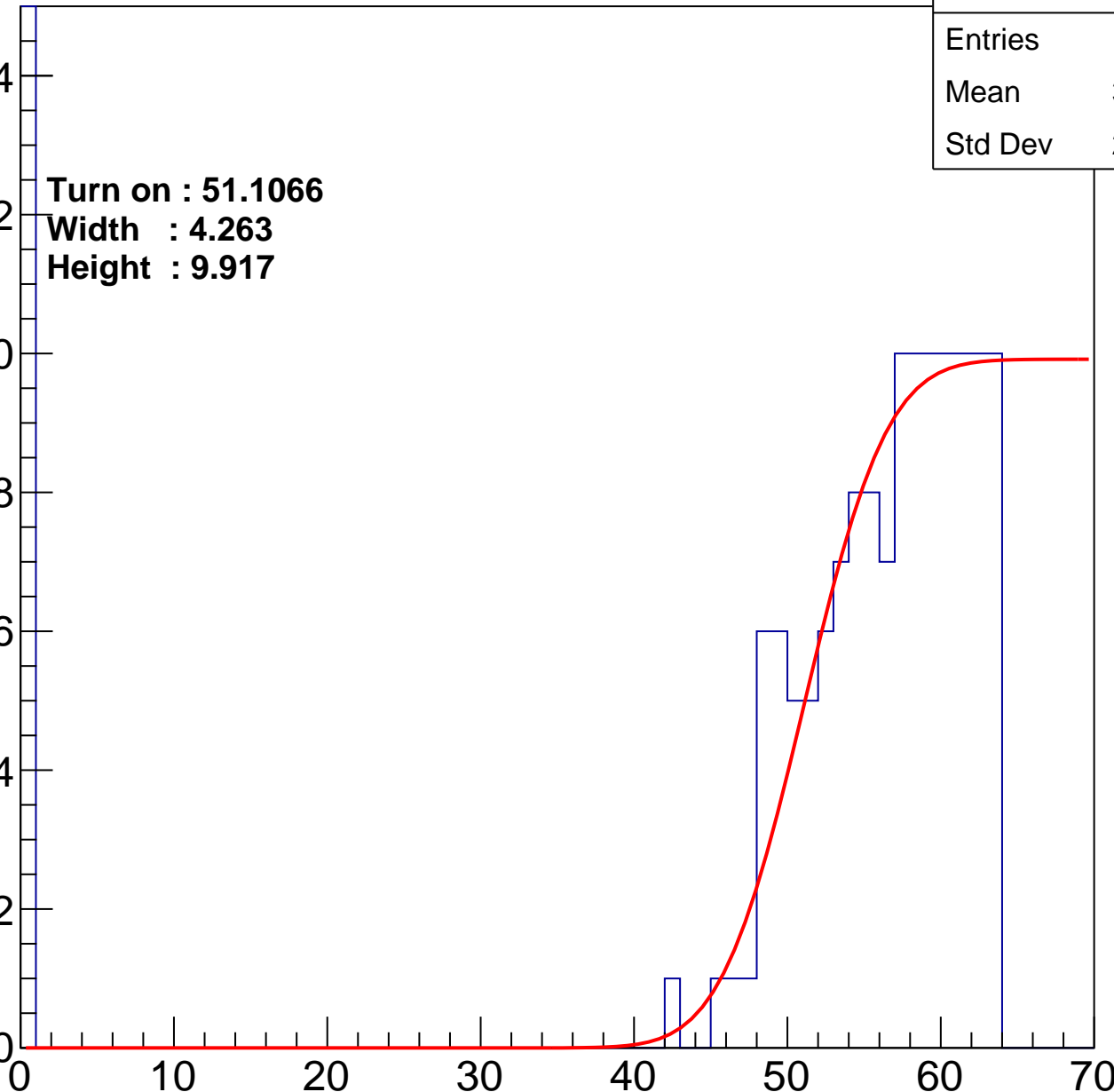
Width : 4.263

Height : 9.917

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch53

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	30.97
Std Dev	29.1

Turn on : 54.3510

Width : 4.150

Height : 10.329

Entry

14

12

10

8

6

4

2

0

0

10

20

30

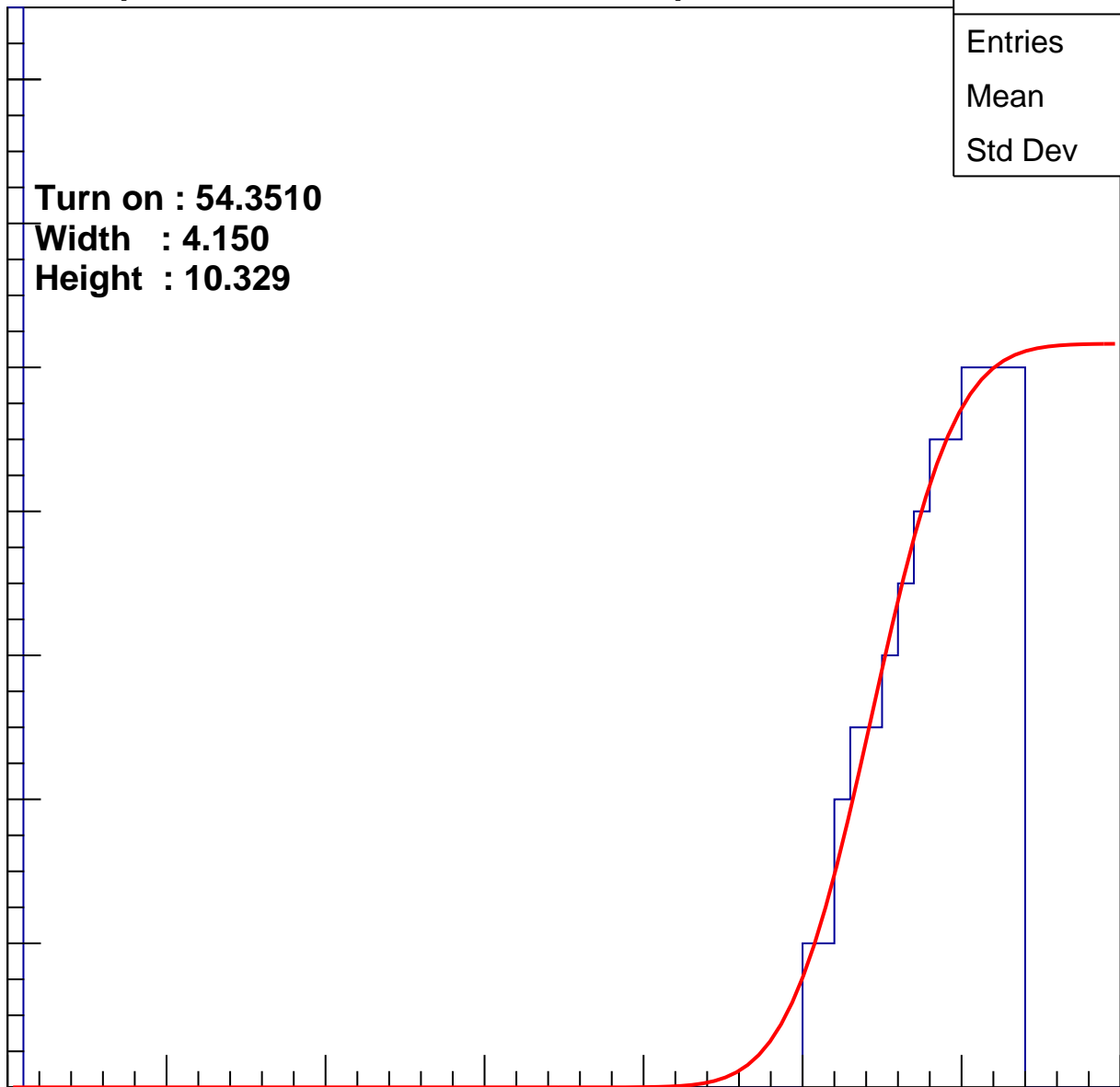
40

50

60

70

ampl



B1L104S, U9-ch54

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	38.2
Std Dev	26.66

Turn on : 52.0743

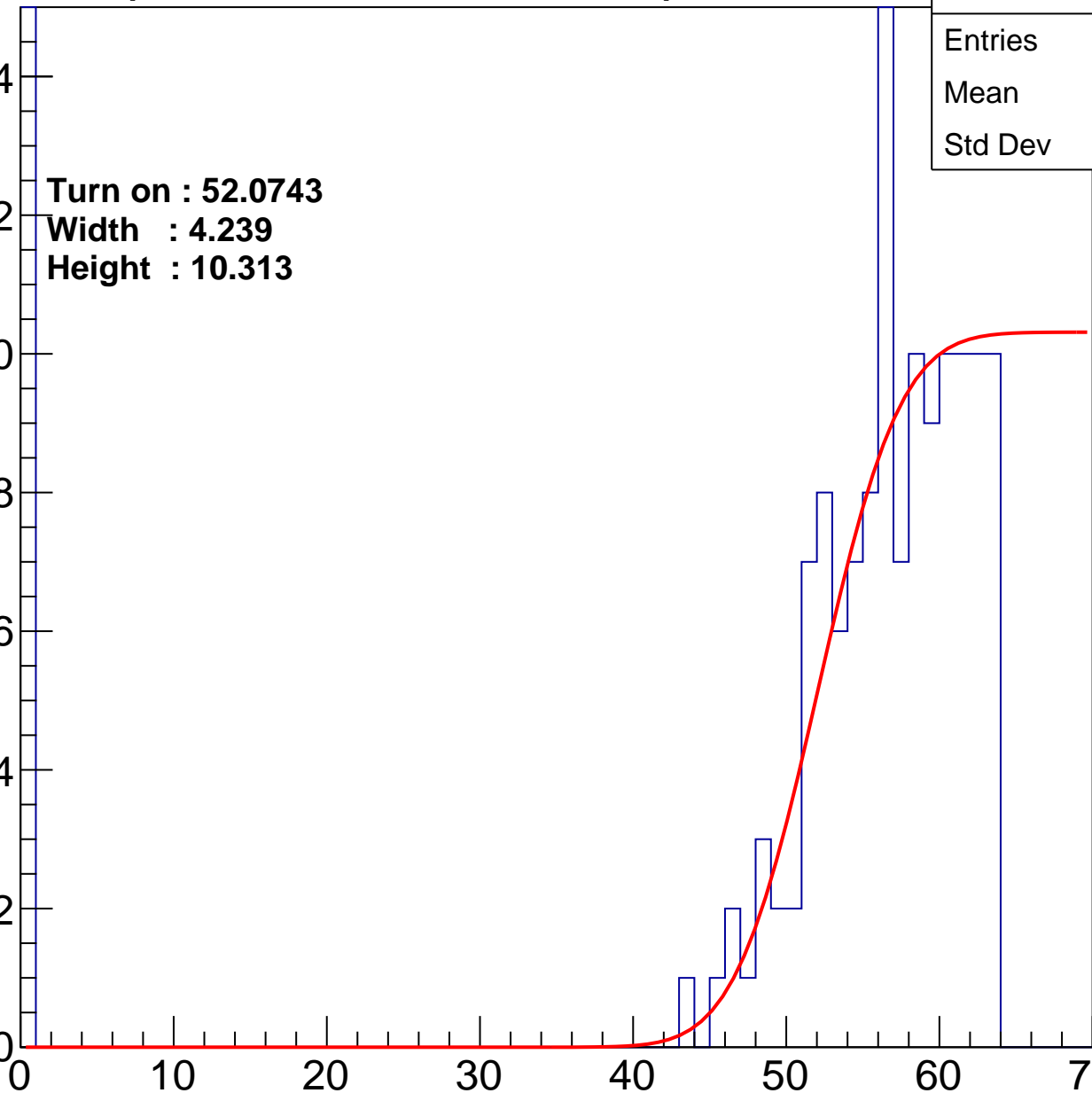
Width : 4.239

Height : 10.313

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch55

calib_packv5_033123_0516.root, FC#4, port A1

Entries	205
Mean	35.36
Std Dev	27.64

Turn on : 51.2467

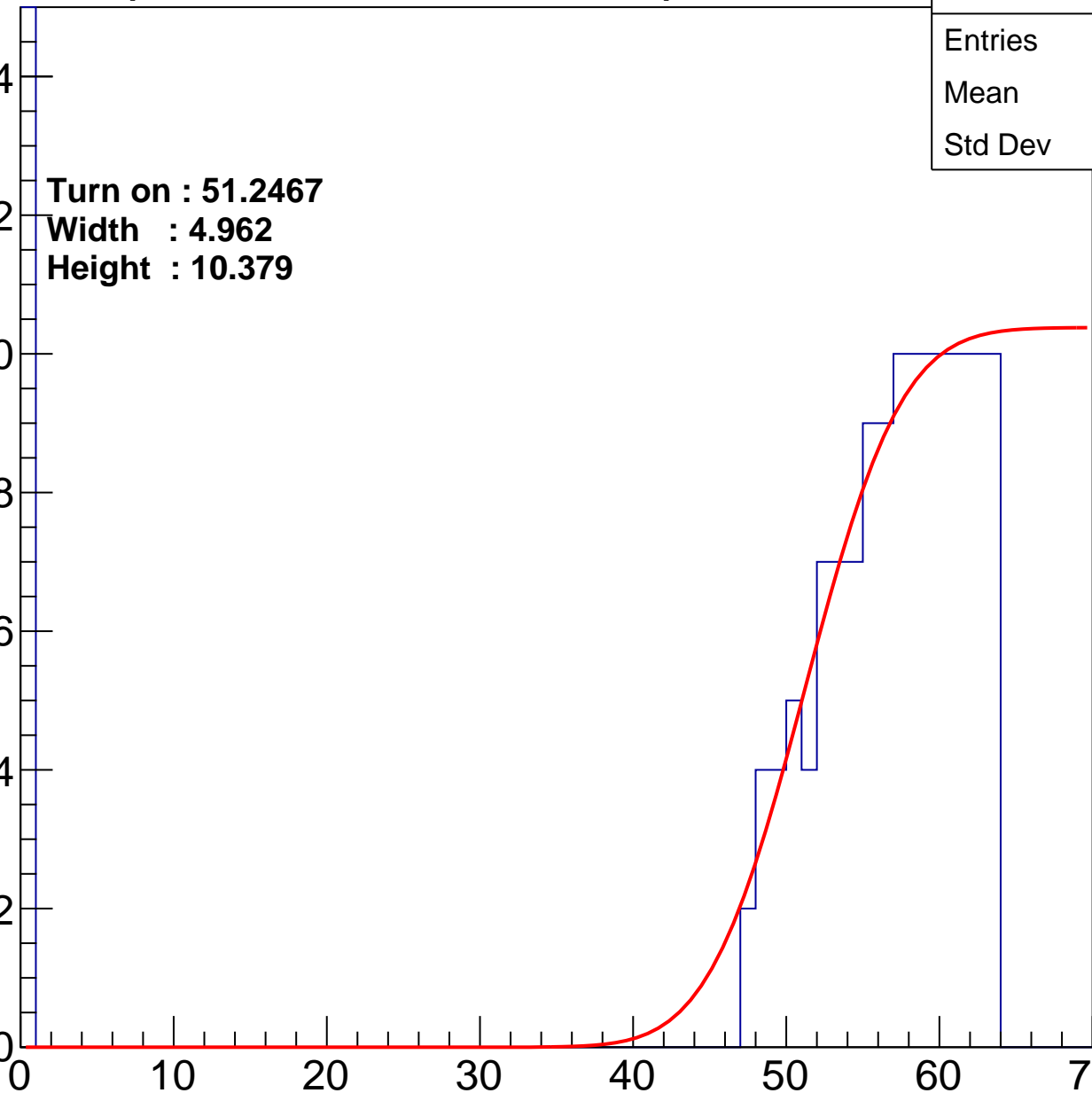
Width : 4.962

Height : 10.379

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch56

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	34.01
Std Dev	28.21

Turn on : 53.5384

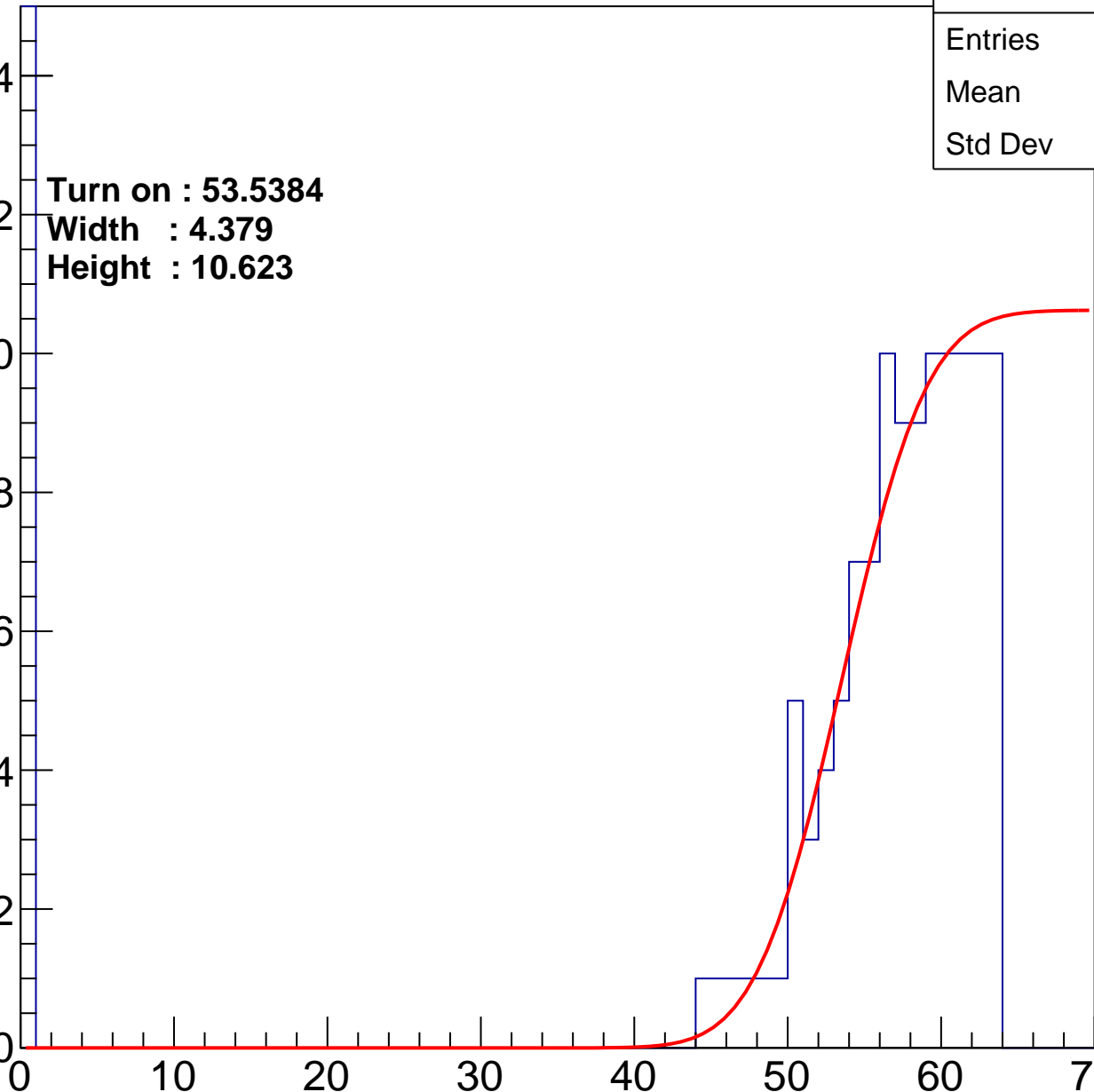
Width : 4.379

Height : 10.623

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch57

calib_packv5_033123_0516.root, FC#4, port A1

Entries	185
Mean	30.8
Std Dev	29.13

Turn on : 54.6971

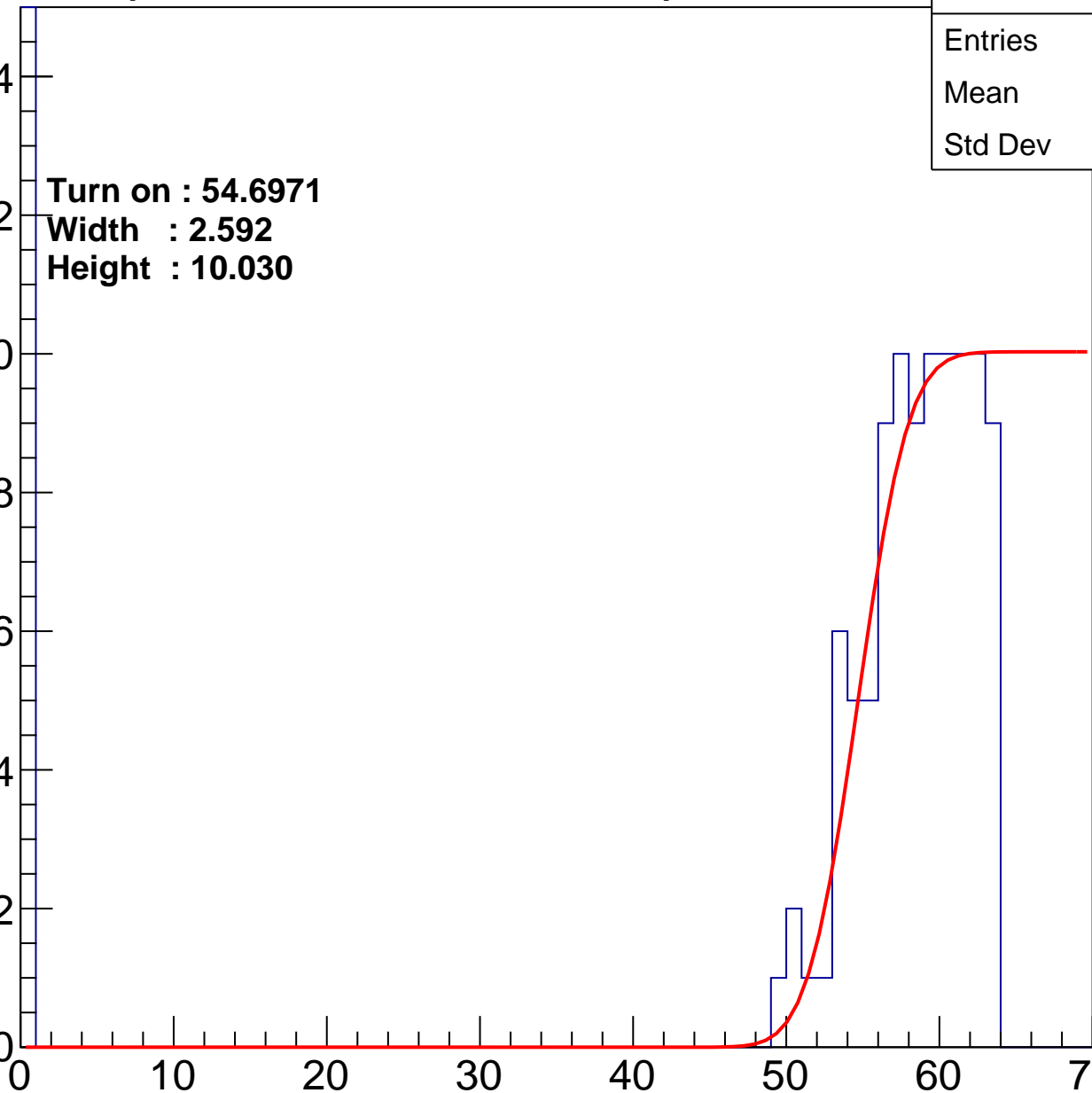
Width : 2.592

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch58

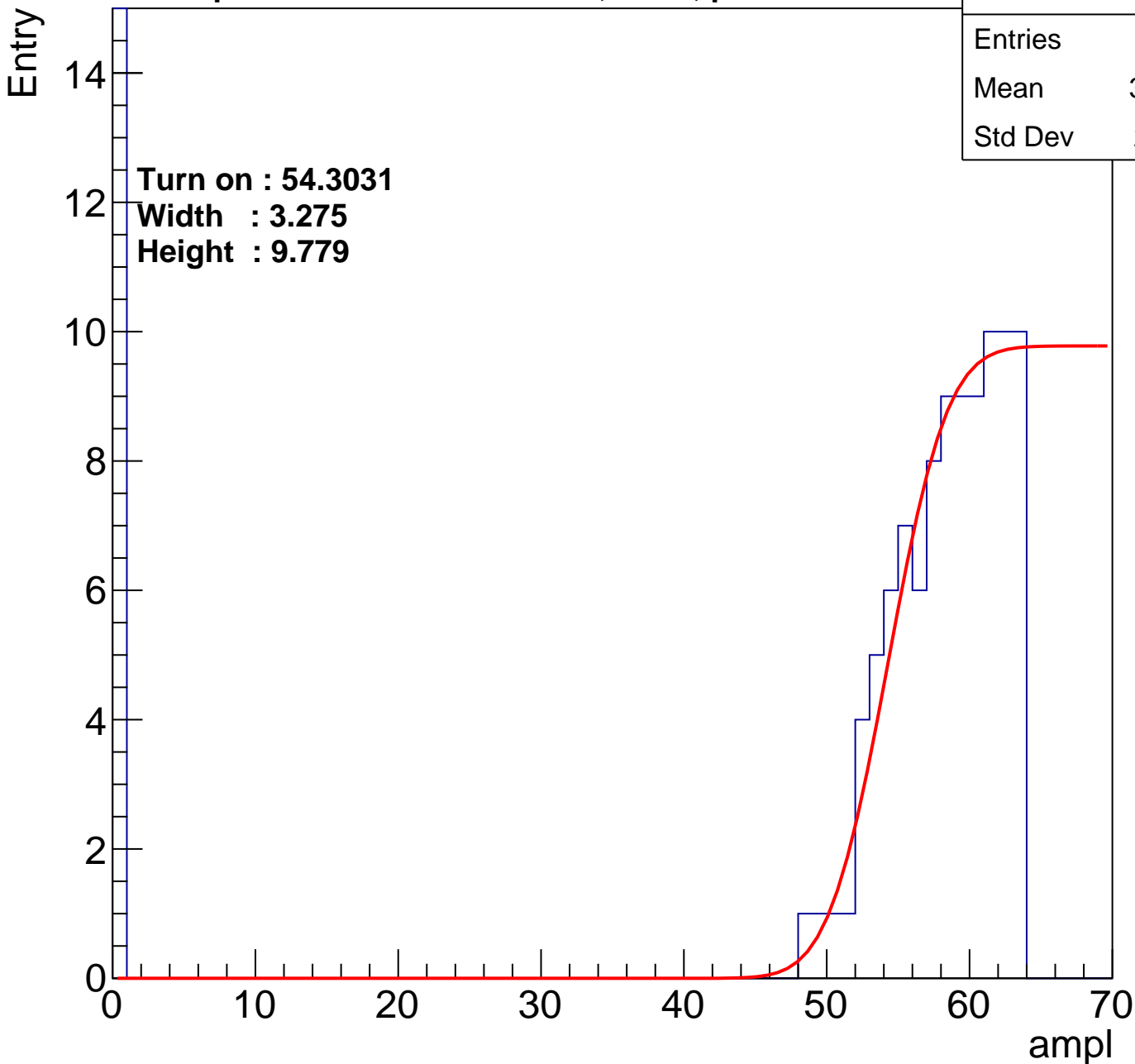
calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	33.28
Std Dev	28.81

Turn on : 54.3031

Width : 3.275

Height : 9.779



B1L104S, U9-ch59

calib_packv5_033123_0516.root, FC#4, port A1

Entries	156
Mean	31.29
Std Dev	29.44

Turn on : 56.6537

Width : 3.236

Height : 10.333

Entry

14

12

10

8

6

4

2

0

0

10

20

30

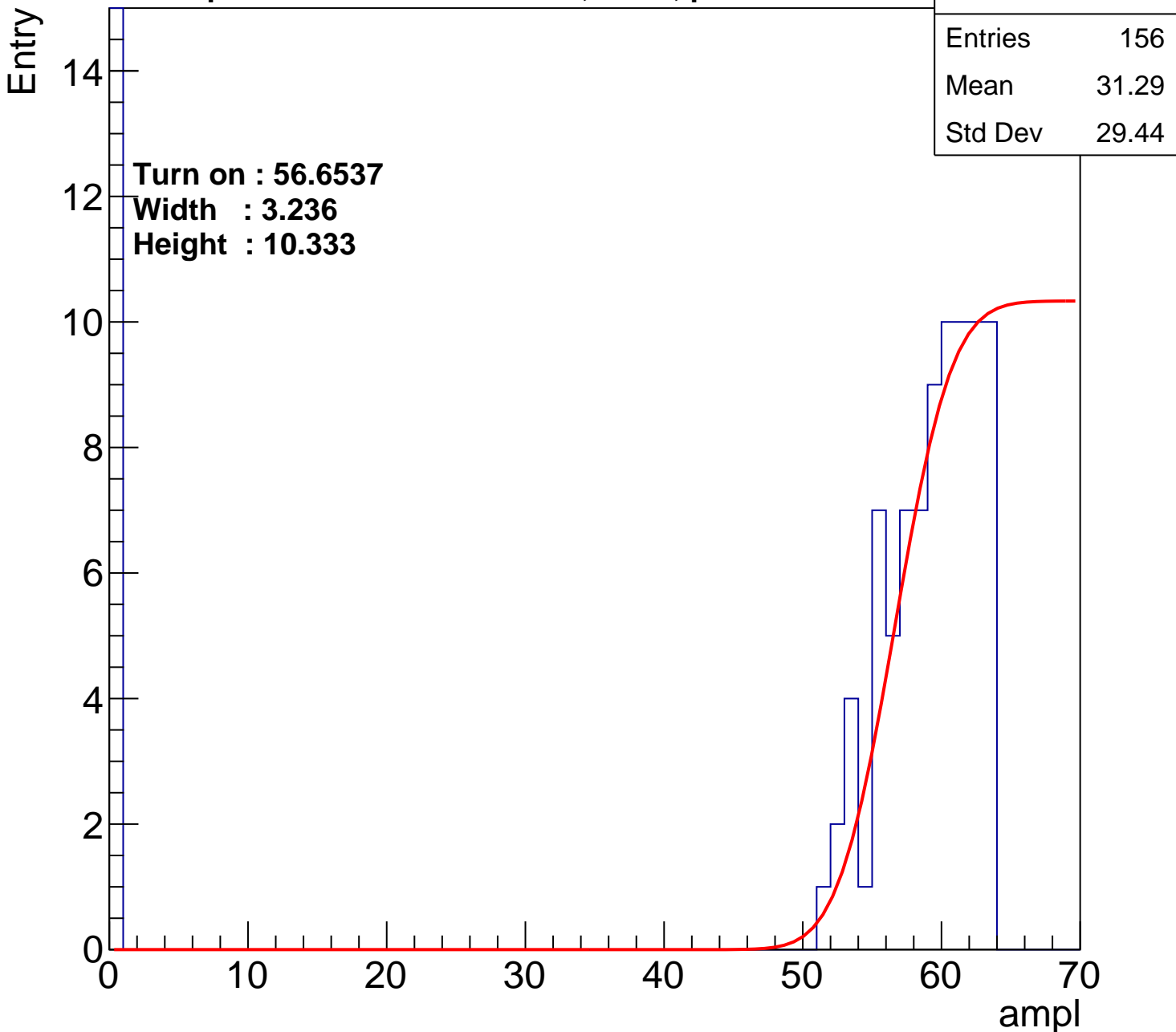
40

50

60

70

ampl



B1L104S, U9-ch60

calib_packv5_033123_0516.root, FC#4, port A1

Entries	175
Mean	36.29
Std Dev	27.76

Turn on : 52.9798

Width : 3.775

Height : 9.893

Entry

14

12

10

8

6

4

2

0

0

10

20

30

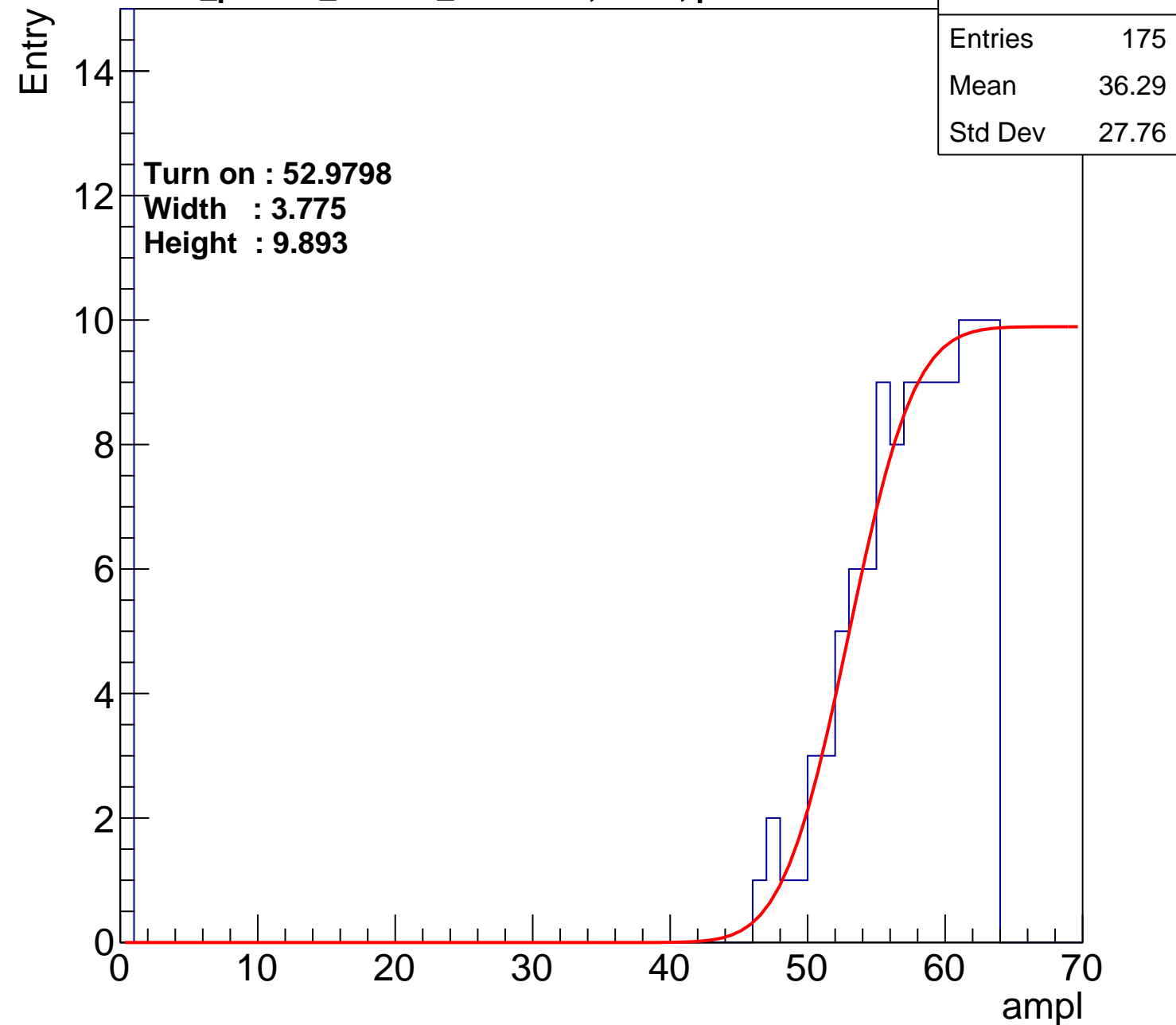
40

50

60

70

ampl



B1L104S, U9-ch61

calib_packv5_033123_0516.root, FC#4, port A1

Entries	171
Mean	31.78
Std Dev	29.21

Turn on : 55.4457

Width : 4.283

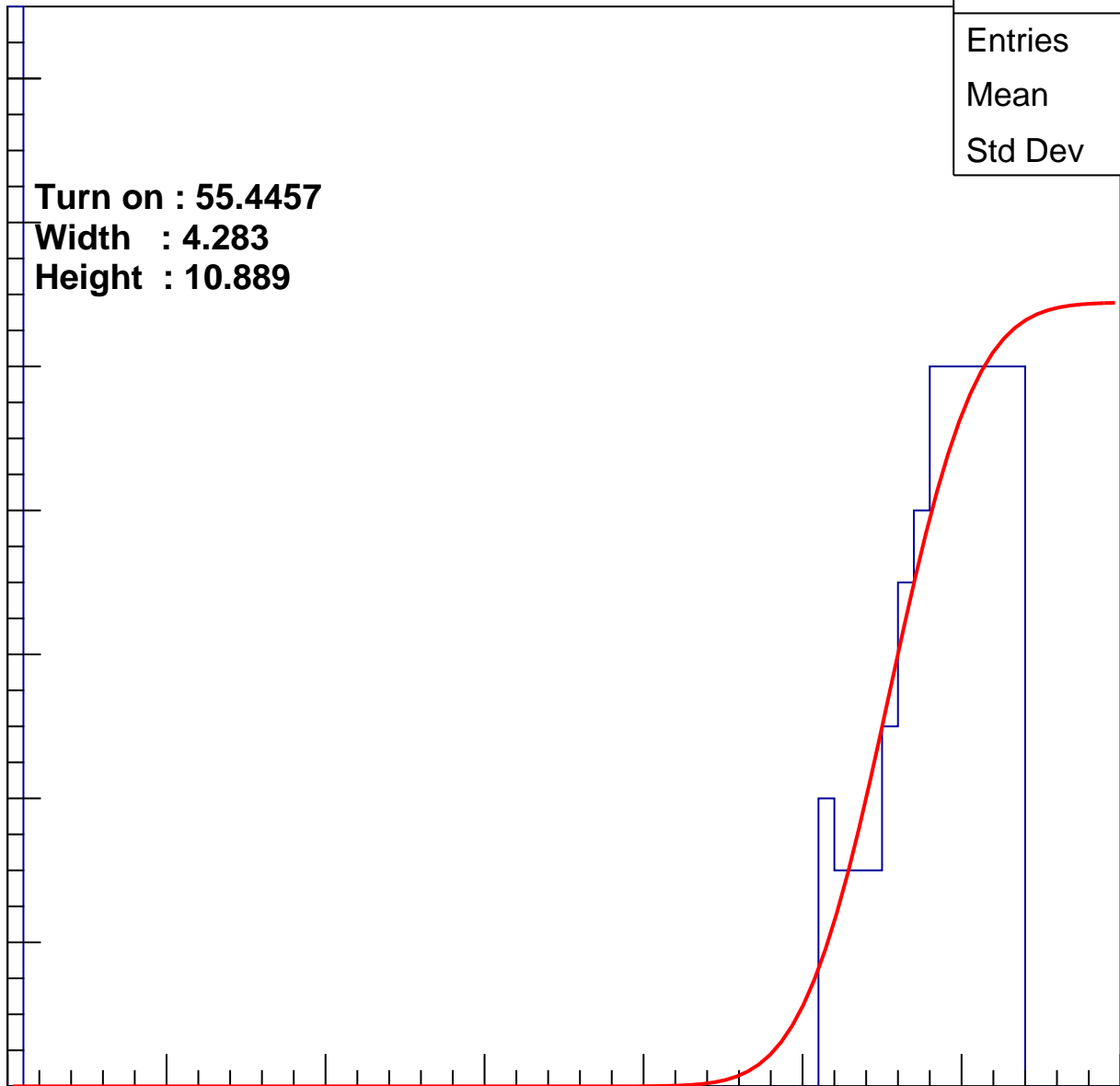
Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch62

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	36.47
Std Dev	27.35

Turn on : 52.0851

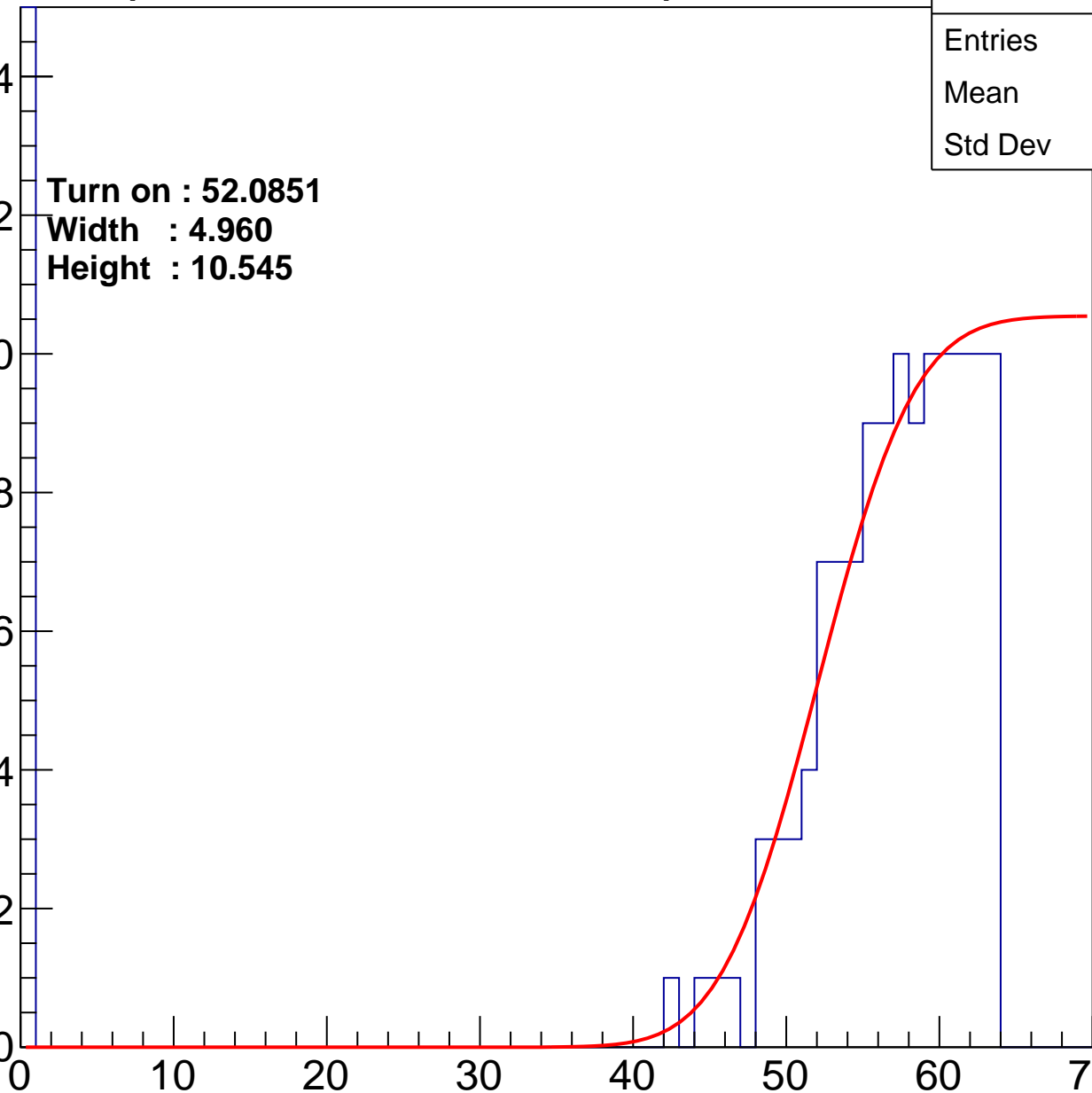
Width : 4.960

Height : 10.545

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch63

calib_packv5_033123_0516.root, FC#4, port A1

Entries	176
Mean	32.95
Std Dev	28.86

Turn on : 54.0953

Width : 2.497

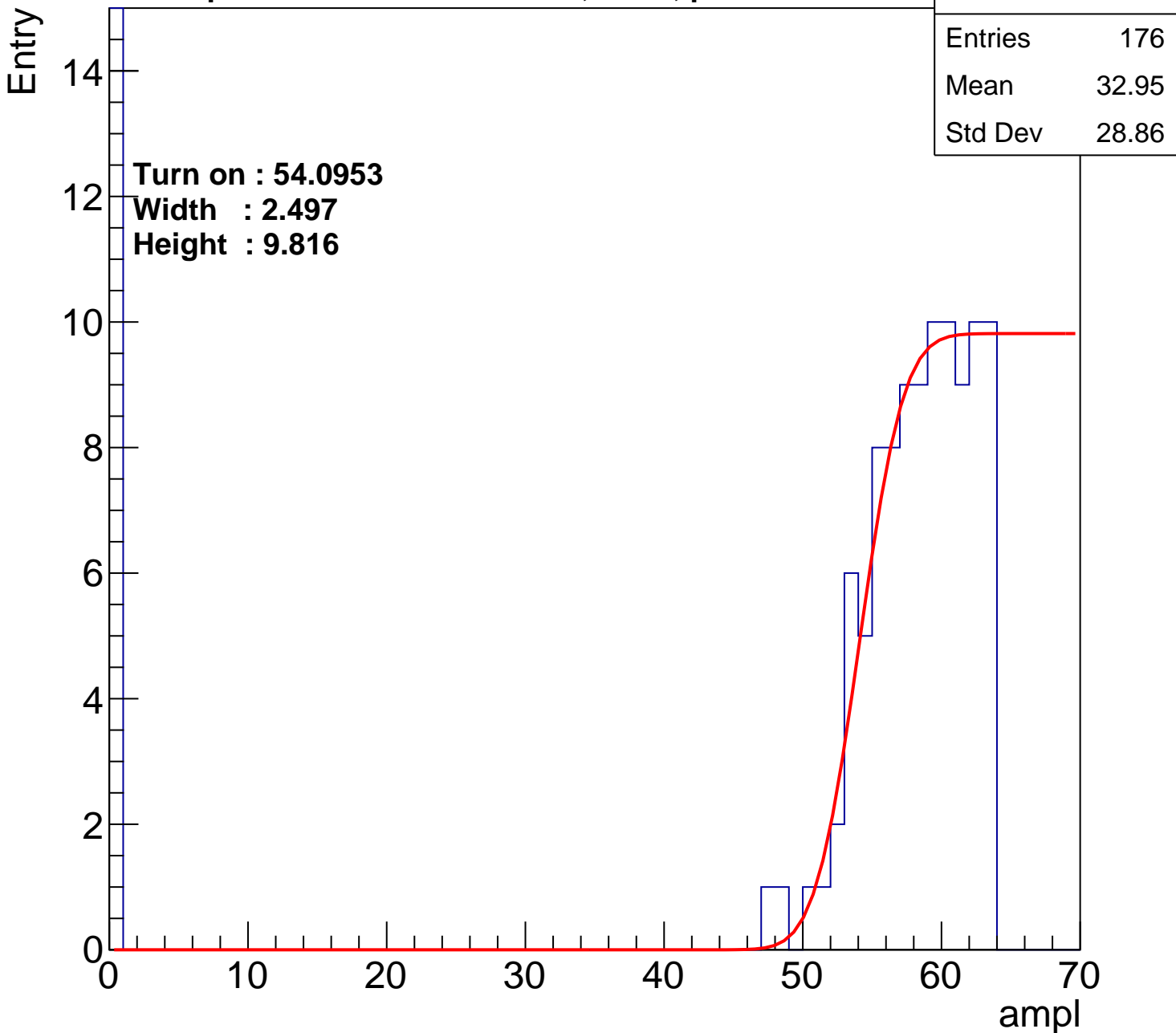
Height : 9.816

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch64

calib_packv5_033123_0516.root, FC#4, port A1

Entries	219
Mean	31.15
Std Dev	28.49

Turn on : 52.8474

Width : 4.921

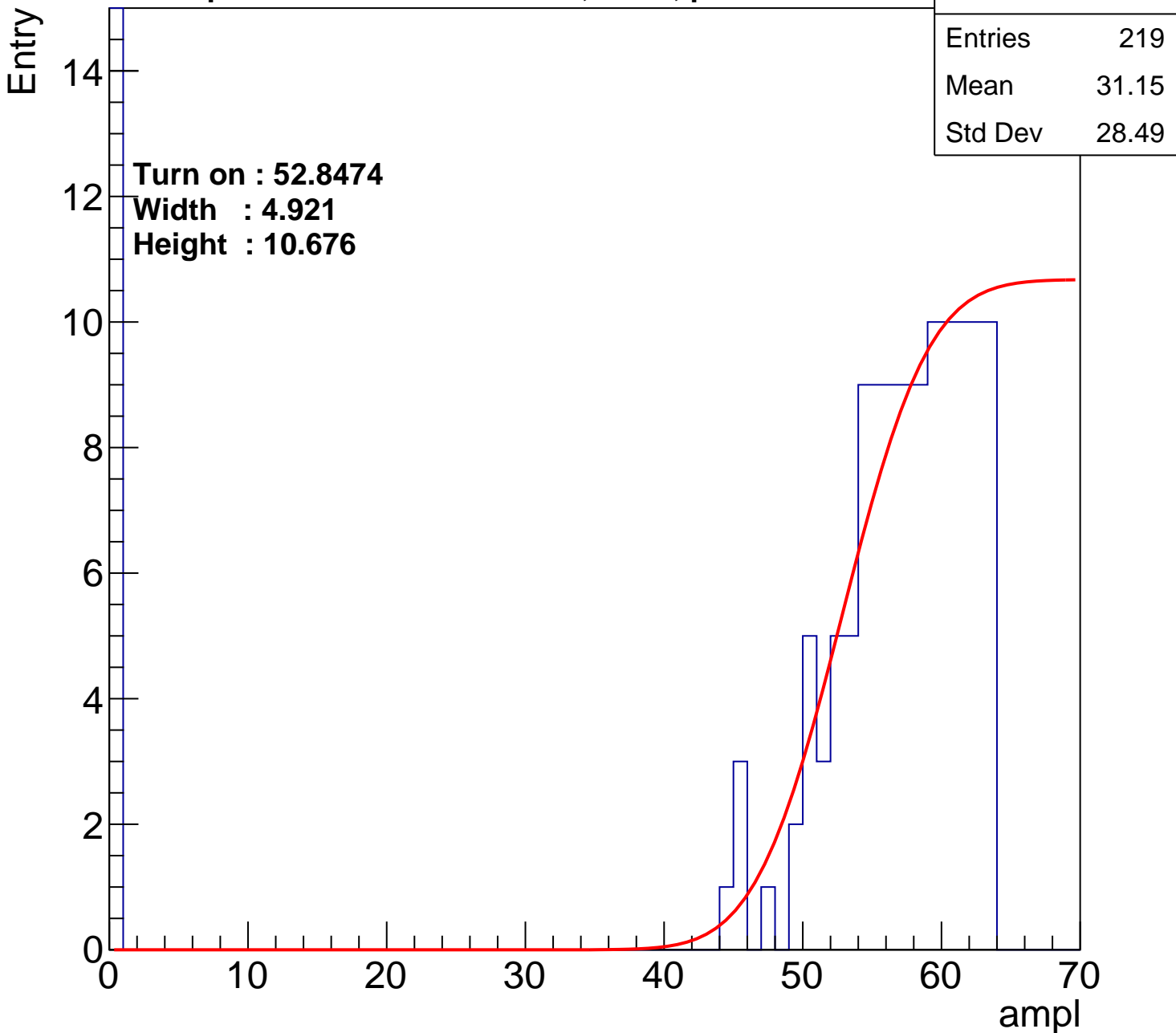
Height : 10.676

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch65

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	31.03
Std Dev	29.37

Turn on : 56.6403

Width : 3.556

Height : 10.111

Entry

70

60

50

40

30

20

10

0

0

10

20

30

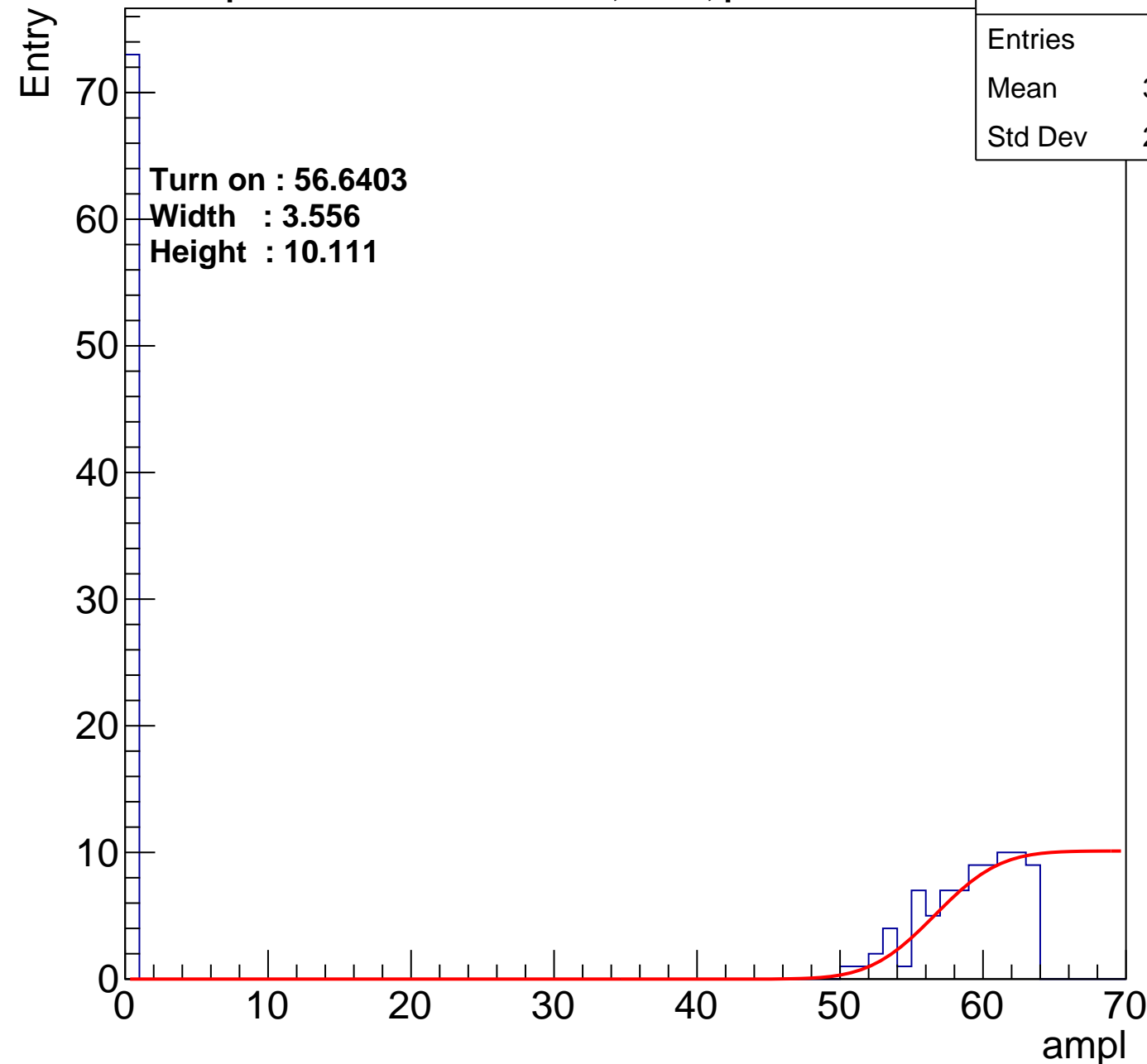
40

50

60

70

ampl



B1L104S, U9-ch66

calib_packv5_033123_0516.root, FC#4, port A1

Entries	240
Mean	32.34
Std Dev	27.83

Turn on : 50.5810

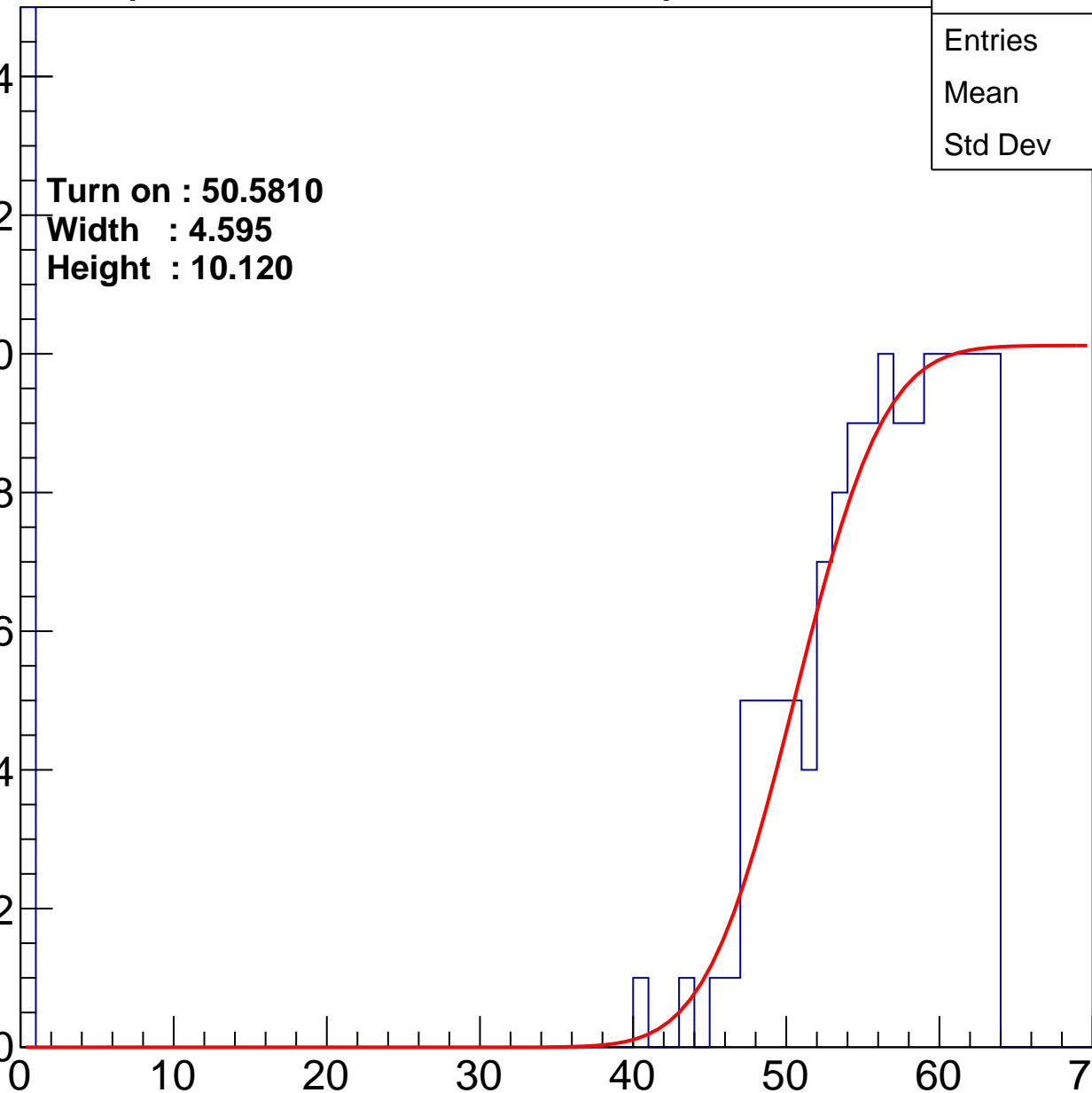
Width : 4.595

Height : 10.120

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch67

calib_packv5_033123_0516.root, FC#4, port A1

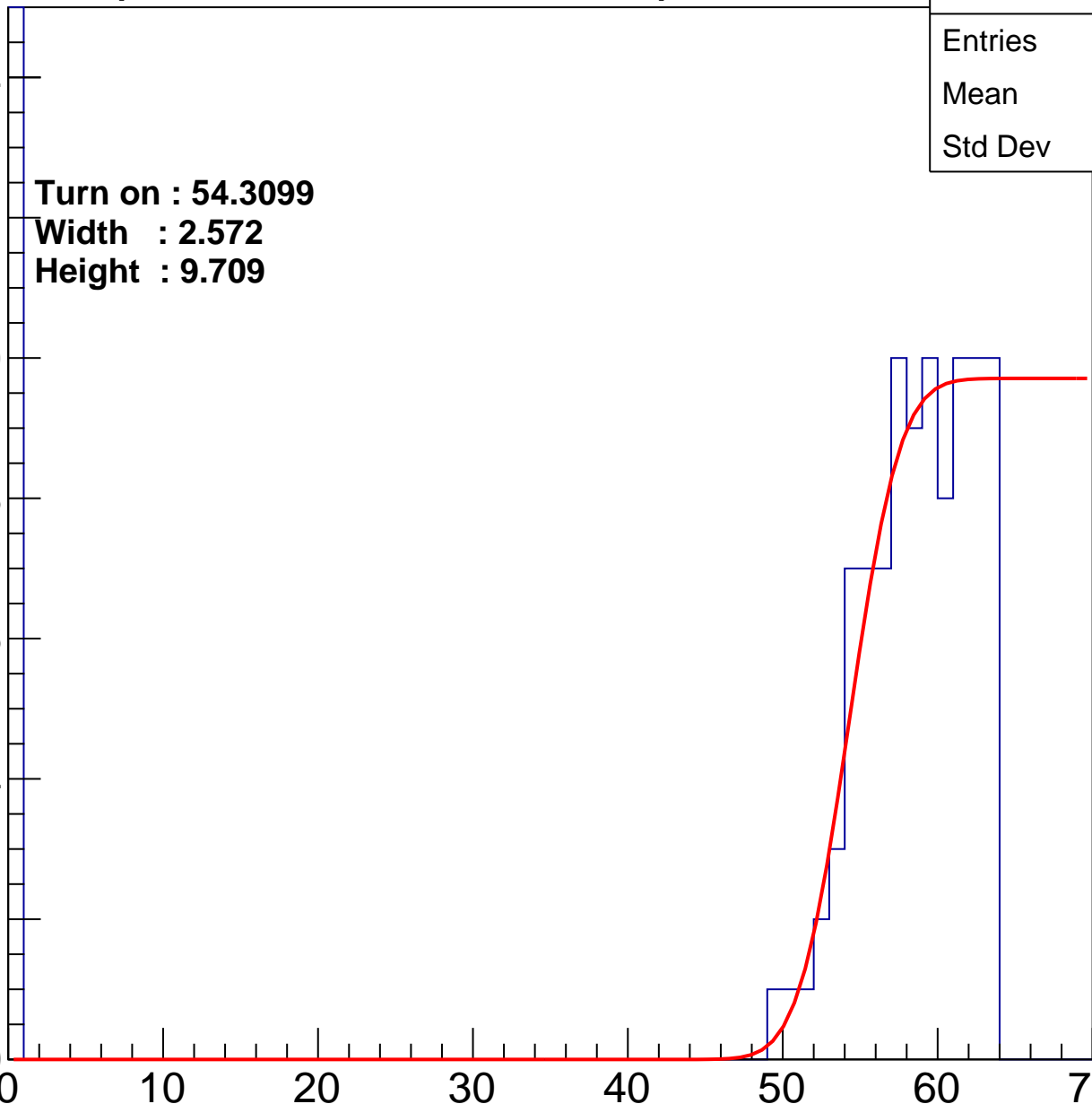
Entry

14
12
10
8
6
4
2
0

Turn on : 54.3099
Width : 2.572
Height : 9.709

Entries	188
Mean	29.73
Std Dev	29.21

ampl



B1L104S, U9-ch68

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	34.91
Std Dev	28.07

Turn on : 53.1469

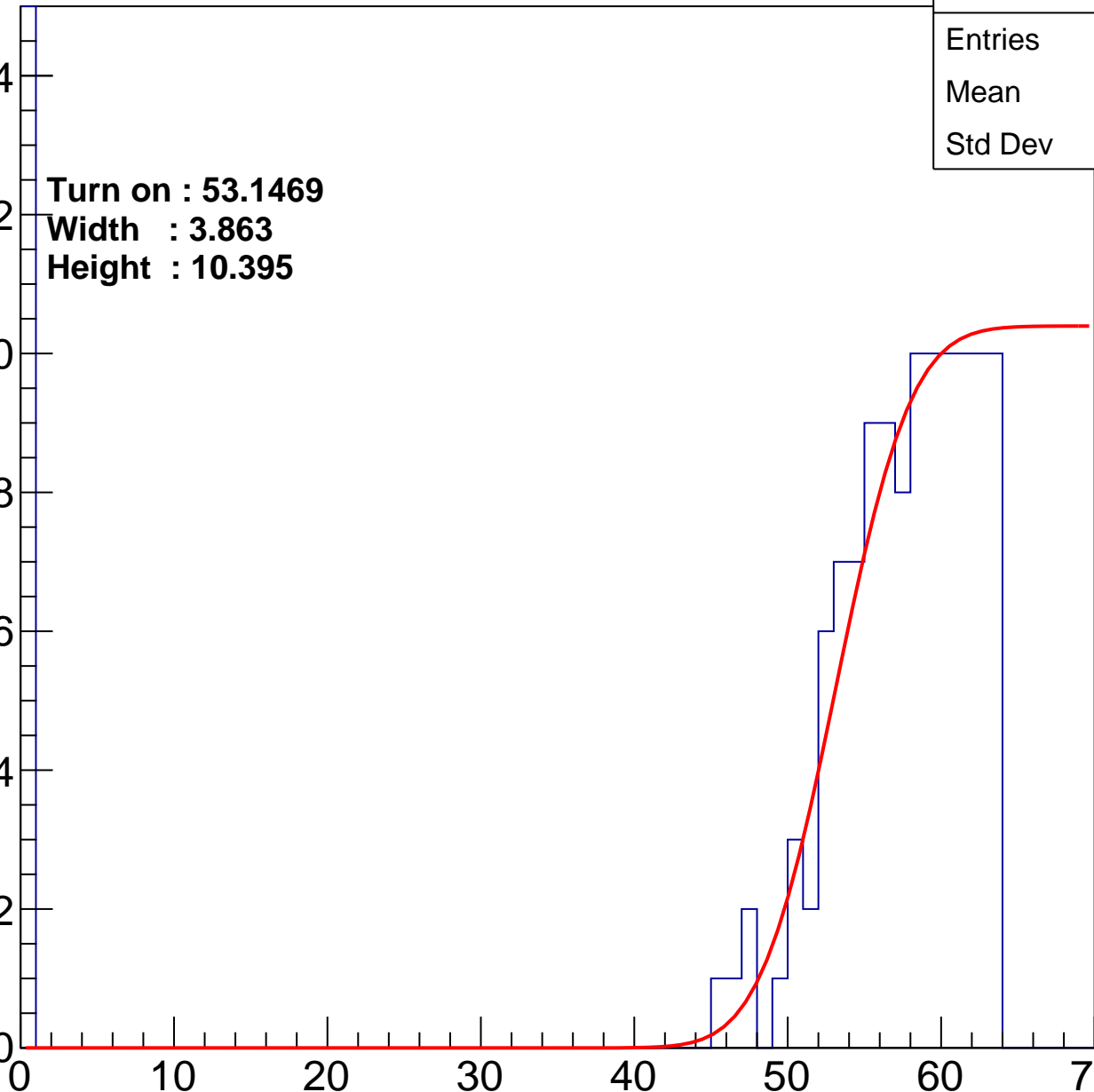
Width : 3.863

Height : 10.395

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch69

calib_packv5_033123_0516.root, FC#4, port A1

Entries	194
Mean	31.72
Std Dev	28.75

Turn on : 53.6636

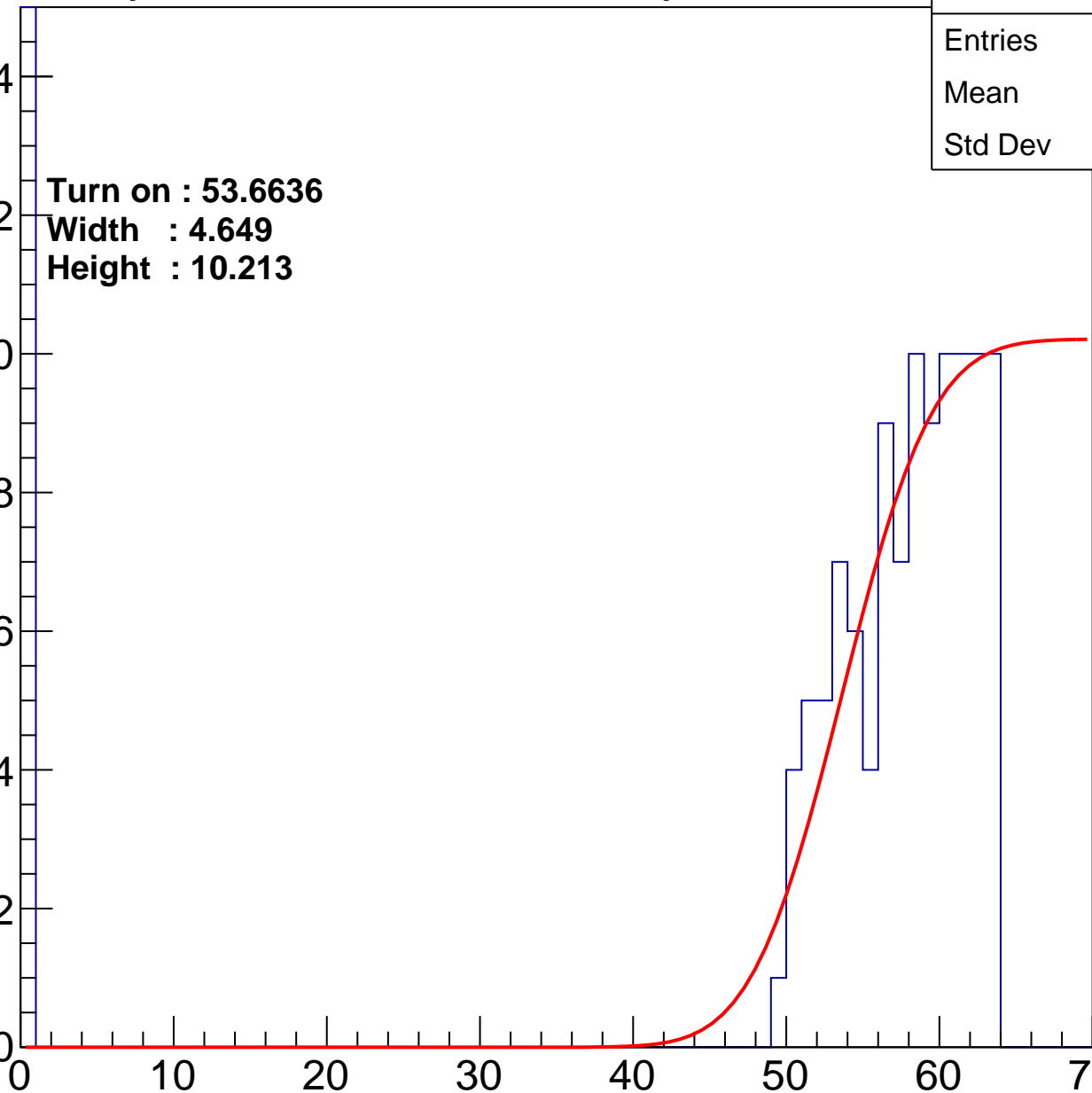
Width : 4.649

Height : 10.213

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch70

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	31.98
Std Dev	28.33

Turn on : 51.5321

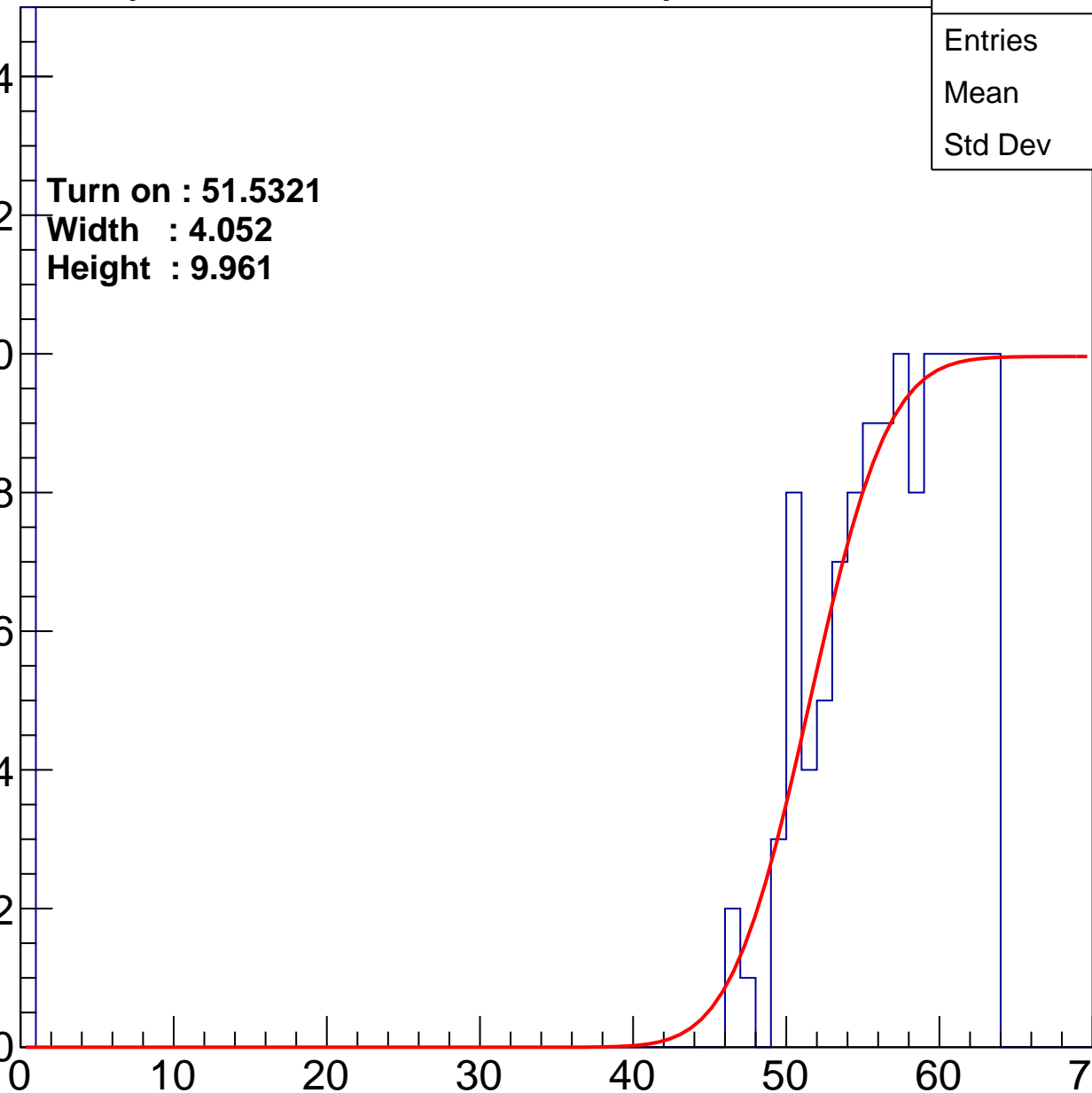
Width : 4.052

Height : 9.961

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch71

calib_packv5_033123_0516.root, FC#4, port A1

Entries	221
Mean	23.79
Std Dev	28.77

Turn on : 54.4504

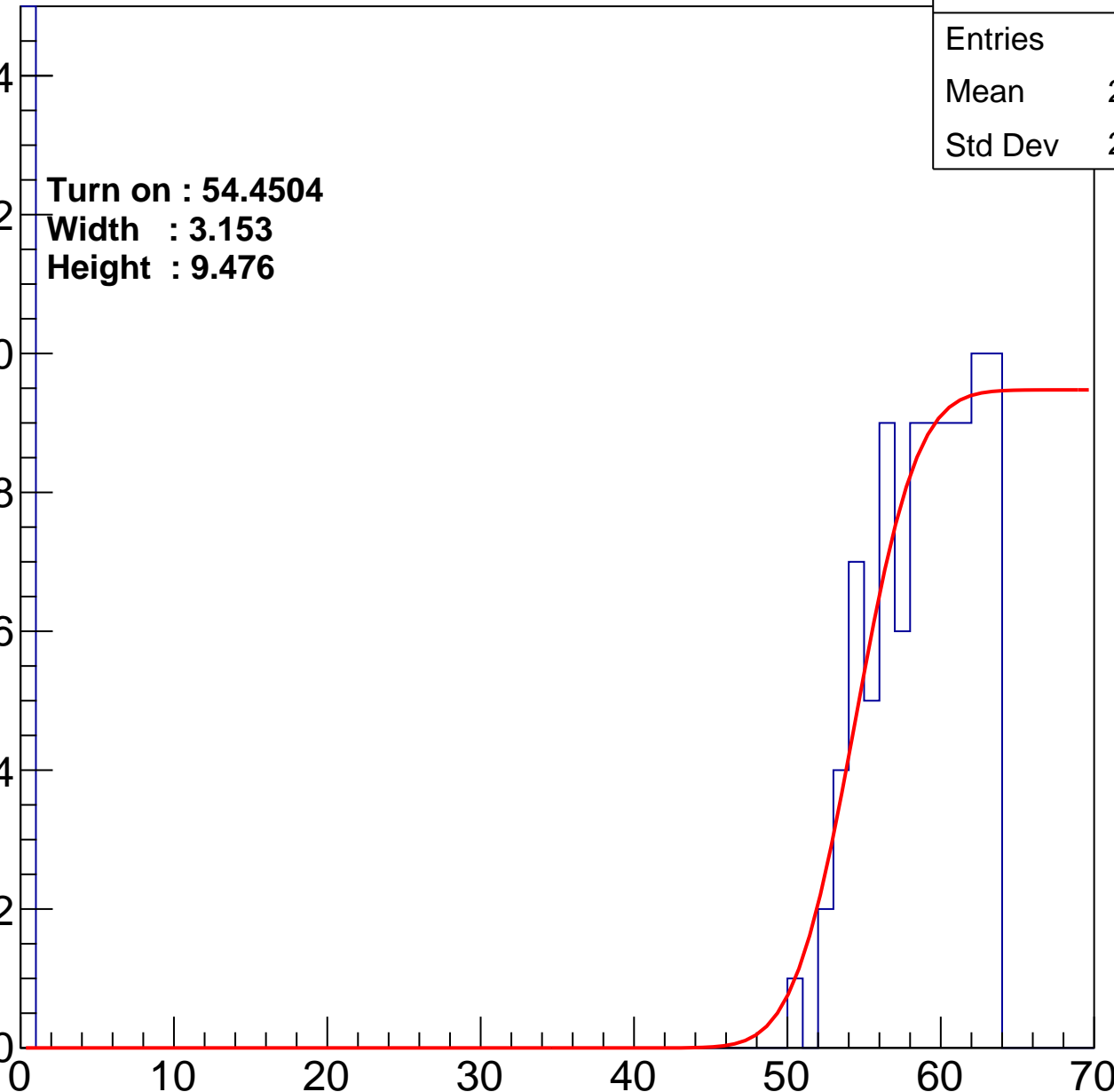
Width : 3.153

Height : 9.476

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch72

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	29.51
Std Dev	28.82

Turn on : 54.9065

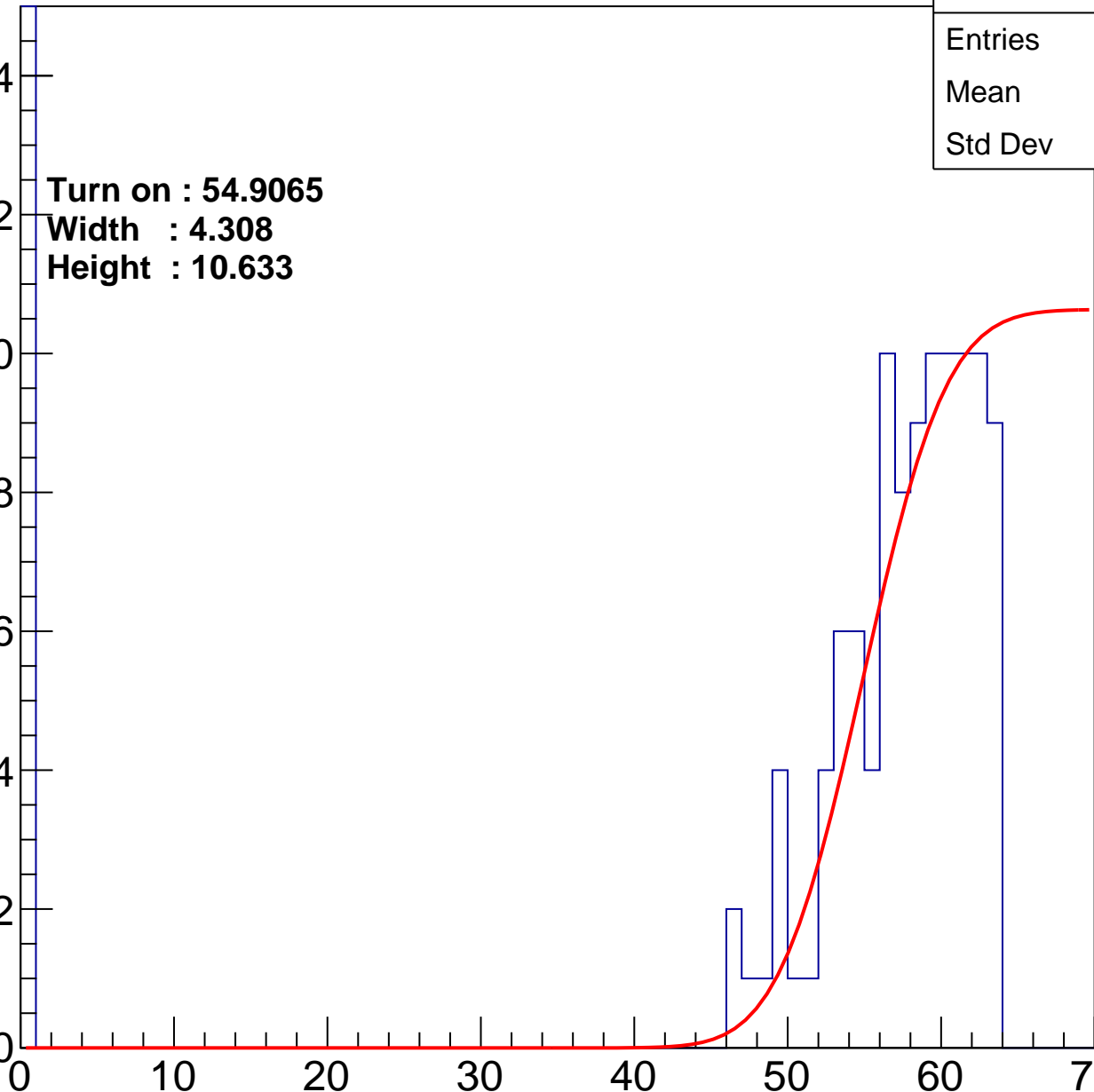
Width : 4.308

Height : 10.633

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch73

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	29.13
Std Dev	28.98

Turn on : 55.1751

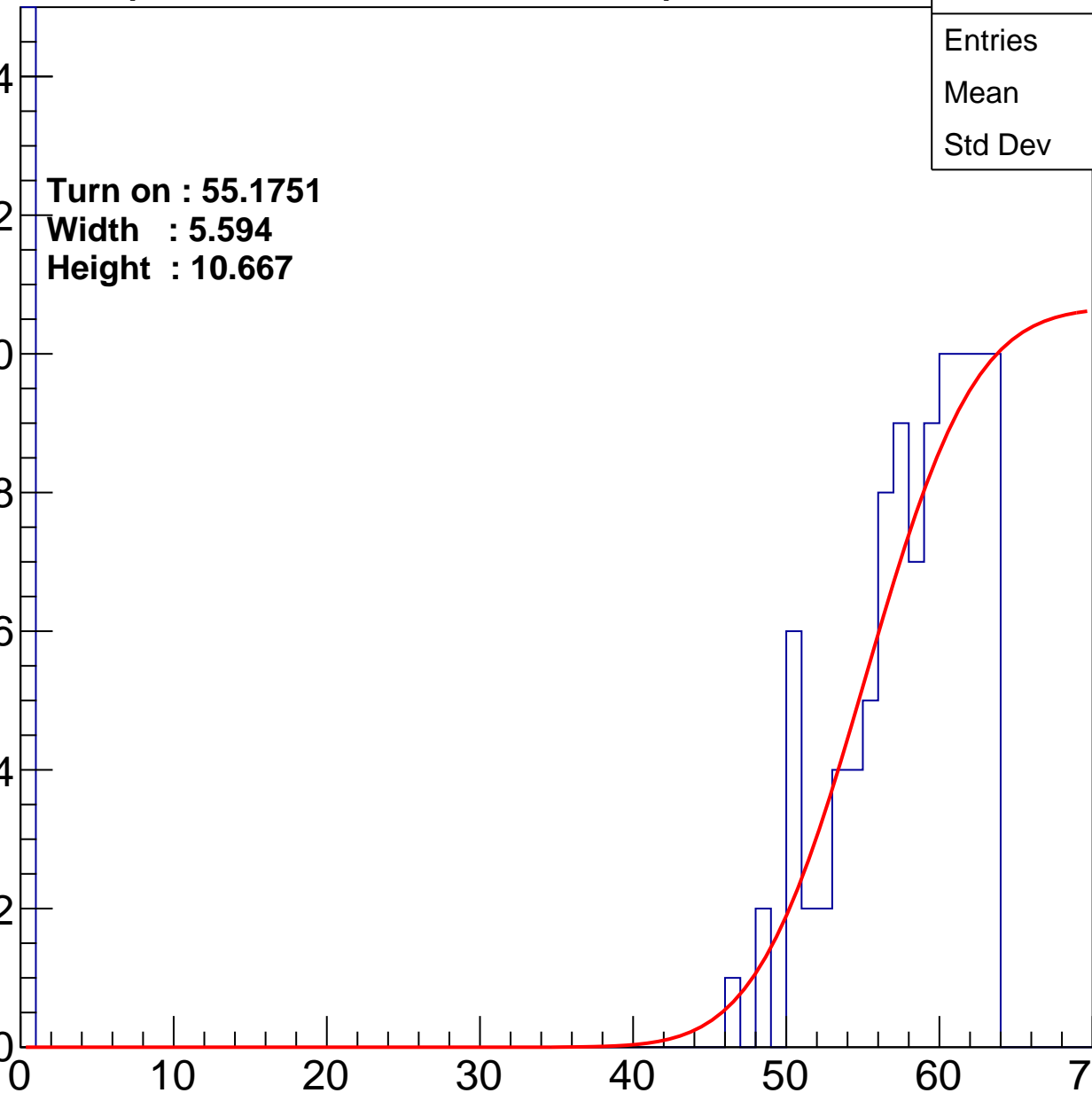
Width : 5.594

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch74

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	28.05
Std Dev	28.86

Turn on : 54.8628

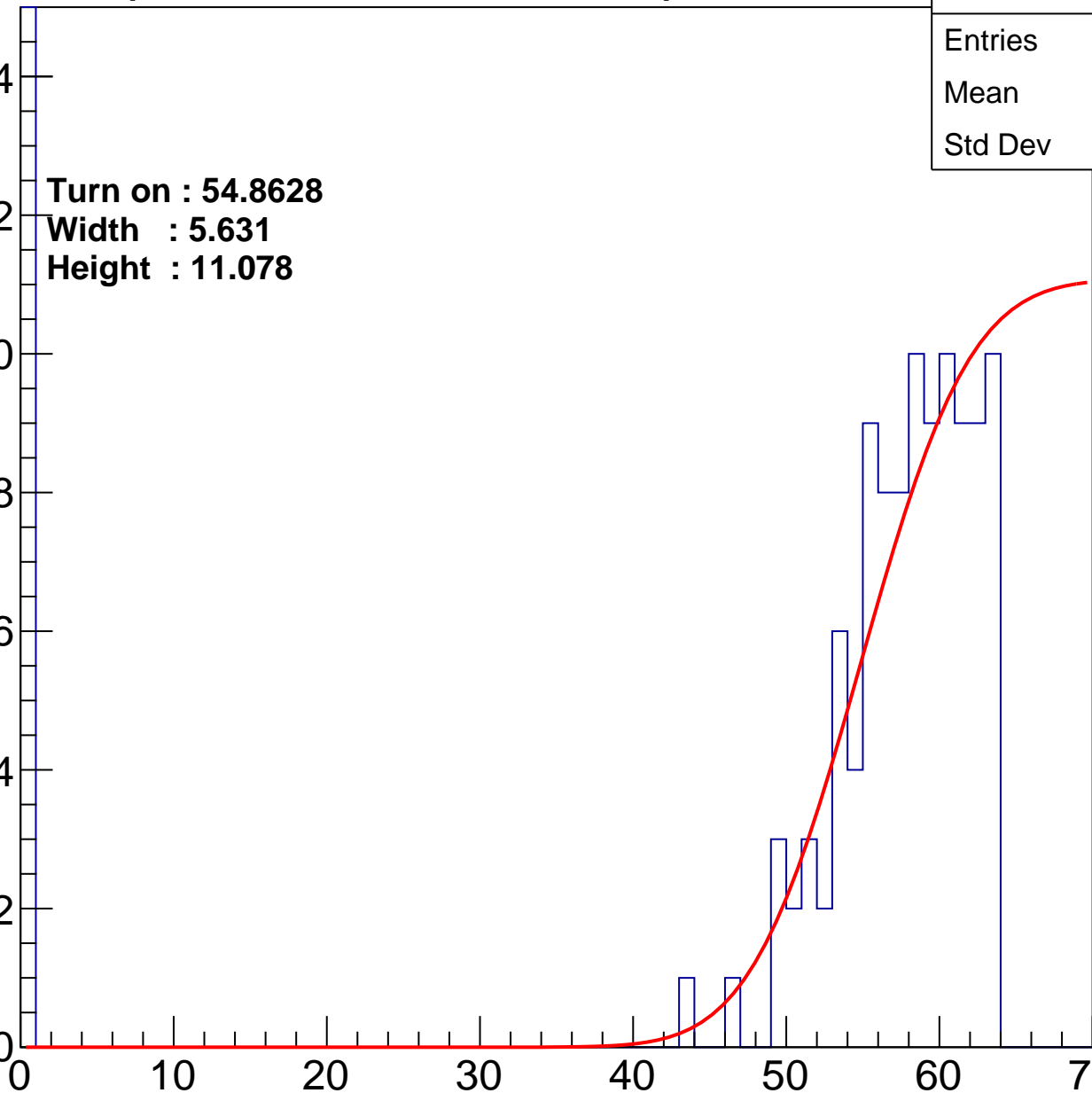
Width : 5.631

Height : 11.078

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch75

calib_packv5_033123_0516.root, FC#4, port A1

Entries	162
Mean	33.92
Std Dev	28.95

Turn on : 54.7248

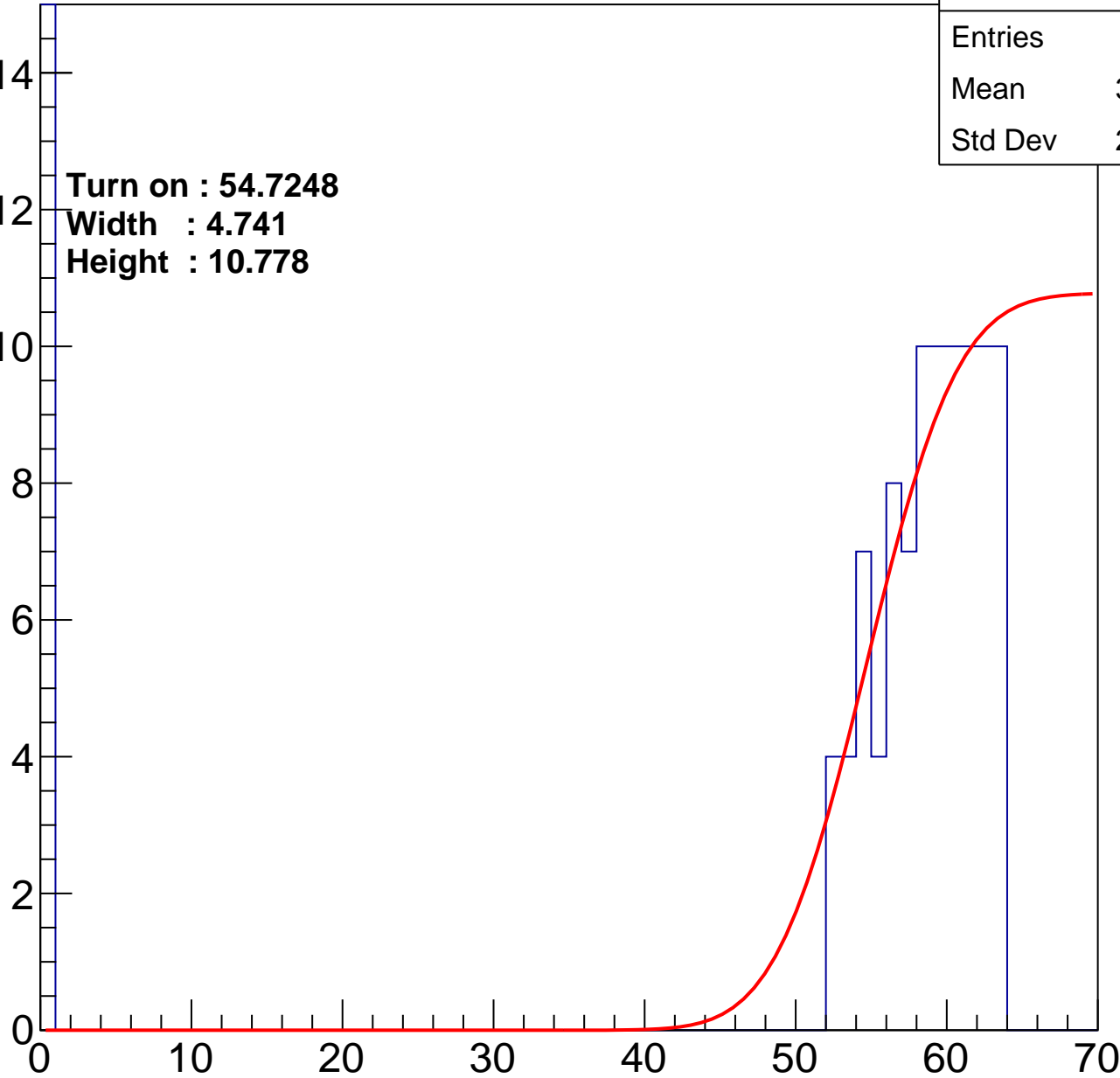
Width : 4.741

Height : 10.778

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch76

calib_packv5_033123_0516.root, FC#4, port A1

Entries	211
Mean	31.03
Std Dev	28.55

Turn on : 54.3547

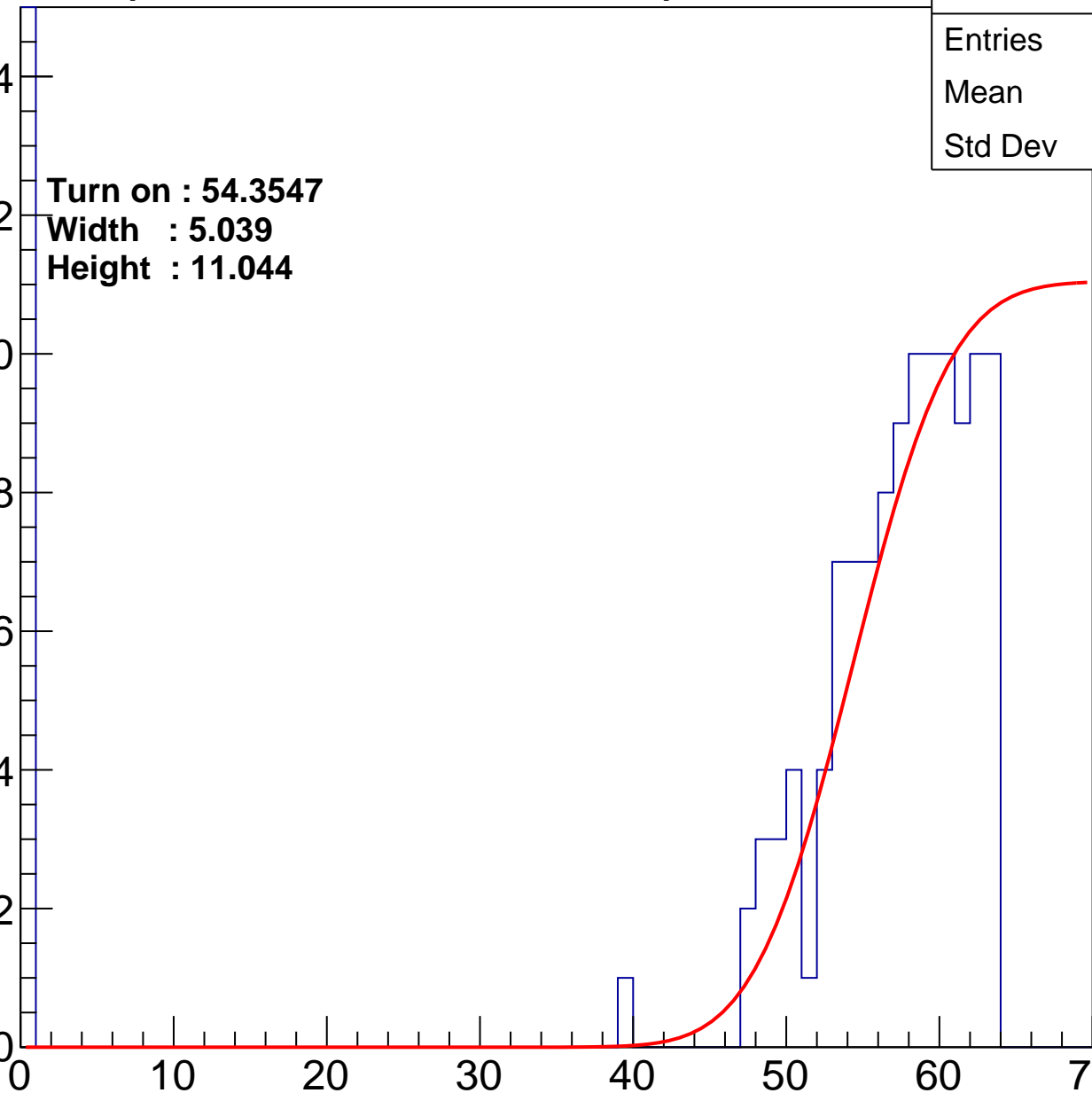
Width : 5.039

Height : 11.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch77

calib_packv5_033123_0516.root, FC#4, port A1

Entry

100

80

60

40

20

0

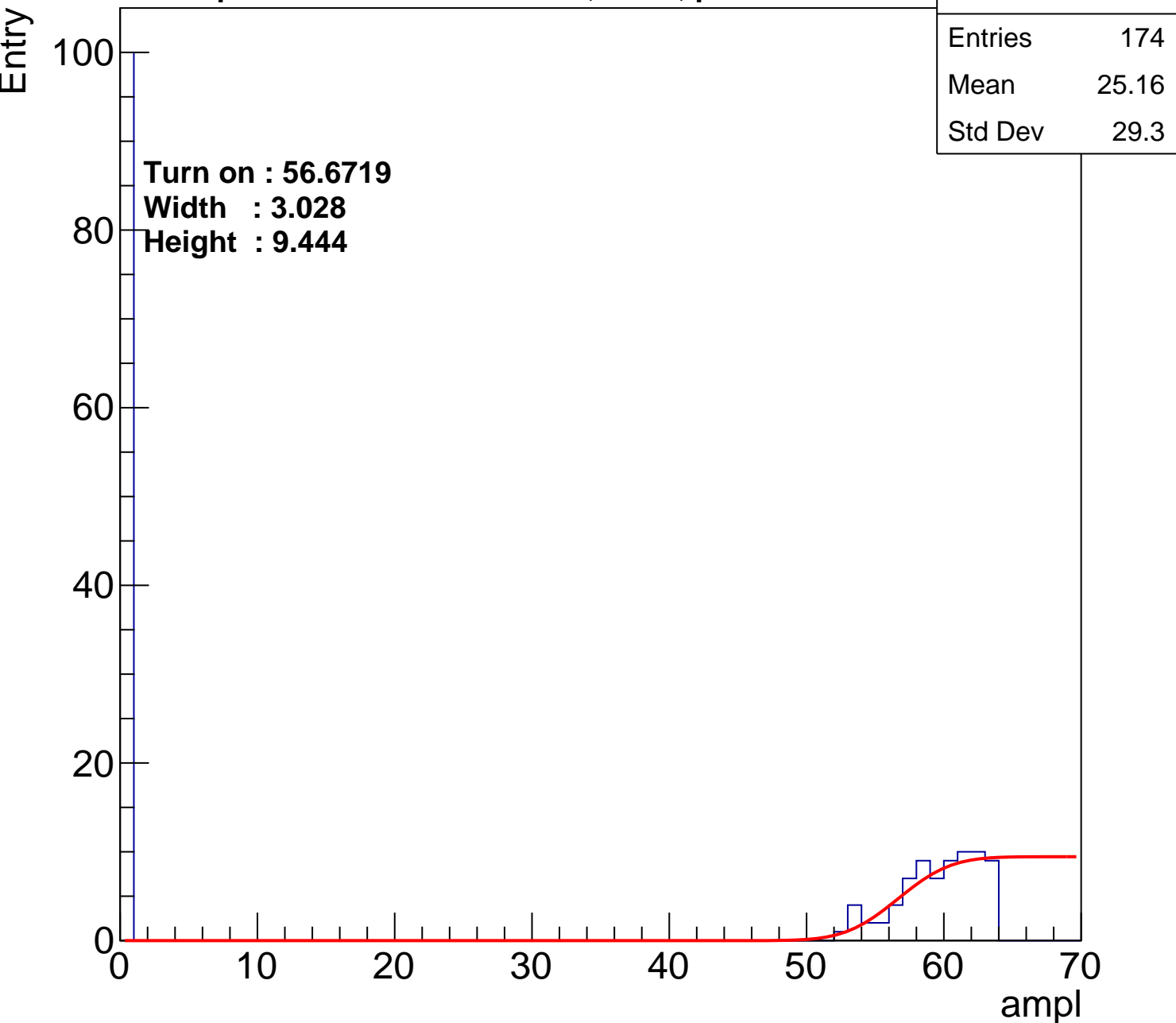
Turn on : 56.6719

Width : 3.028

Height : 9.444

Entries	174
Mean	25.16
Std Dev	29.3

ampl



B1L104S, U9-ch78

calib_packv5_033123_0516.root, FC#4, port A1

Entries	232
Mean	31.6
Std Dev	28.2

Turn on : 51.0924

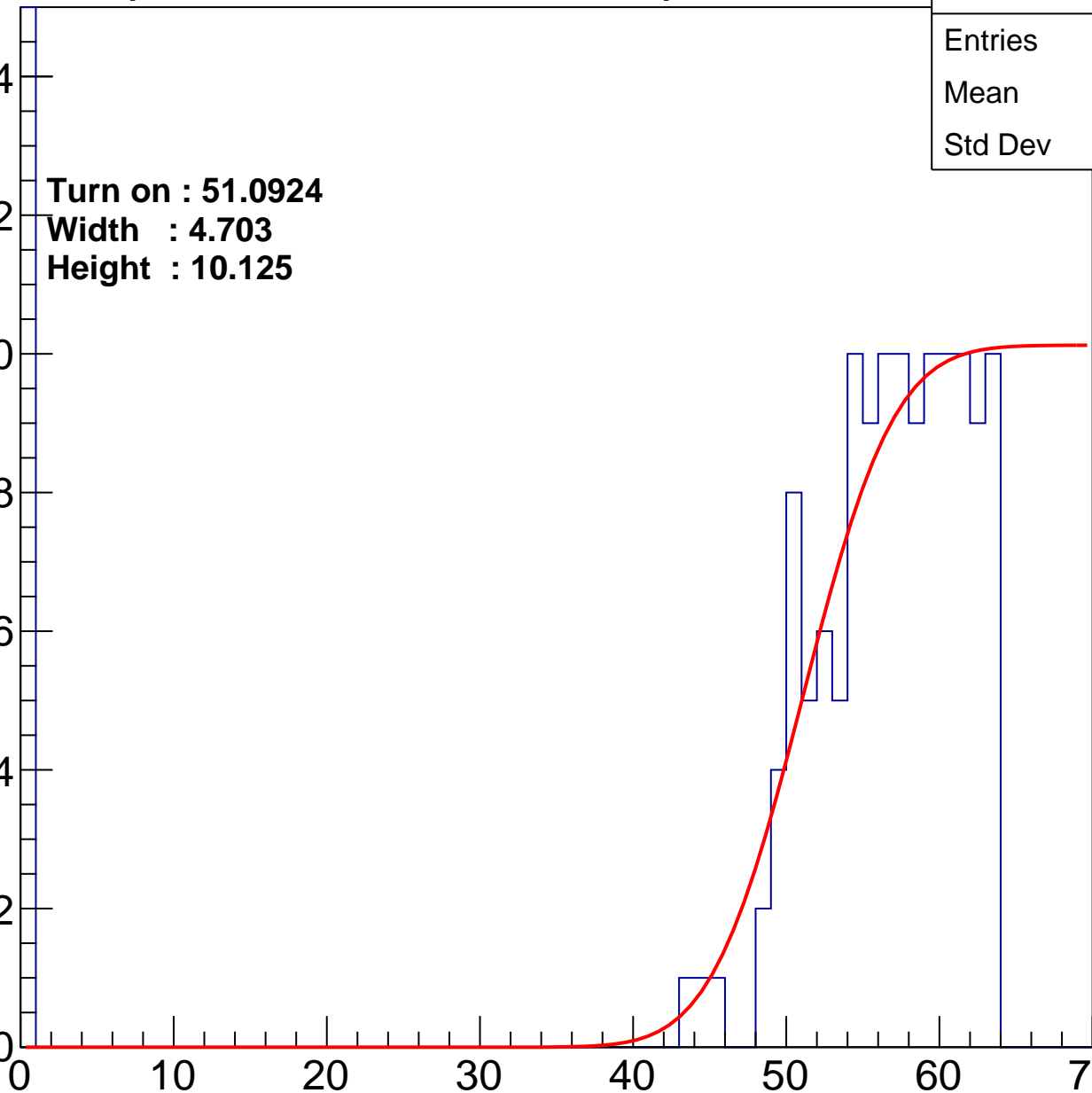
Width : 4.703

Height : 10.125

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch79

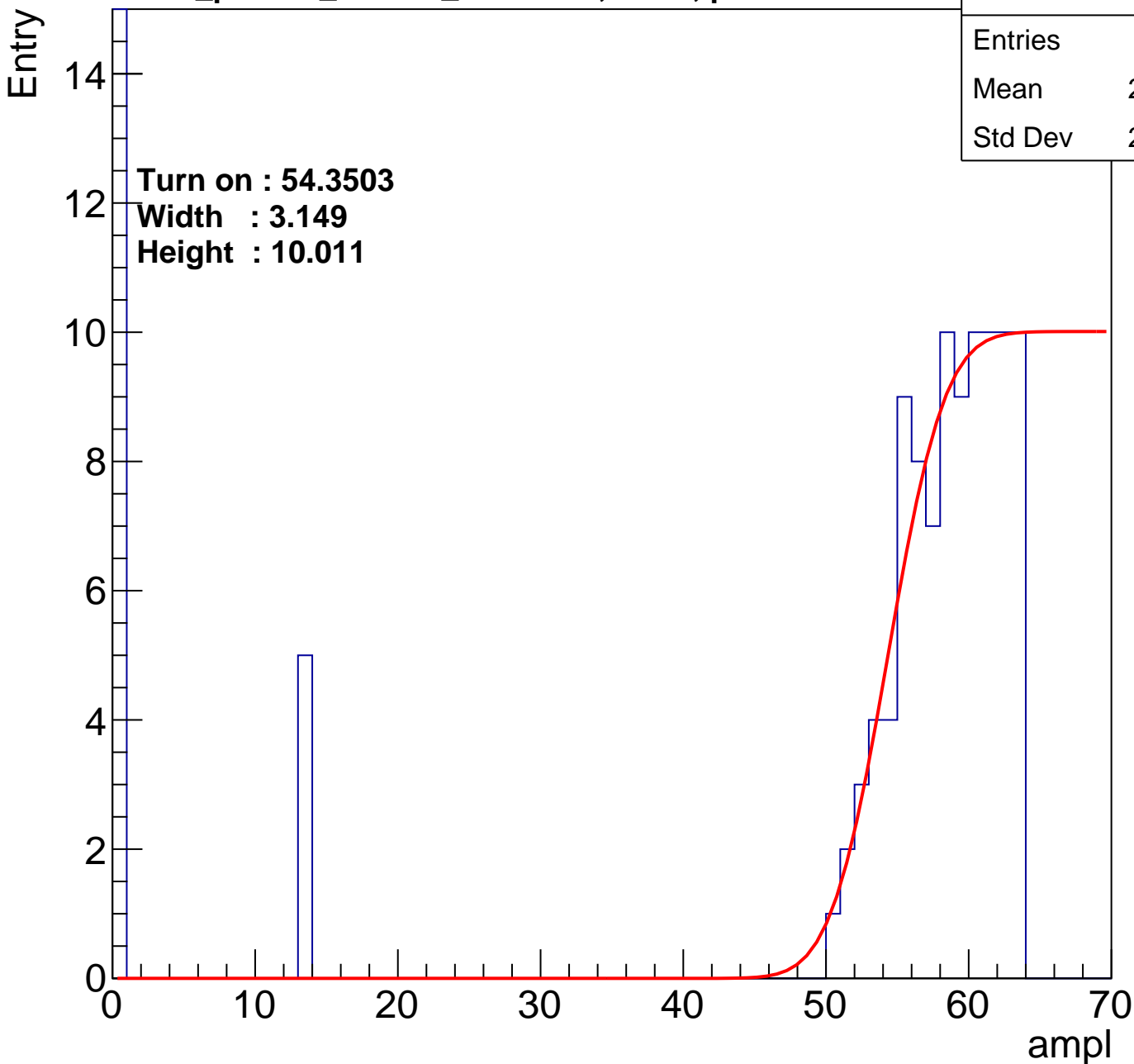
calib_packv5_033123_0516.root, FC#4, port A1

Entries	192
Mean	29.76
Std Dev	28.95

Turn on : 54.3503

Width : 3.149

Height : 10.011



B1L104S, U9-ch80

calib_packv5_033123_0516.root, FC#4, port A1

Entries	242
Mean	29.86
Std Dev	28.38

Turn on : 52.4669

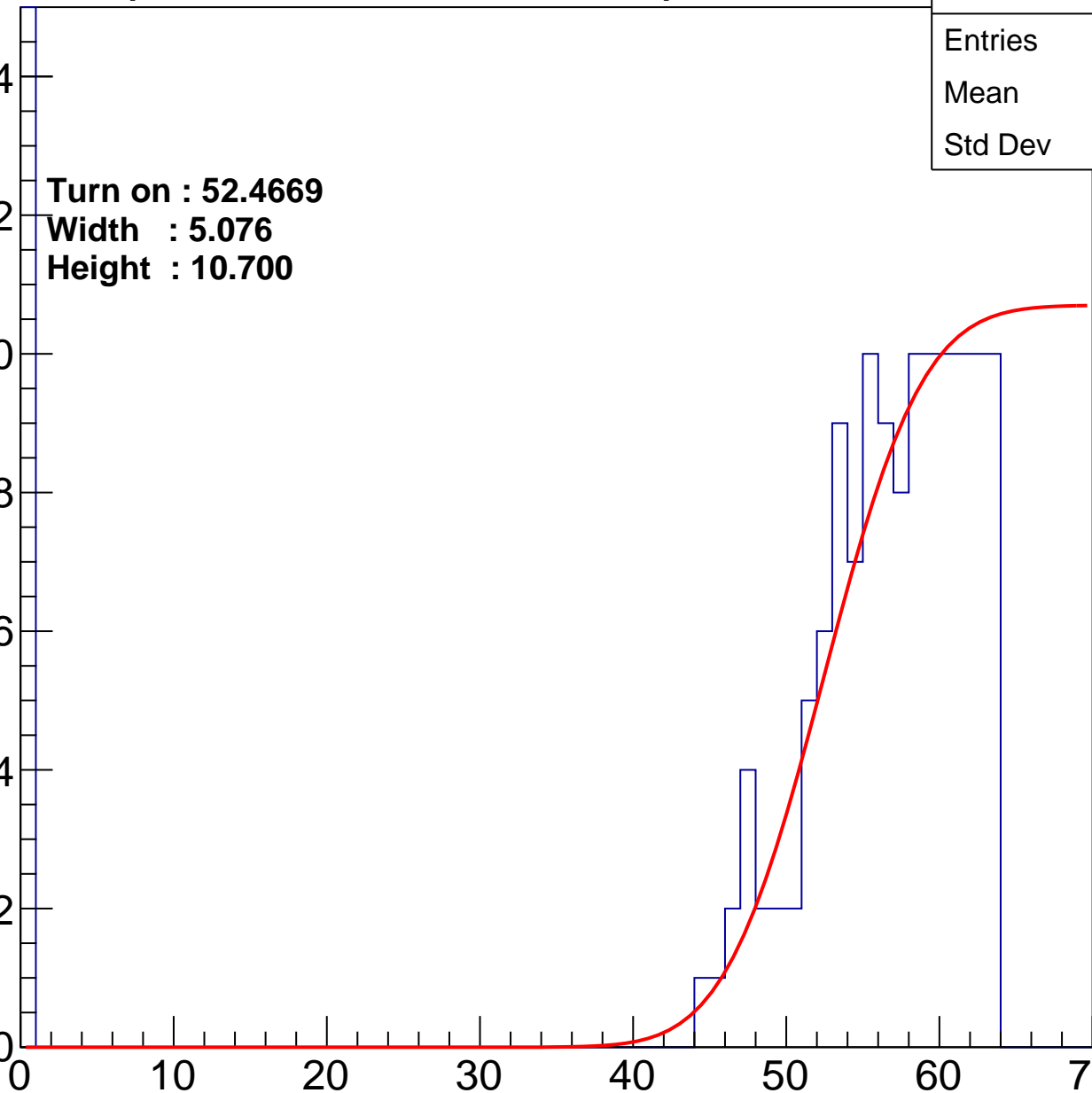
Width : 5.076

Height : 10.700

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch81

calib_packv5_033123_0516.root, FC#4, port A1

Entries	191
Mean	32.49
Std Dev	28.64

Turn on : 53.6390

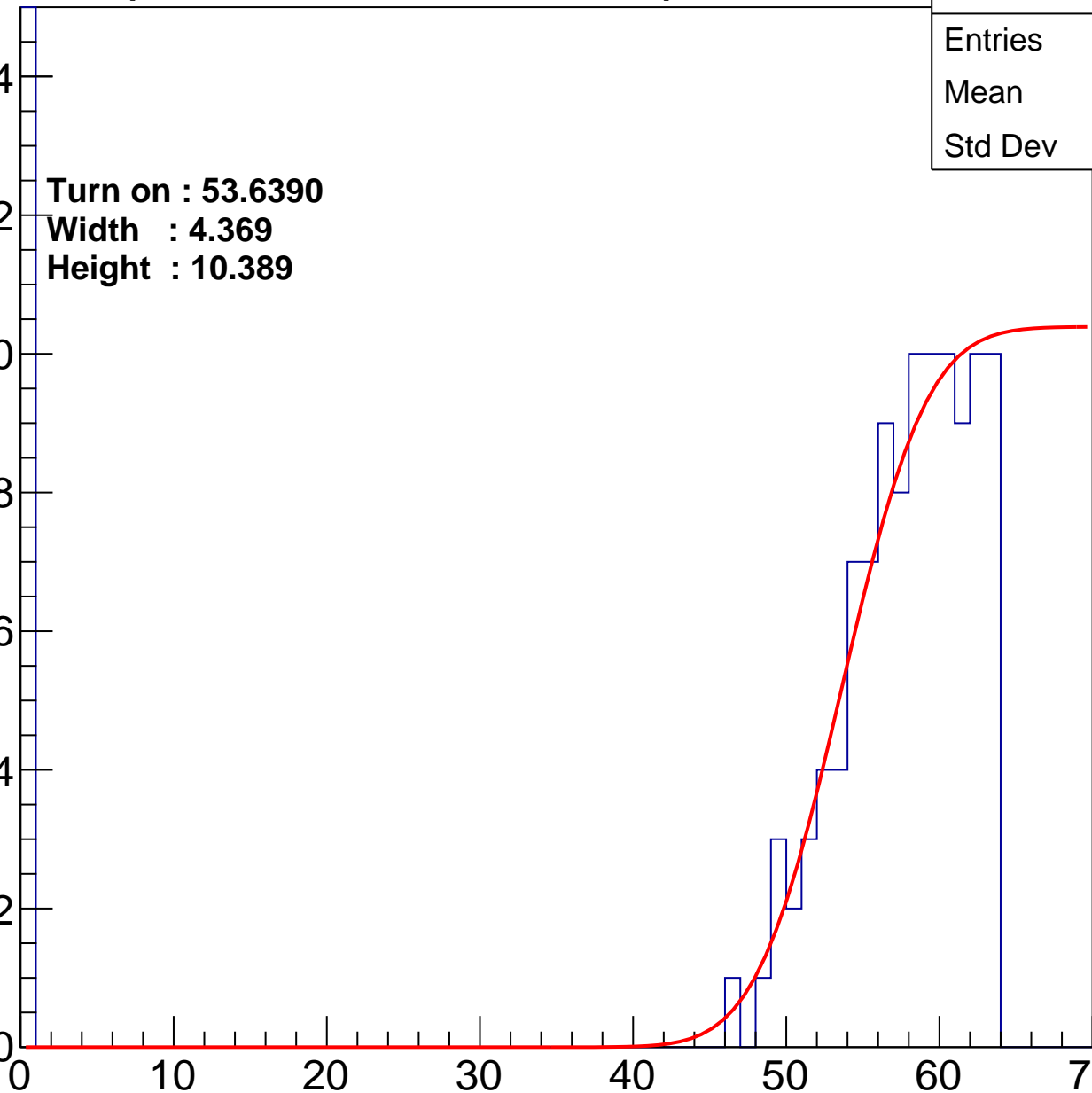
Width : 4.369

Height : 10.389

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch82

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	31.84
Std Dev	28.55

Turn on : 54.2766

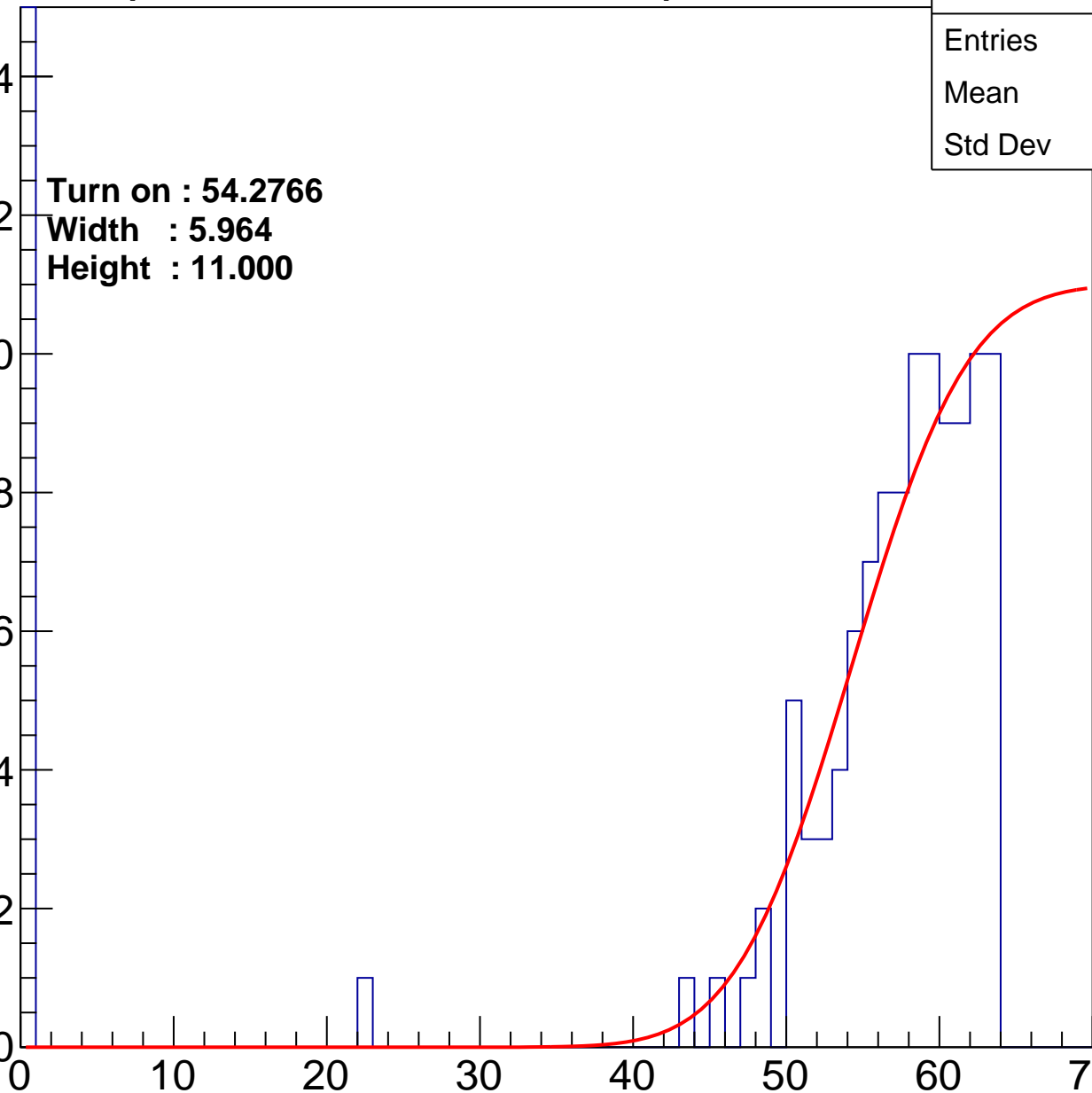
Width : 5.964

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch83

calib_packv5_033123_0516.root, FC#4, port A1

Entries	188
Mean	30.86
Std Dev	29.06

Turn on : 53.9424

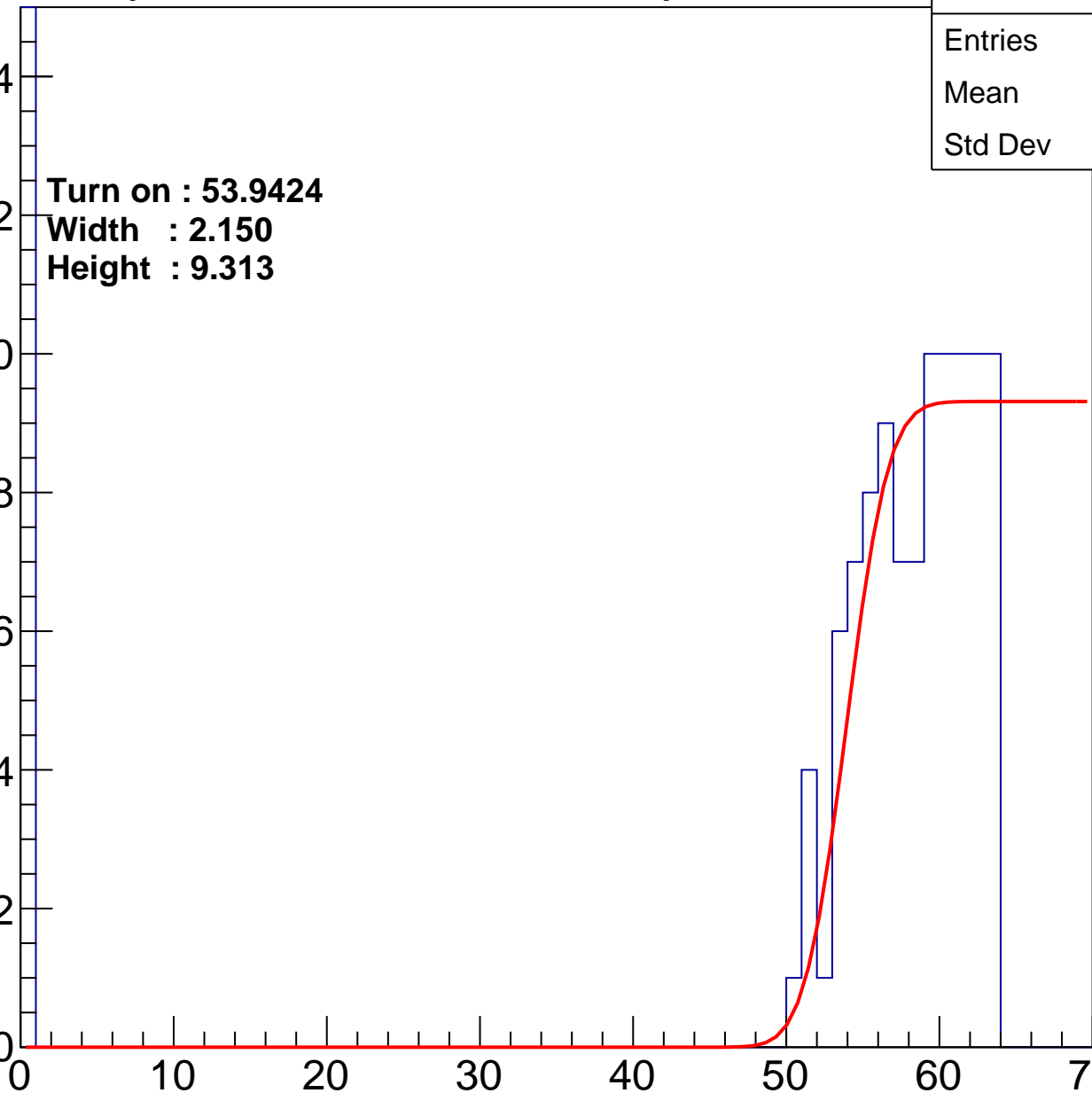
Width : 2.150

Height : 9.313

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch84

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	32.32
Std Dev	28.41

Turn on : 54.3562

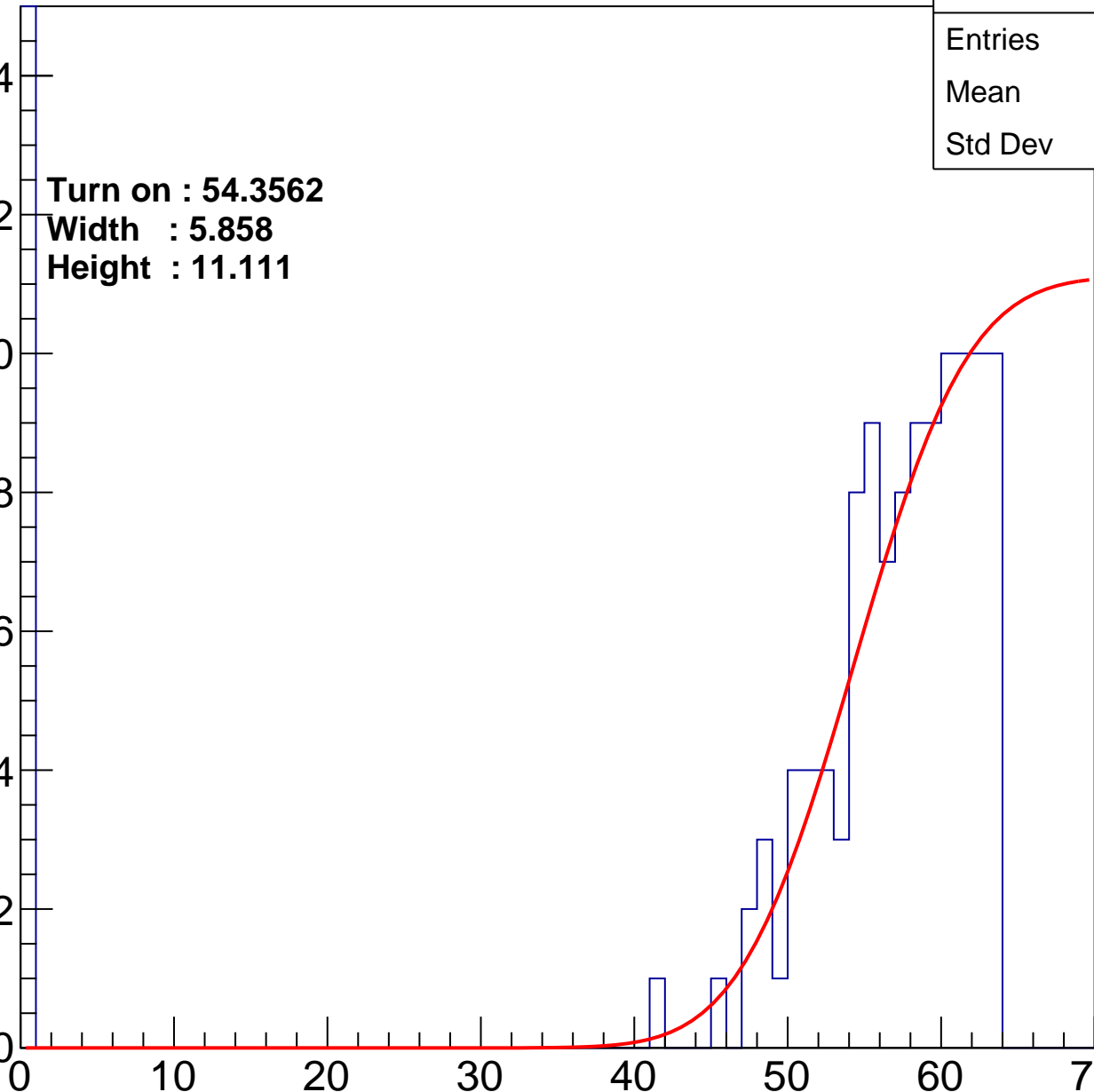
Width : 5.858

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch85

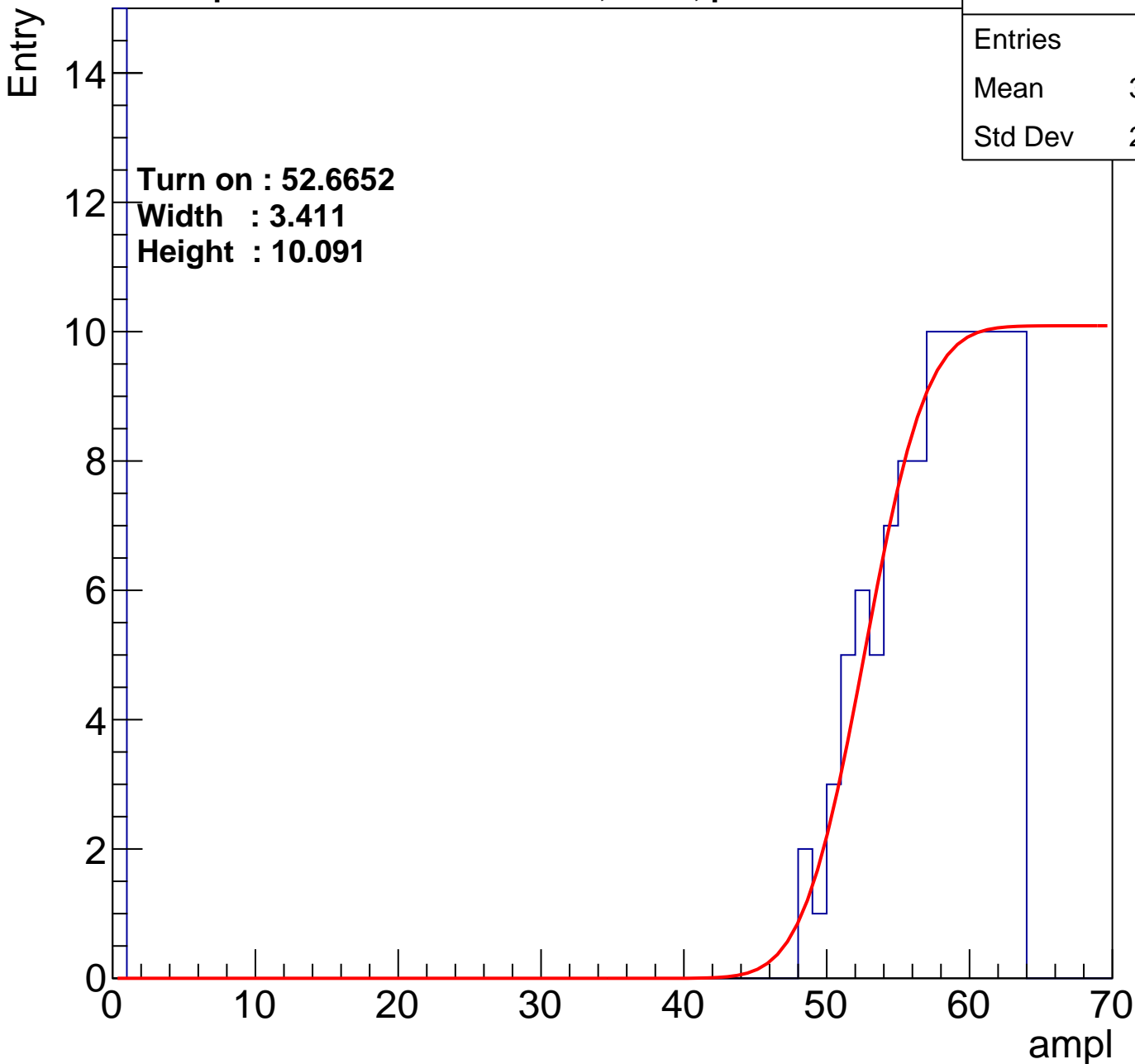
calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	33.64
Std Dev	28.39

Turn on : 52.6652

Width : 3.411

Height : 10.091



B1L104S, U9-ch86

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	29.73
Std Dev	28.98

Turn on : 54.7428

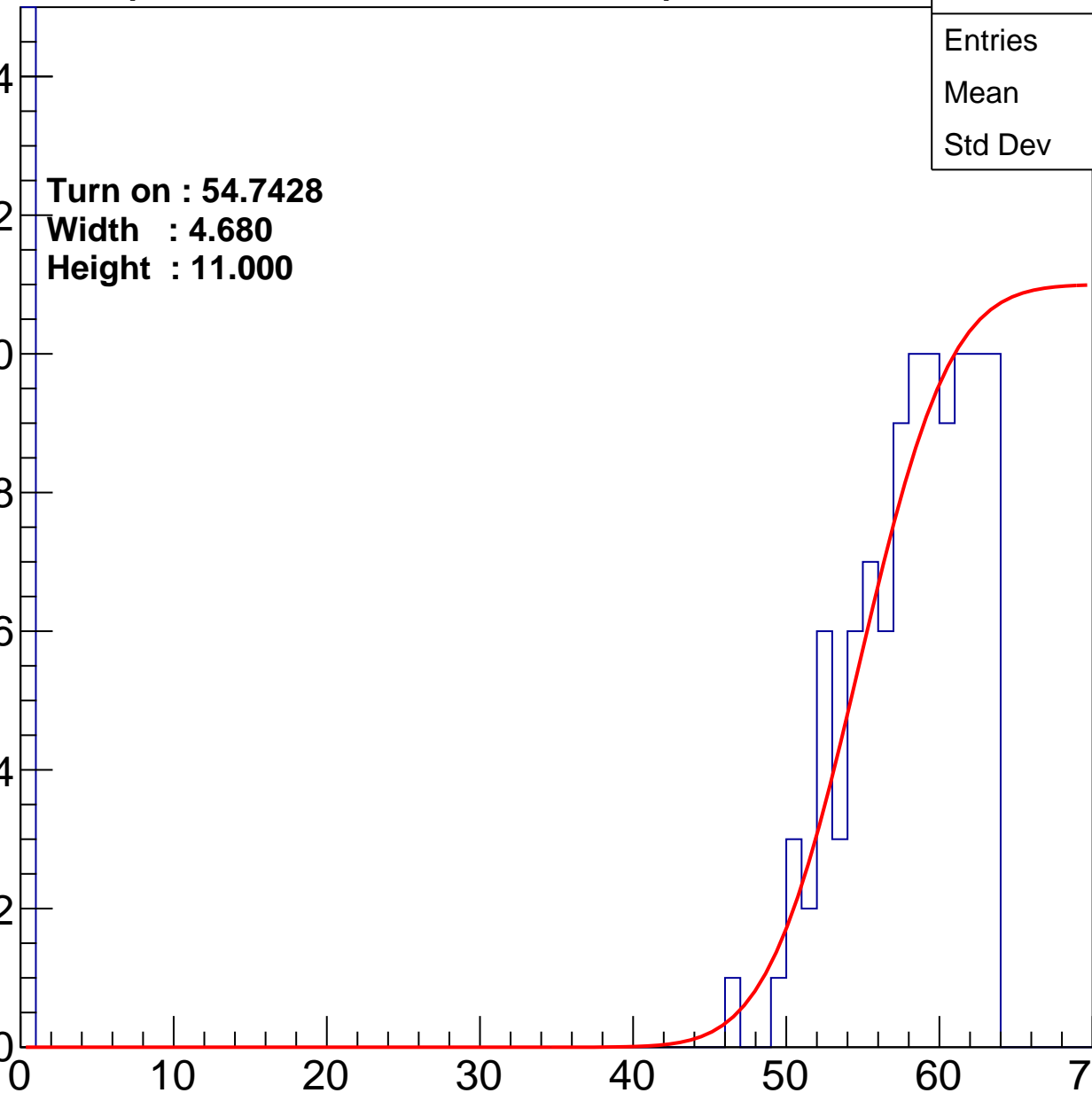
Width : 4.680

Height : 11.000

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch87

calib_packv5_033123_0516.root, FC#4, port A1

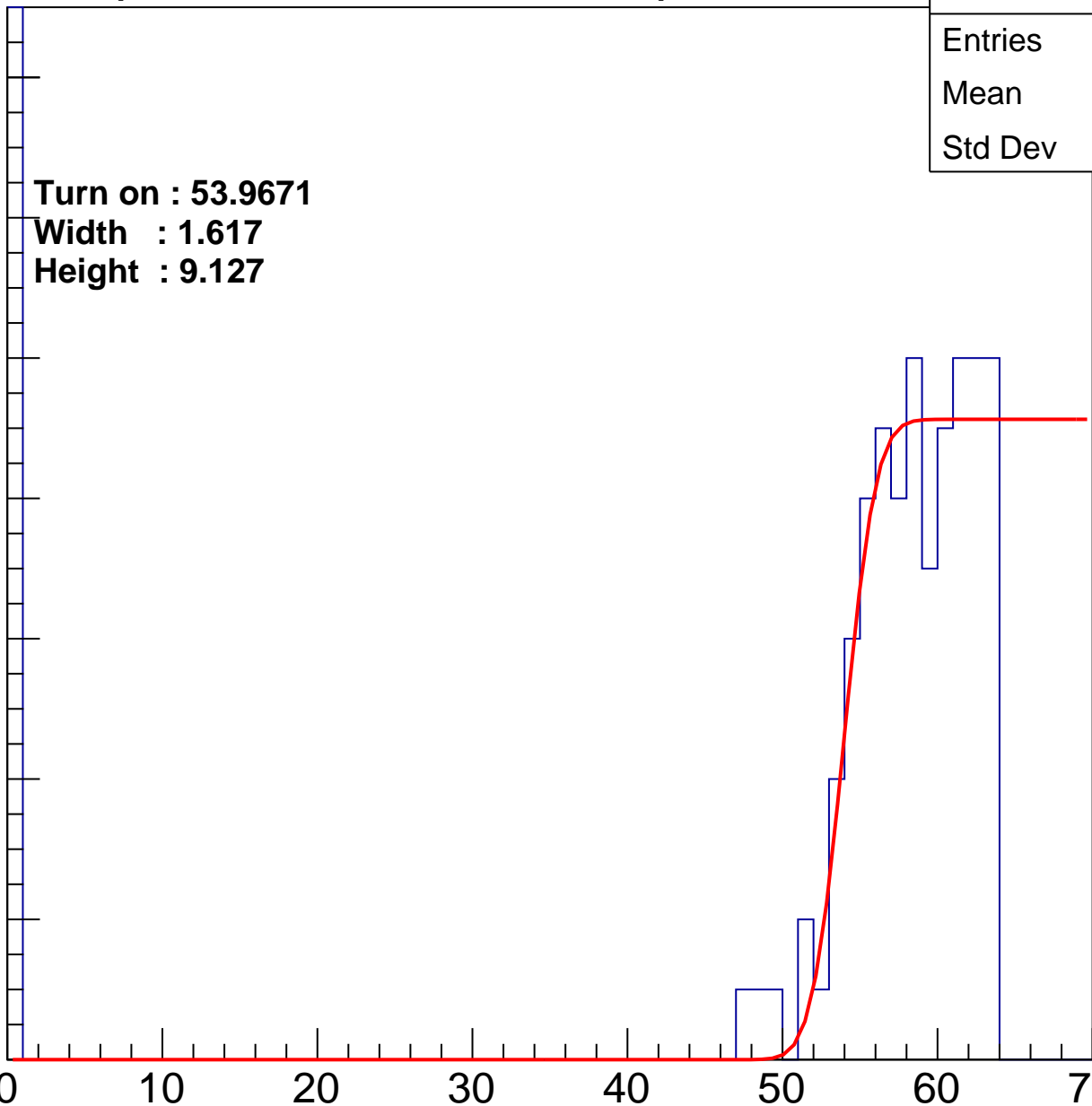
Entry

14
12
10
8
6
4
2
0

Turn on : 53.9671
Width : 1.617
Height : 9.127

Entries	191
Mean	29.46
Std Dev	29.12

ampl



B1L104S, U9-ch88

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	24.56
Std Dev	28.82

Turn on : 54.9669

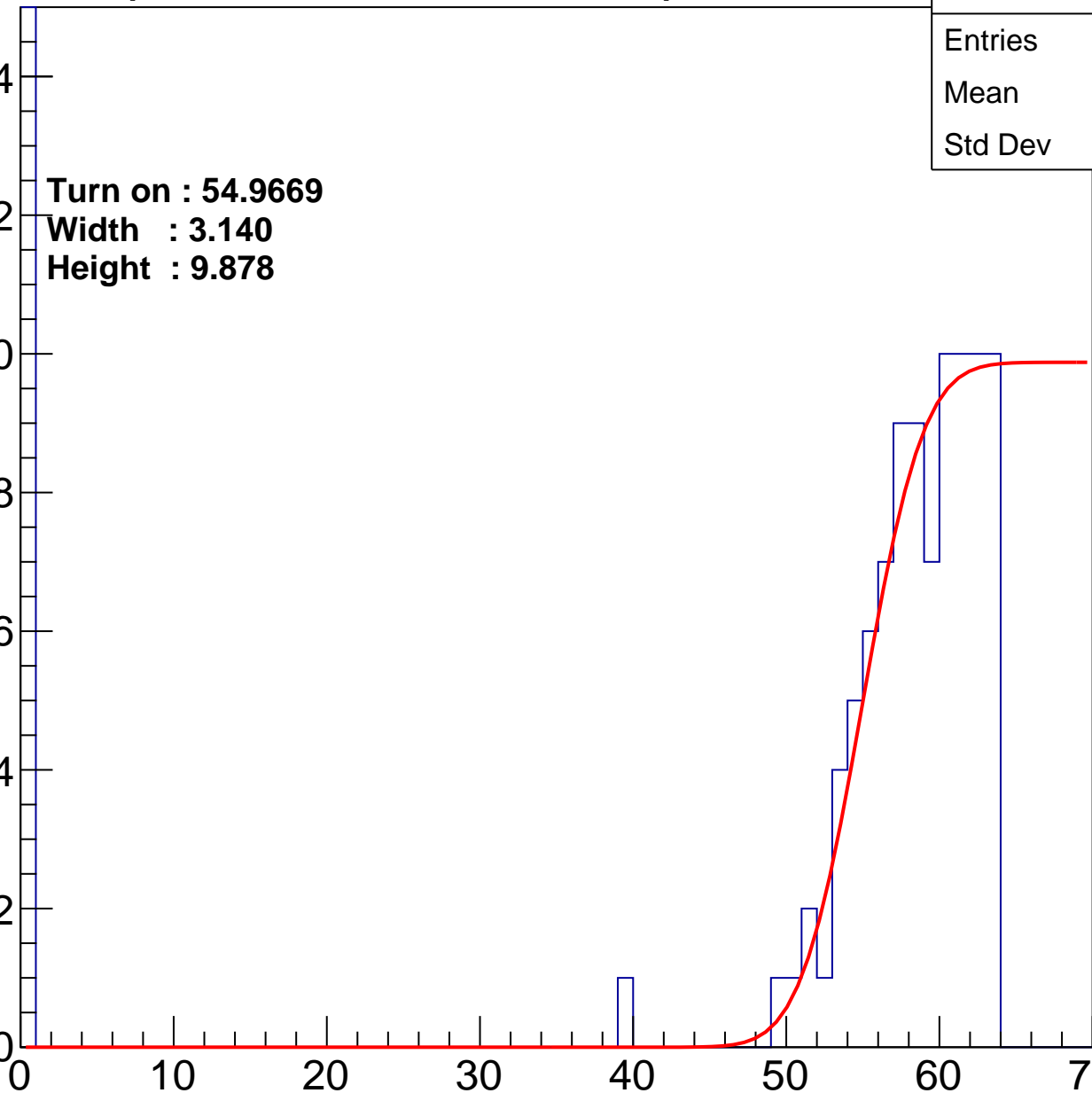
Width : 3.140

Height : 9.878

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch89

calib_packv5_033123_0516.root, FC#4, port A1

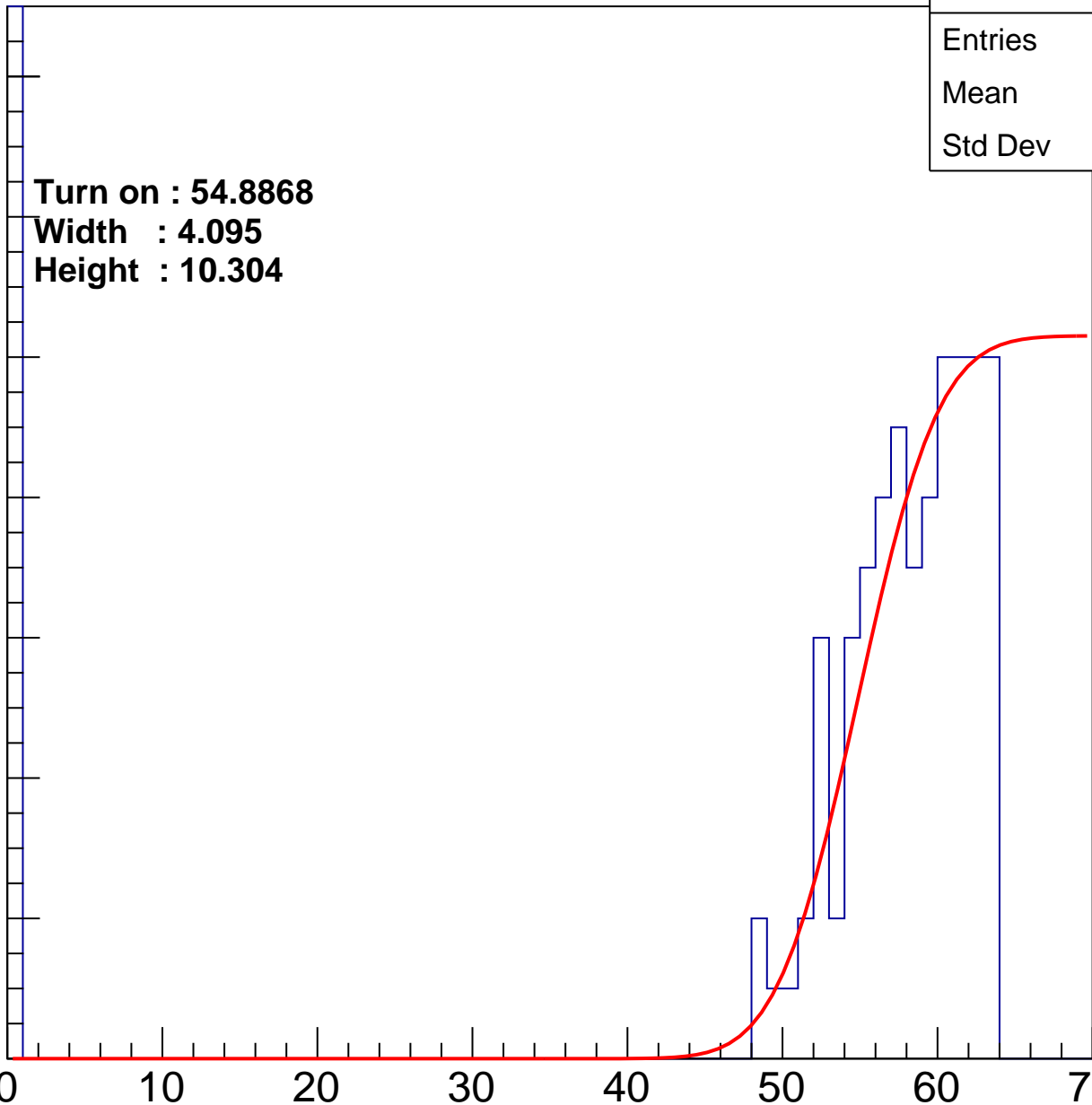
Entry

14
12
10
8
6
4
2
0

Turn on : 54.8868
Width : 4.095
Height : 10.304

Entries	190
Mean	30.12
Std Dev	29.01

ampl



B1L104S, U9-ch90

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	33.24
Std Dev	28.28

Turn on : 53.1395

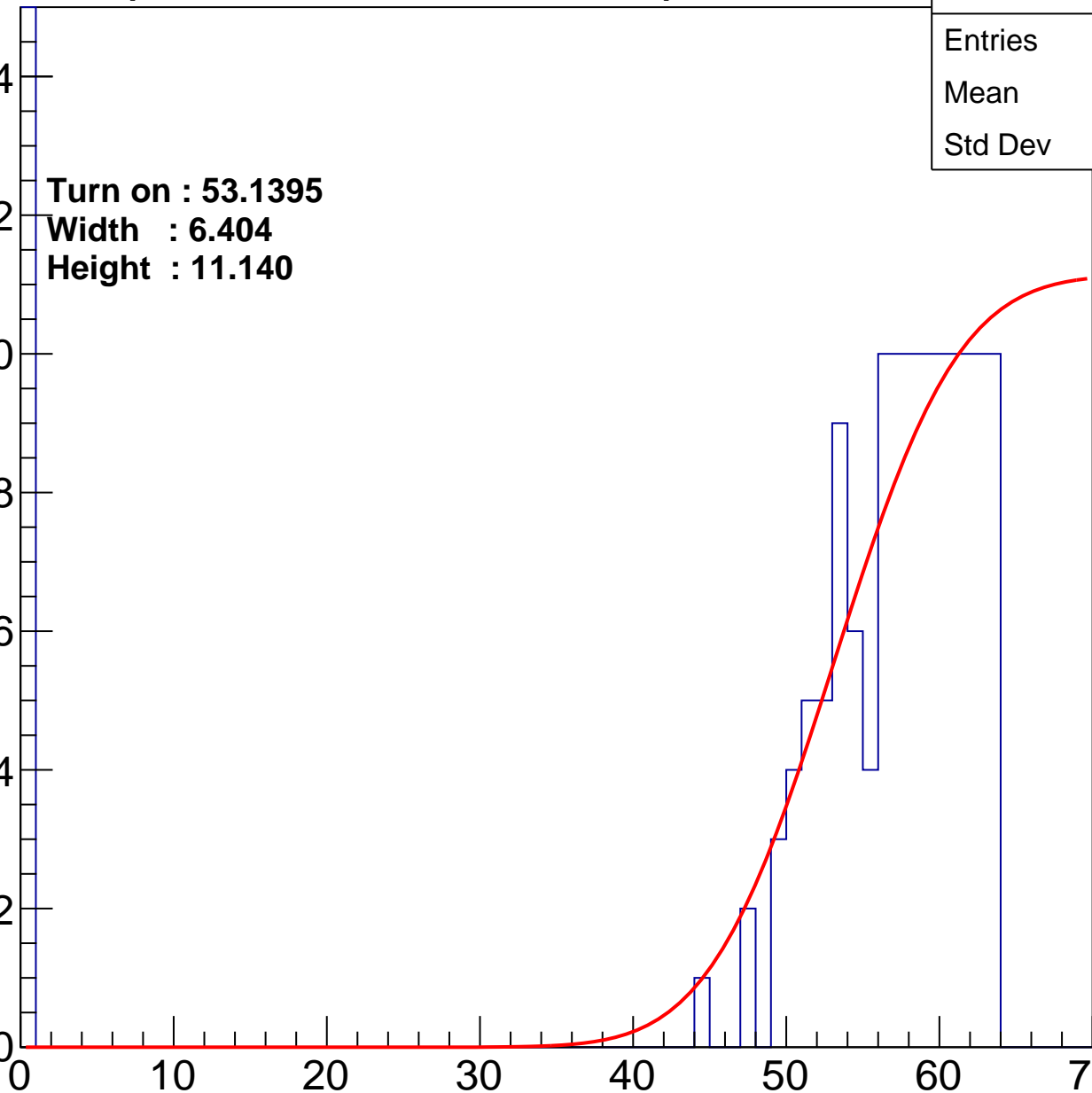
Width : 6.404

Height : 11.140

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch91

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	30.05
Std Dev	28.81

Turn on : 52.6912

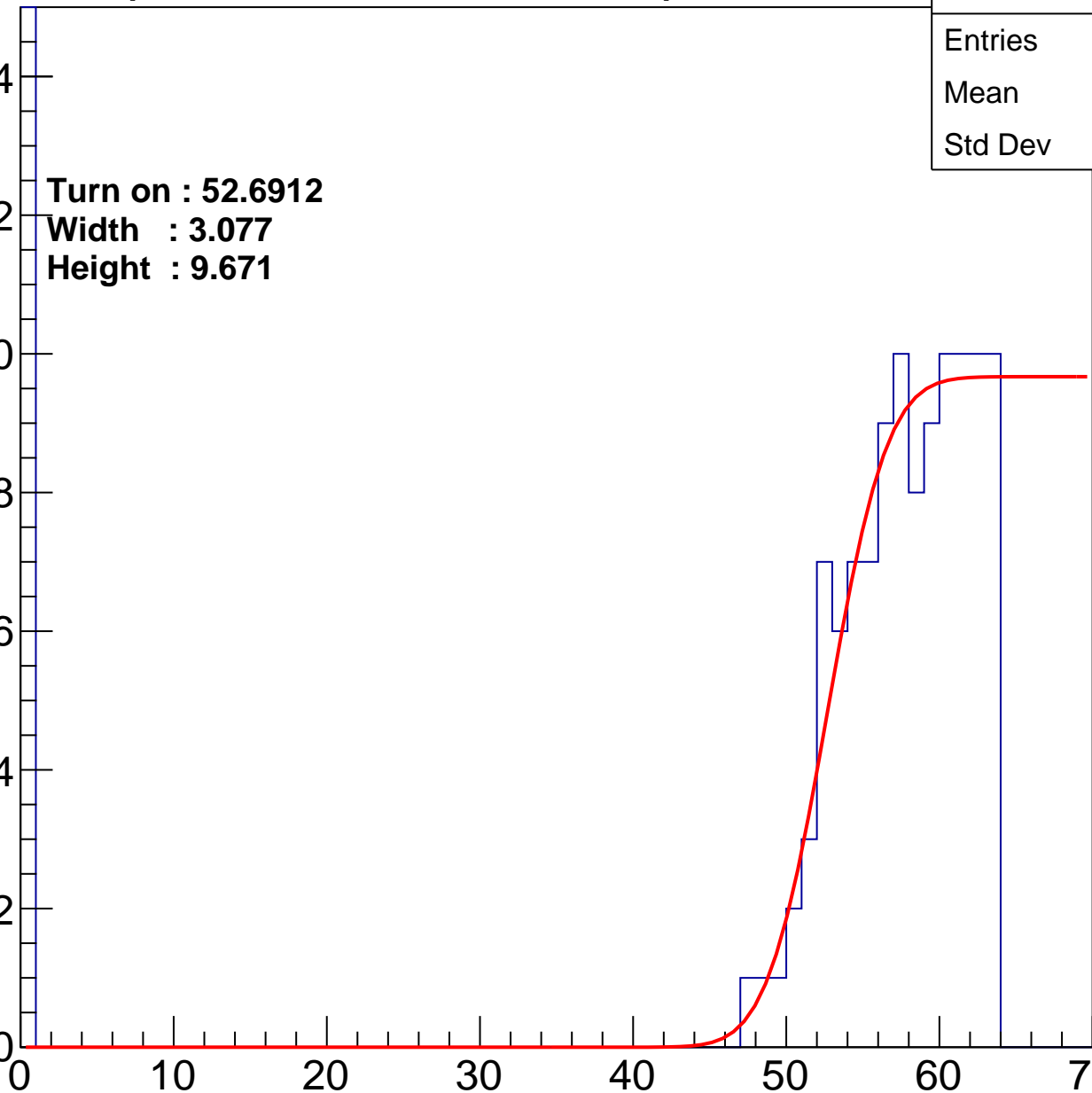
Width : 3.077

Height : 9.671

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch92

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	33.09
Std Dev	28.23

Turn on : 51.8473

Width : 4.577

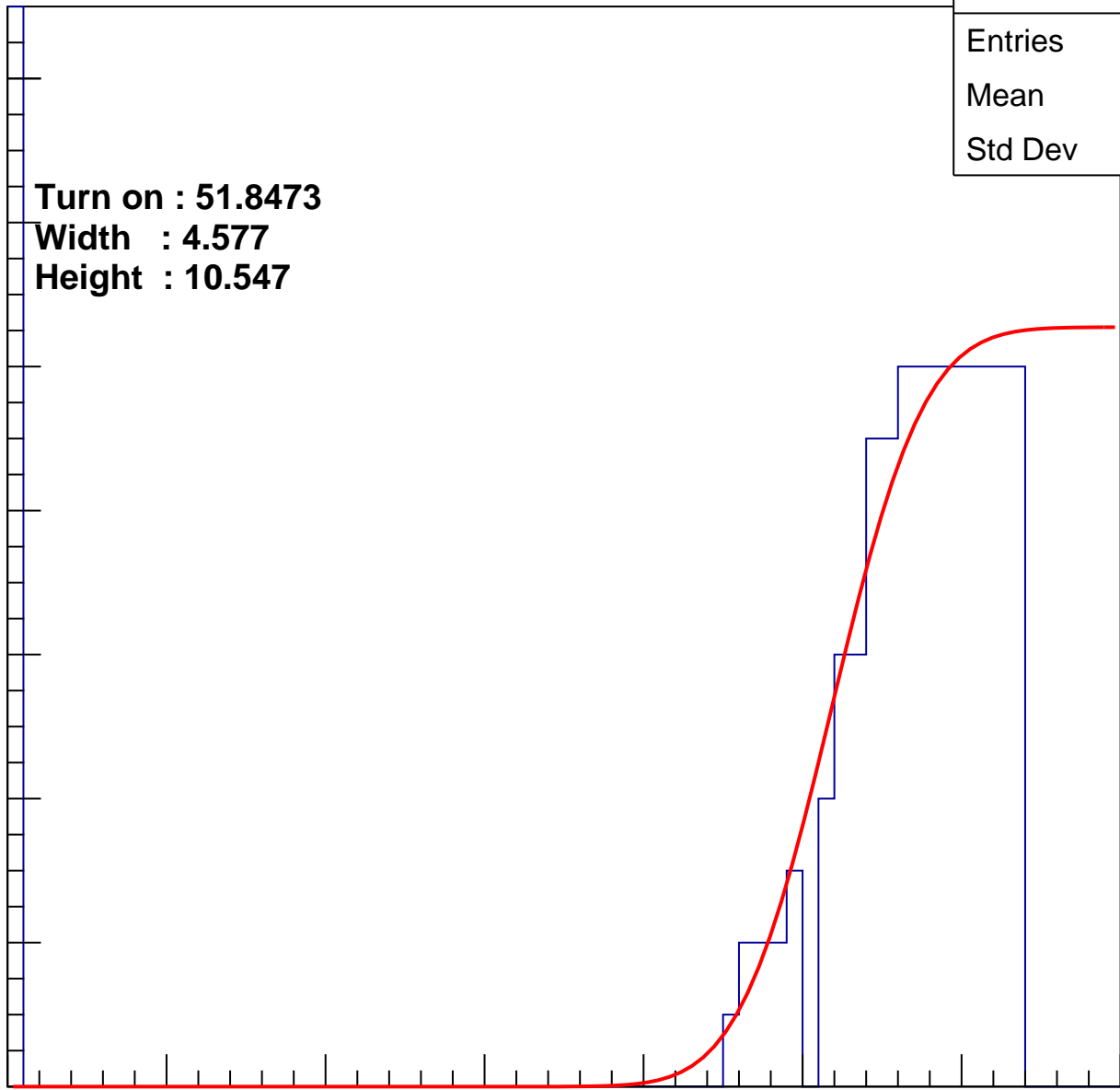
Height : 10.547

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch93

calib_packv5_033123_0516.root, FC#4, port A1

Entries	209
Mean	28.18
Std Dev	28.99

Turn on : 55.1414

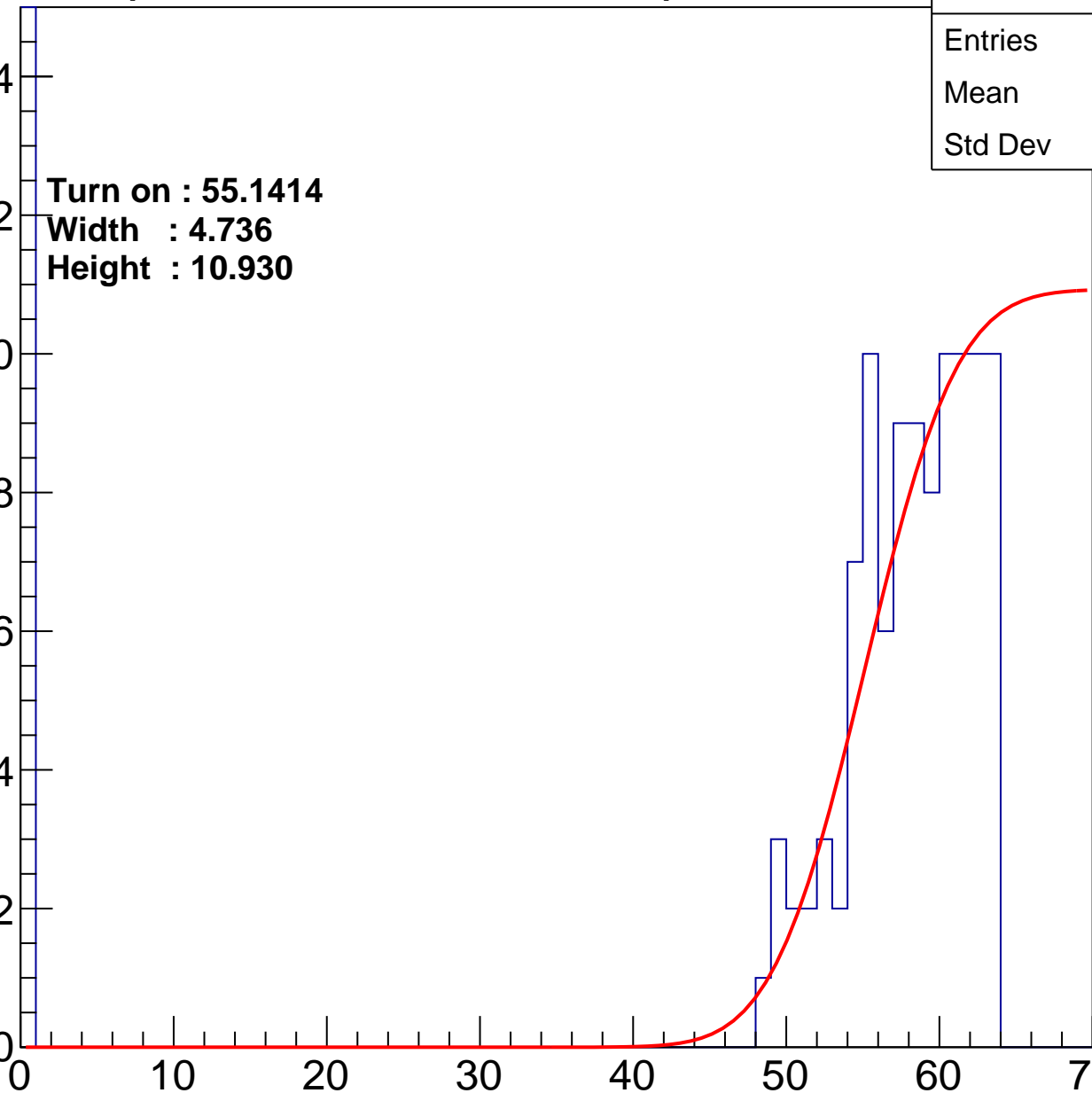
Width : 4.736

Height : 10.930

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch94

calib_packv5_033123_0516.root, FC#4, port A1

Entries	235
Mean	29.52
Std Dev	28.59

Turn on : 52.7155

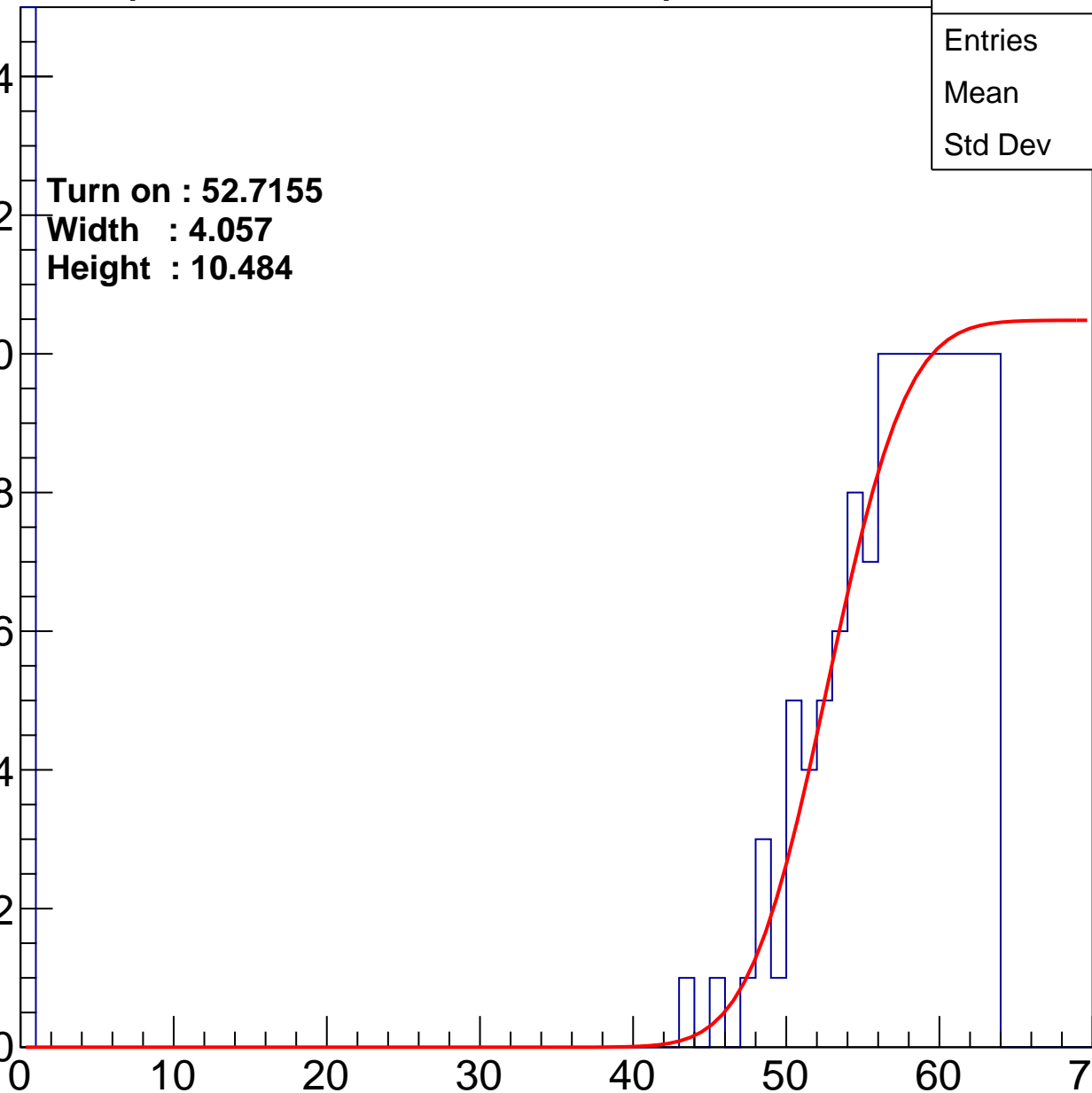
Width : 4.057

Height : 10.484

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch95

calib_packv5_033123_0516.root, FC#4, port A1

Entries	190
Mean	30.24
Std Dev	29.11

Turn on : 54.2443

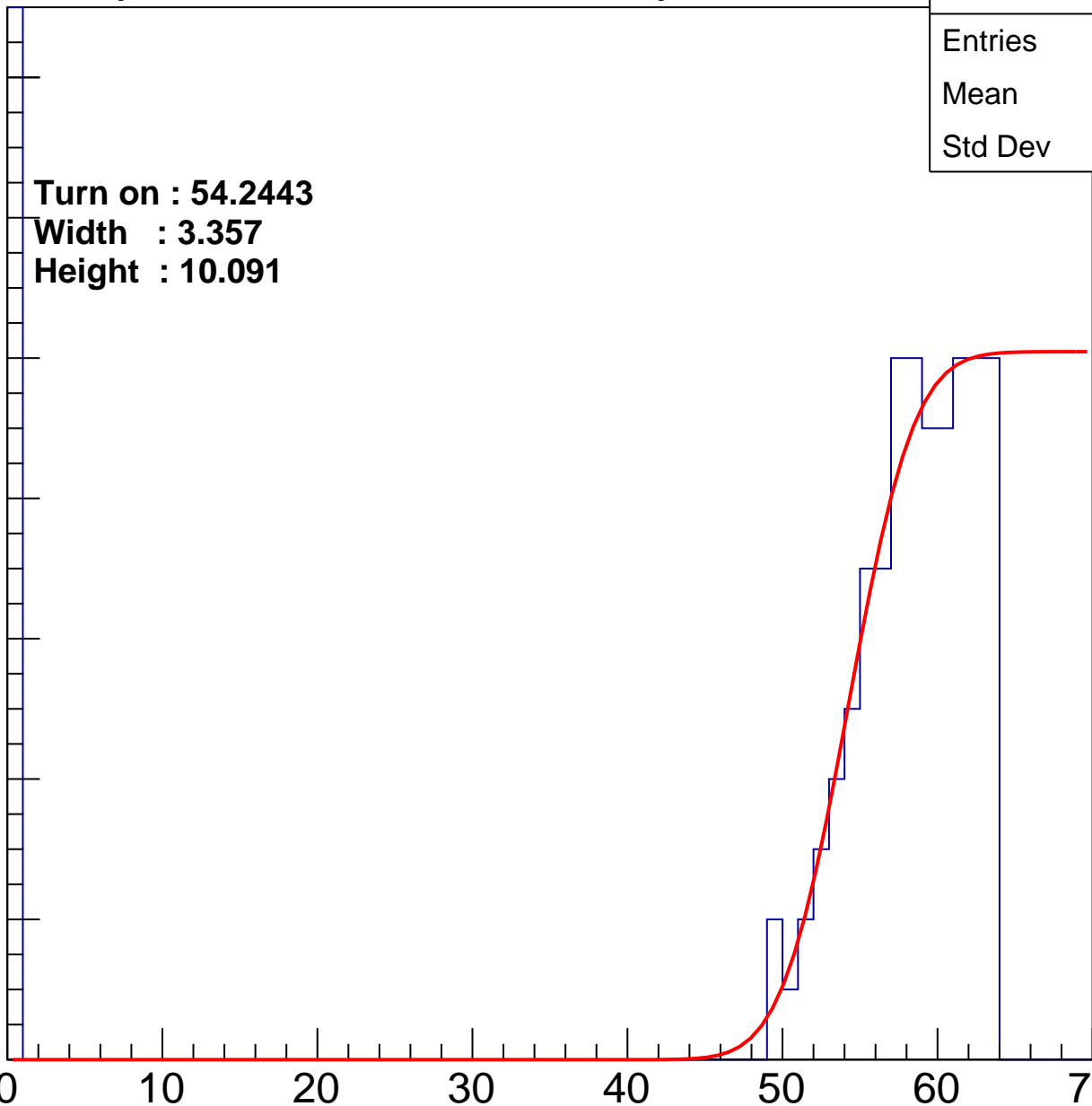
Width : 3.357

Height : 10.091

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch96

calib_packv5_033123_0516.root, FC#4, port A1

Entries	198
Mean	31.27
Std Dev	28.72

Turn on : 53.7930

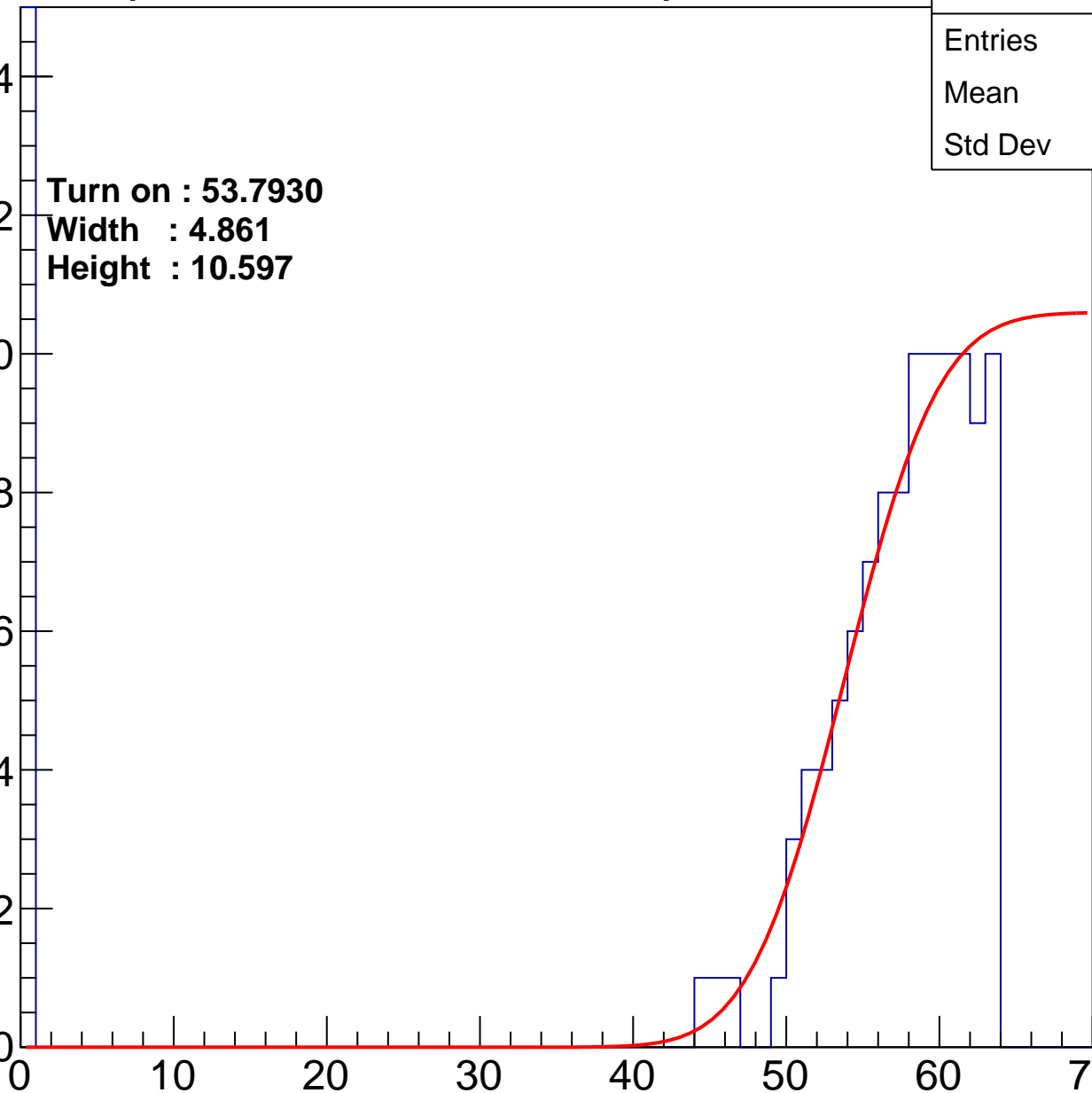
Width : 4.861

Height : 10.597

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch97

calib_packv5_033123_0516.root, FC#4, port A1

Entries	178
Mean	36.65
Std Dev	27.67

Turn on : 52.1326

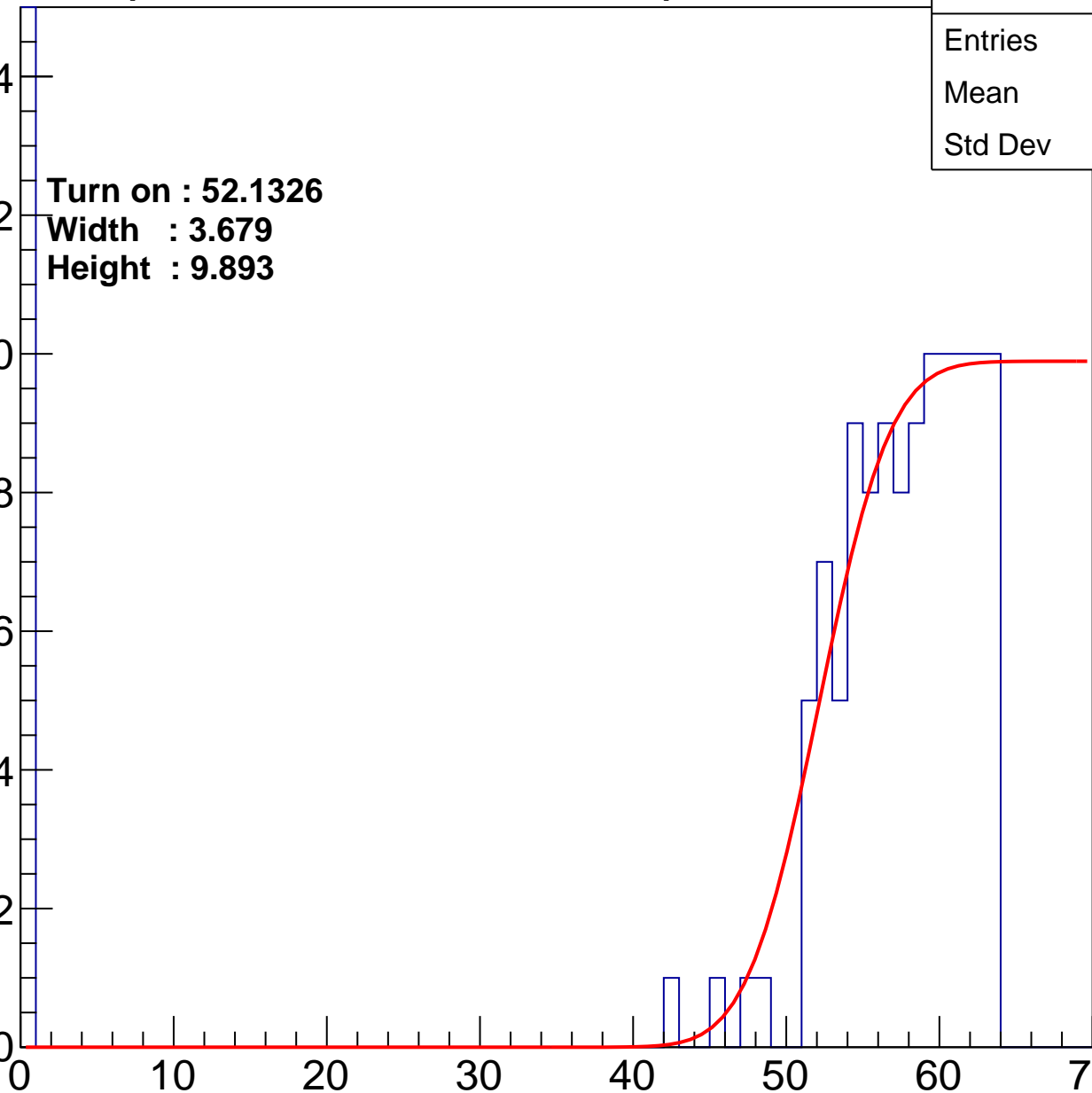
Width : 3.679

Height : 9.893

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch98

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	31.48
Std Dev	28.46

Turn on : 53.9084

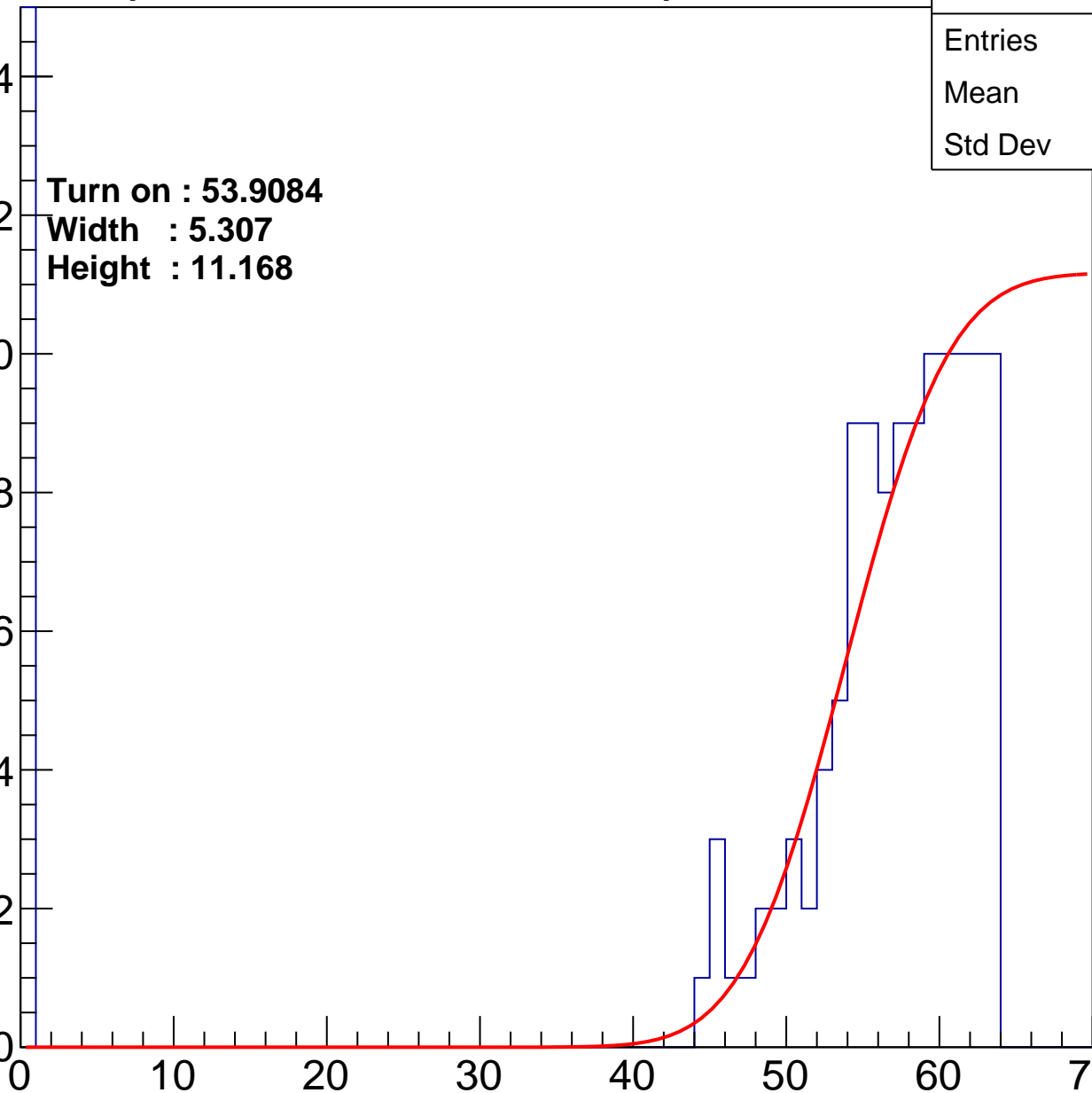
Width : 5.307

Height : 11.168

Entry

14
12
10
8
6
4
2
0

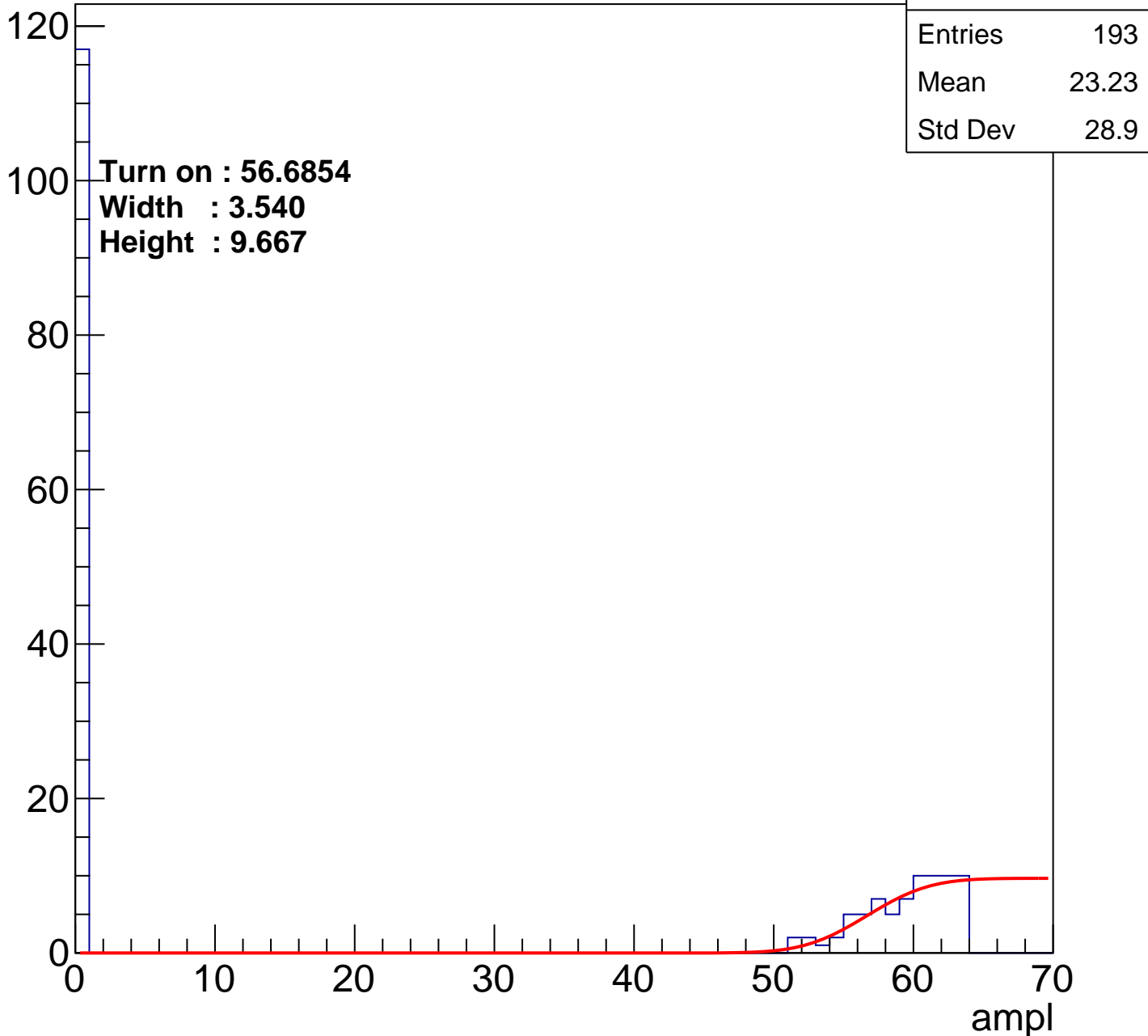
ampl



B1L104S, U9-ch99

calib_packv5_033123_0516.root, FC#4, port A1

Entry



B1L104S, U9-ch100

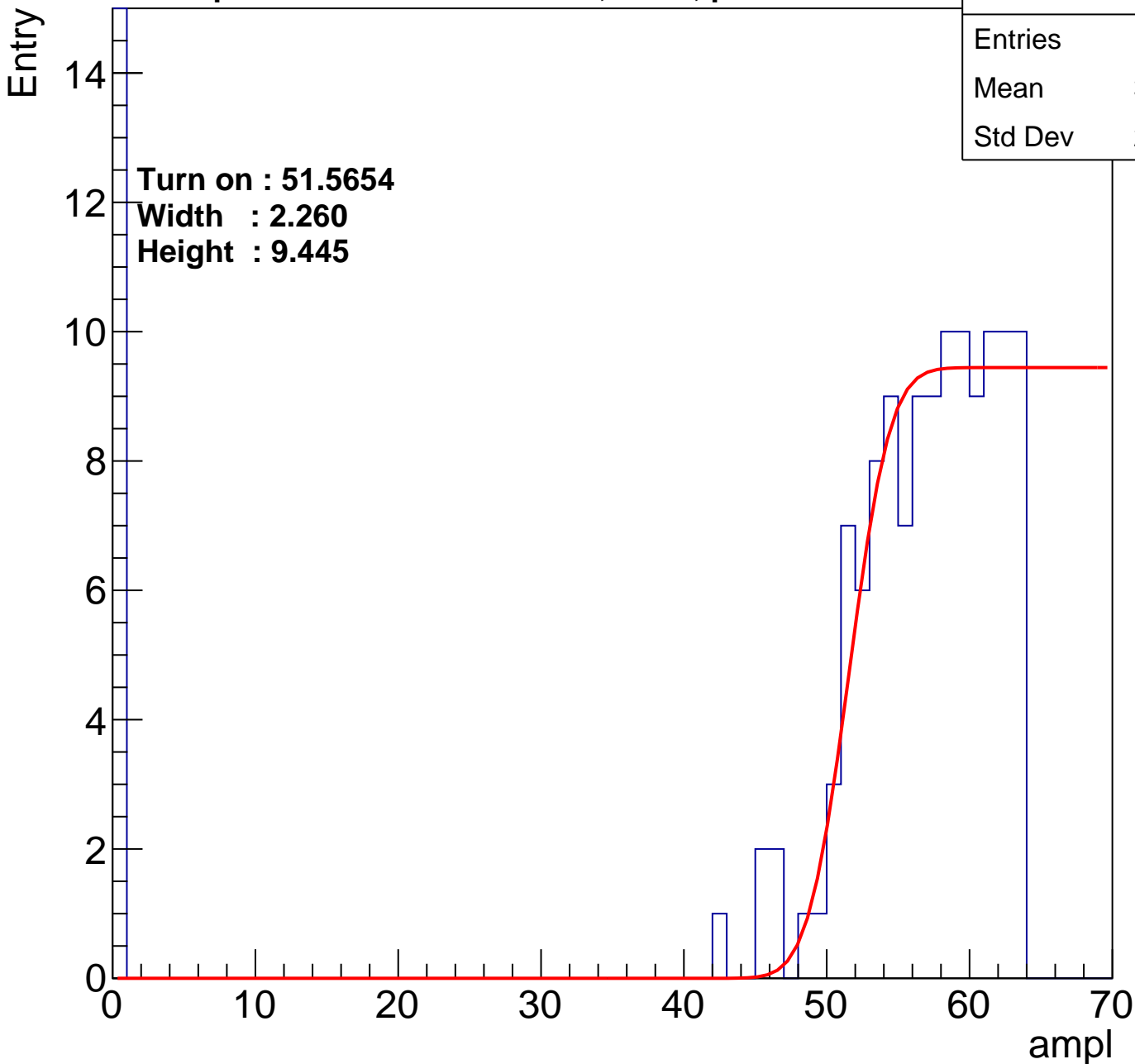
calib_packv5_033123_0516.root, FC#4, port A1

Entries	216
Mean	32.51
Std Dev	28.21

Turn on : 51.5654

Width : 2.260

Height : 9.445



B1L104S, U9-ch101

calib_packv5_033123_0516.root, FC#4, port A1

Entries	169
Mean	35.1
Std Dev	28.27

Turn on : 54.4014

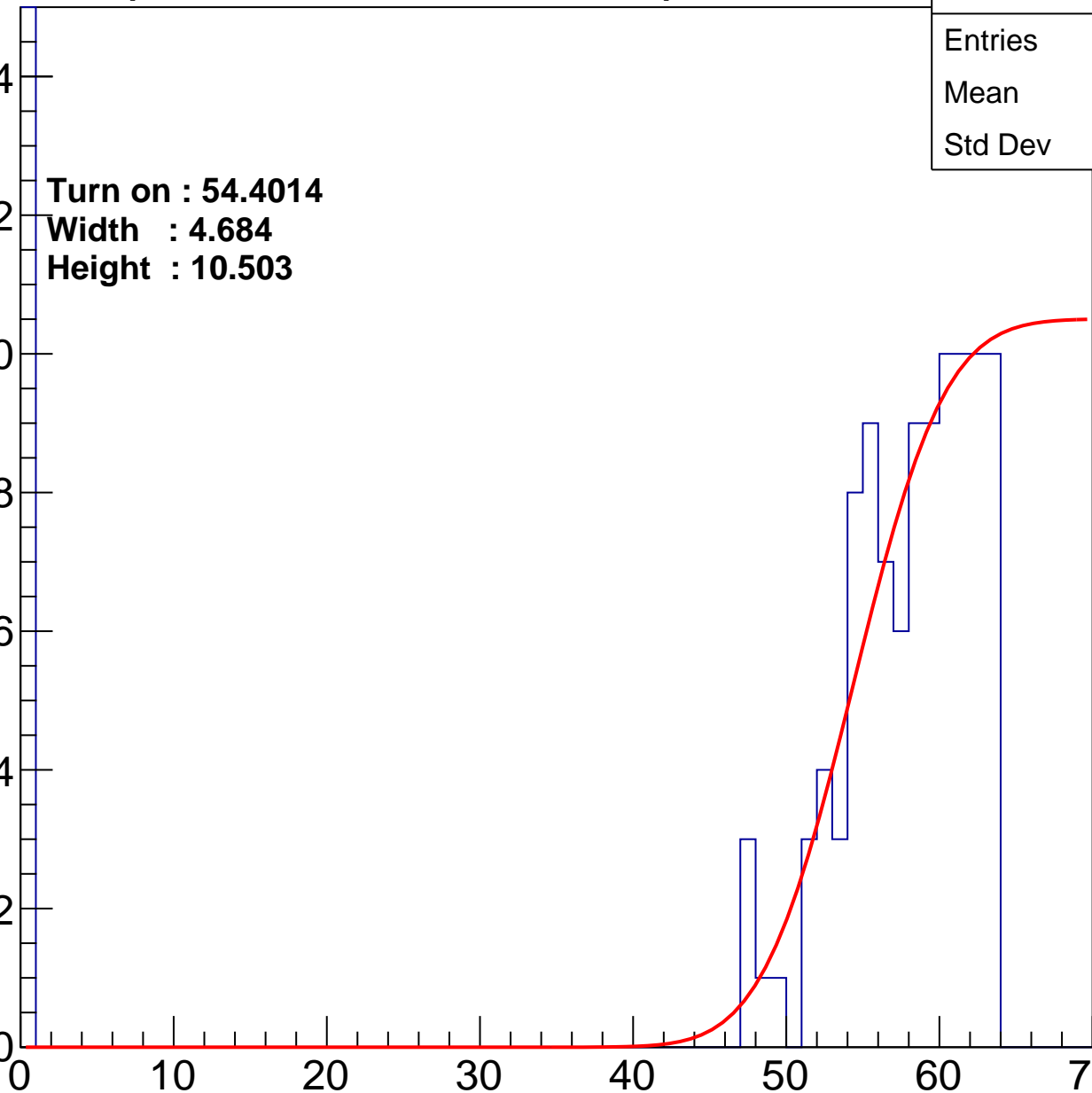
Width : 4.684

Height : 10.503

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch102

calib_packv5_033123_0516.root, FC#4, port A1

Entries	201
Mean	36.94
Std Dev	26.99

Turn on : 50.7179

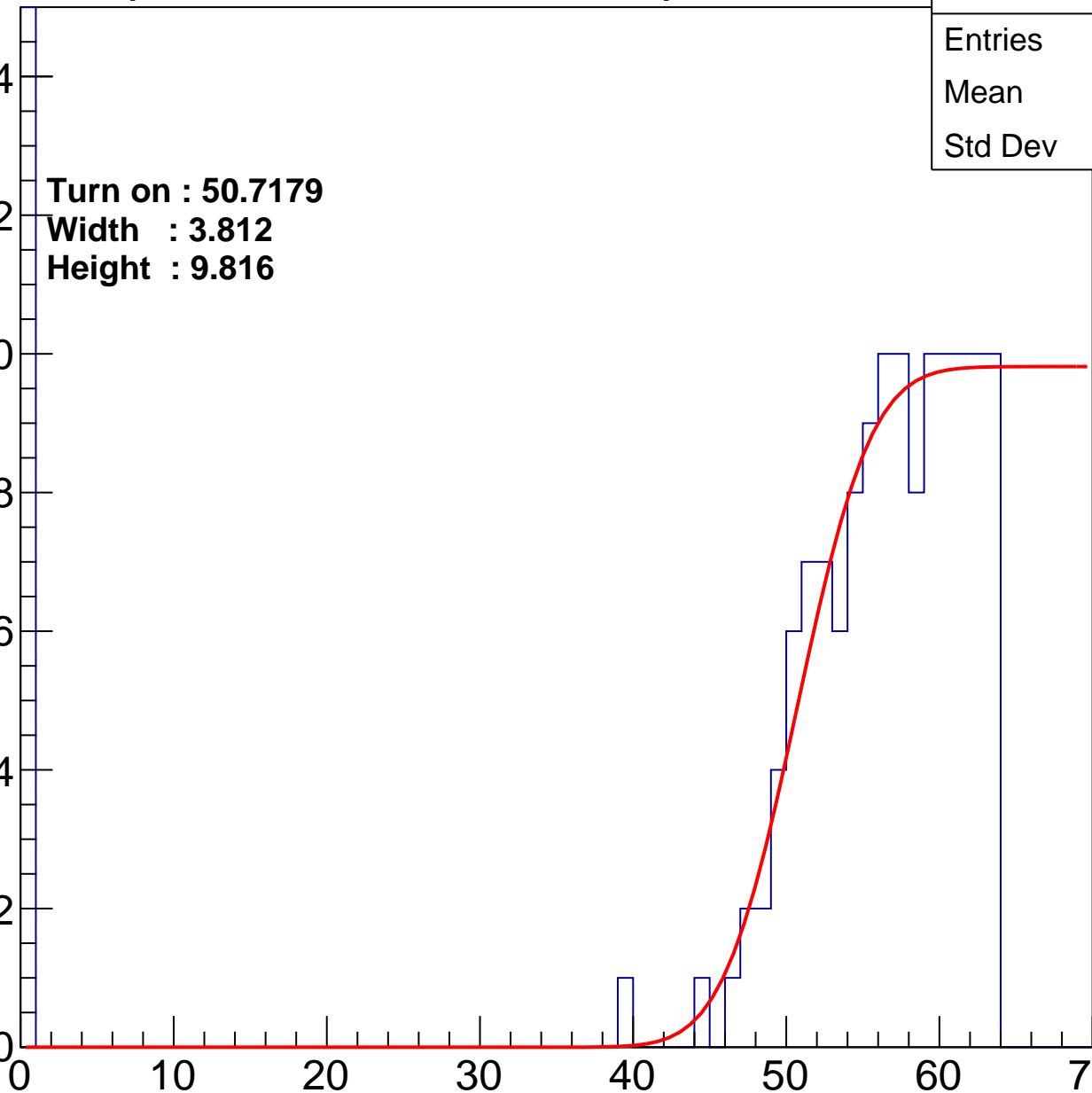
Width : 3.812

Height : 9.816

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch103

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	31.1
Std Dev	28.48

Turn on : 53.4724

Width : 3.725

Height : 9.808

Entry

14

12

10

8

6

4

2

0

0

10

20

30

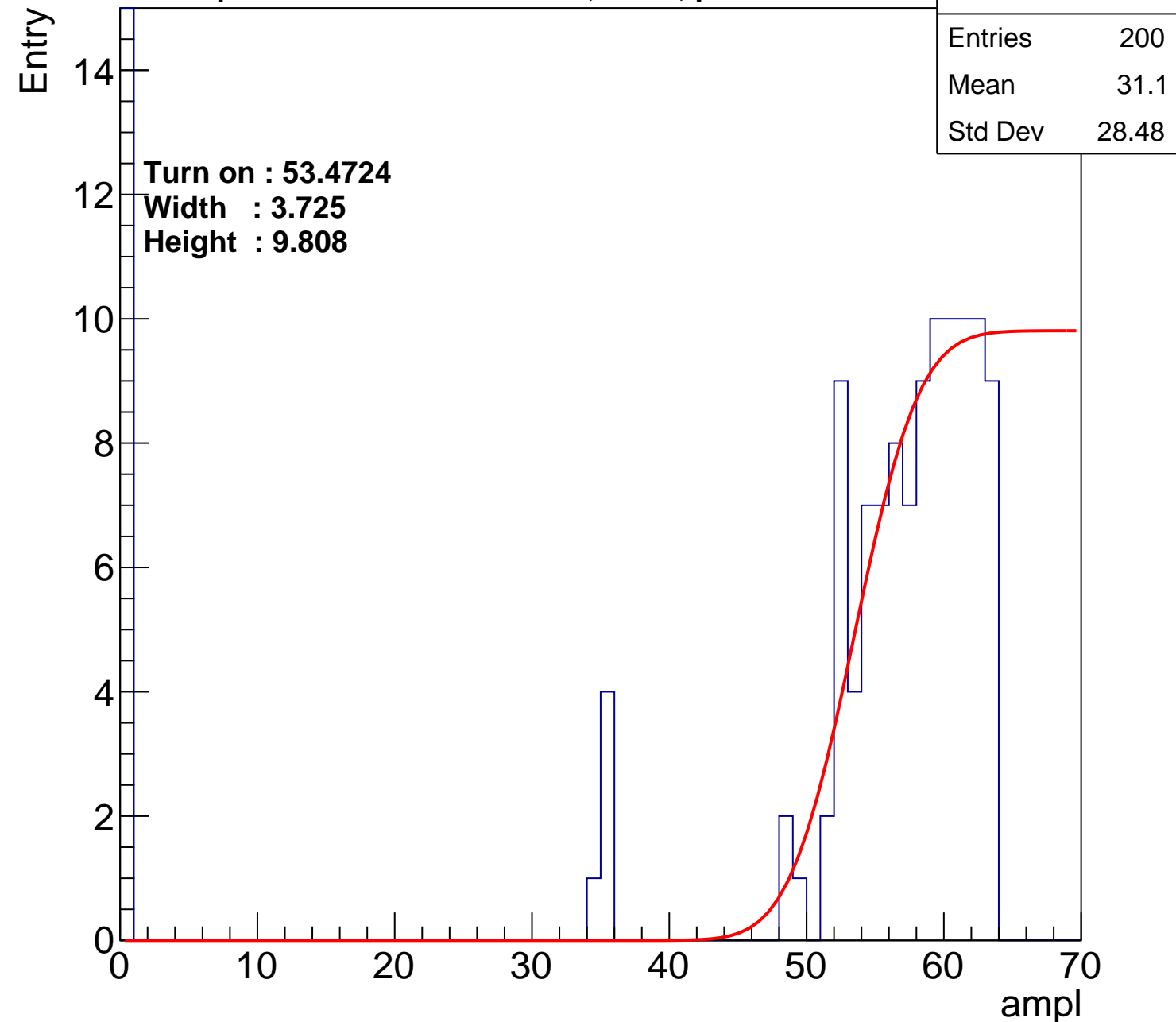
40

50

60

70

ampl



B1L104S, U9-ch104

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	33.91
Std Dev	27.99

Turn on : 52.4450

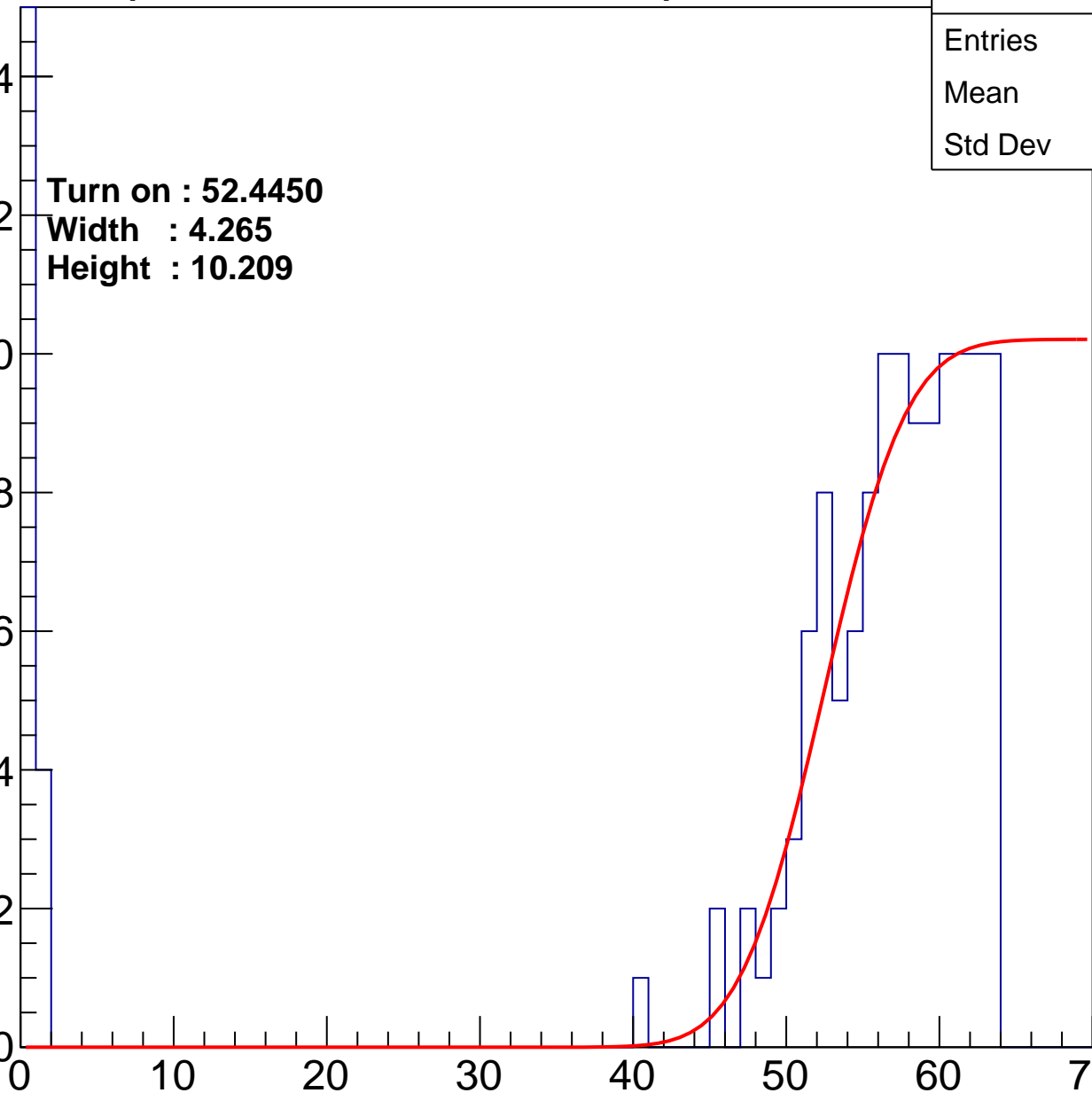
Width : 4.265

Height : 10.209

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch105

calib_packv5_033123_0516.root, FC#4, port A1

Entries	182
Mean	29.42
Std Dev	29.21

Turn on : 55.2046

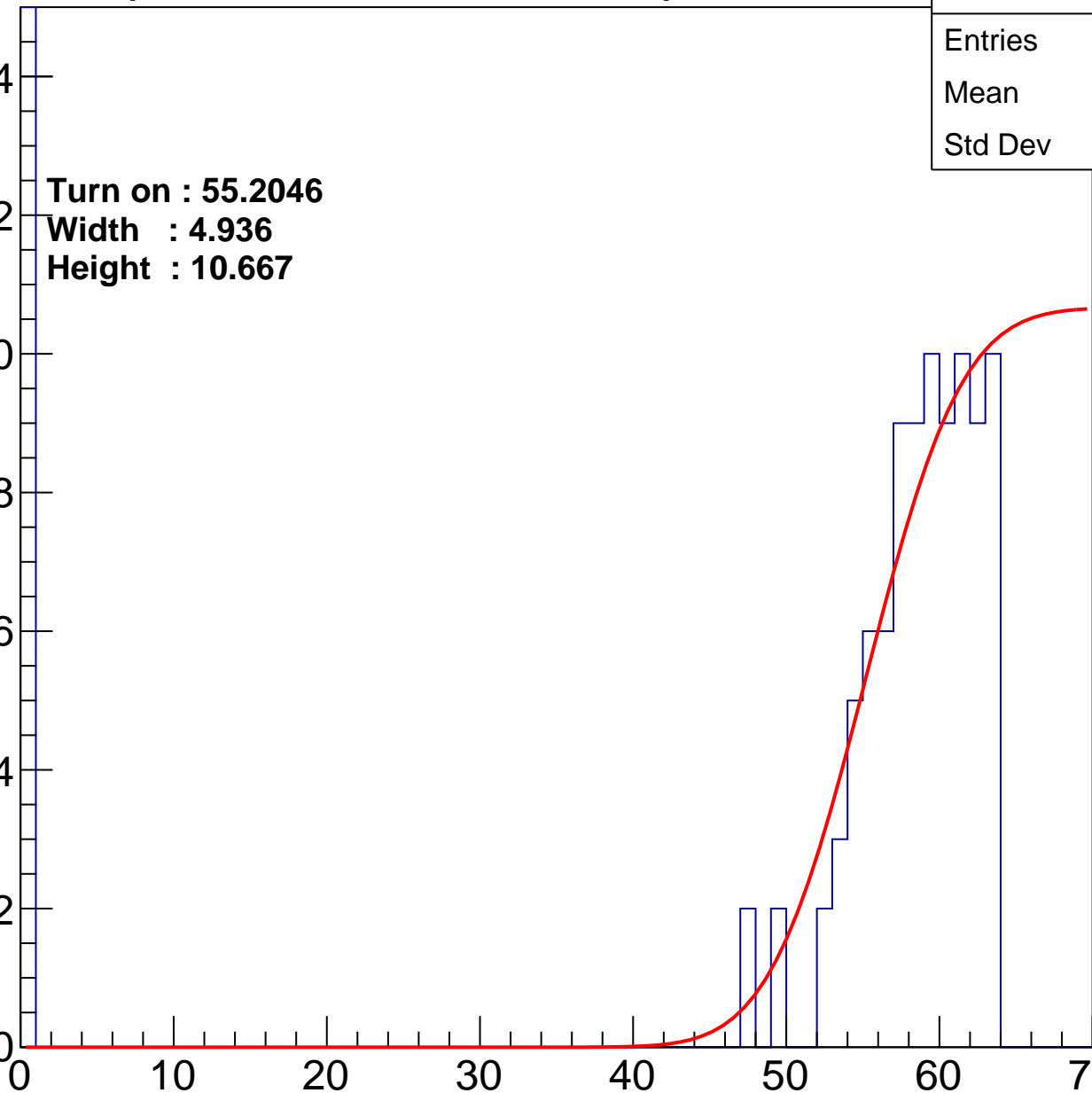
Width : 4.936

Height : 10.667

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch106

calib_packv5_033123_0516.root, FC#4, port A1

Entries	196
Mean	34.76
Std Dev	27.89

Turn on : 52.3586

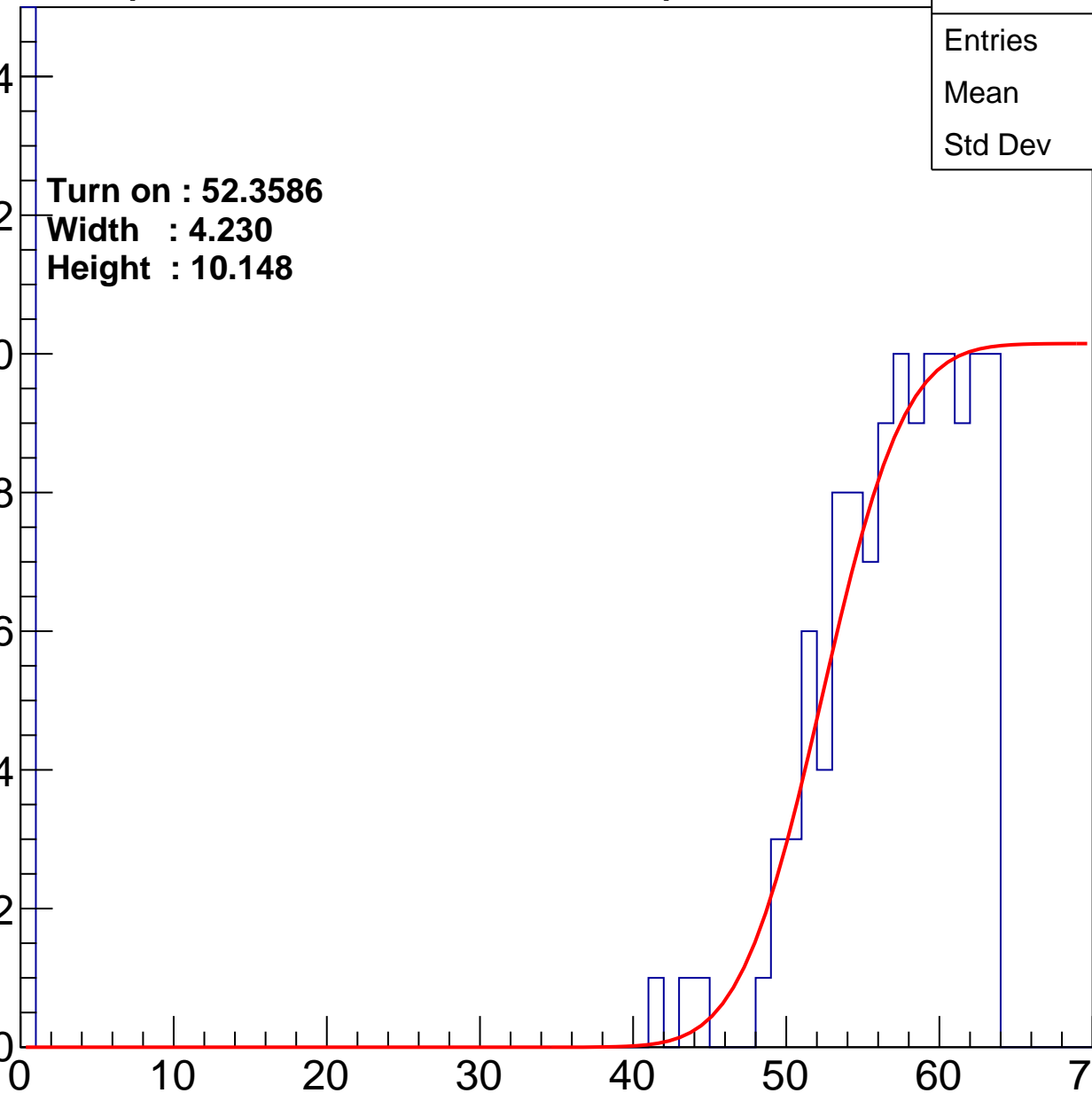
Width : 4.230

Height : 10.148

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch107

calib_packv5_033123_0516.root, FC#4, port A1

Entries	155
Mean	34.55
Std Dev	28.71

Turn on : 54.5993

Width : 3.415

Height : 9.738

Entry

14

12

10

8

6

4

2

0

0

10

20

30

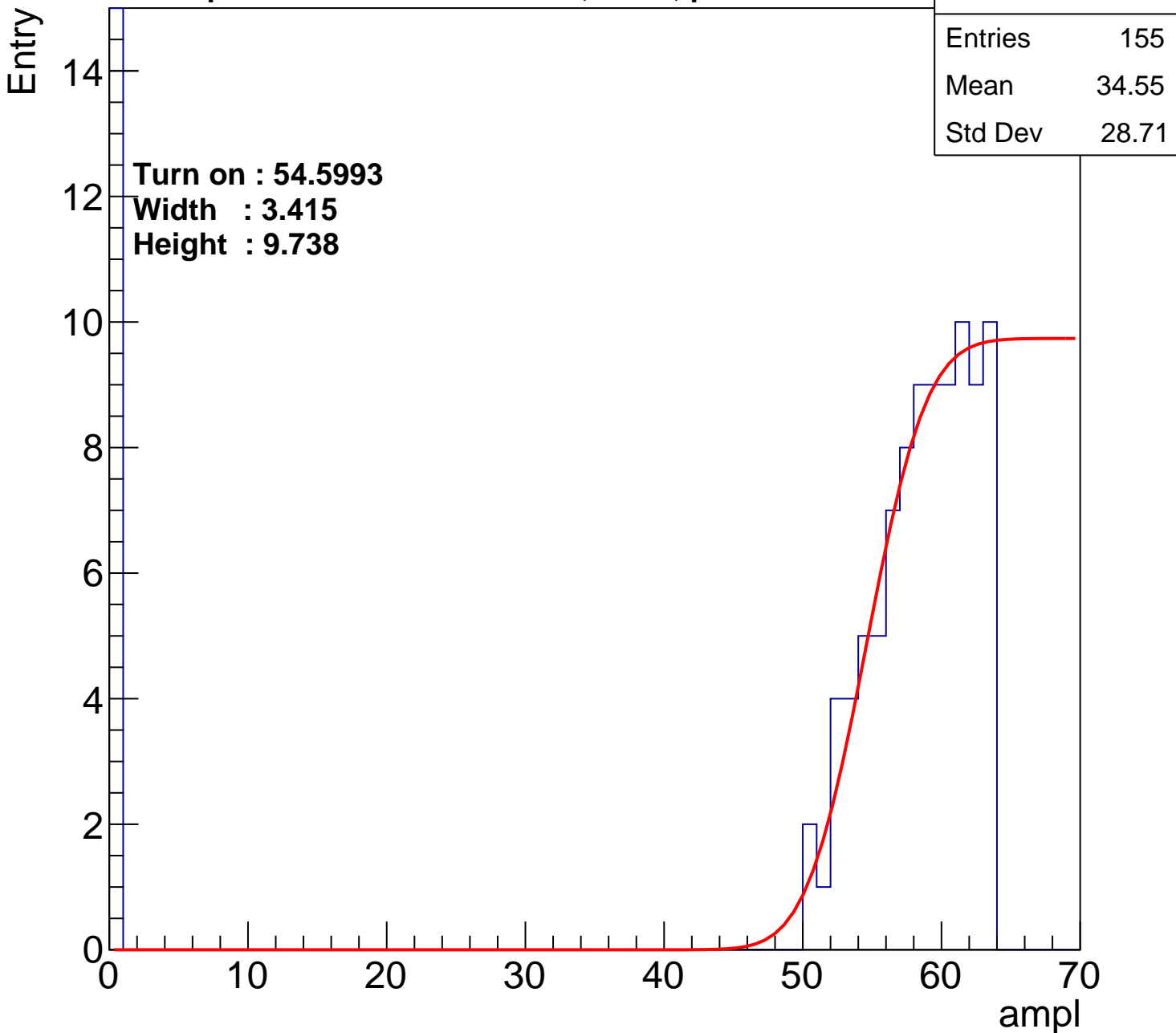
40

50

60

70

ampl



B1L104S, U9-ch108

calib_packv5_033123_0516.root, FC#4, port A1

Entries	204
Mean	30.54
Std Dev	28.7

Turn on : 54.3143

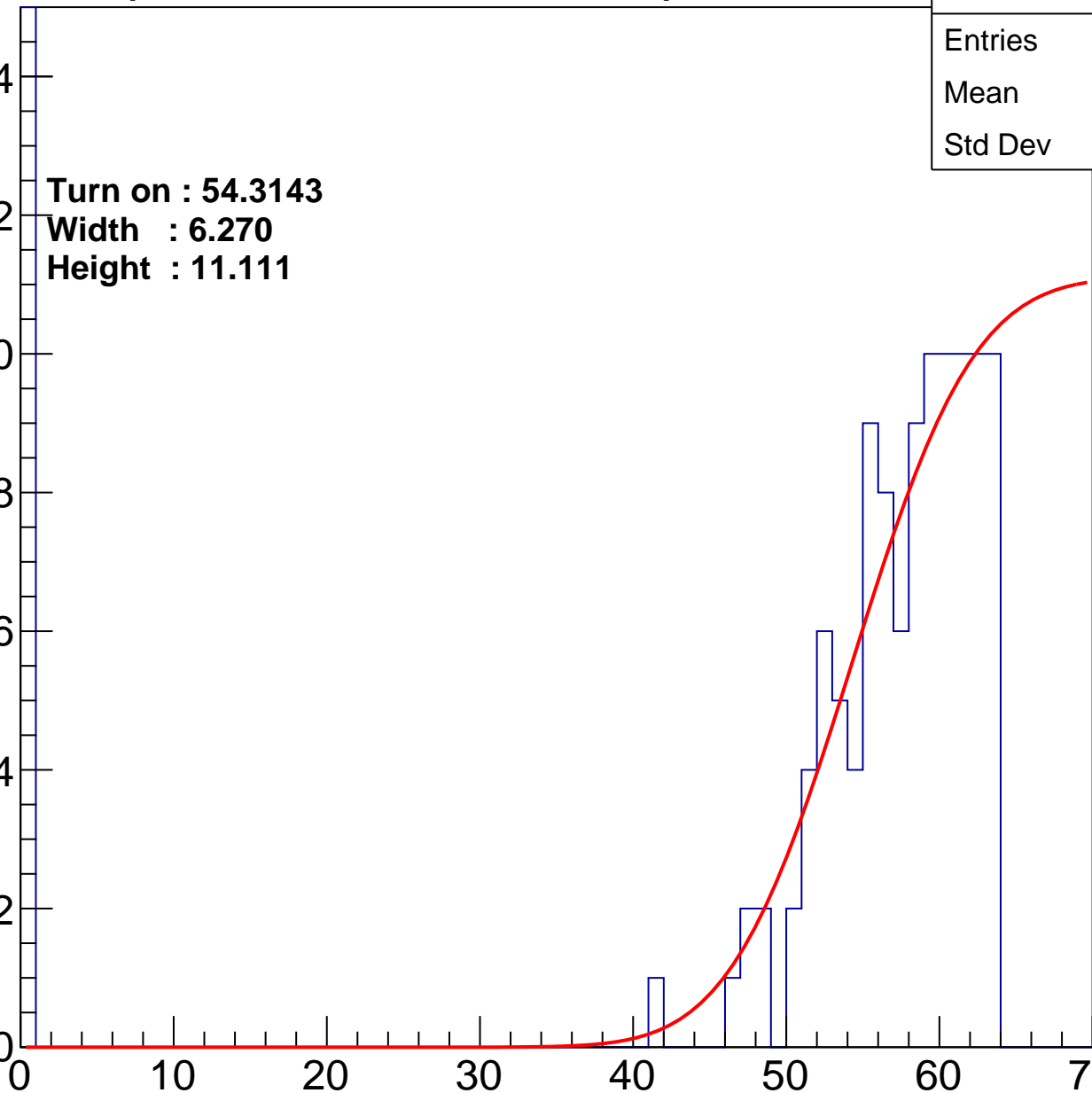
Width : 6.270

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch109

calib_packv5_033123_0516.root, FC#4, port A1

Entry

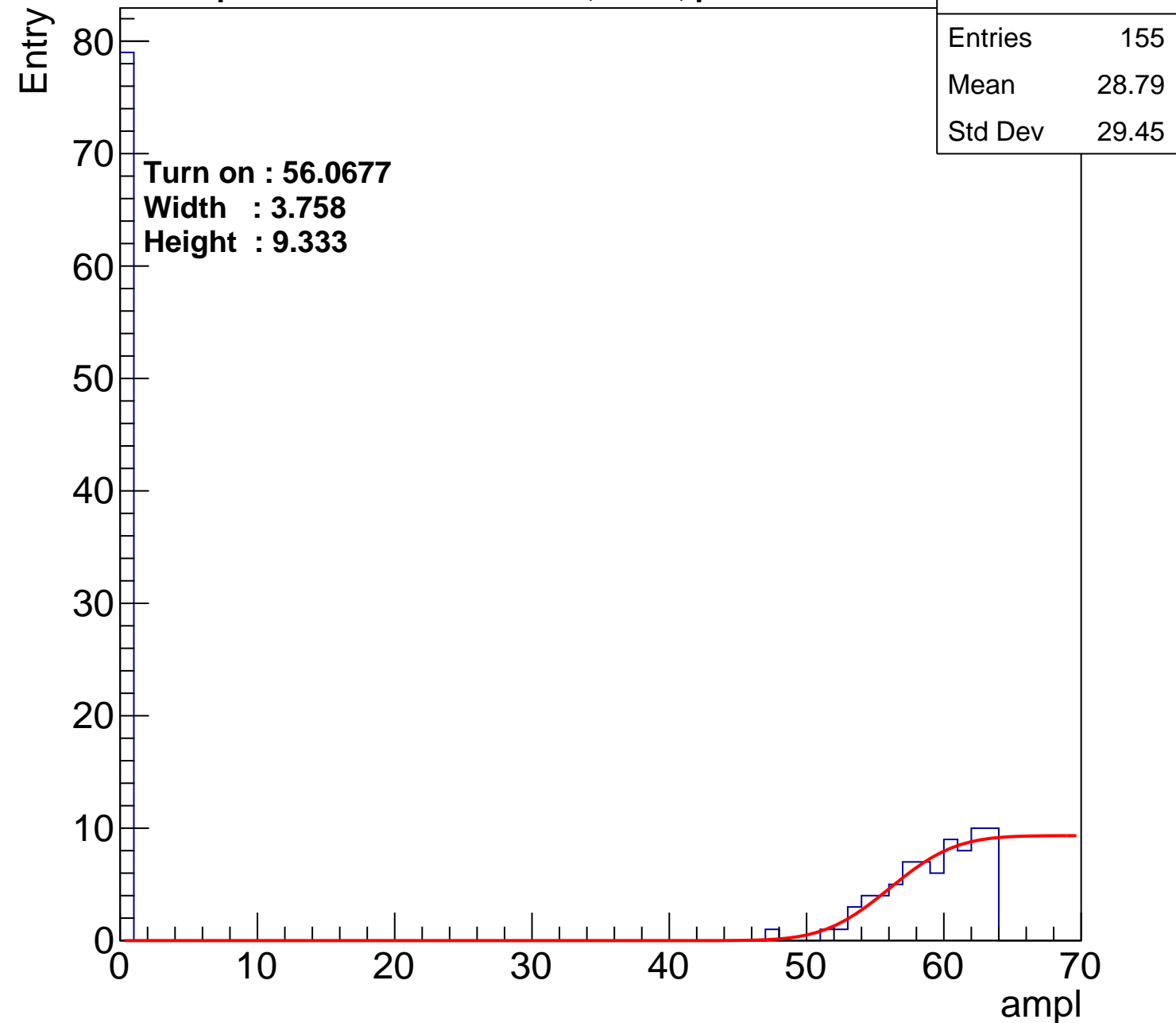
80
70
60
50
40
30
20
10
0

Turn on : 56.0677
Width : 3.758
Height : 9.333

Entries	155
Mean	28.79
Std Dev	29.45

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch110

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	35.2
Std Dev	27.77

Turn on : 52.3877

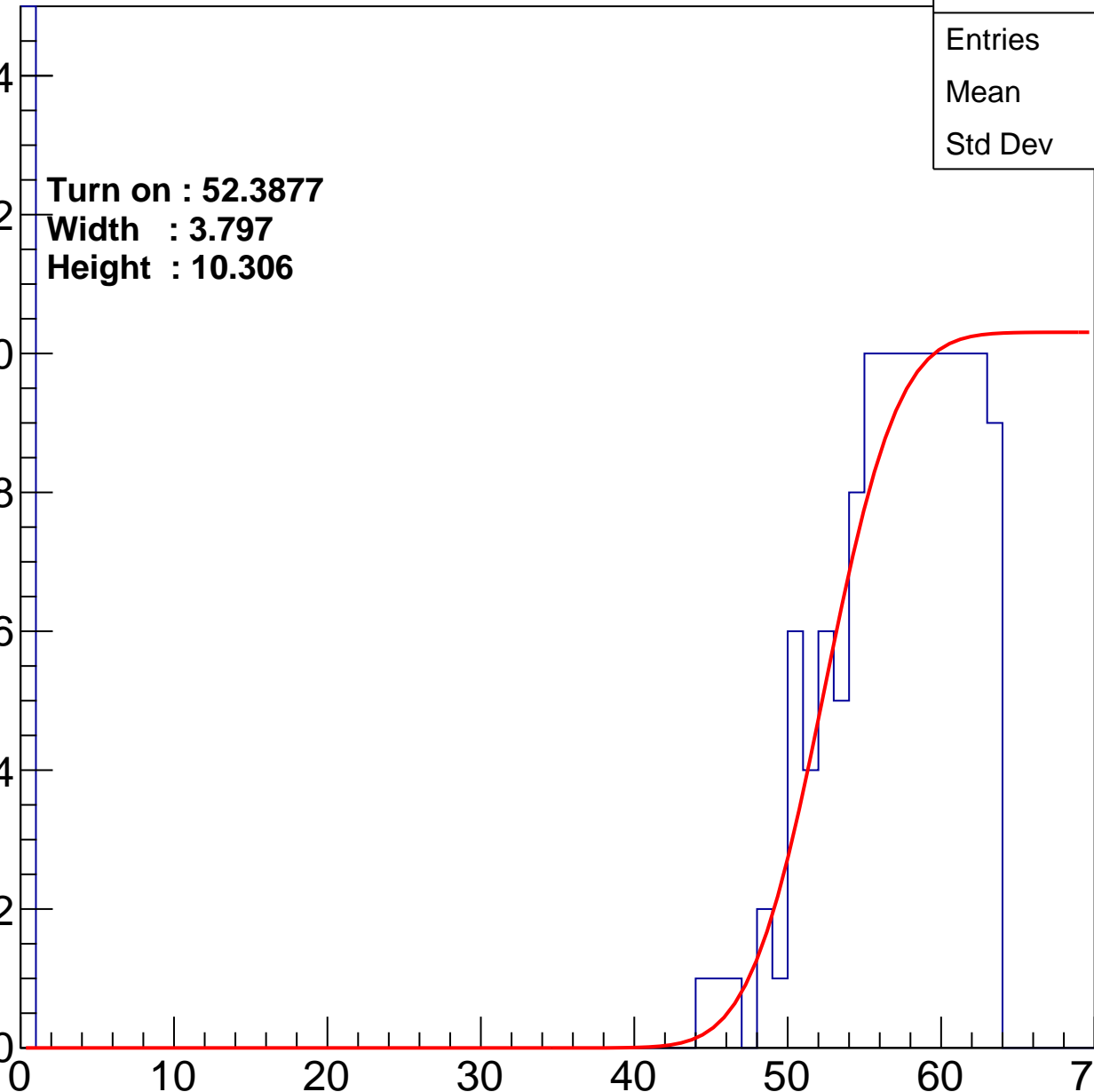
Width : 3.797

Height : 10.306

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch111

calib_packv5_033123_0516.root, FC#4, port A1

Entries	199
Mean	29.57
Std Dev	28.97

Turn on : 53.8534

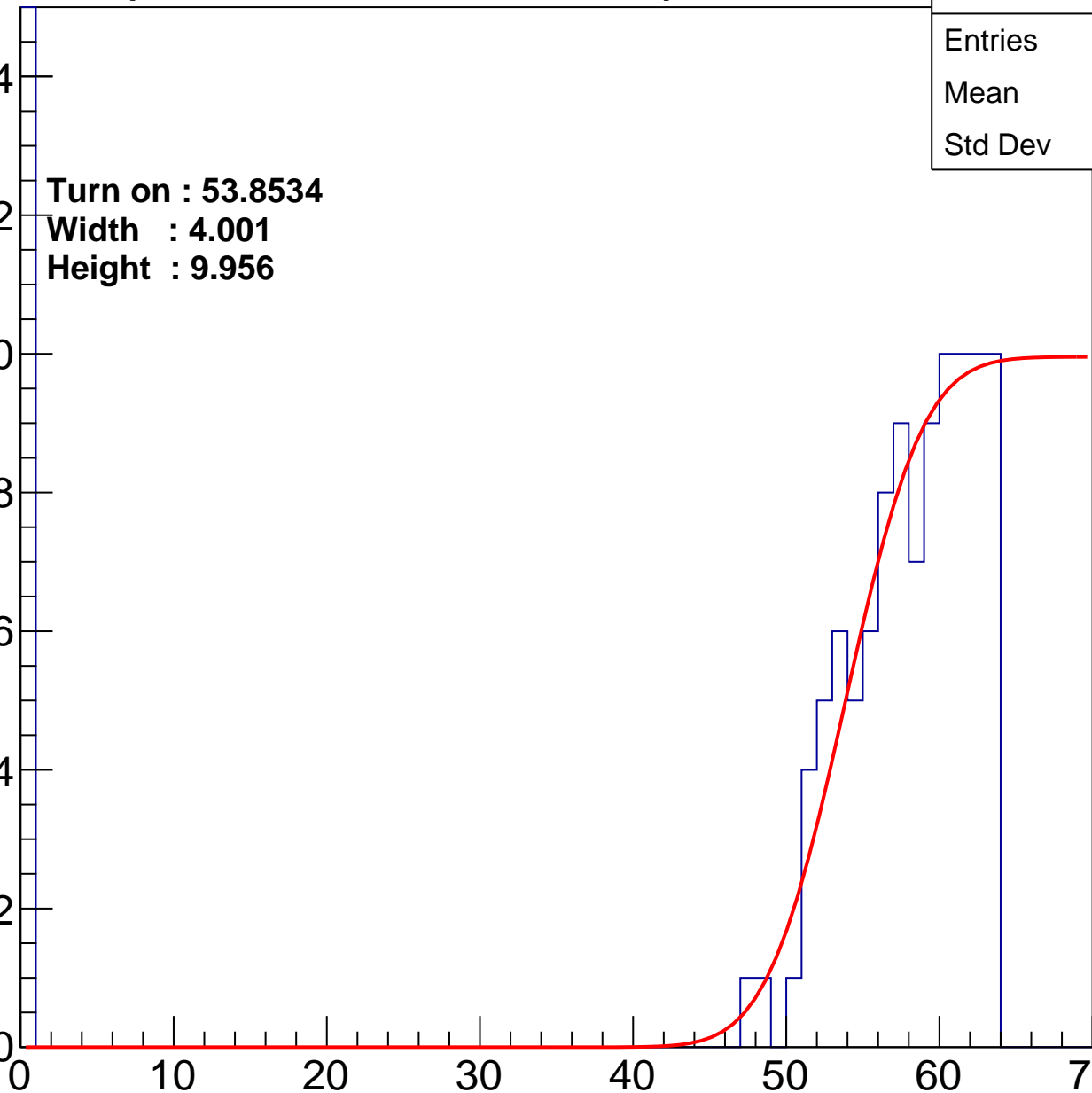
Width : 4.001

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch112

calib_packv5_033123_0516.root, FC#4, port A1

Entries	202
Mean	29.62
Std Dev	28.91

Turn on : 55.1381

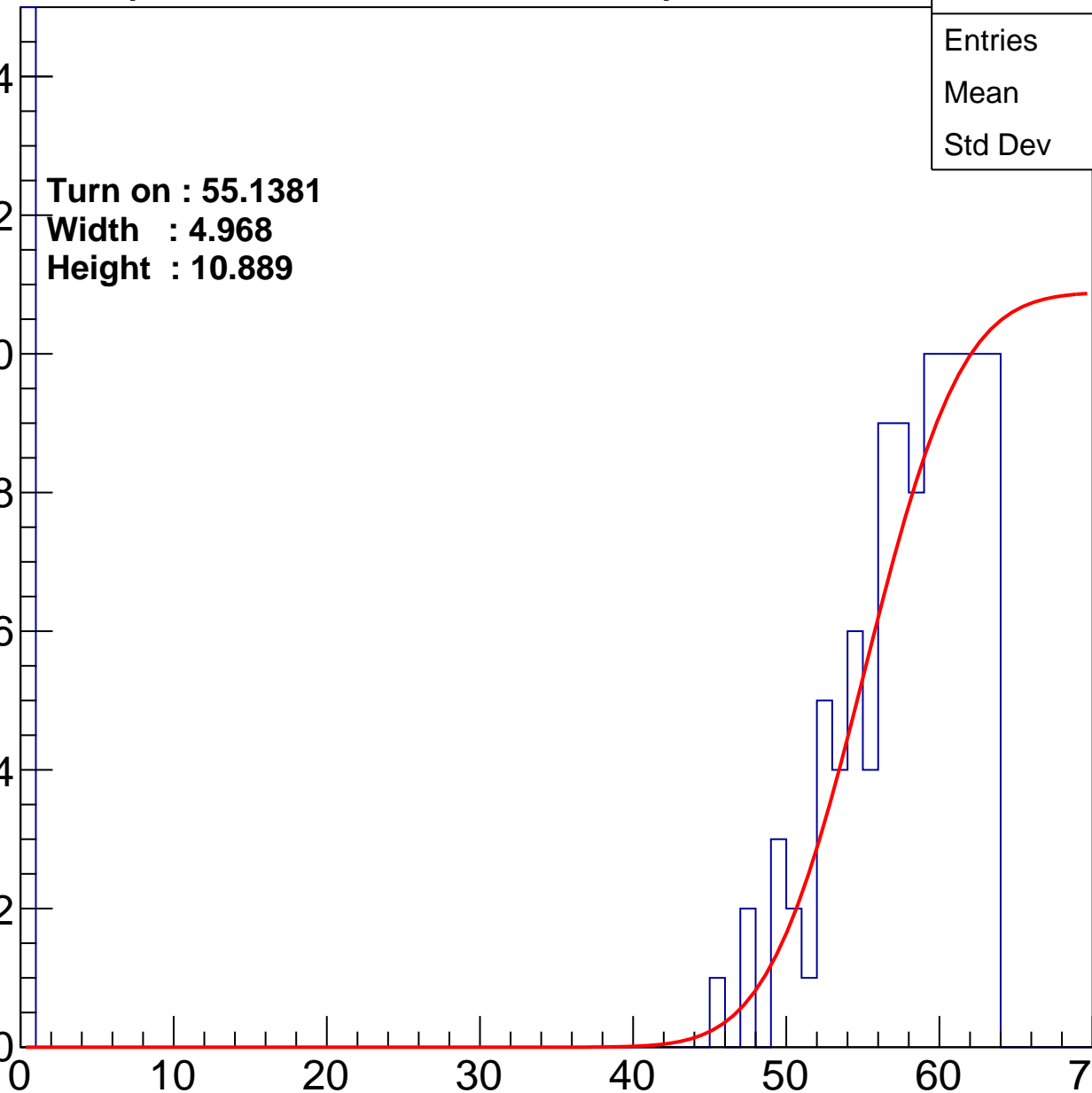
Width : 4.968

Height : 10.889

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch113

calib_packv5_033123_0516.root, FC#4, port A1

Entries	212
Mean	30.92
Std Dev	28.58

Turn on : 54.3233

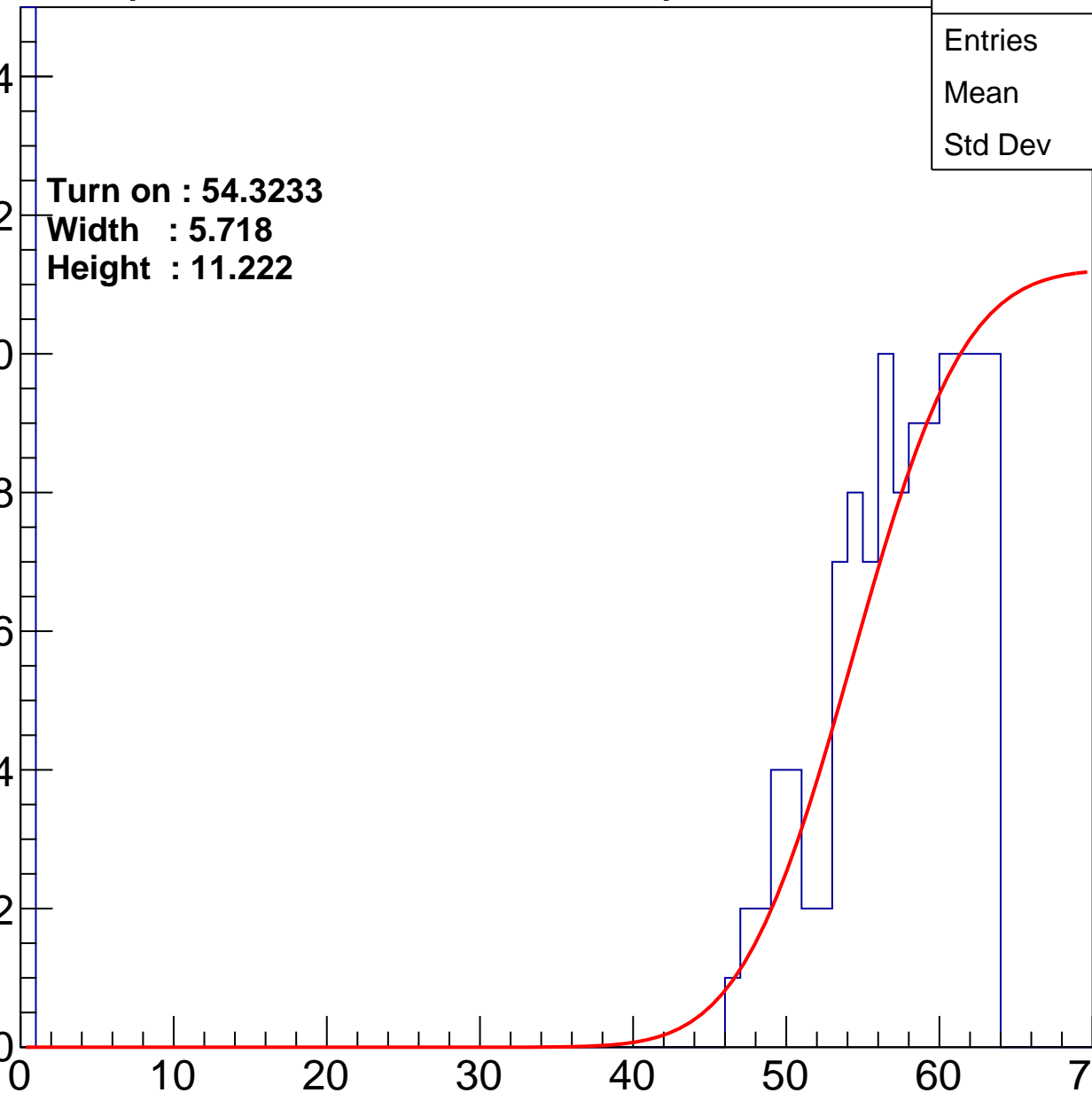
Width : 5.718

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch114

calib_packv5_033123_0516.root, FC#4, port A1

Entries	248
Mean	28.79
Std Dev	28.51

Turn on : 51.8215

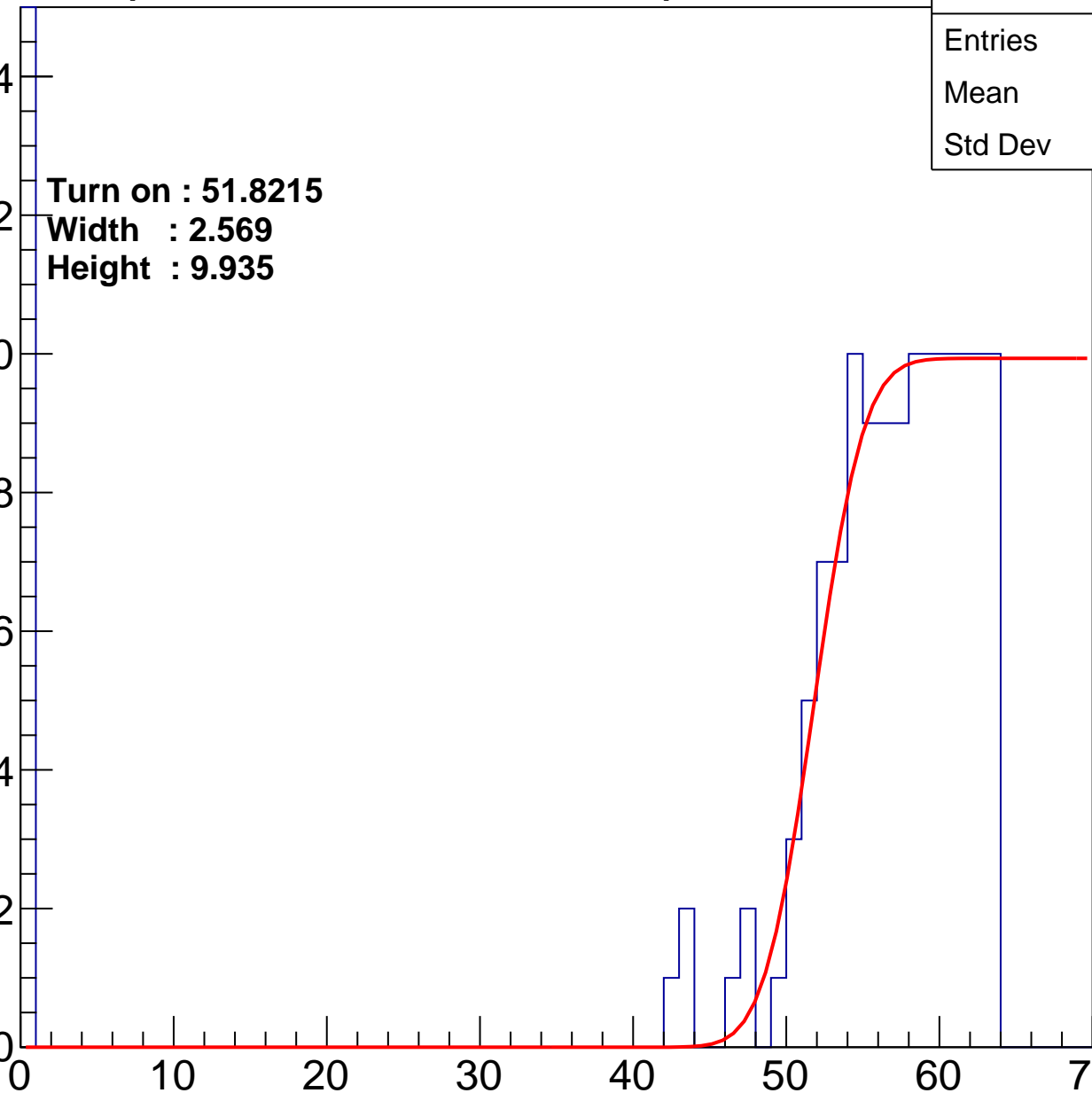
Width : 2.569

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch115

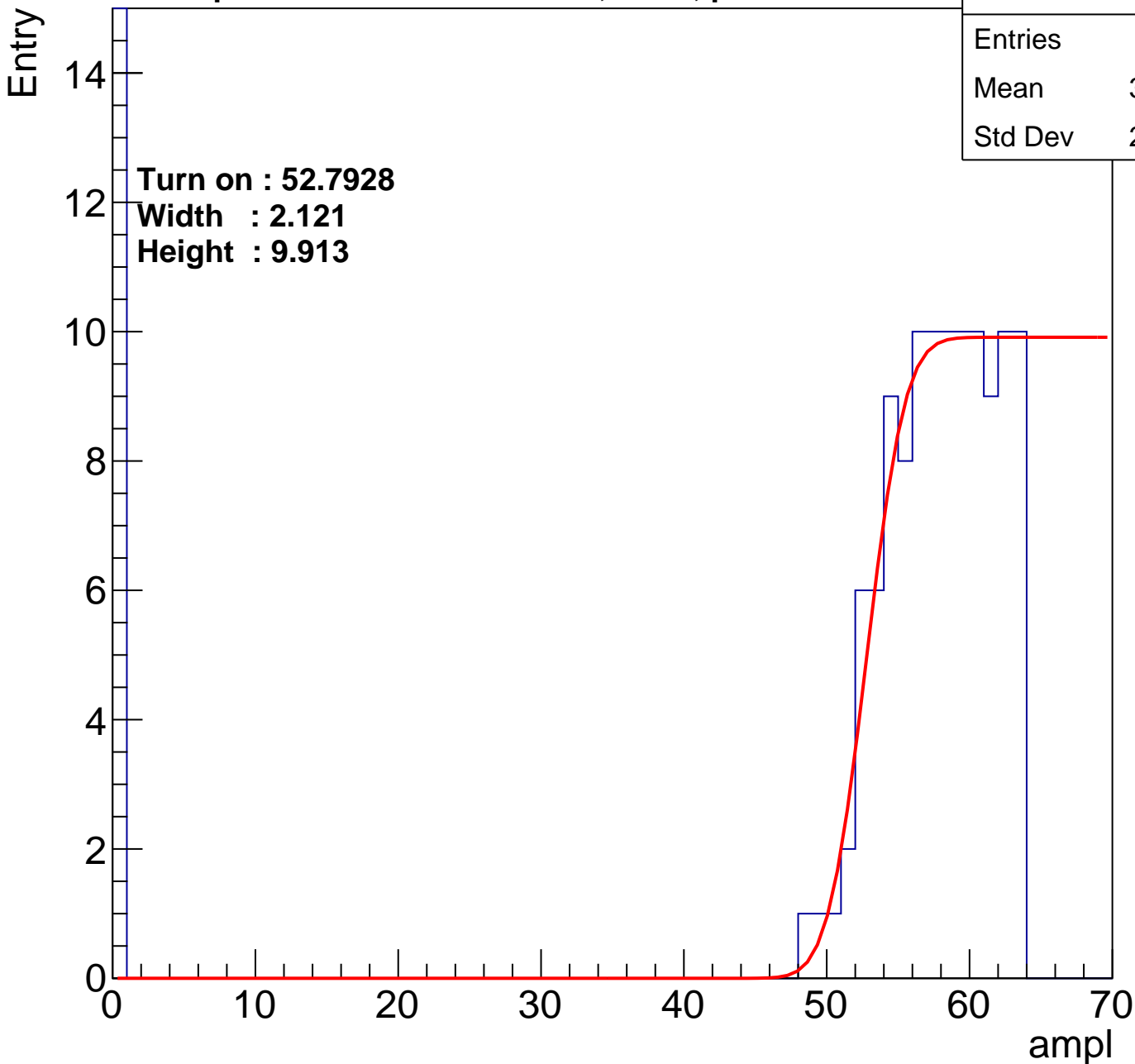
calib_packv5_033123_0516.root, FC#4, port A1

Entries	183
Mean	35.54
Std Dev	28.12

Turn on : 52.7928

Width : 2.121

Height : 9.913



B1L104S, U9-ch116

calib_packv5_033123_0516.root, FC#4, port A1

Entries	206
Mean	29.37
Std Dev	28.95

Turn on : 55.1770

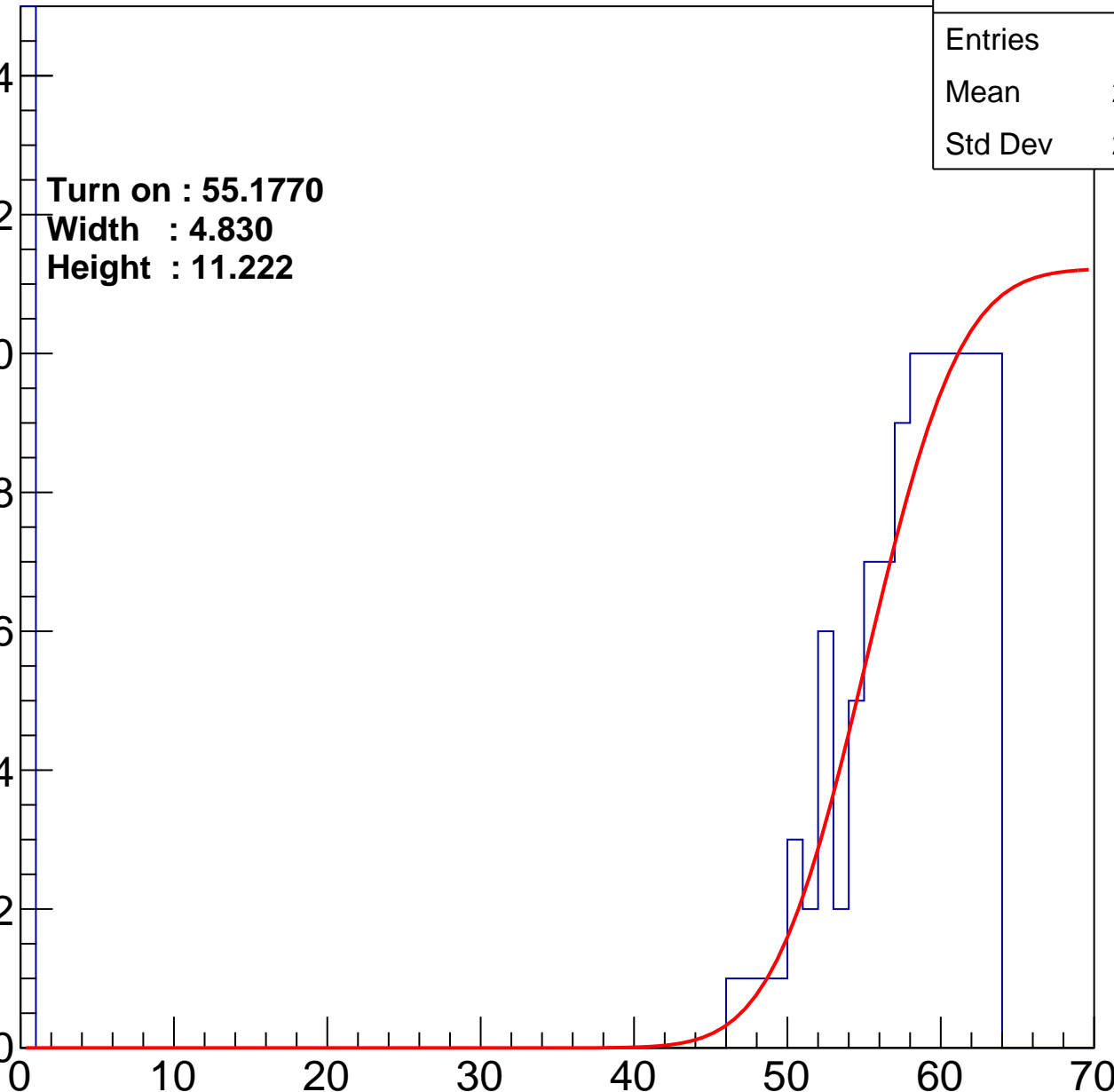
Width : 4.830

Height : 11.222

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch117

calib_packv5_033123_0516.root, FC#4, port A1

Entries	173
Mean	32.25
Std Dev	29.02

Turn on : 55.9024

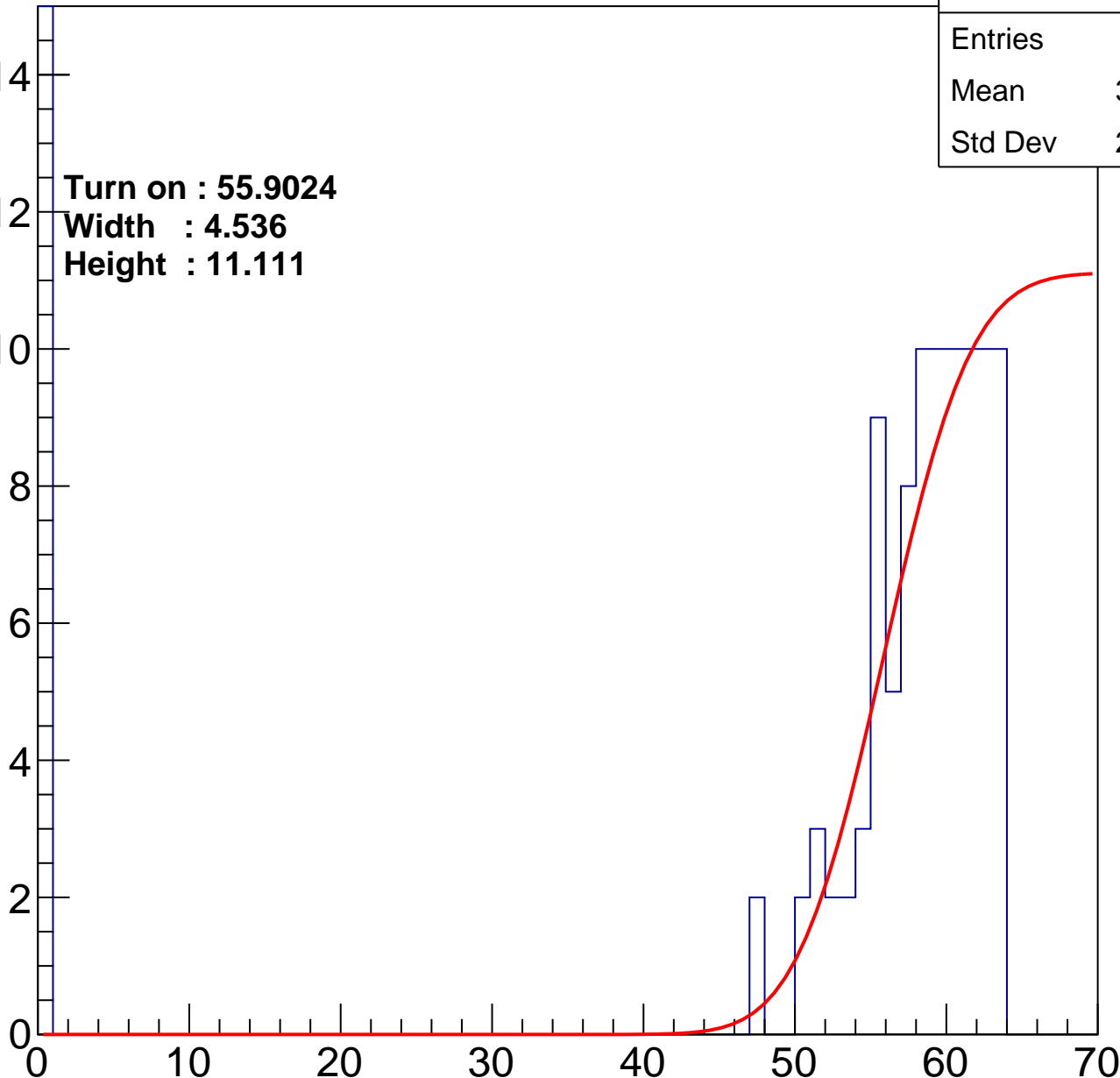
Width : 4.536

Height : 11.111

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch118

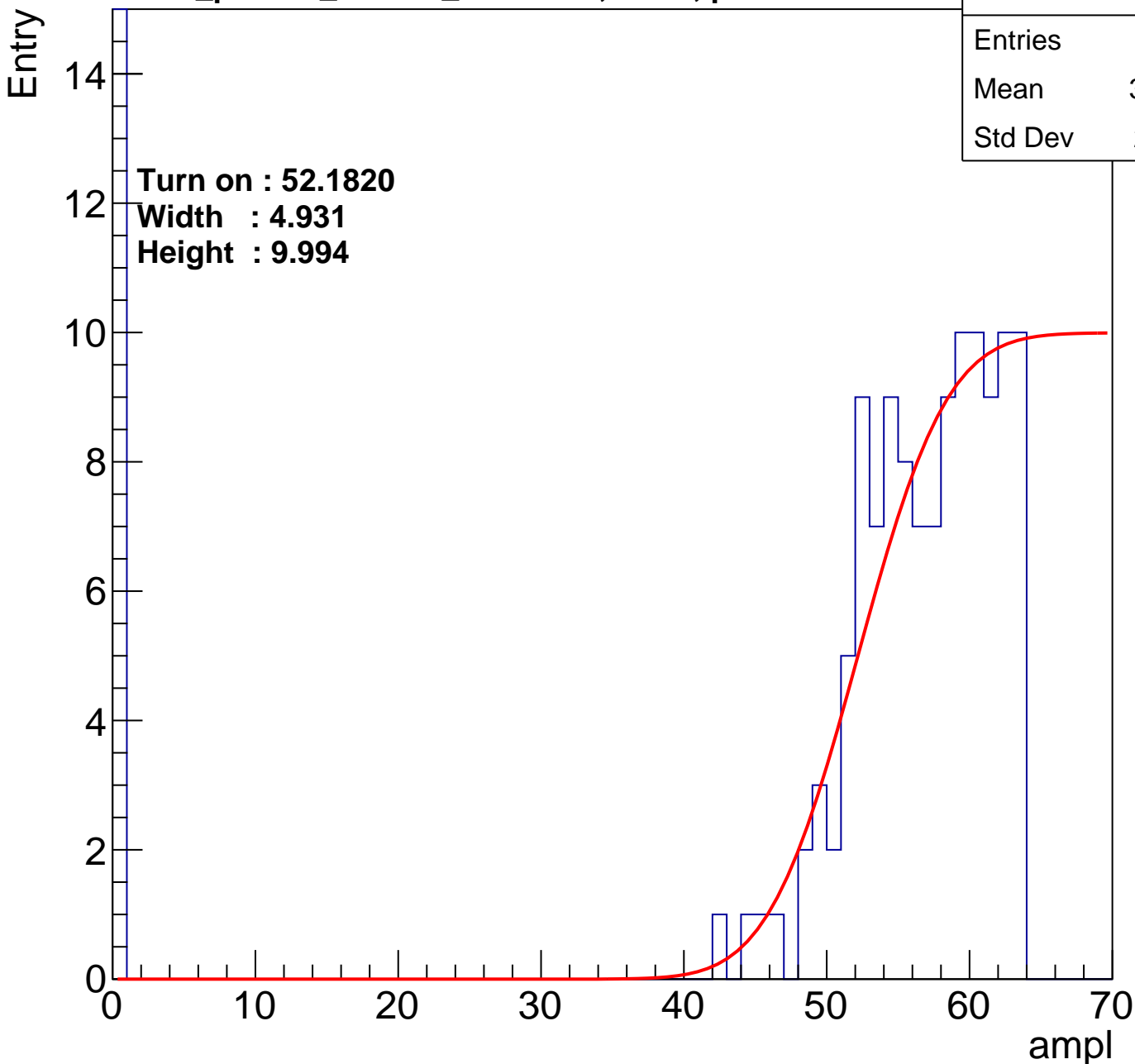
calib_packv5_033123_0516.root, FC#4, port A1

Entries	218
Mean	31.39
Std Dev	28.31

Turn on : 52.1820

Width : 4.931

Height : 9.994



B1L104S, U9-ch119

calib_packv5_033123_0516.root, FC#4, port A1

Entries	220
Mean	30.35
Std Dev	28.86

Turn on : 52.7845

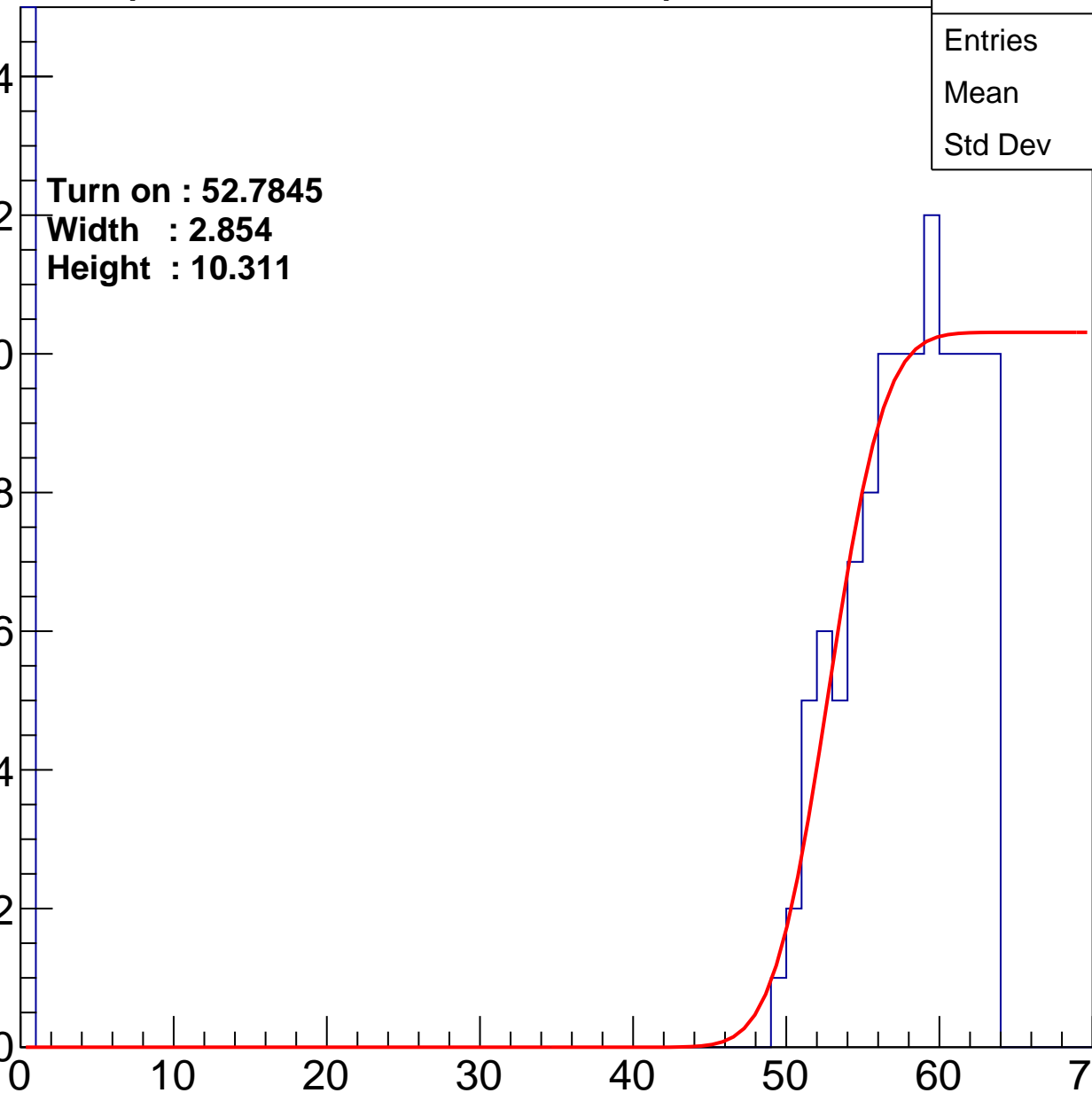
Width : 2.854

Height : 10.311

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch120

calib_packv5_033123_0516.root, FC#4, port A1

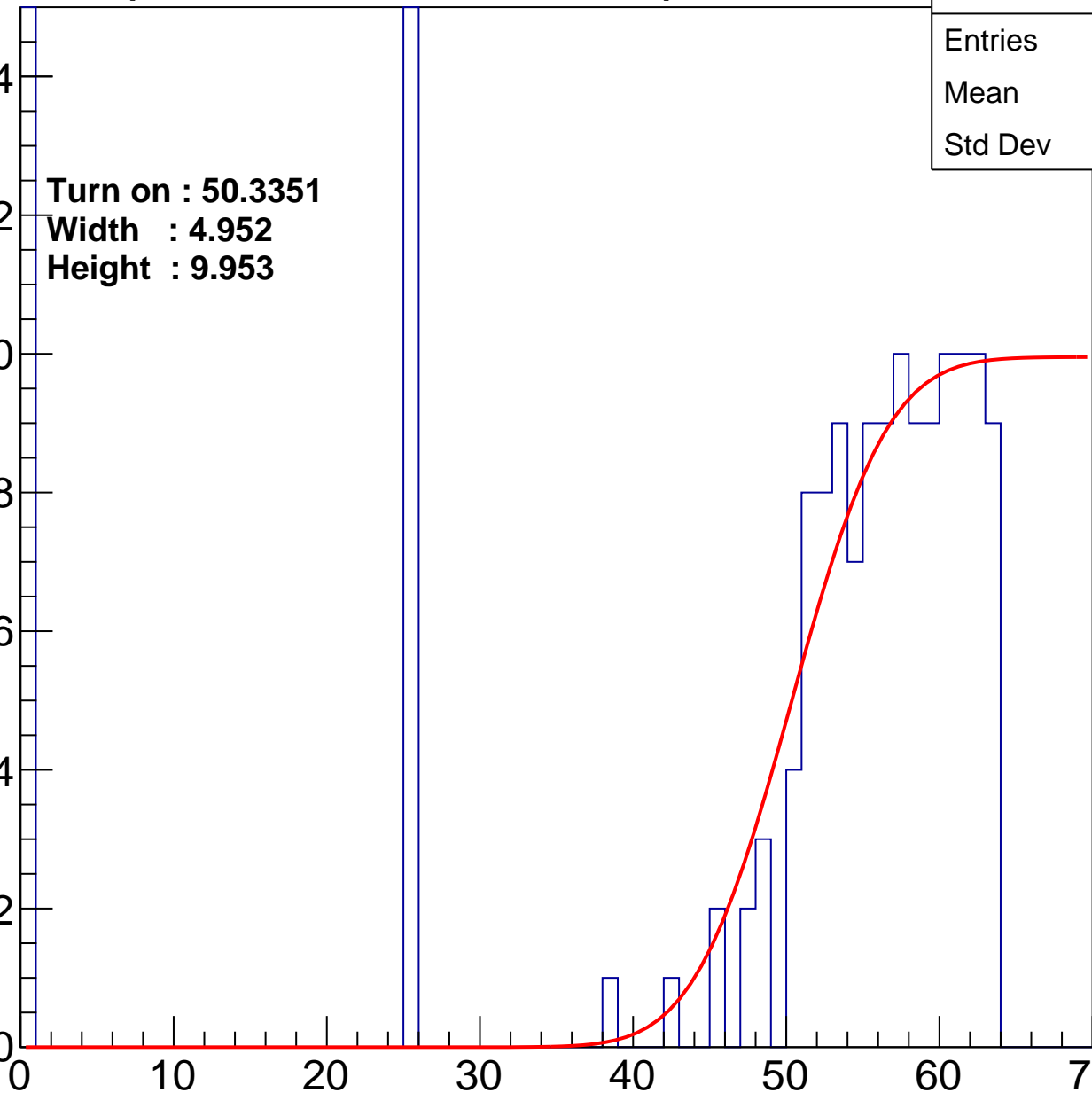
Entries	276
Mean	28.74
Std Dev	27.01

Turn on : 50.3351
Width : 4.952
Height : 9.953

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch121

calib_packv5_033123_0516.root, FC#4, port A1

Entries	189
Mean	28.79
Std Dev	29.07

Turn on : 54.6578

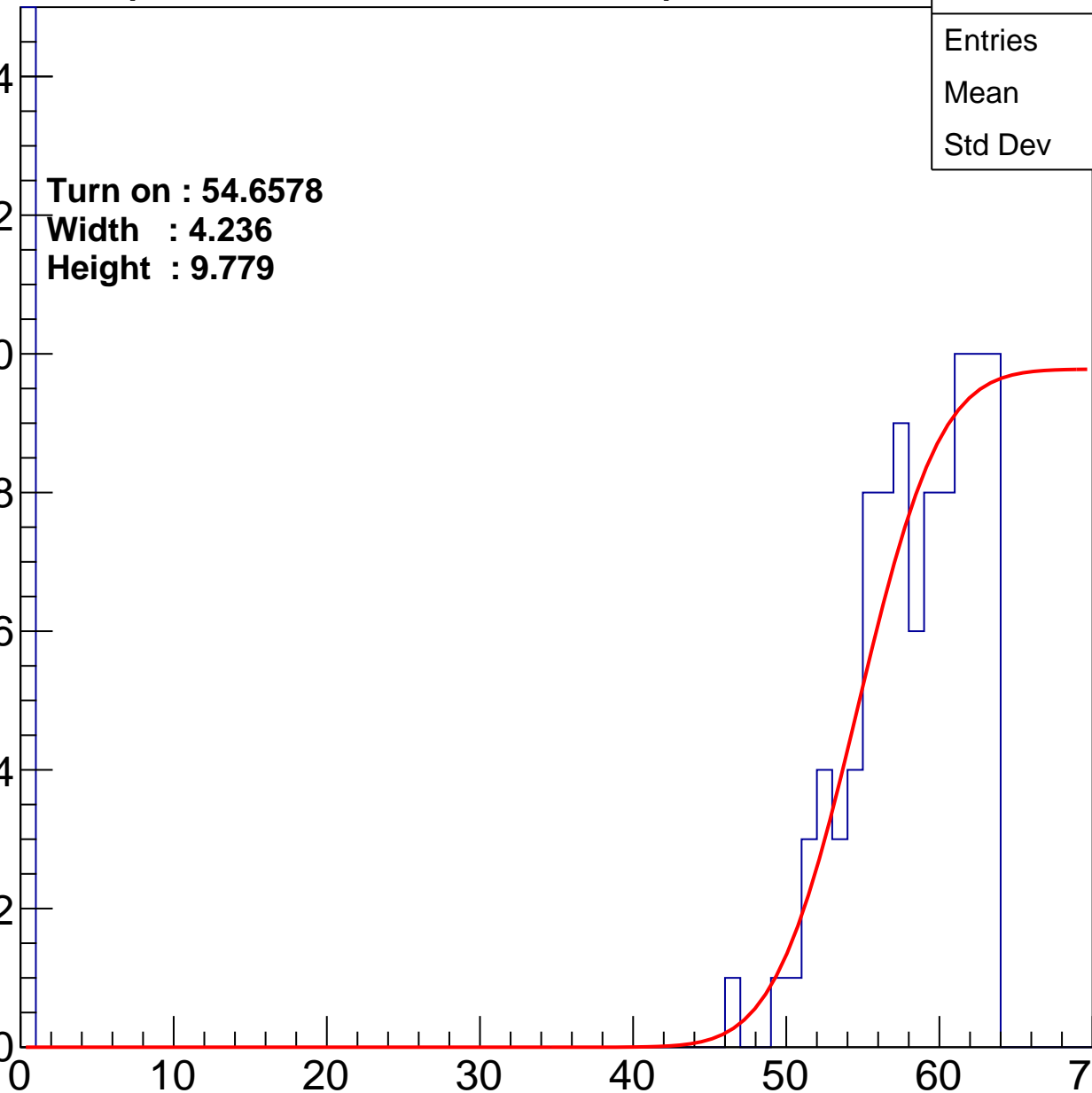
Width : 4.236

Height : 9.779

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch122

calib_packv5_033123_0516.root, FC#4, port A1

Entries	207
Mean	32.42
Std Dev	28.13

Turn on : 51.7000

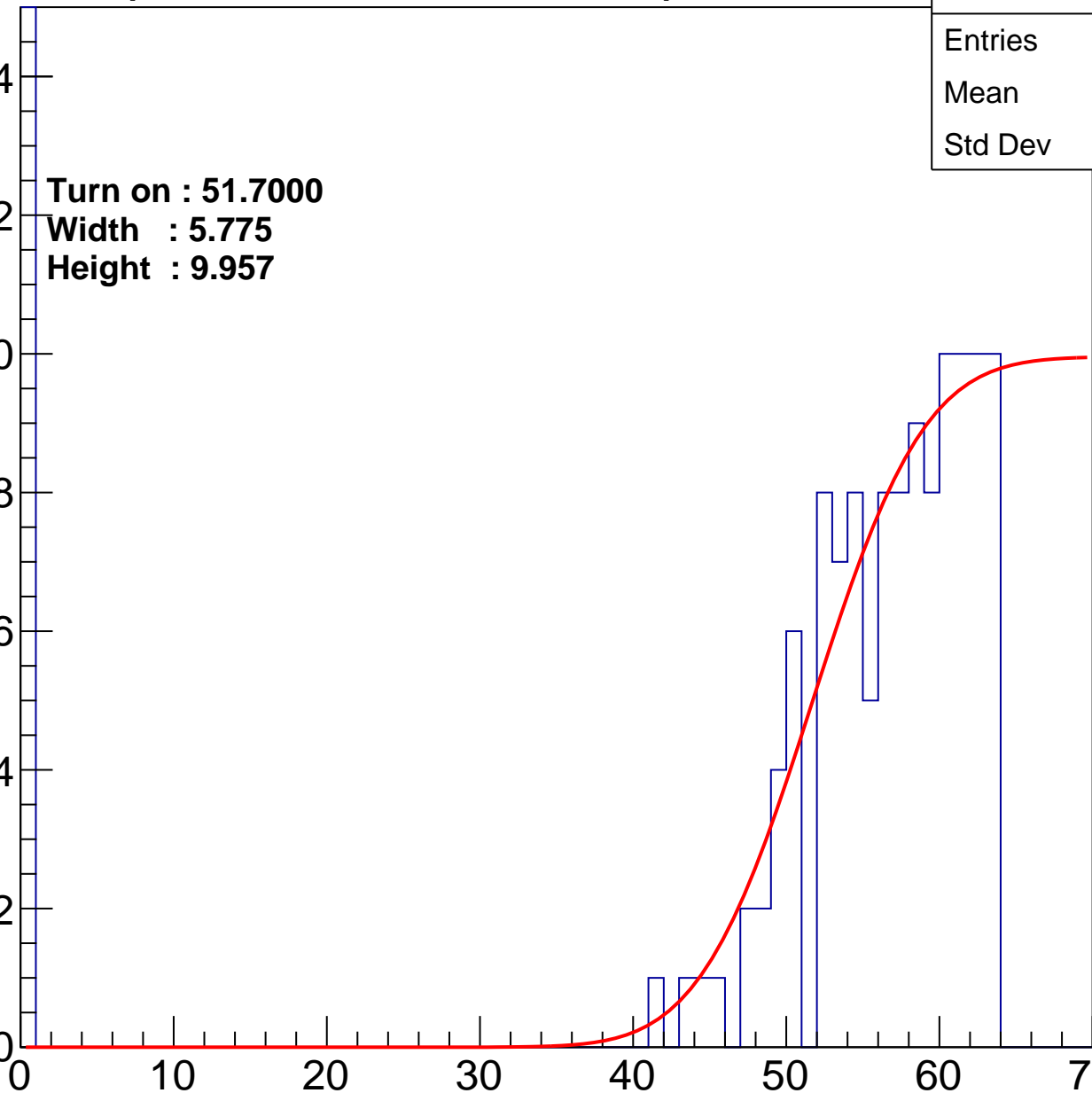
Width : 5.775

Height : 9.957

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch123

calib_packv5_033123_0516.root, FC#4, port A1

Entries	213
Mean	33.12
Std Dev	28.24

Turn on : 51.8774

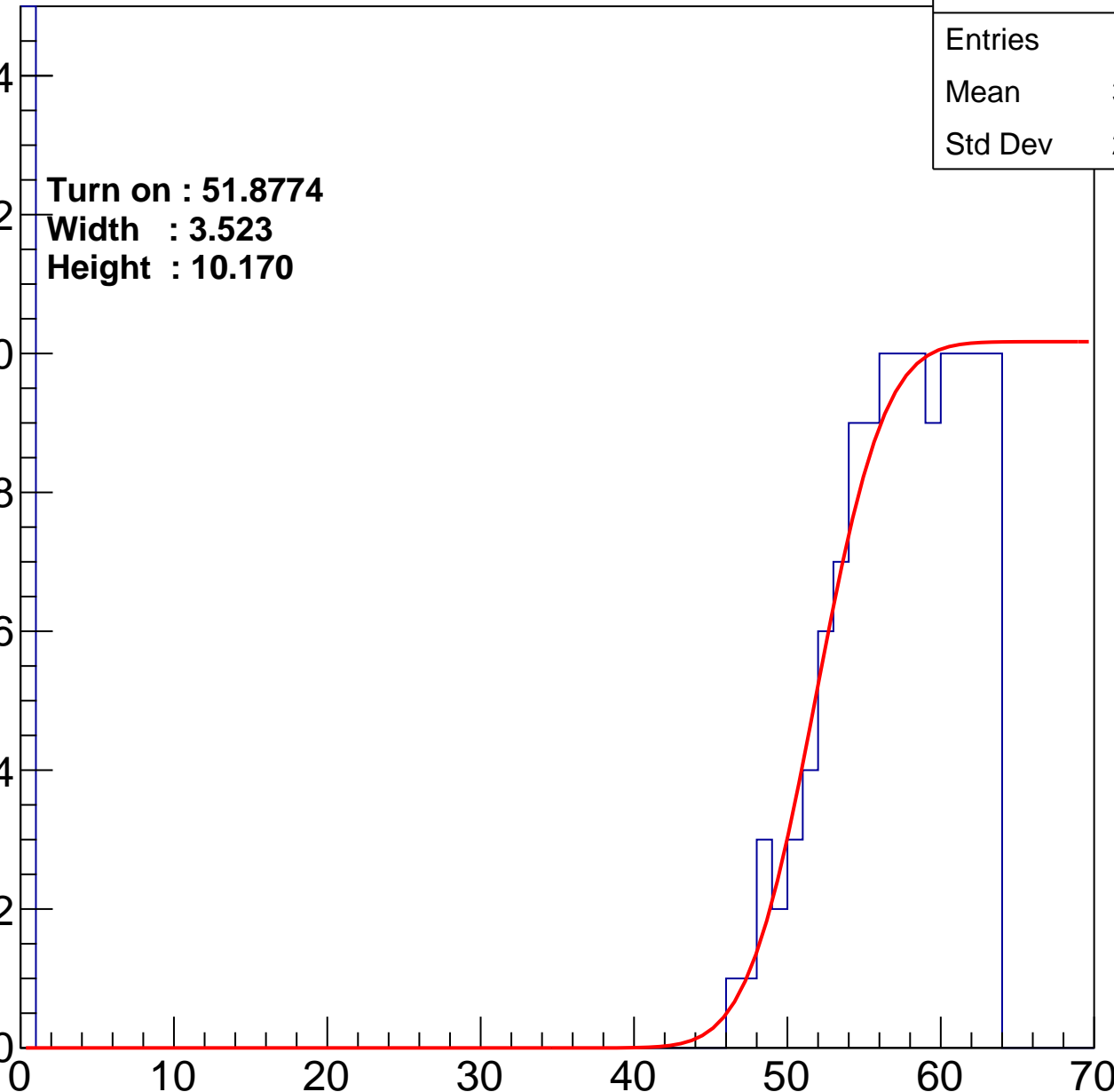
Width : 3.523

Height : 10.170

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch124

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	33.13
Std Dev	27.95

Turn on : 51.8454

Width : 5.283

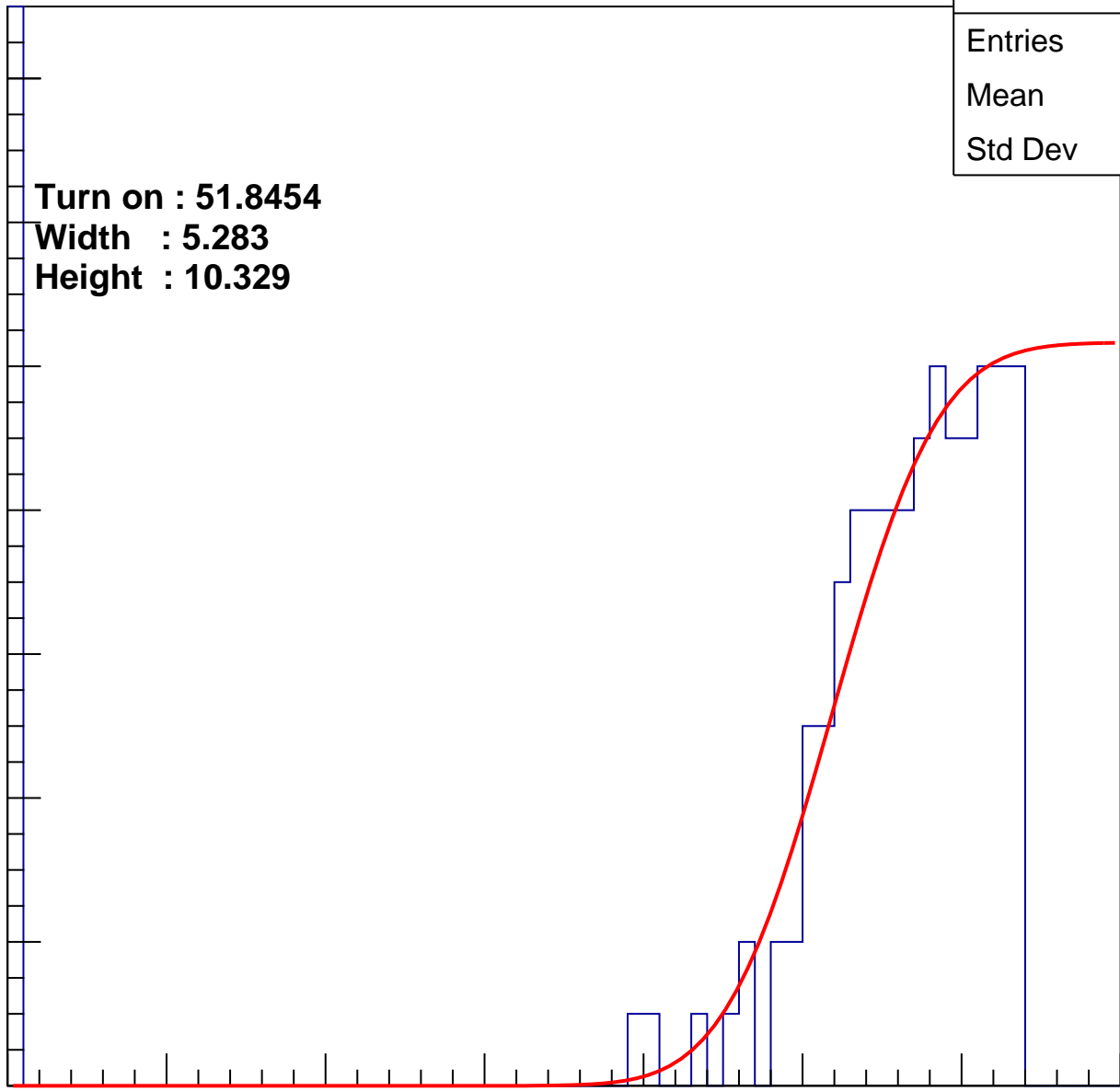
Height : 10.329

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L104S, U9-ch125

calib_packv5_033123_0516.root, FC#4, port A1

Entries	193
Mean	28.3
Std Dev	29.16

Turn on : 55.2029

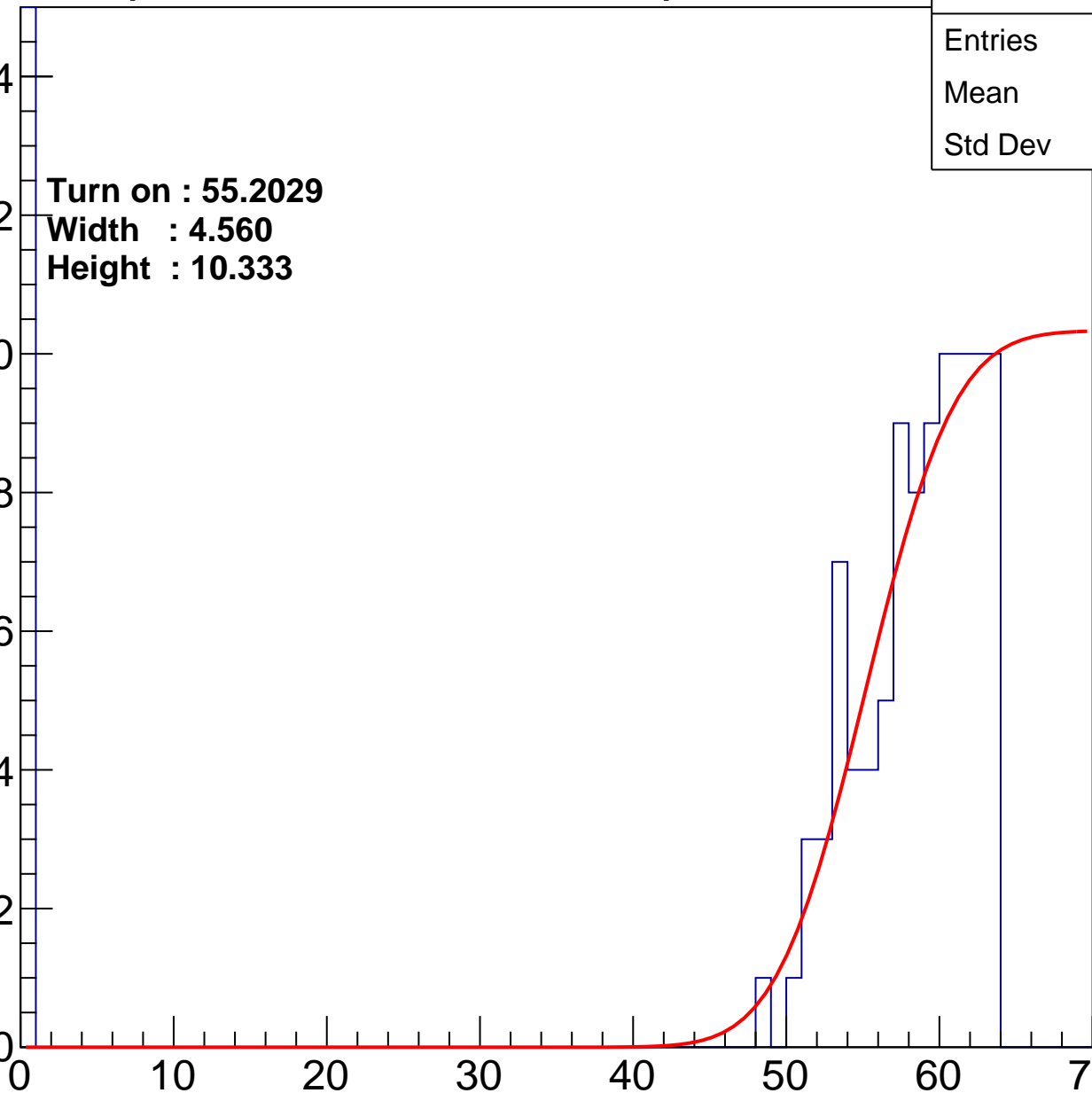
Width : 4.560

Height : 10.333

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch126

calib_packv5_033123_0516.root, FC#4, port A1

Entries	214
Mean	28.78
Std Dev	28.68

Turn on : 54.4976

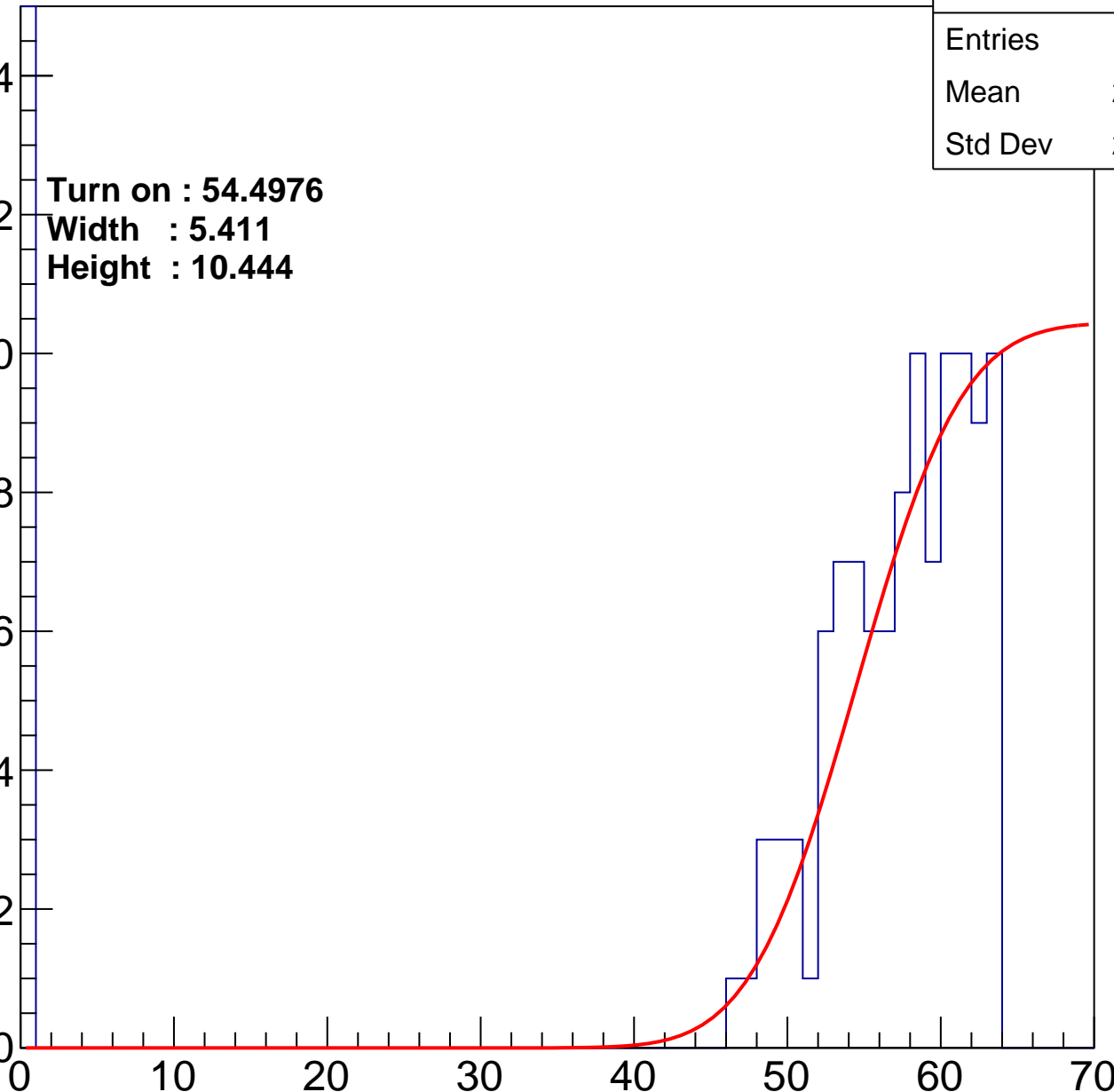
Width : 5.411

Height : 10.444

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entries	200
Mean	30.09
Std Dev	28.79

Turn on : 53.4965

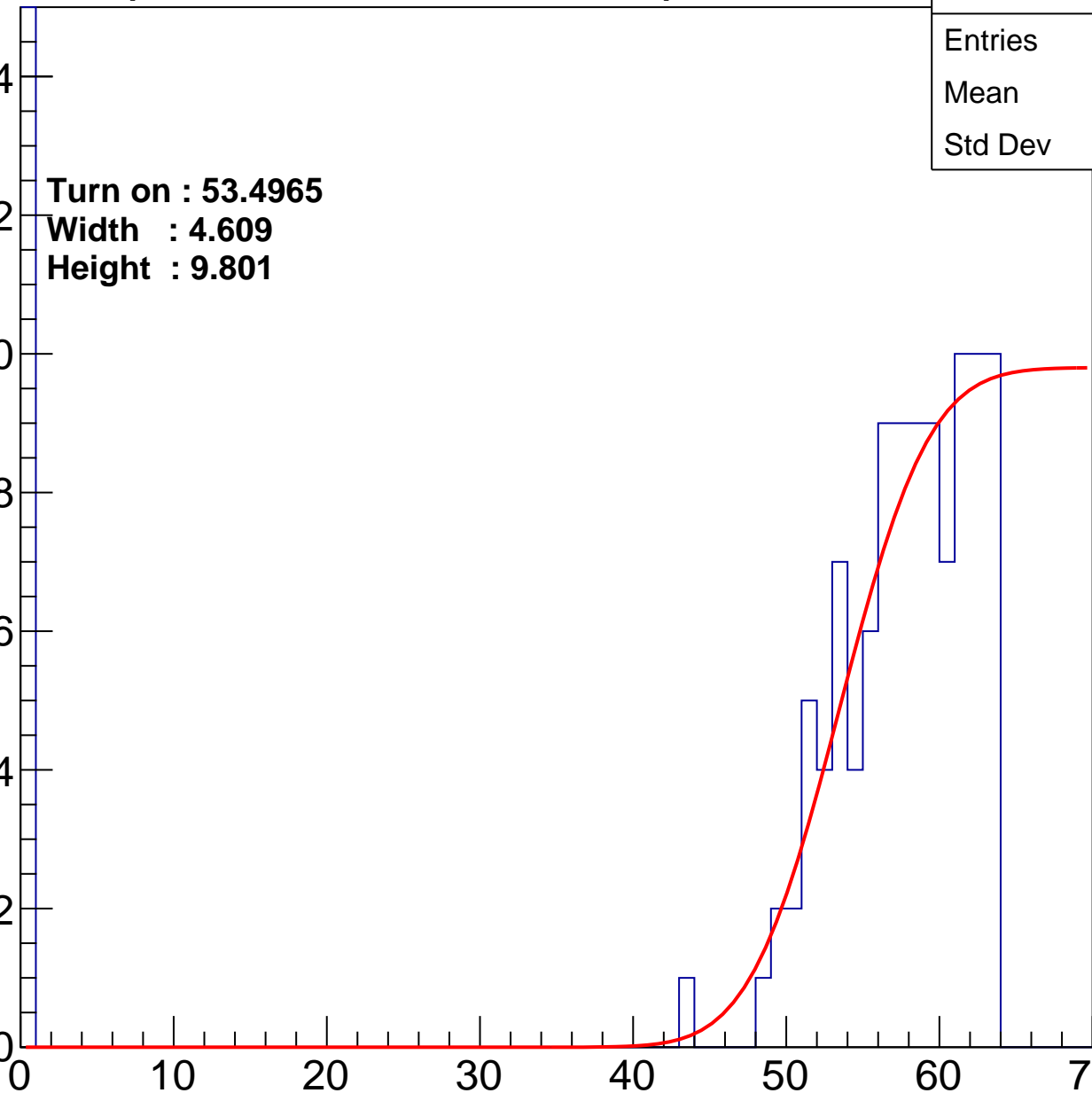
Width : 4.609

Height : 9.801

Entry

14
12
10
8
6
4
2
0

ampl



B1L104S, U9-ch127

calib_packv5_033123_0516.root, FC#4, port A1

Entry

14
12
10
8
6
4
2
0

Turn on : 53.4965
Width : 4.609
Height : 9.801

Entries	200
Mean	30.09
Std Dev	28.79

ampl

