

B1L003S, U21-ch0

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.1
Std Dev	11.57

Turn on : 25.6574

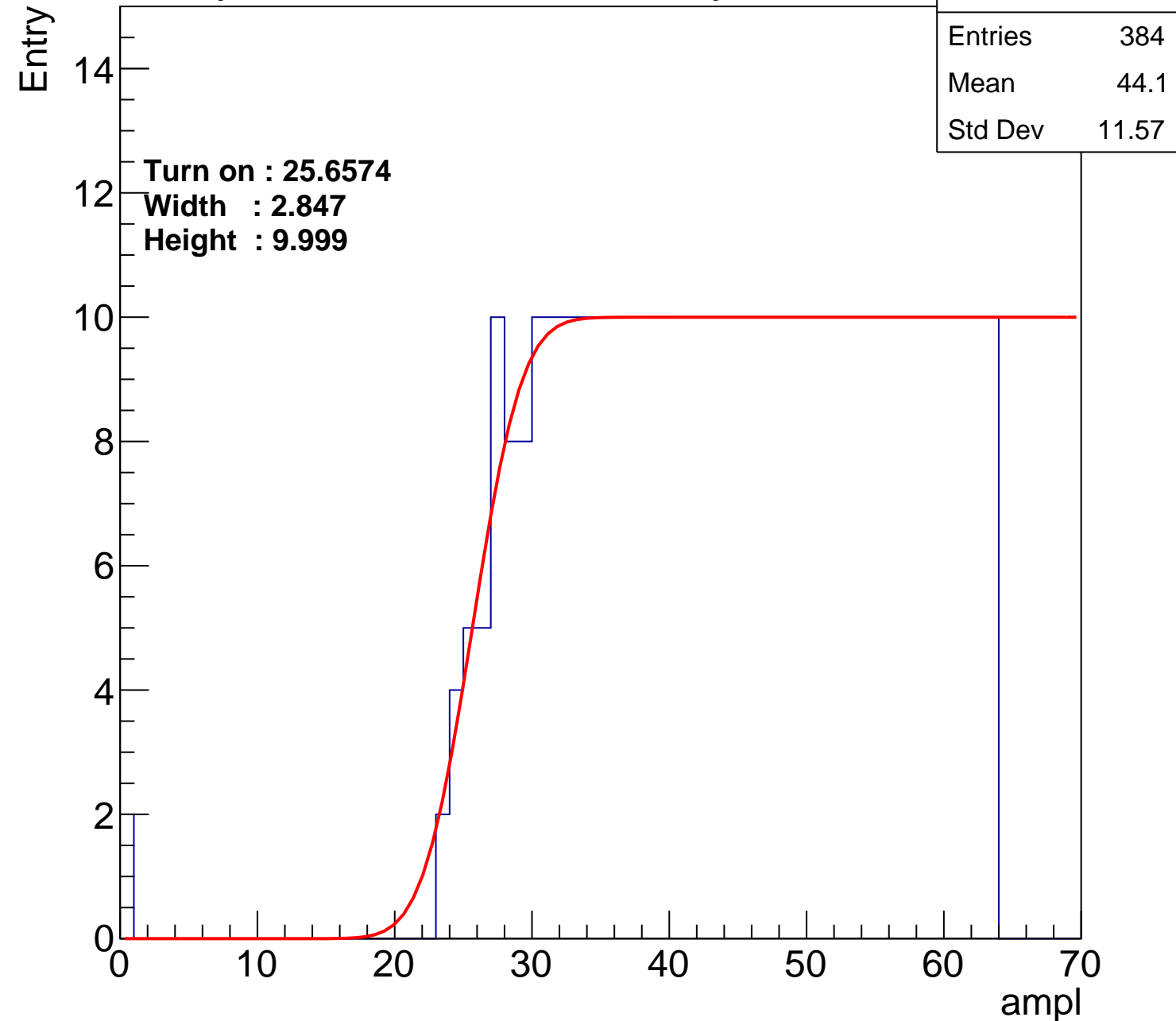
Width : 2.847

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch1

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 27.2787

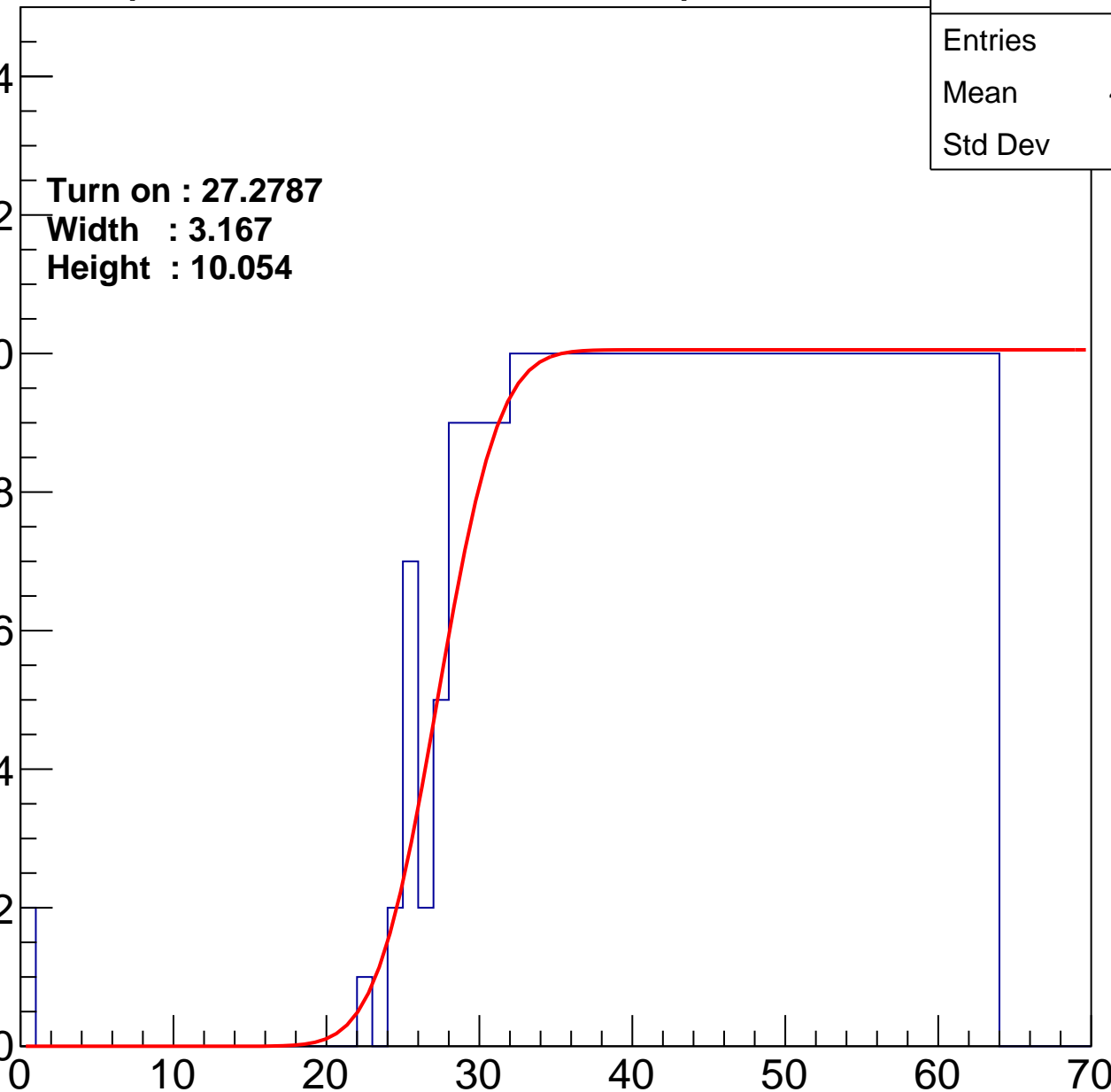
Width : 3.167

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch2

calib_packv5_042523_0143.root, FC#13, port D2

Entries	360
Mean	45.3
Std Dev	10.82

Turn on : 28.4715

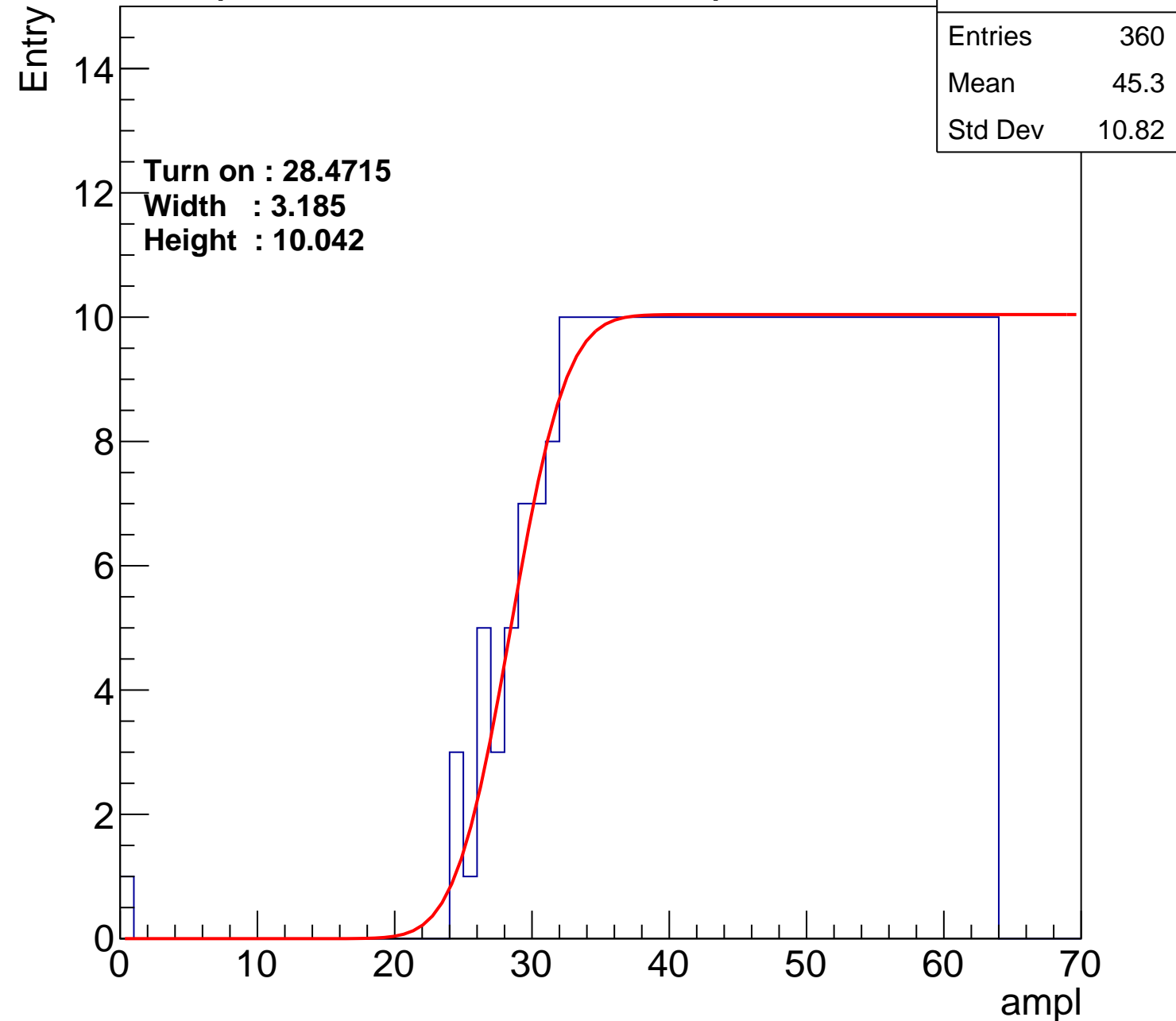
Width : 3.185

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch3

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.78
Std Dev	11.36

Turn on : 27.4058

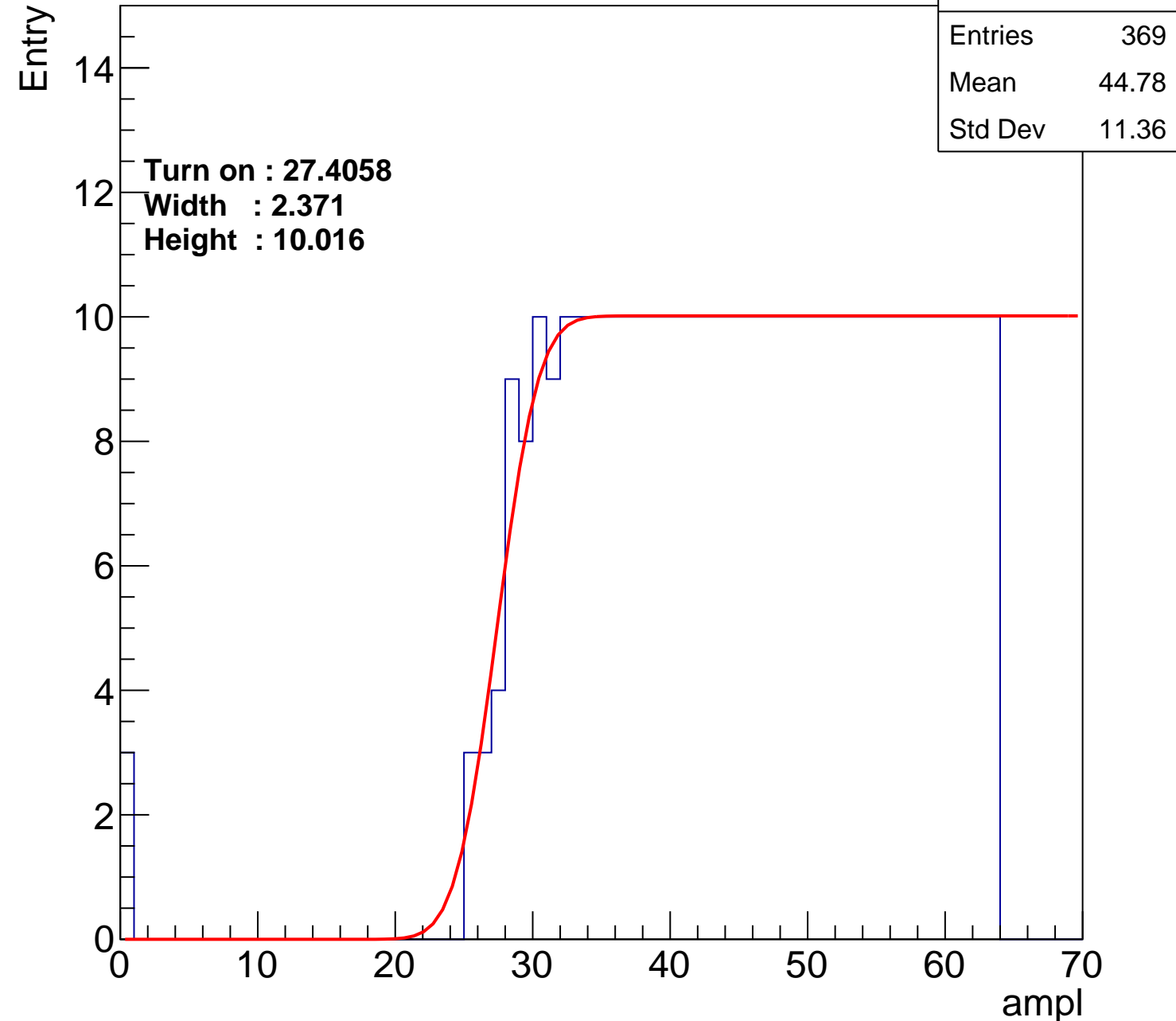
Width : 2.371

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch4

calib_packv5_042523_0143.root, FC#13, port D2

Entries	361
Mean	45.3
Std Dev	10.76

Turn on : 27.7966

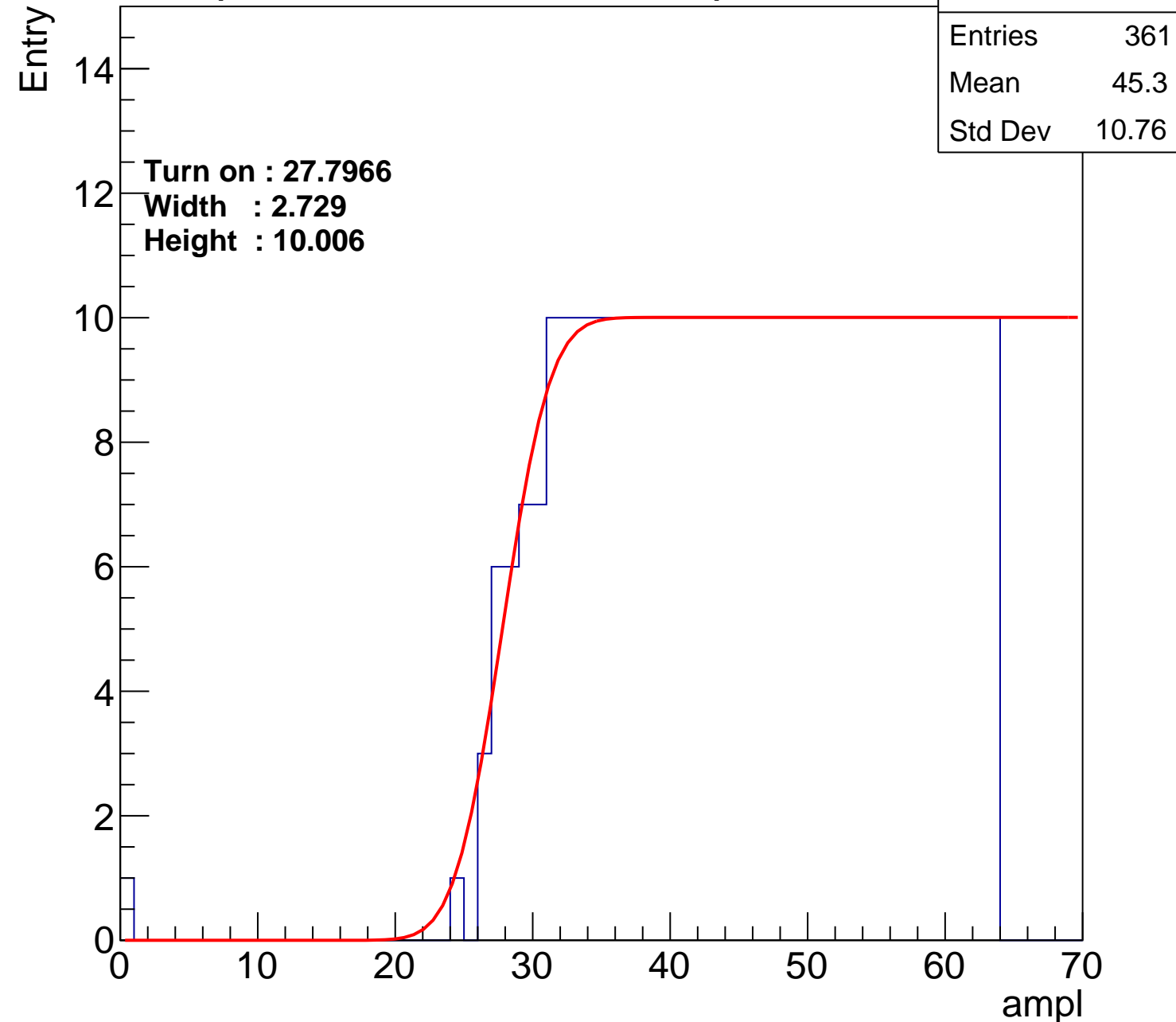
Width : 2.729

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch5

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.79
Std Dev	11.06

Turn on : 27.3272

Width : 2.773

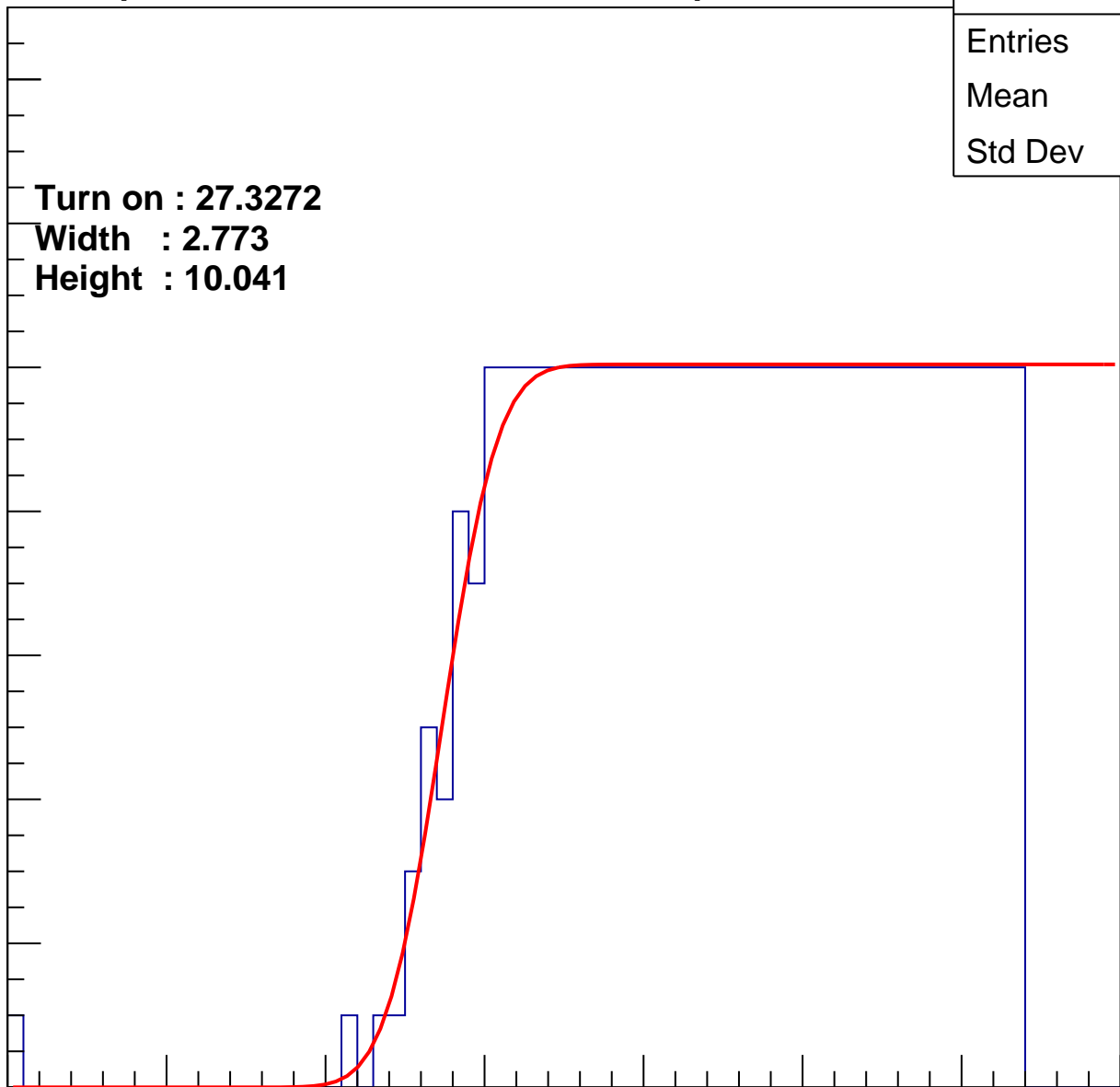
Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch6

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.57
Std Dev	11.34

Turn on : 27.1730

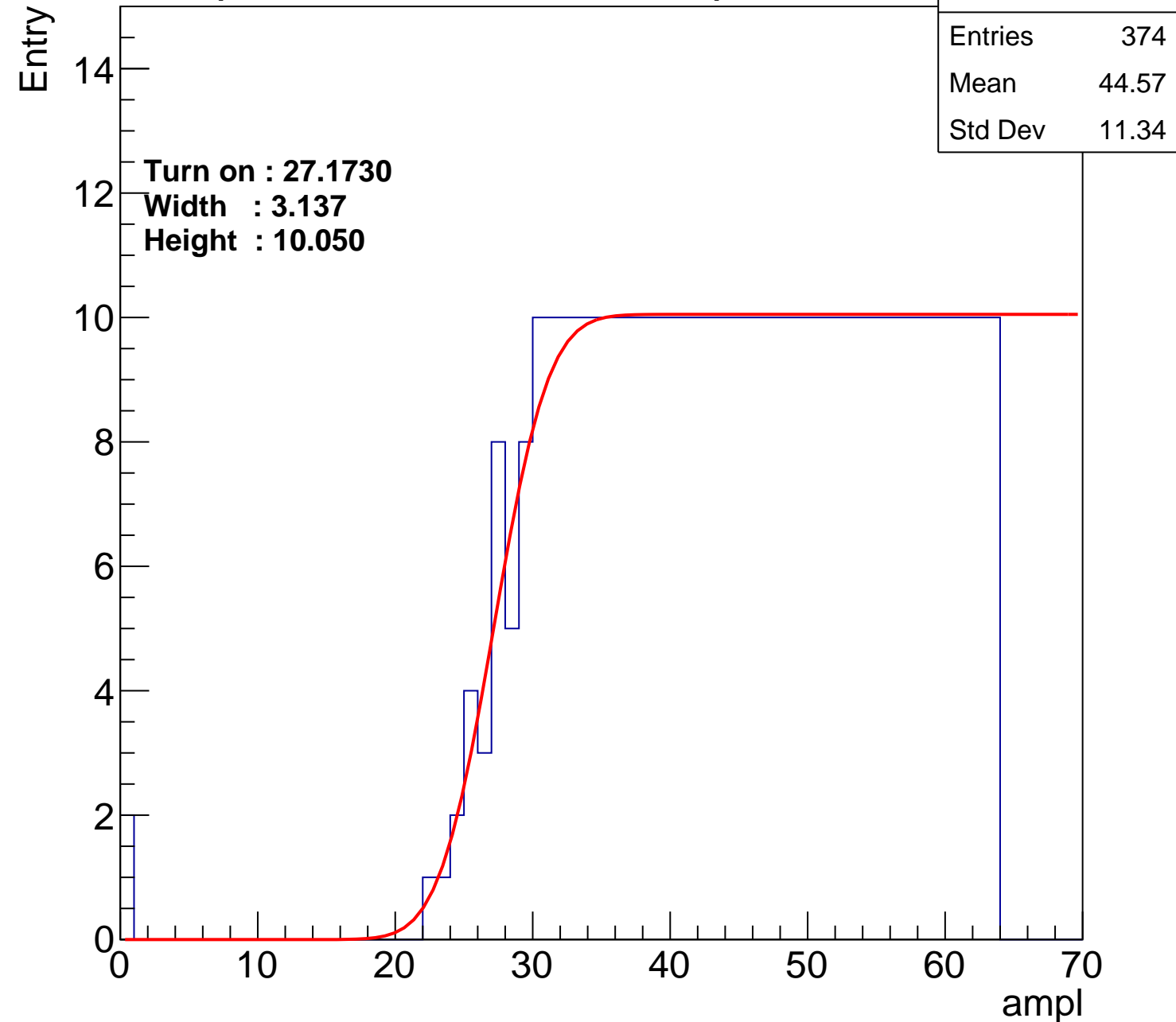
Width : 3.137

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch7

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.5717

Width : 3.240

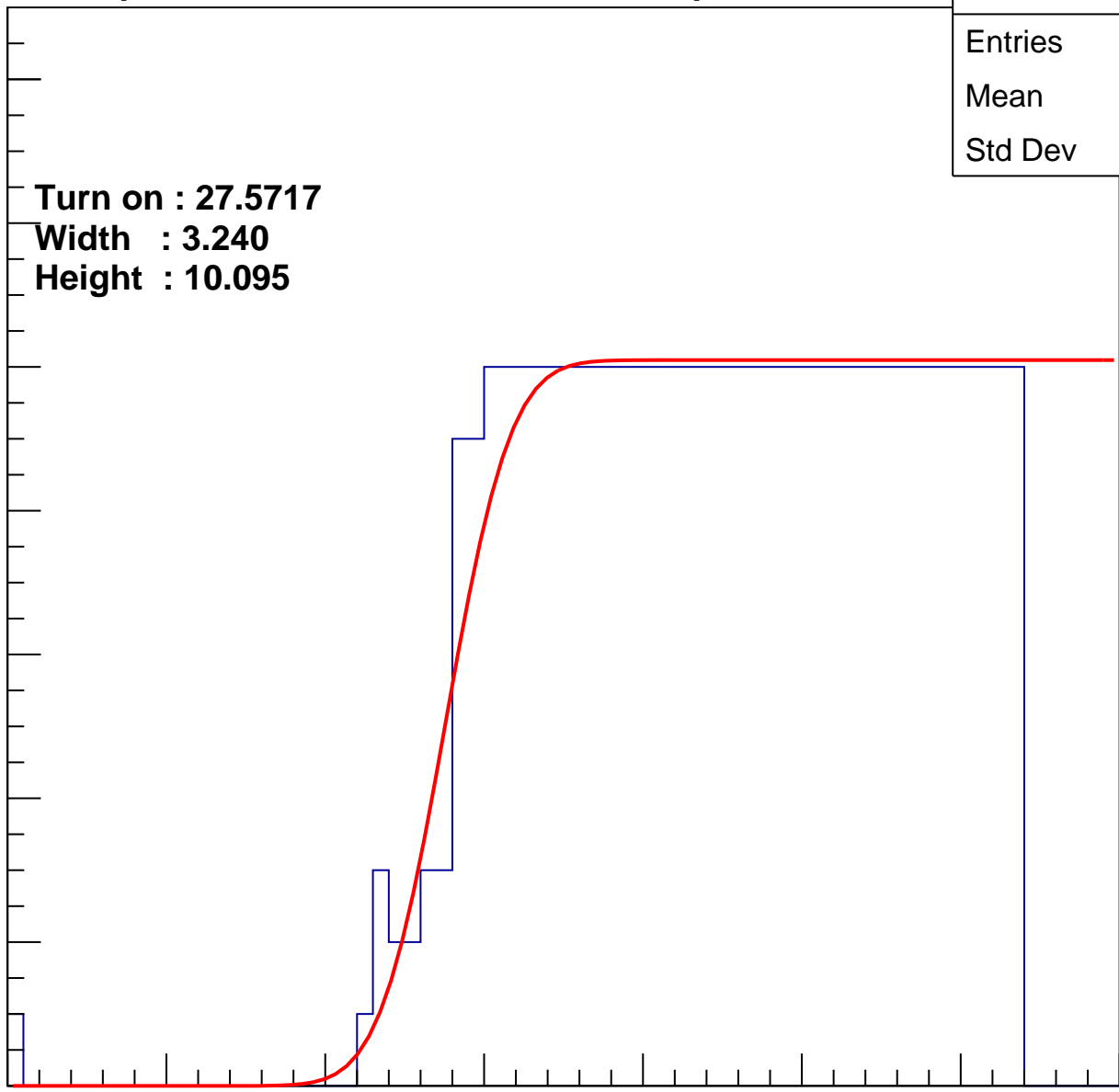
Height : 10.095

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch8

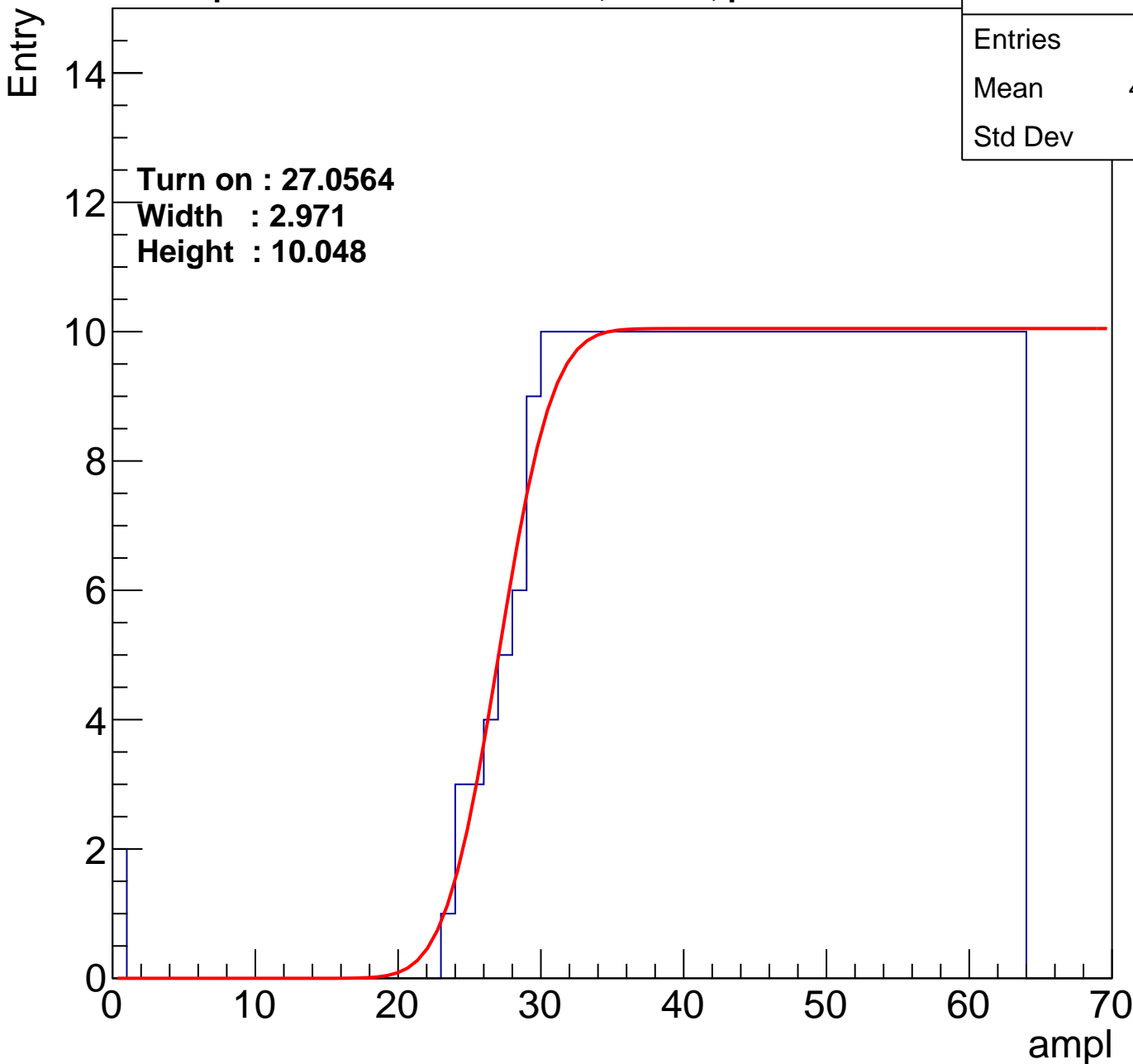
calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.63
Std Dev	11.3

Turn on : 27.0564

Width : 2.971

Height : 10.048



B1L003S, U21-ch9

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.19
Std Dev	11.34

Turn on : 25.7573

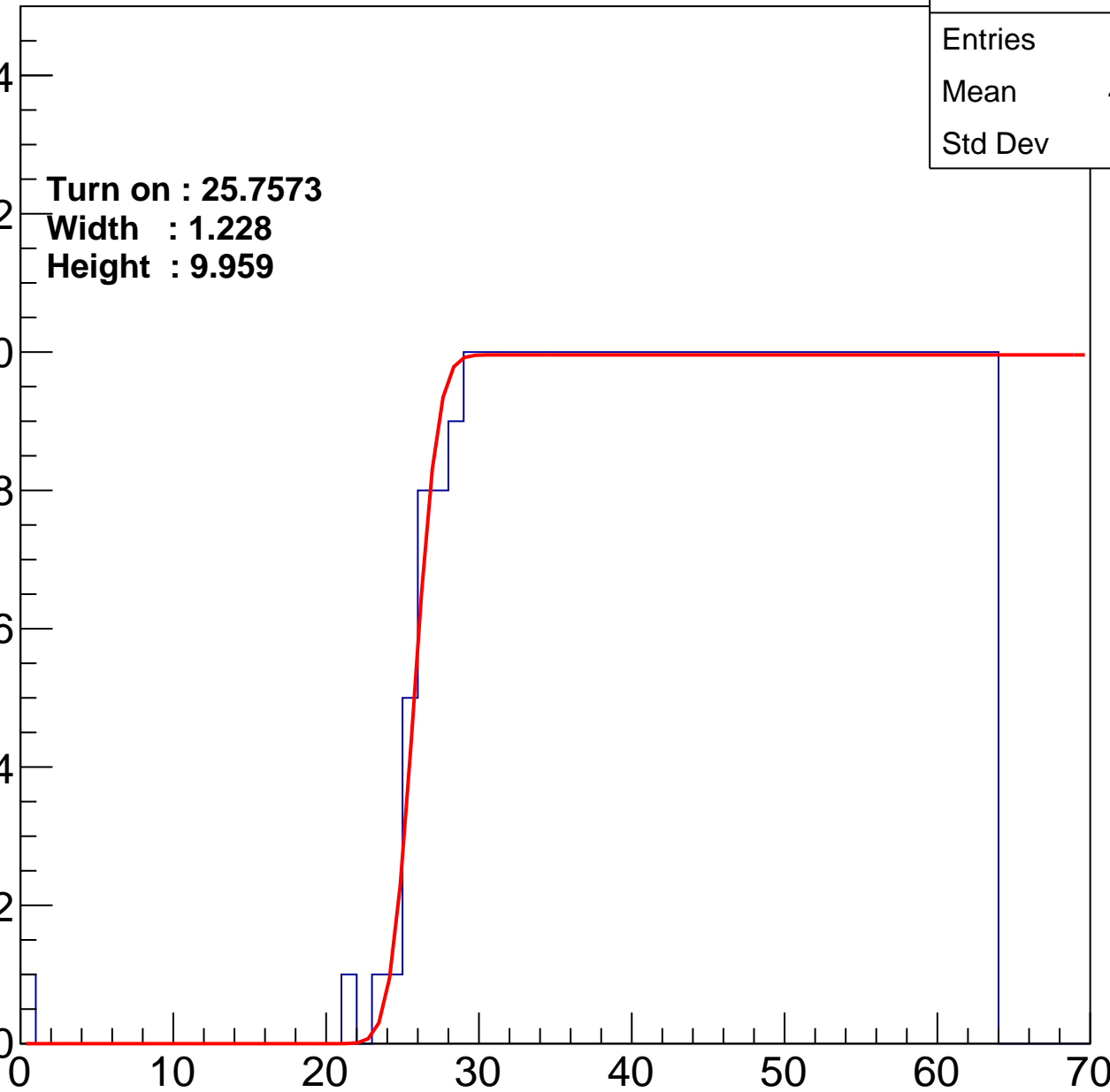
Width : 1.228

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch10

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.72
Std Dev	11.28

Turn on : 27.5584

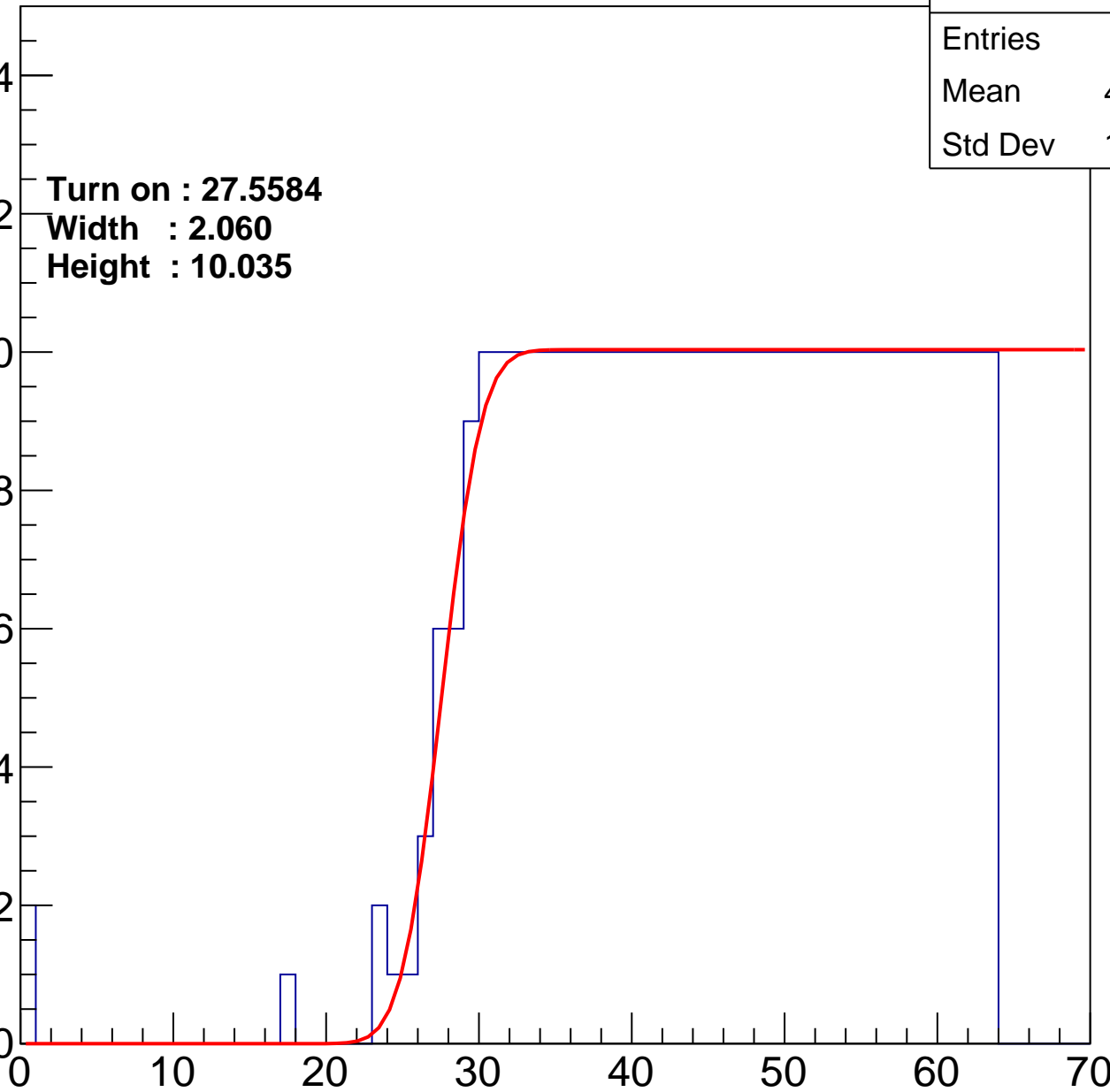
Width : 2.060

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch11

calib_packv5_042523_0143.root, FC#13, port D2

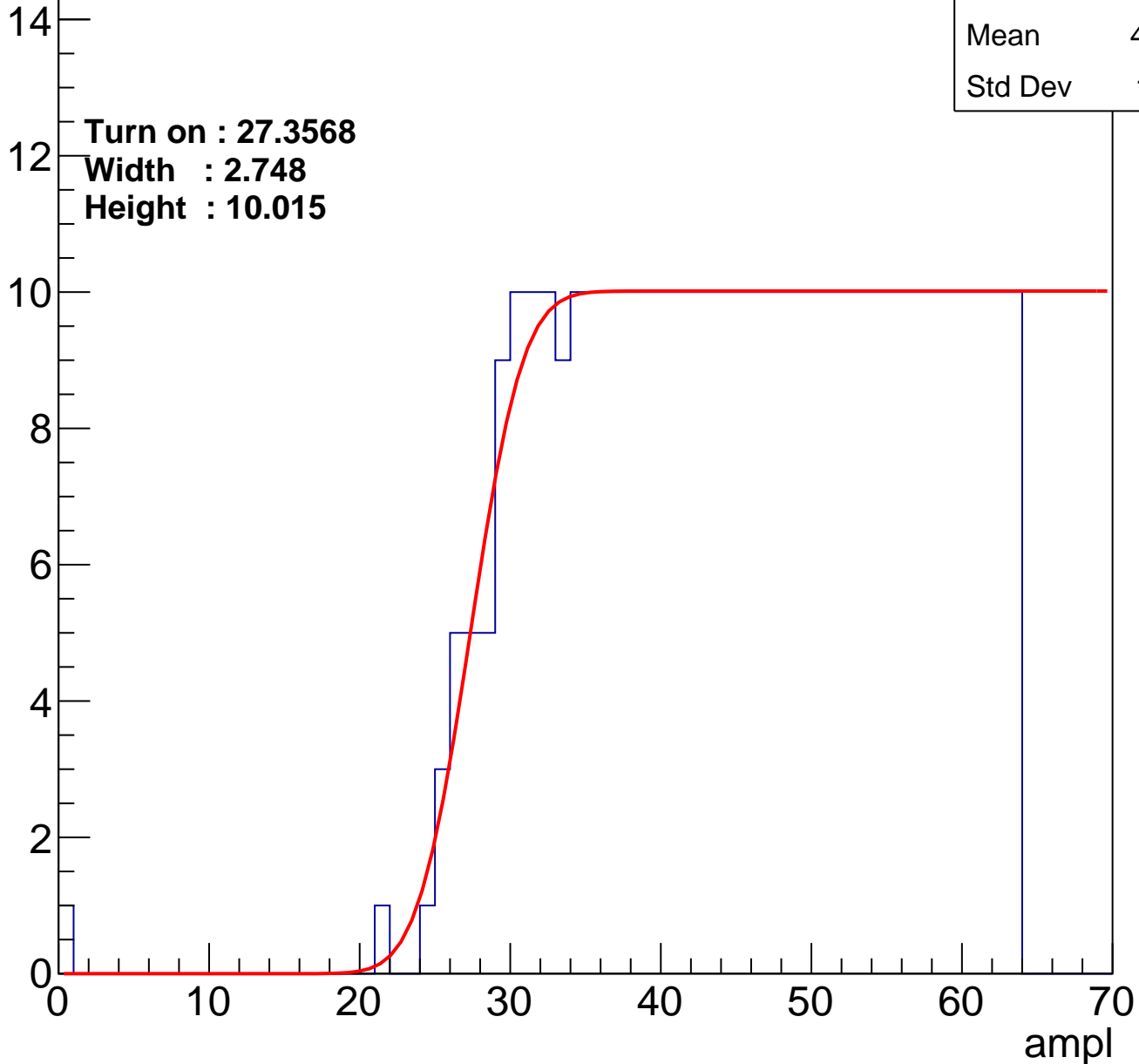
Entries	369
Mean	44.89
Std Dev	11.01

Turn on : 27.3568

Width : 2.748

Height : 10.015

Entry



B1L003S, U21-ch12

calib_packv5_042523_0143.root, FC#13, port D2

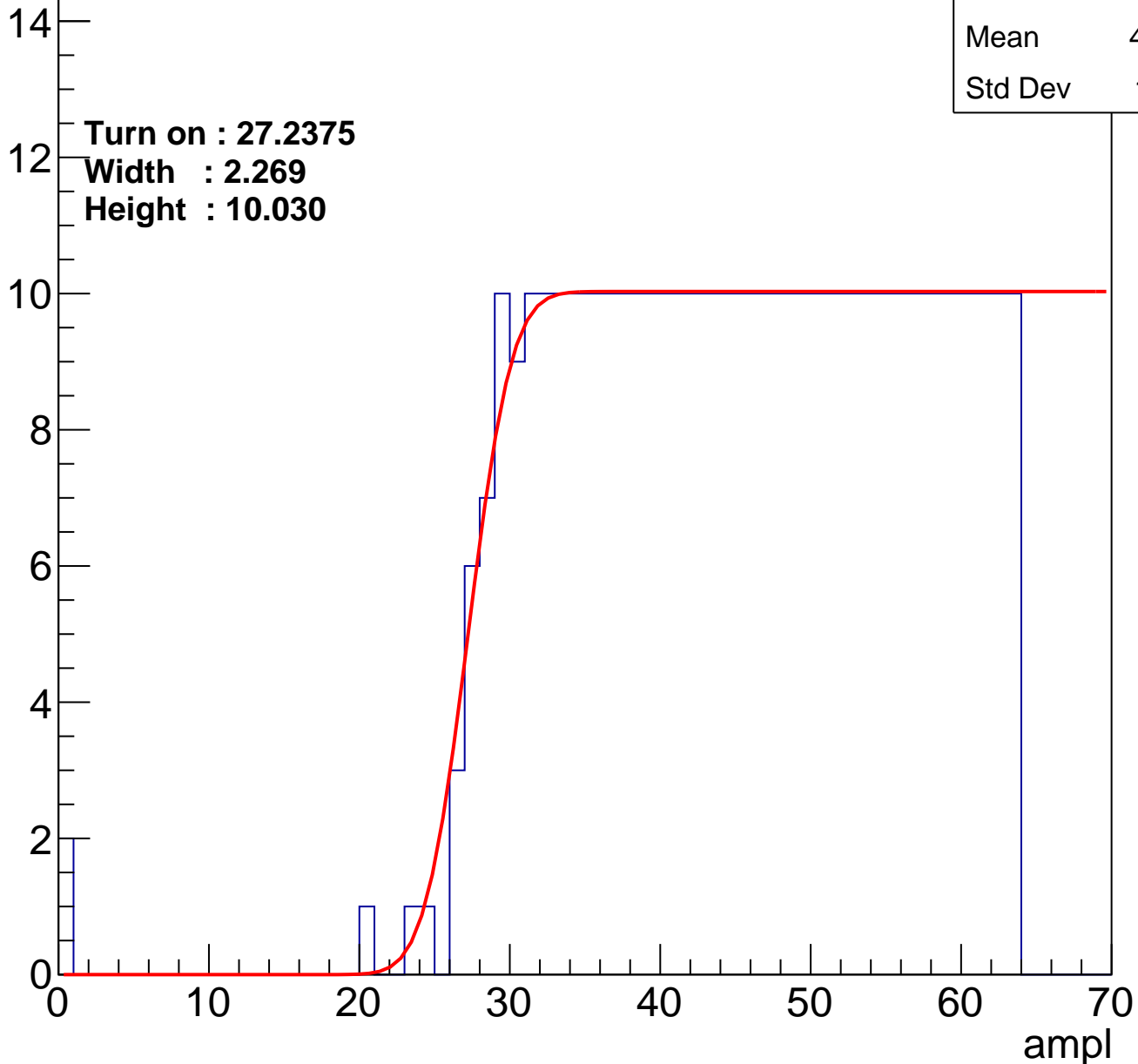
Entries	370
Mean	44.79
Std Dev	11.21

Turn on : 27.2375

Width : 2.269

Height : 10.030

Entry



B1L003S, U21-ch13

calib_packv5_042523_0143.root, FC#13, port D2

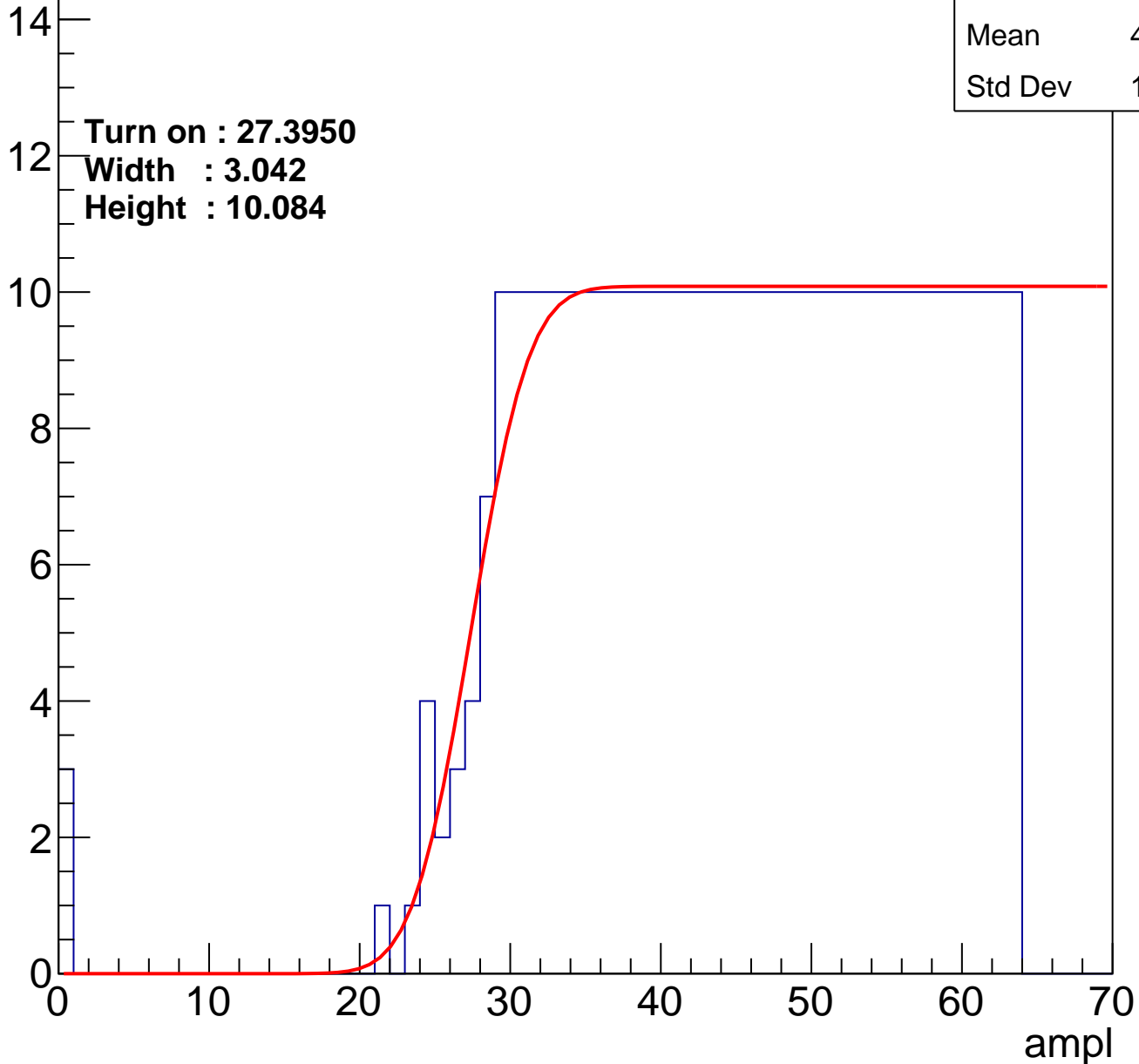
Entries	375
Mean	44.46
Std Dev	11.55

Turn on : 27.3950

Width : 3.042

Height : 10.084

Entry



B1L003S, U21-ch14

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.62
Std Dev	11.44

Turn on : 26.9513

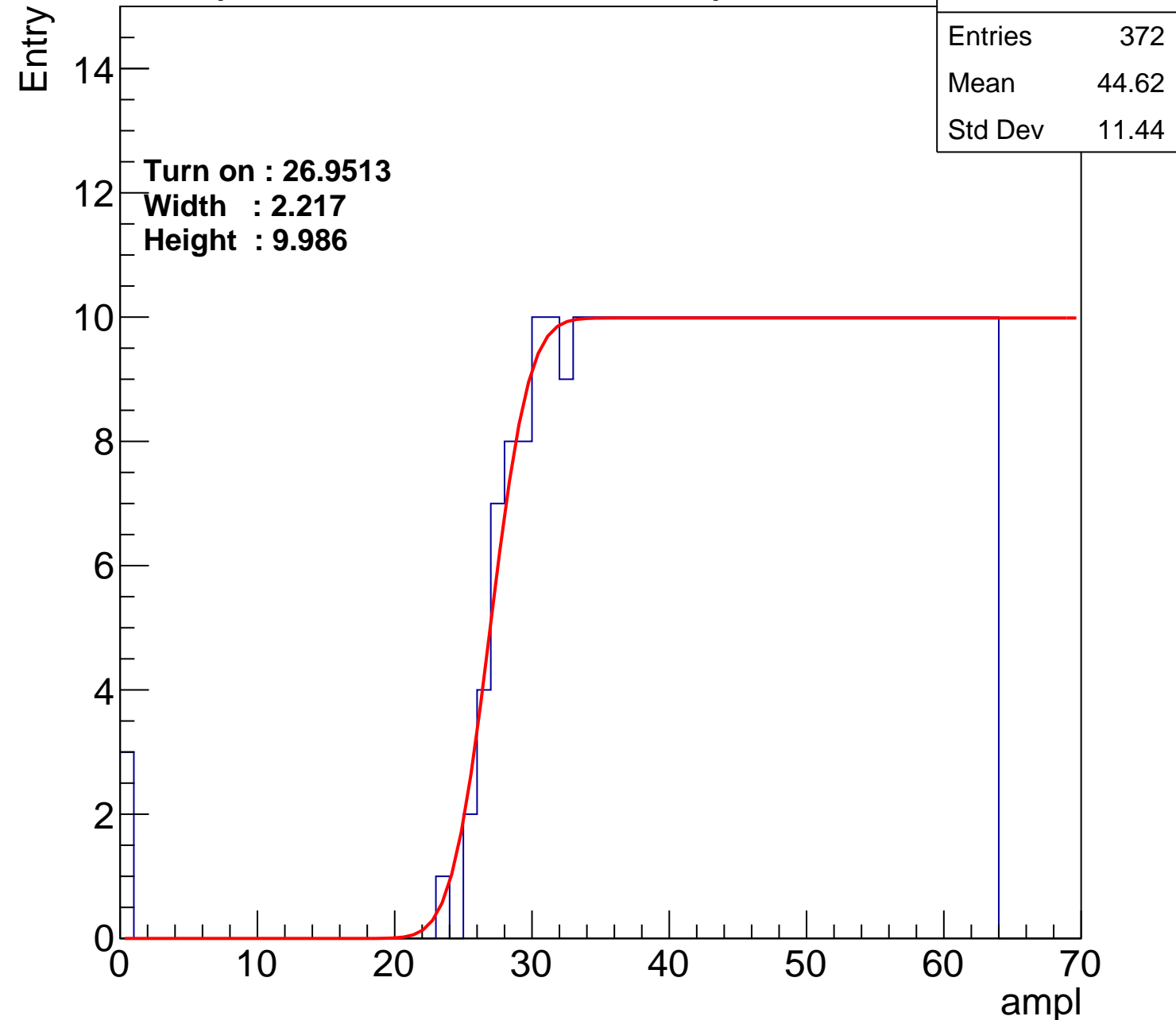
Width : 2.217

Height : 9.986

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch15

calib_packv5_042523_0143.root, FC#13, port D2

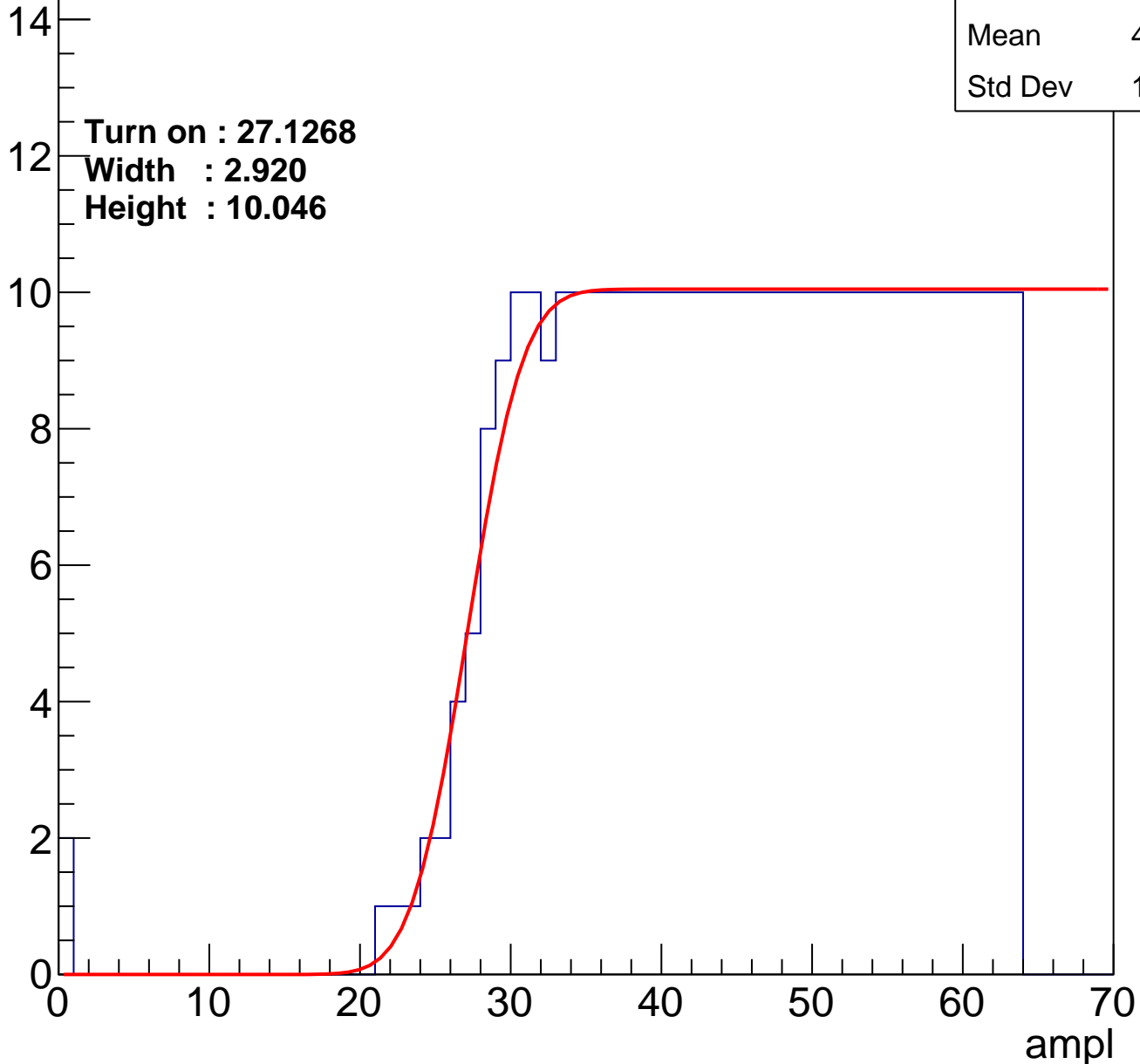
Entries	374
Mean	44.56
Std Dev	11.36

Turn on : 27.1268

Width : 2.920

Height : 10.046

Entry



B1L003S, U21-ch16

calib_packv5_042523_0143.root, FC#13, port D2

Entries	370
Mean	44.8
Std Dev	11.19

Turn on : 27.6147

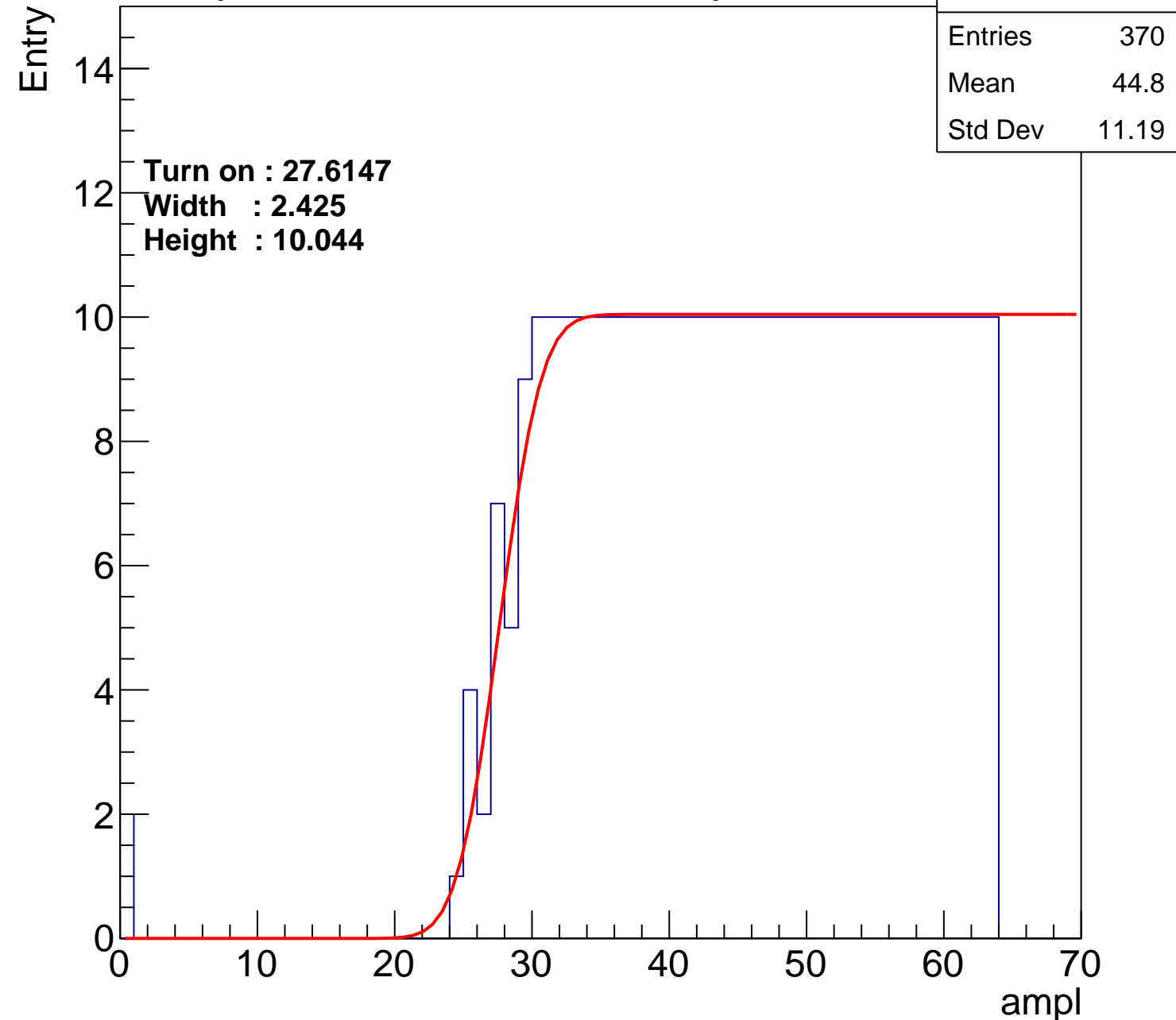
Width : 2.425

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch17

calib_packv5_042523_0143.root, FC#13, port D2

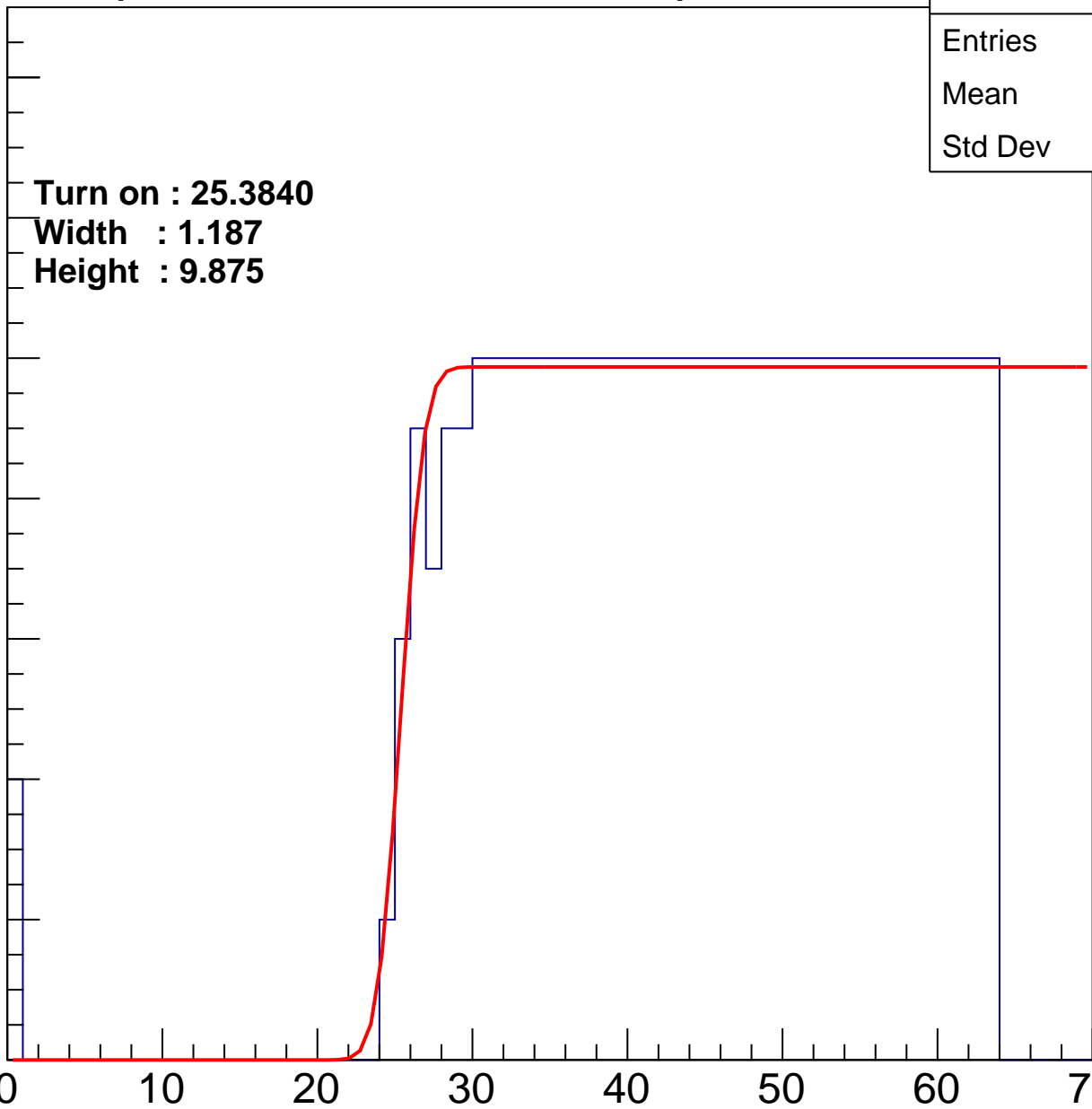
Entry

14
12
10
8
6
4
2
0

Turn on : 25.3840
Width : 1.187
Height : 9.875

Entries	386
Mean	43.9
Std Dev	11.92

ampl



B1L003S, U21-ch18

calib_packv5_042523_0143.root, FC#13, port D2

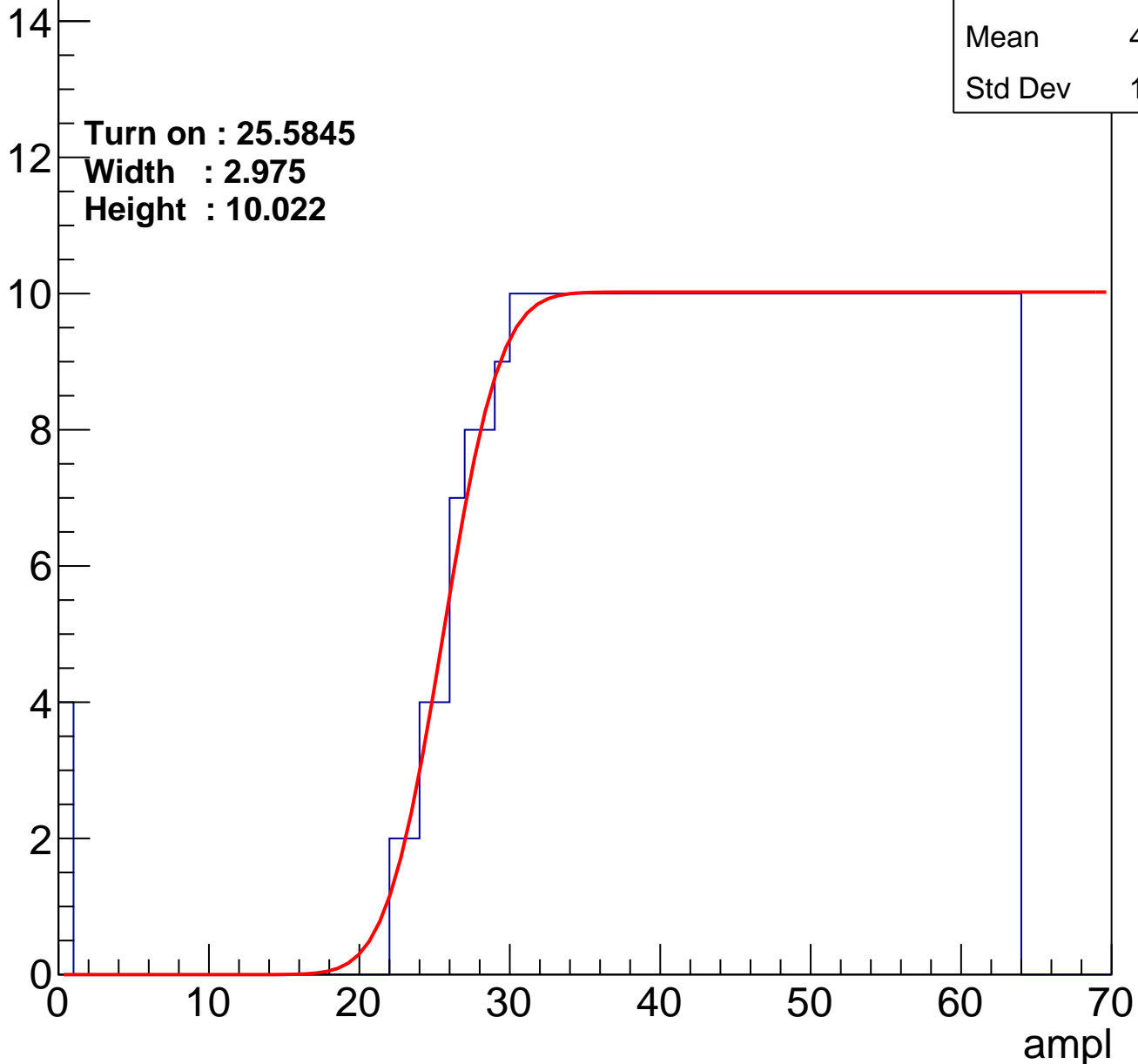
Entries	388
Mean	43.76
Std Dev	12.03

Turn on : 25.5845

Width : 2.975

Height : 10.022

Entry



B1L003S, U21-ch19

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.75
Std Dev	11.55

Turn on : 27.7982

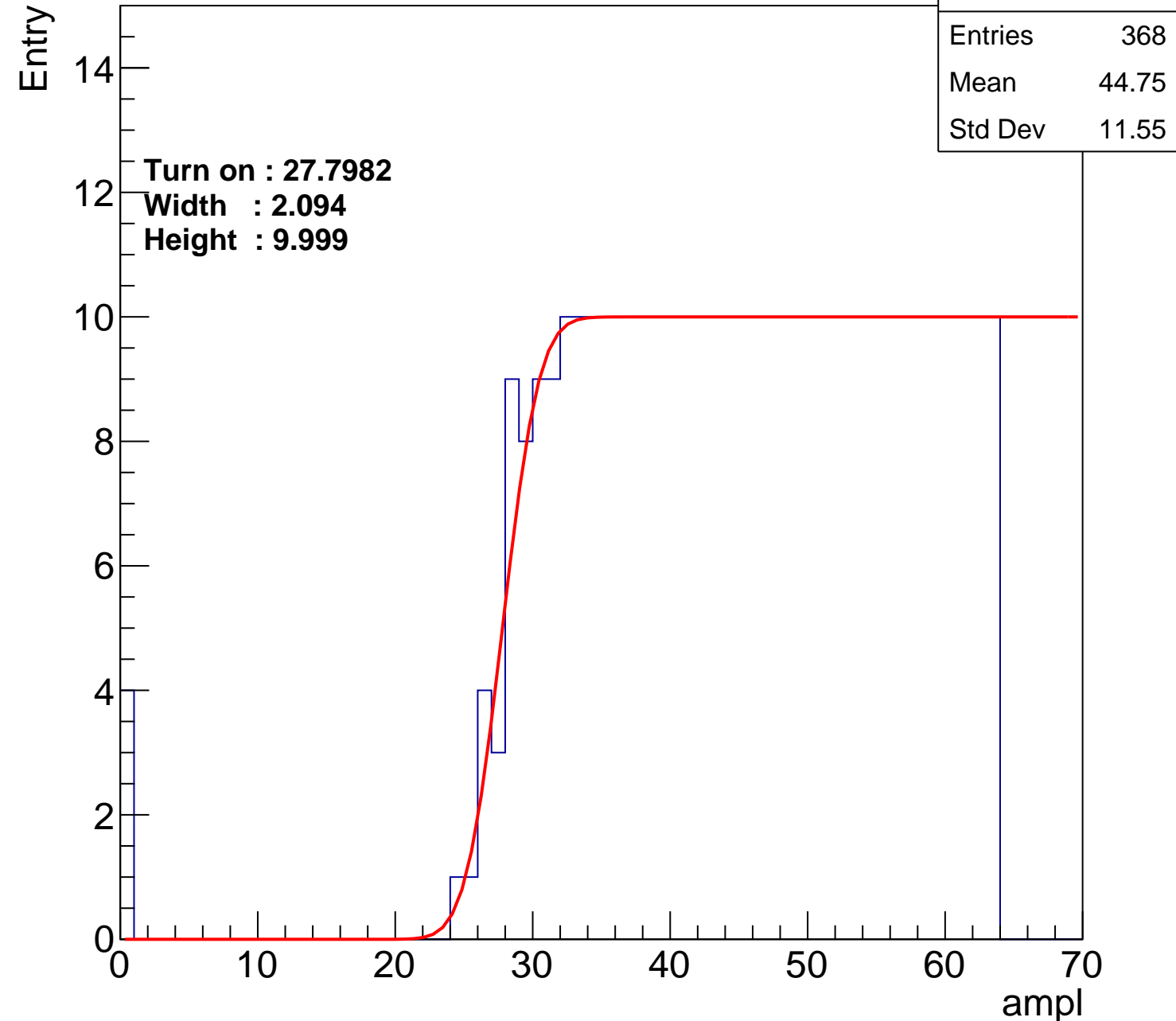
Width : 2.094

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch20

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.7
Std Dev	11.76

Turn on : 28.1322

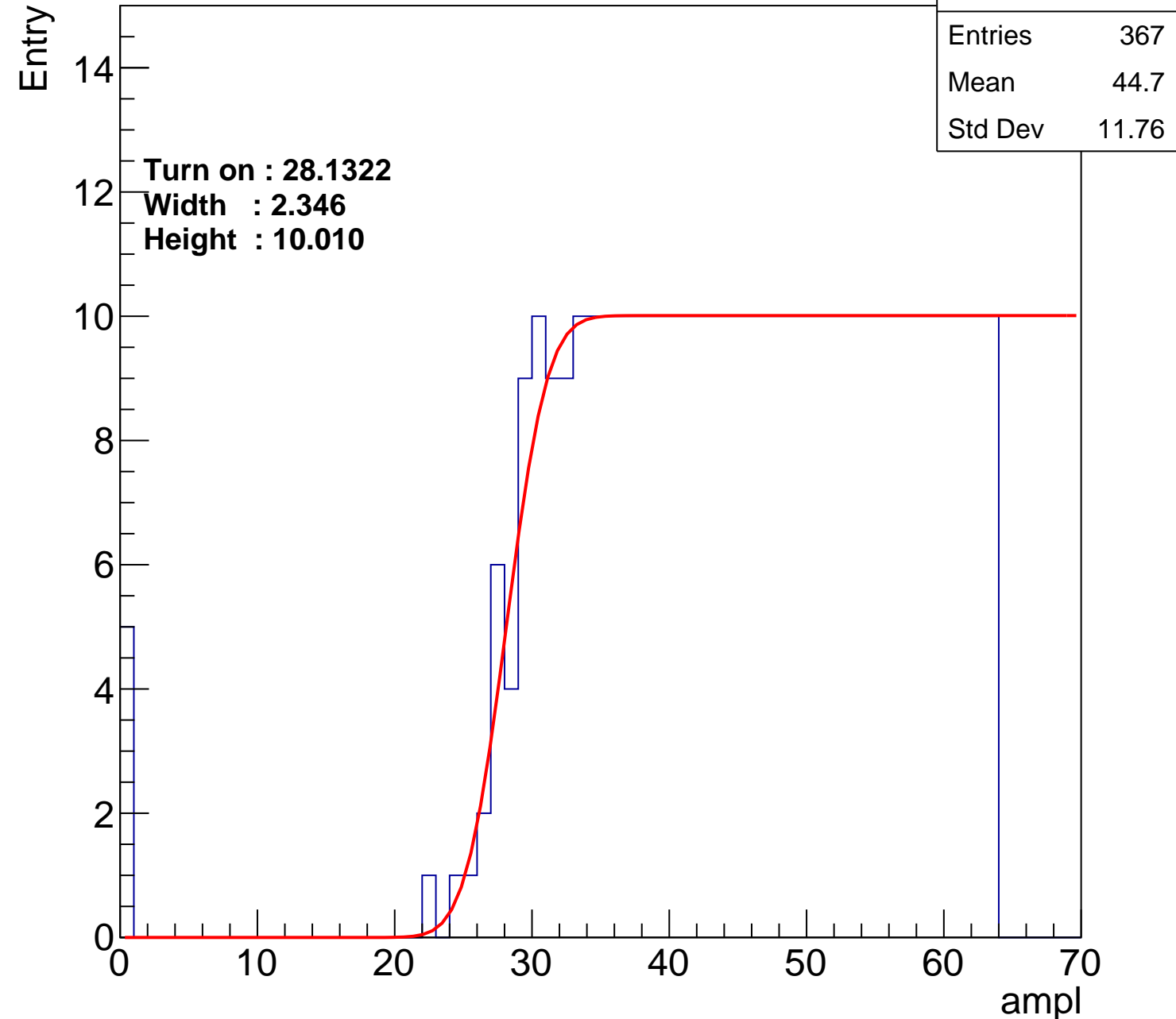
Width : 2.346

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch21

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.75
Std Dev	11.55

Turn on : 27.2167

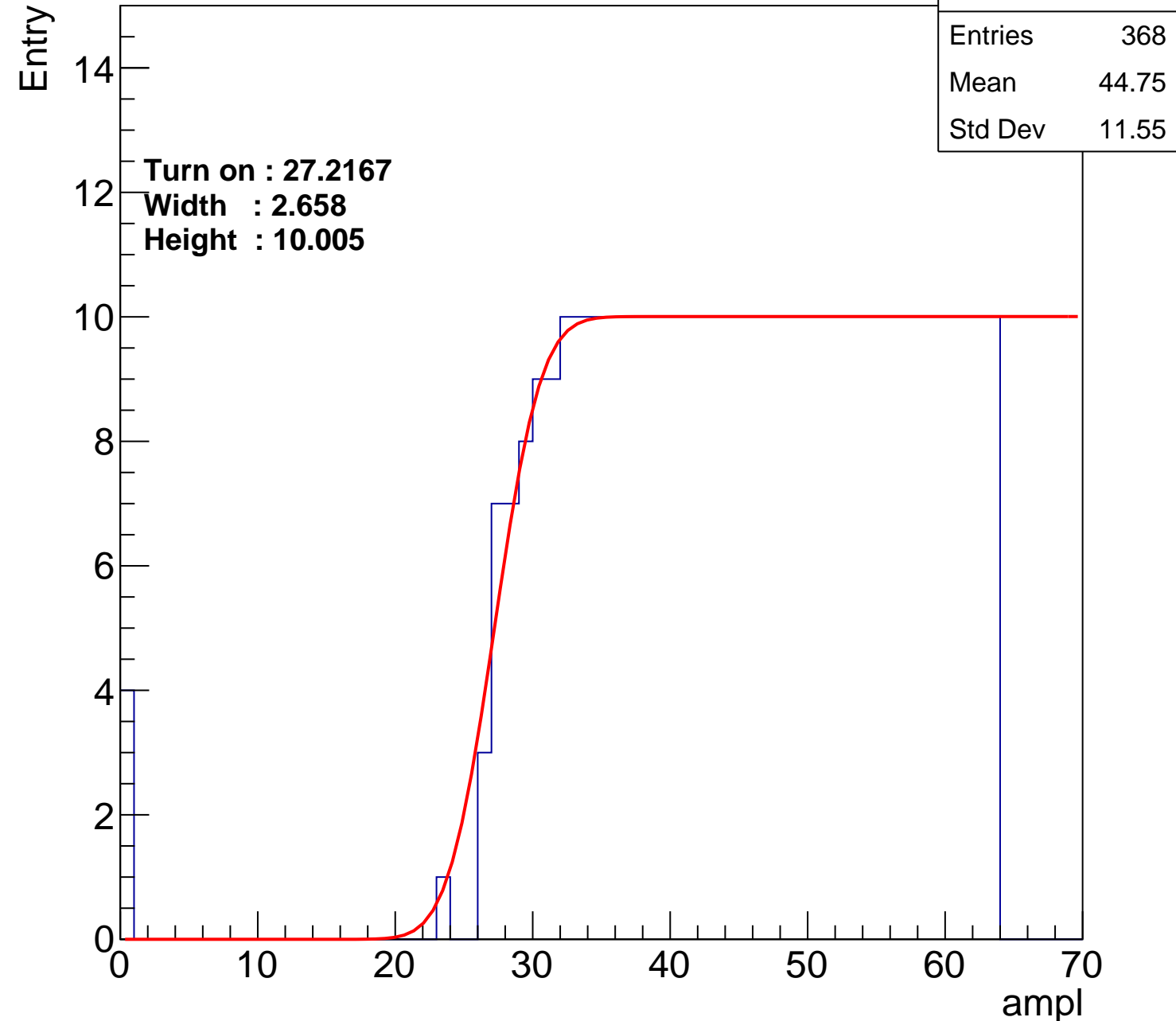
Width : 2.658

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch22

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.36
Std Dev	11.59

Turn on : 26.9322

Width : 2.911

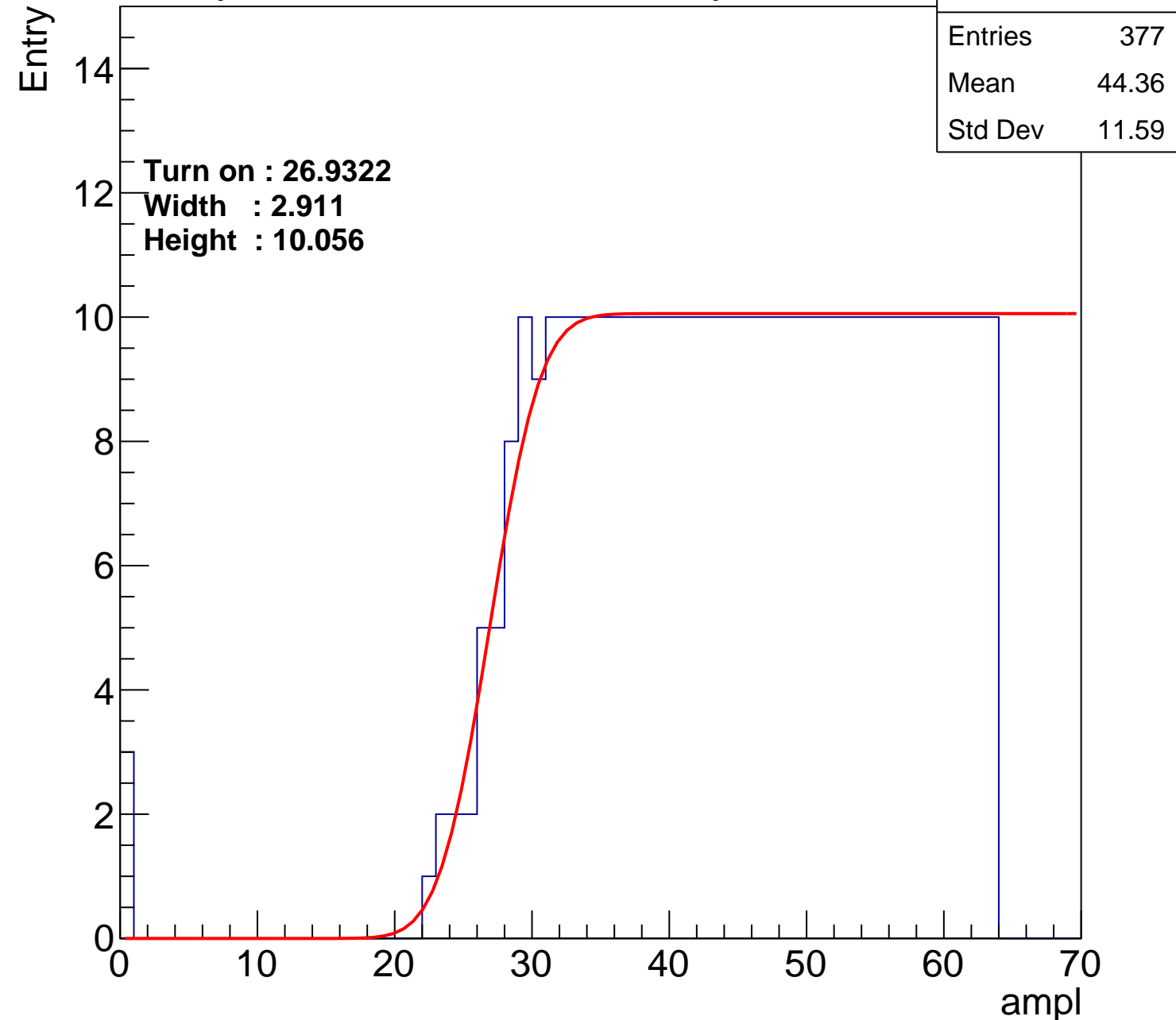
Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L003S, U21-ch23

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.85
Std Dev	11.36

Turn on : 28.1694

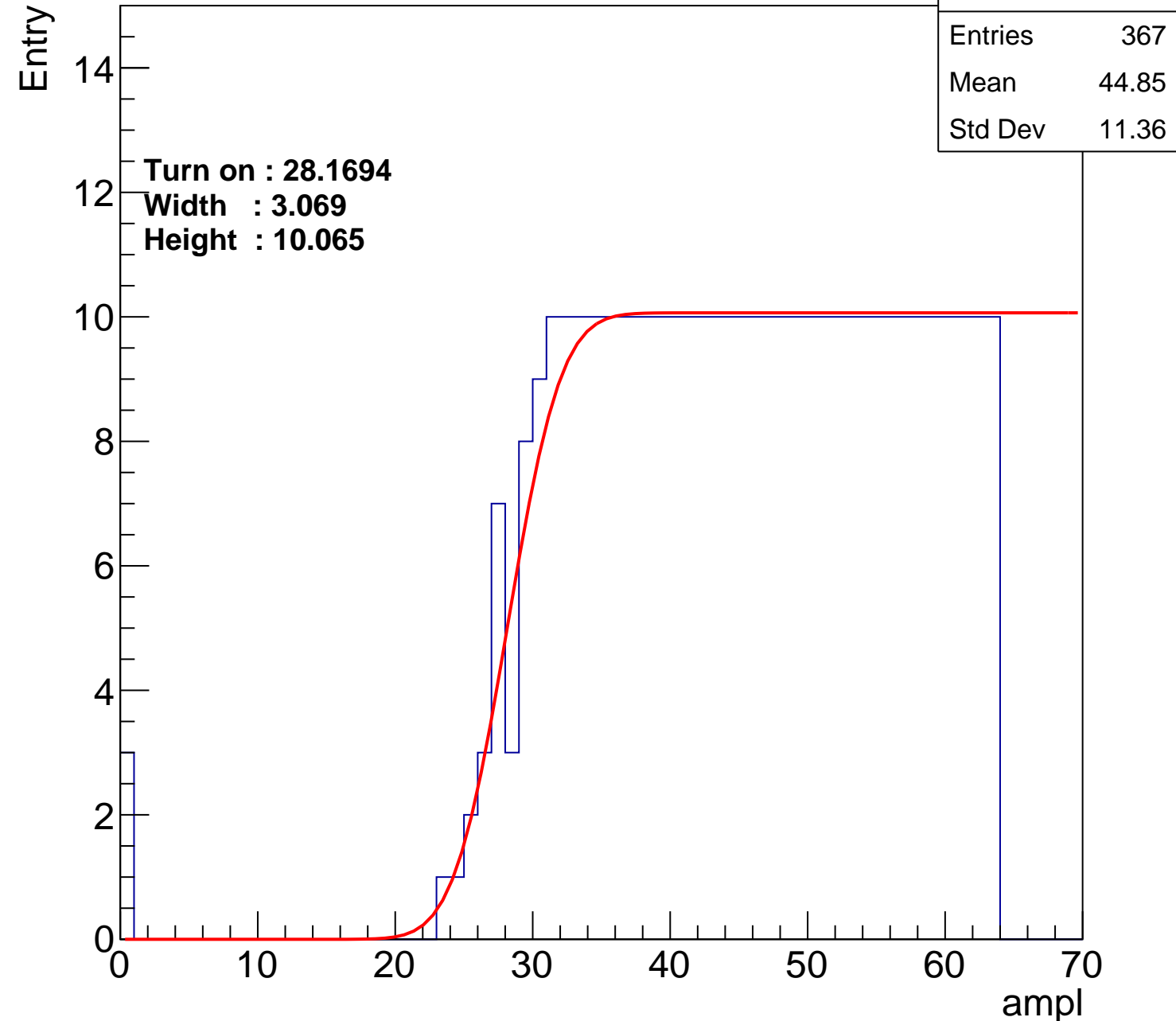
Width : 3.069

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch24

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.66
Std Dev	11.3

Turn on : 27.0413

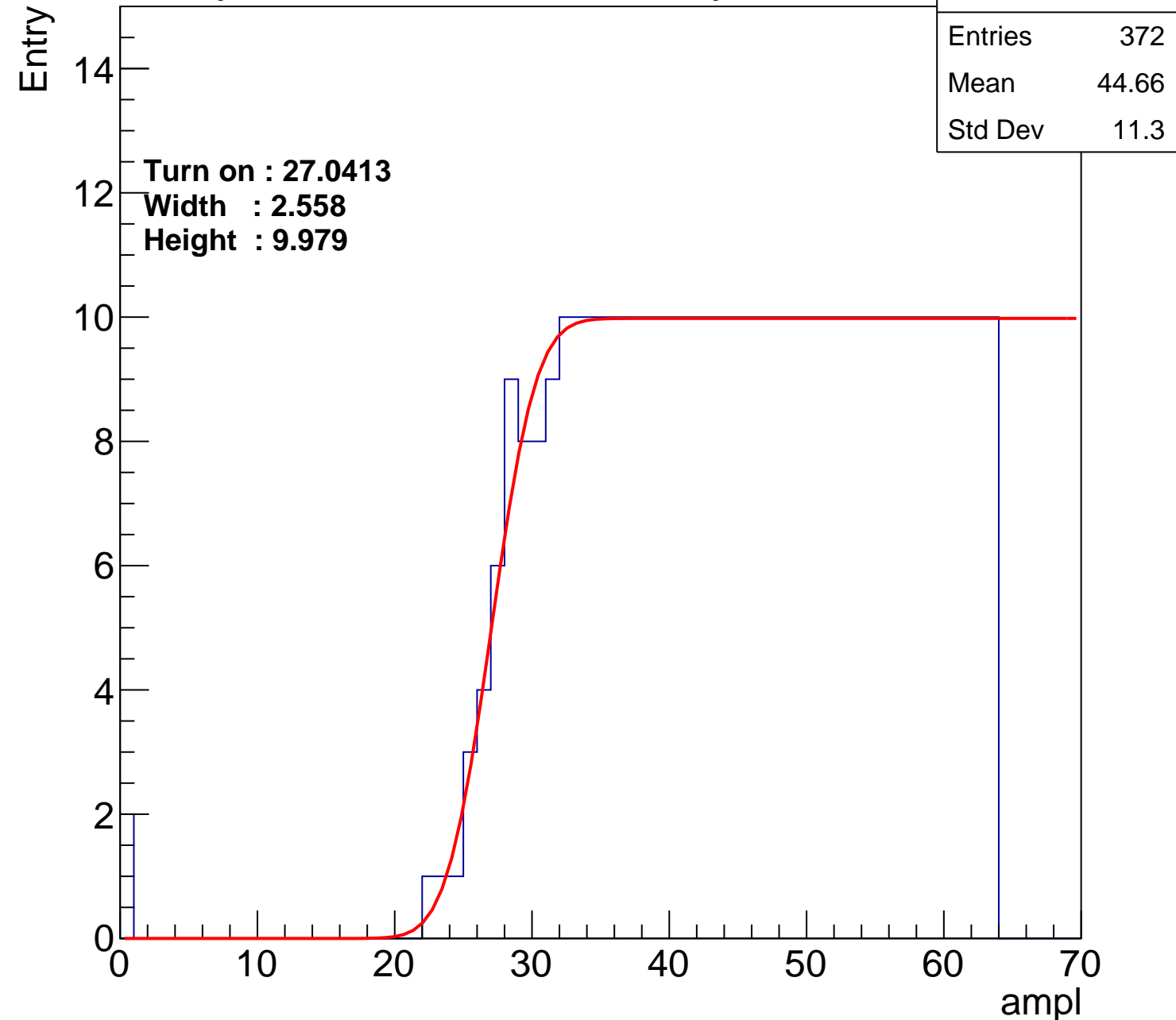
Width : 2.558

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch25

calib_packv5_042523_0143.root, FC#13, port D2

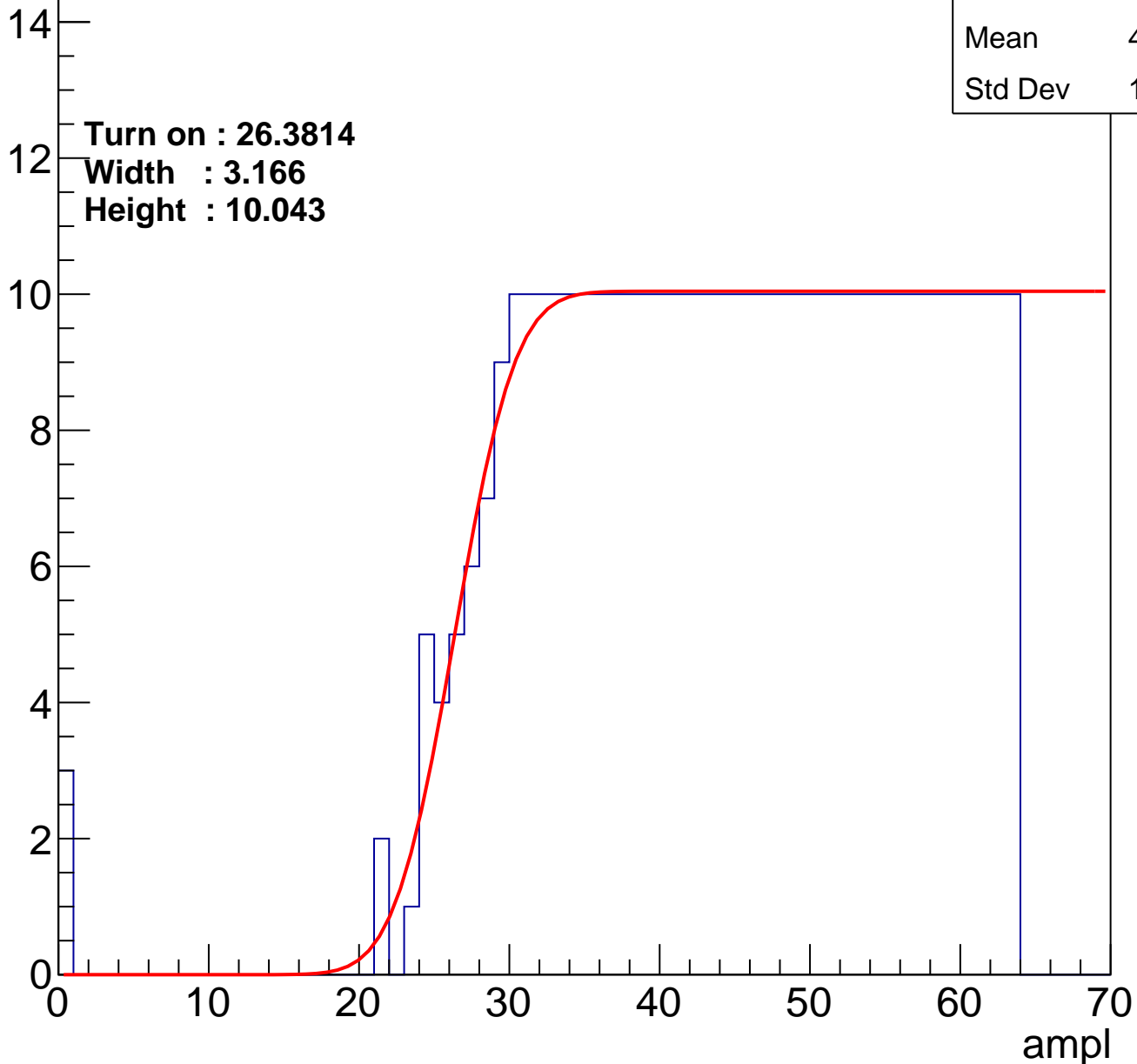
Entry

Entries	382
Mean	44.09
Std Dev	11.75

Turn on : 26.3814

Width : 3.166

Height : 10.043



B1L003S, U21-ch26

calib_packv5_042523_0143.root, FC#13, port D2

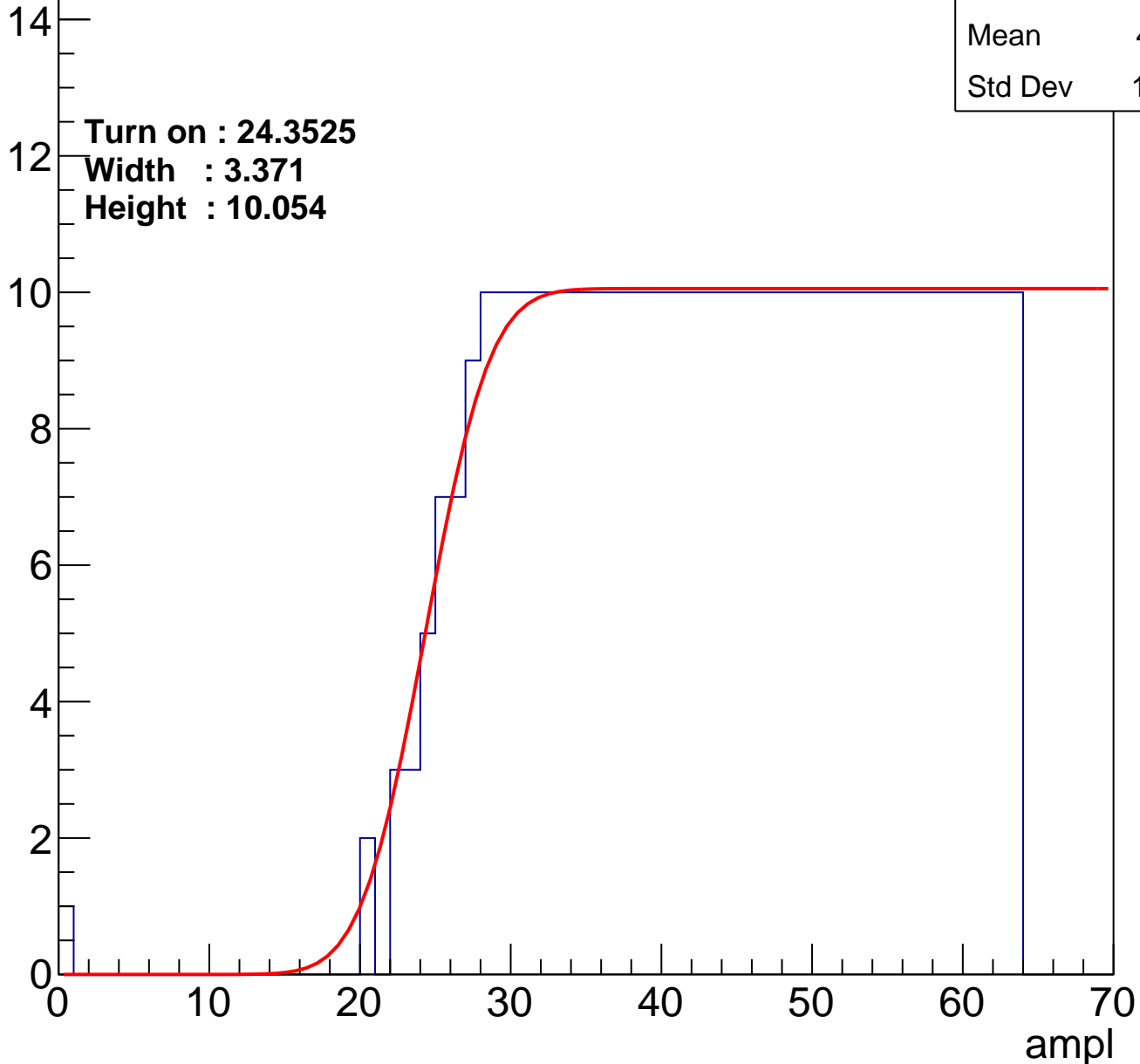
Entries	397
Mean	43.51
Std Dev	11.75

Turn on : 24.3525

Width : 3.371

Height : 10.054

Entry



B1L003S, U21-ch27

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.41
Std Dev	10.91

Turn on : 29.0546

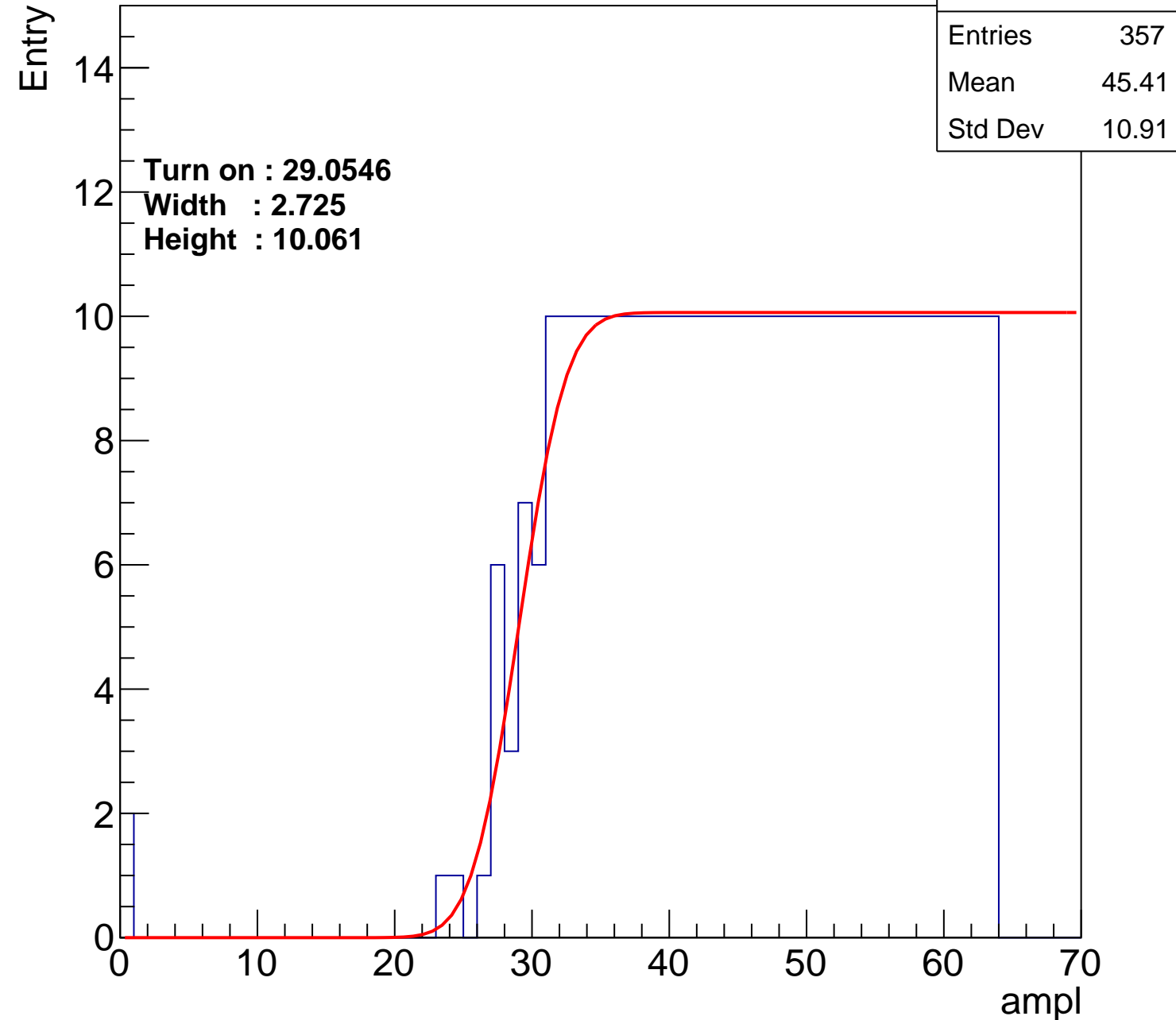
Width : 2.725

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch28

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.05
Std Dev	11.72

Turn on : 25.8941

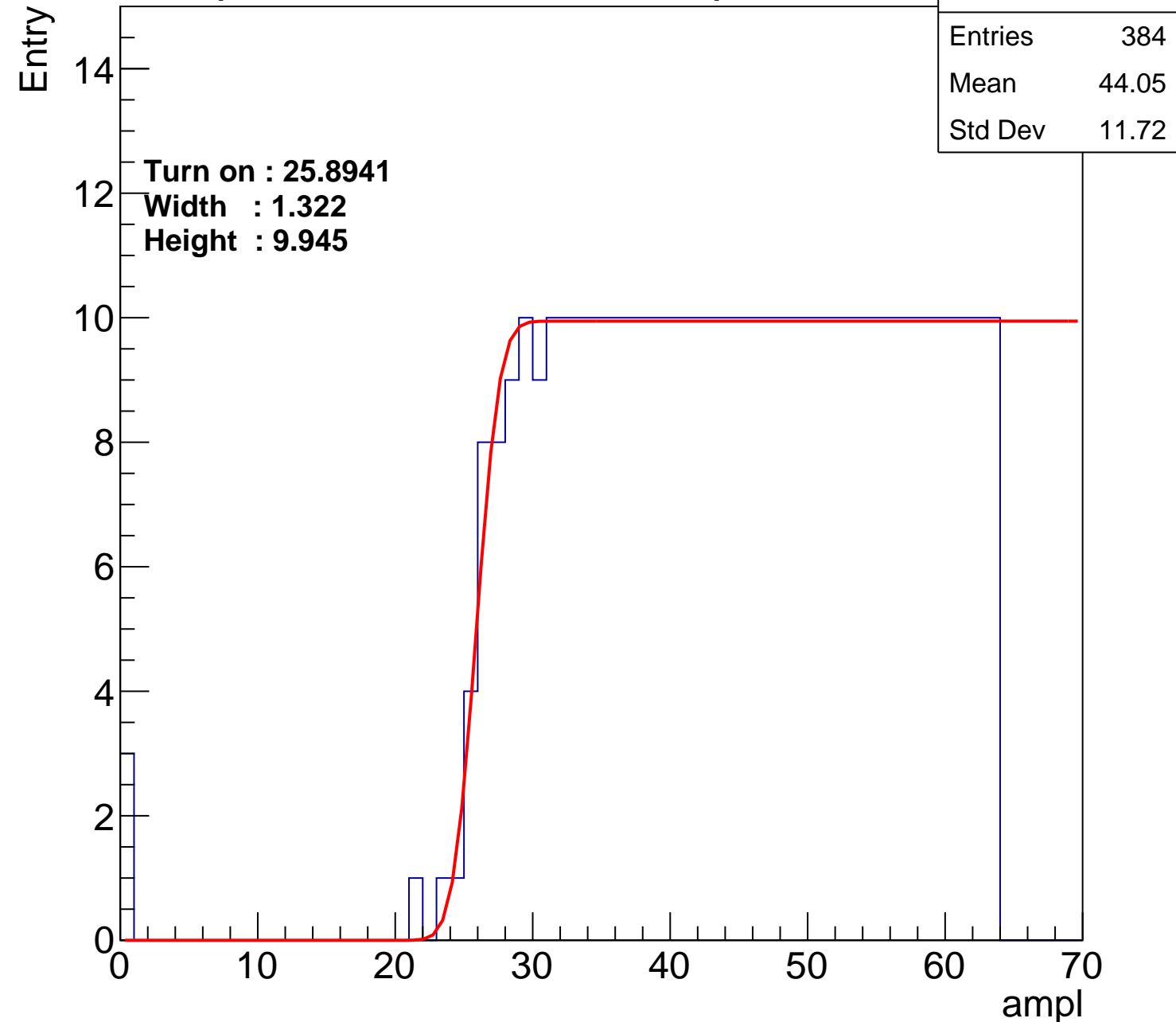
Width : 1.322

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch29

calib_packv5_042523_0143.root, FC#13, port D2

Entries	351
Mean	45.62
Std Dev	11

Turn on : 29.9248

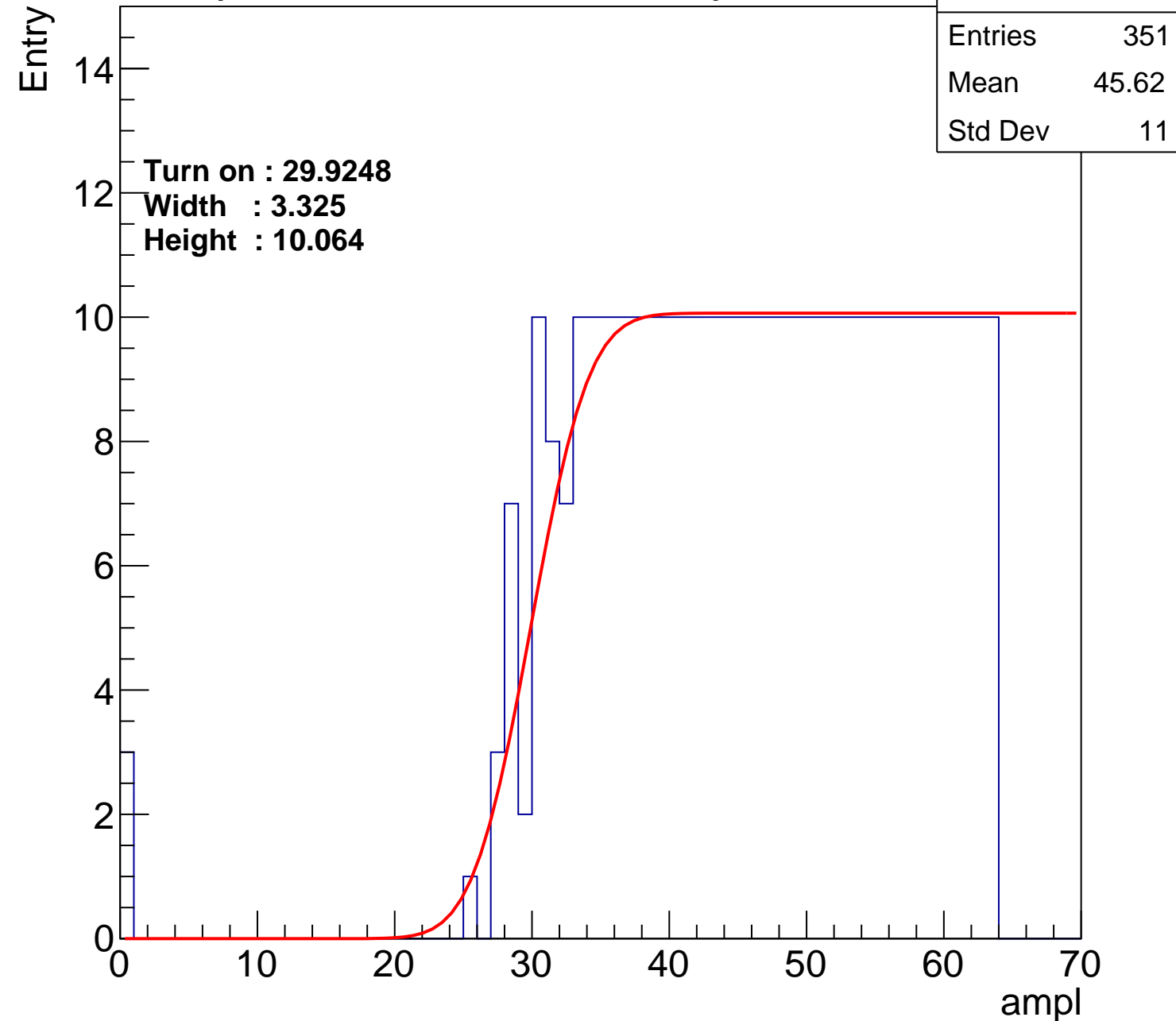
Width : 3.325

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#13, port D2

calib_packv5_042523_0143.root, FC#13, port D2

Turn on : 26.1951
Width : 2.726
Height : 10.014



B1L003S, U21-ch31

calib_packv5_042523_0143.root, FC#13, port D2

Entries	354
Mean	45.49
Std Dev	11.04

Turn on : 29.8276

Width : 1.755

Height : 10.035

Entry

14

12

10

8

6

4

2

0

0

10

20

30

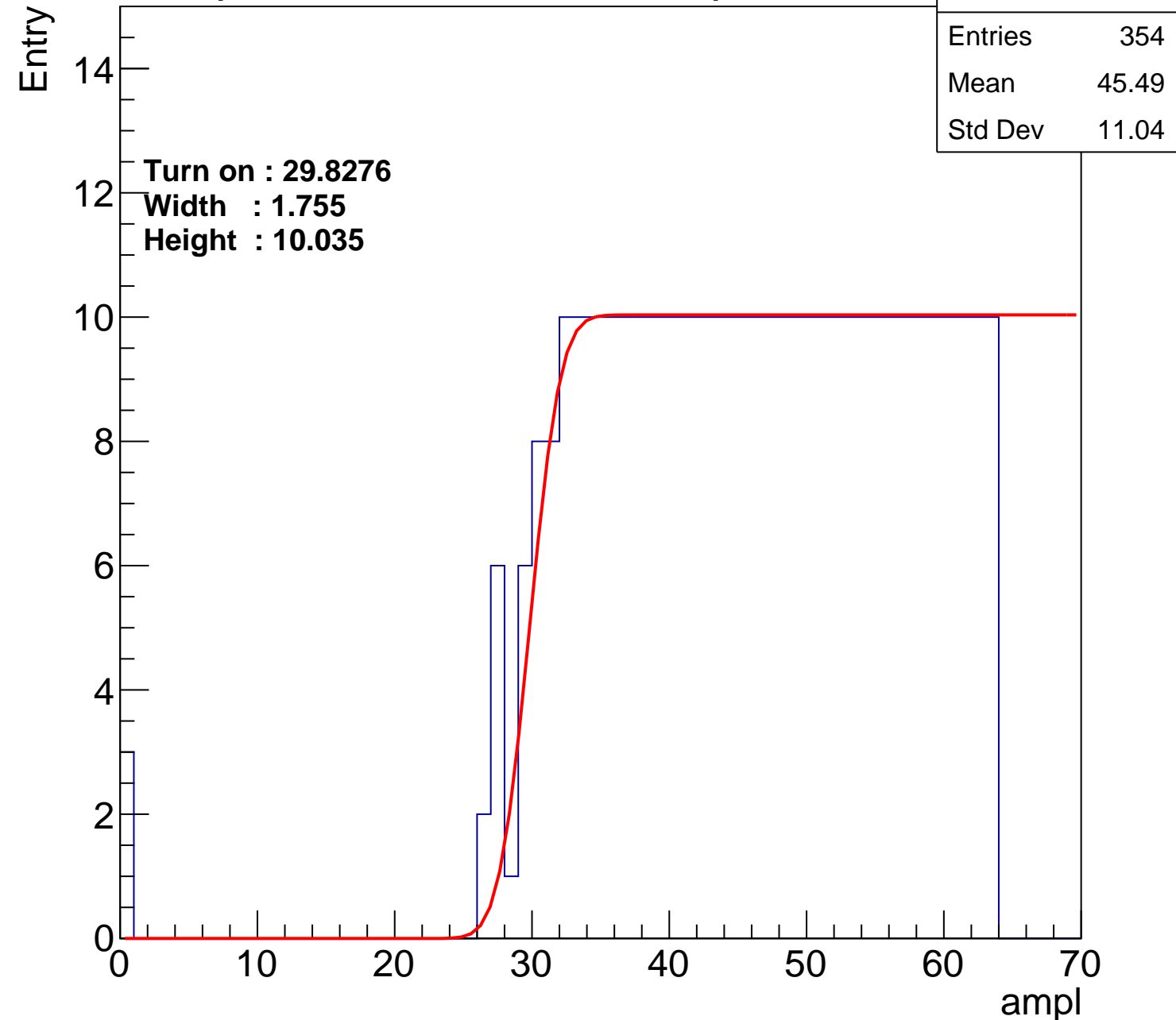
40

50

60

70

ampl



B1L003S, U21-ch32

calib_packv5_042523_0143.root, FC#13, port D2

Entries	396
Mean	43.37
Std Dev	12.23

Turn on : 25.1680

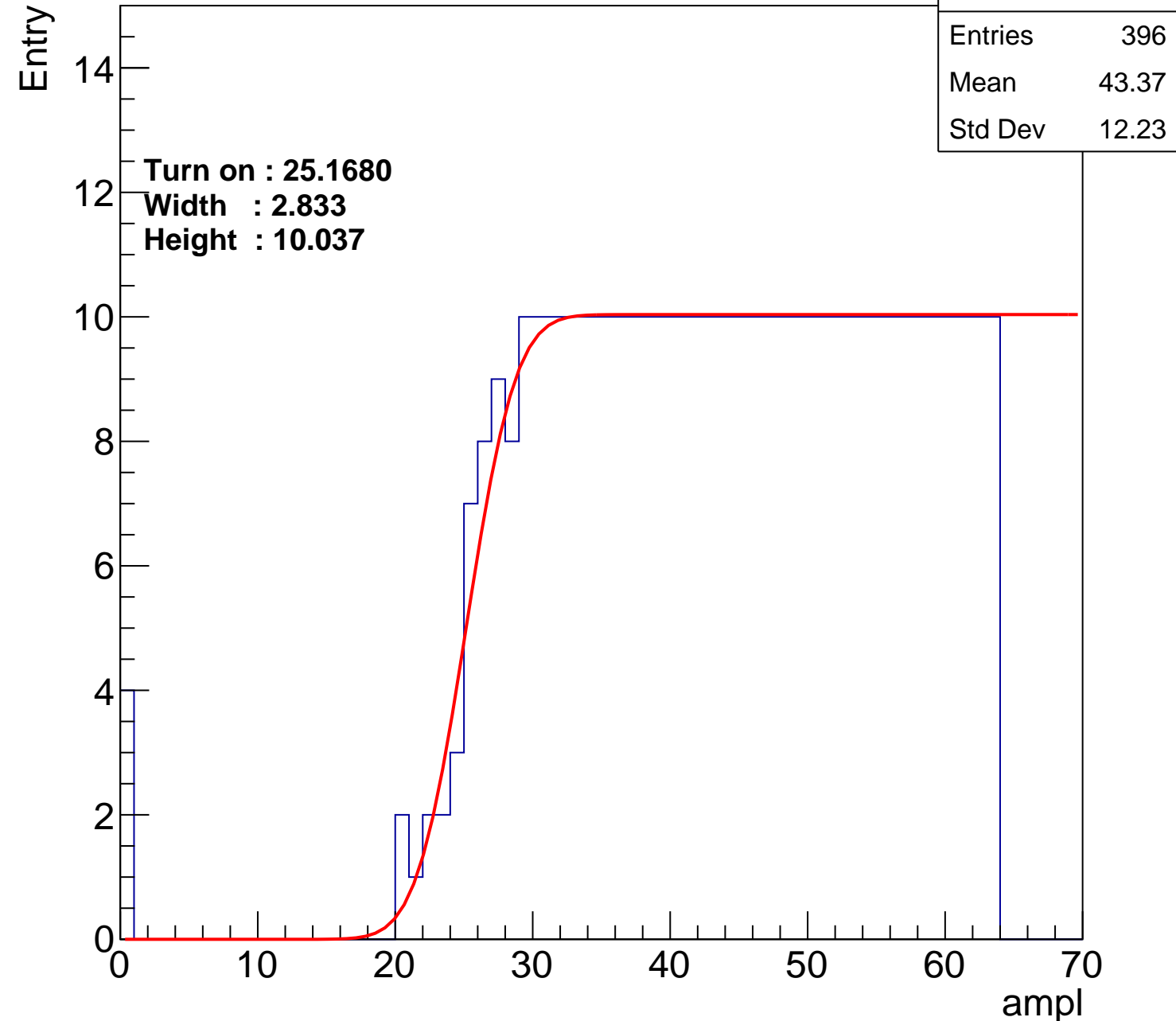
Width : 2.833

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch33

calib_packv5_042523_0143.root, FC#13, port D2

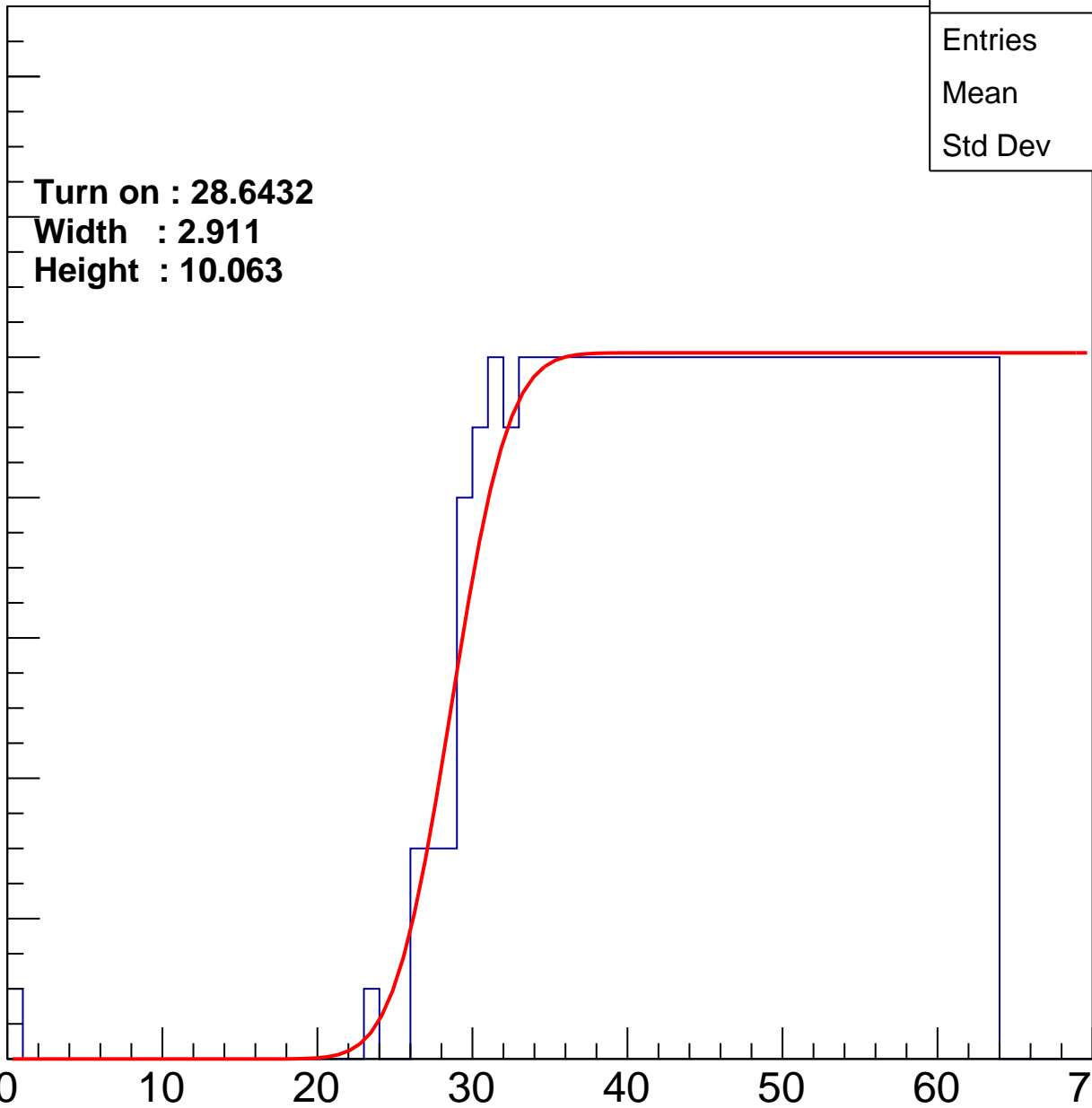
Entry

14
12
10
8
6
4
2
0

Turn on : 28.6432
Width : 2.911
Height : 10.063

Entries	357
Mean	45.51
Std Dev	10.65

ampl



B1L003S, U21-ch34

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.45
Std Dev	11.56

Turn on : 27.2428

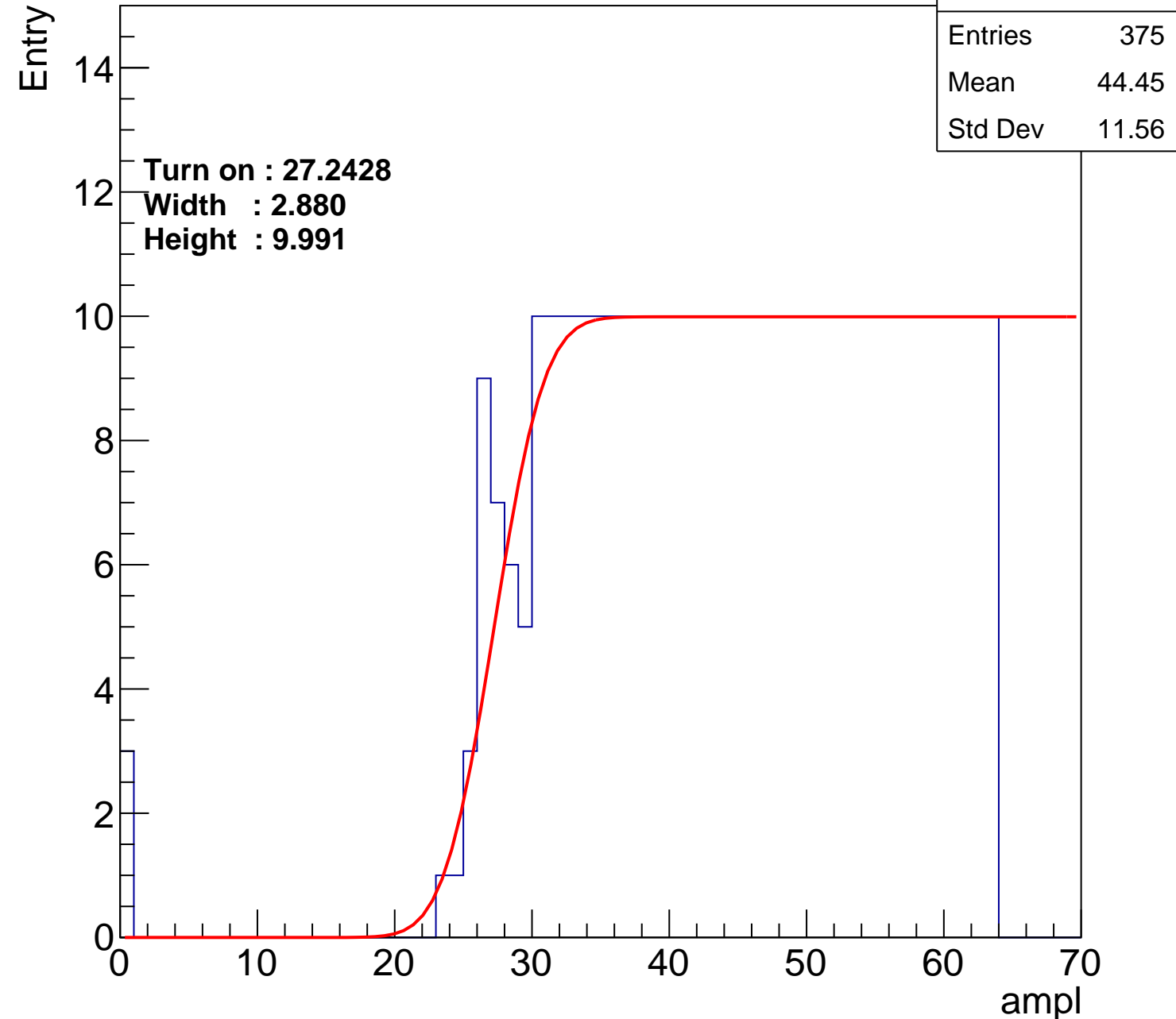
Width : 2.880

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch35

calib_packv5_042523_0143.root, FC#13, port D2

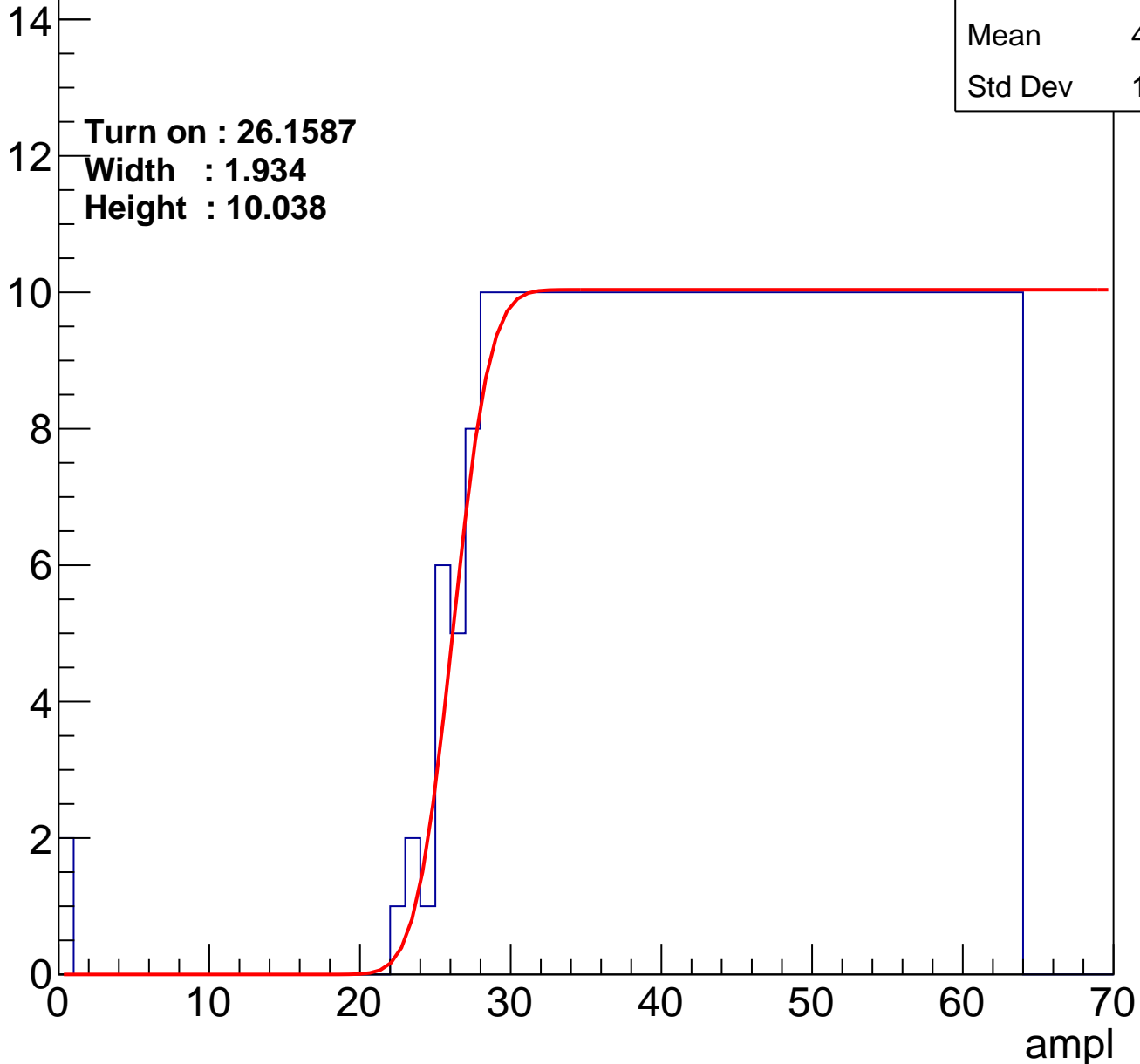
Entries	385
Mean	44.07
Std Dev	11.55

Turn on : 26.1587

Width : 1.934

Height : 10.038

Entry



B1L003S, U21-ch36

calib_packv5_042523_0143.root, FC#13, port D2

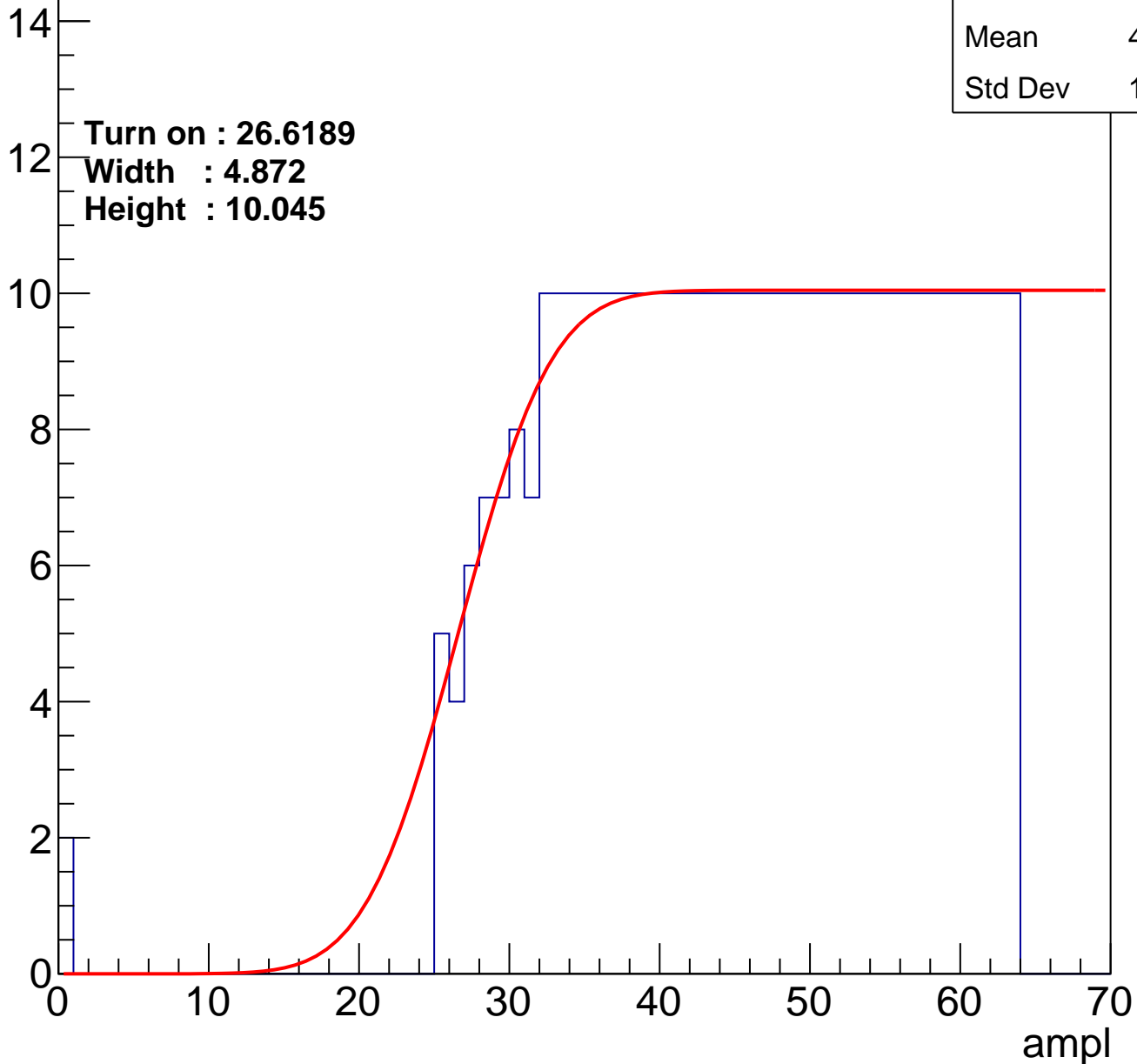
Entries	366
Mean	44.94
Std Dev	11.17

Turn on : 26.6189

Width : 4.872

Height : 10.045

Entry



B1L003S, U21-ch37

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.61
Std Dev	11.33

Turn on : 26.4844

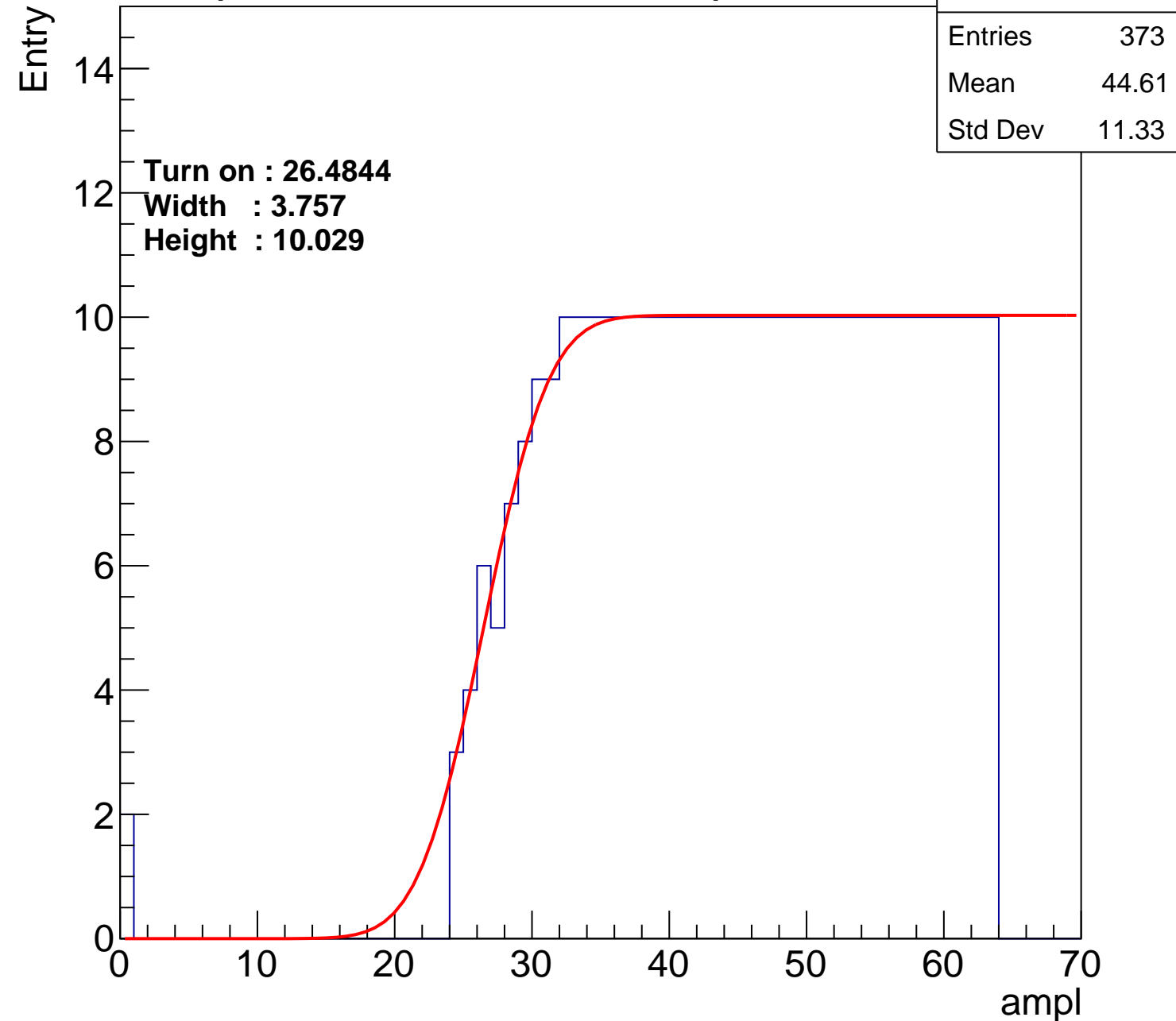
Width : 3.757

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch38

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 27.1039

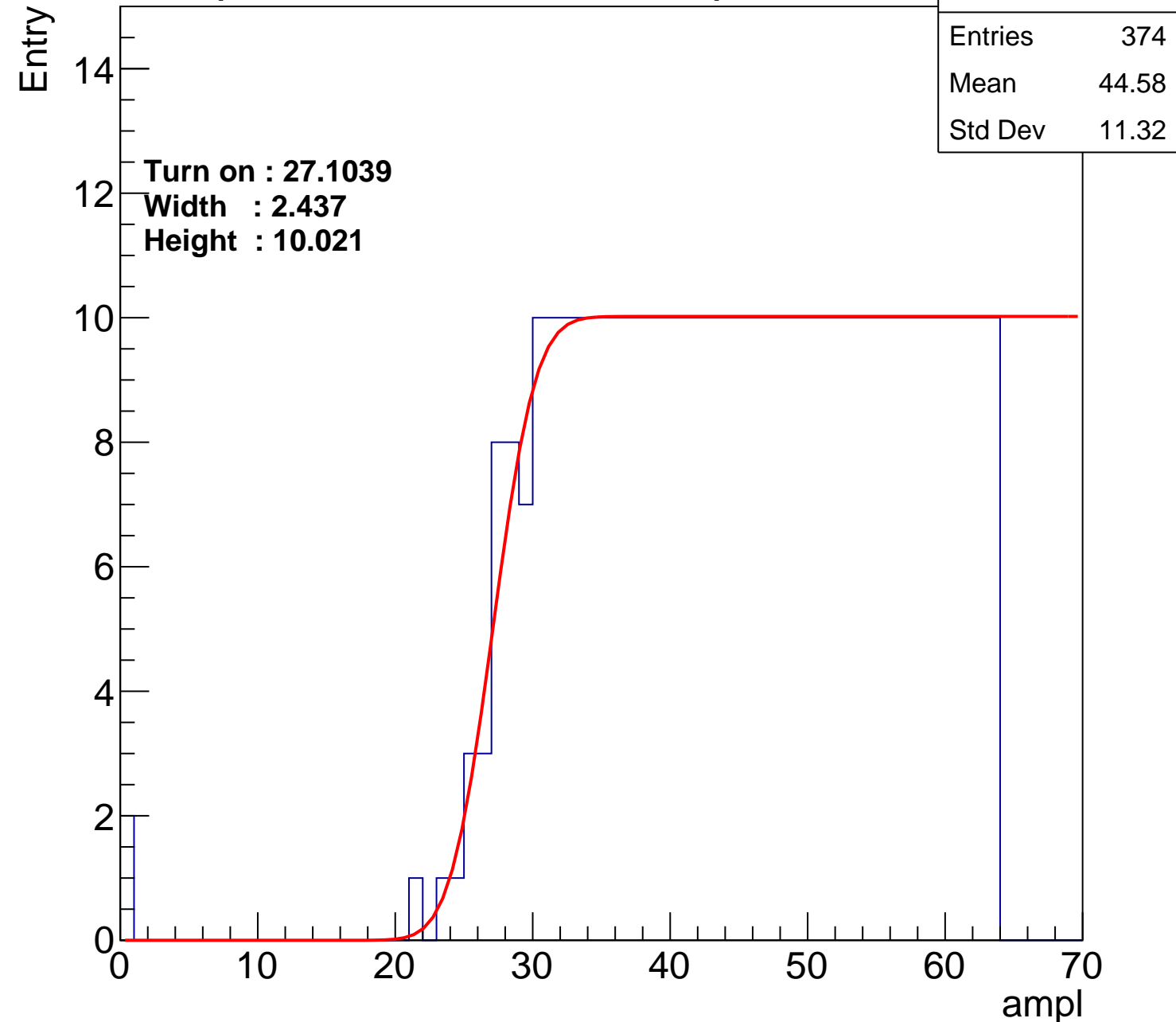
Width : 2.437

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch39

calib_packv5_042523_0143.root, FC#13, port D2

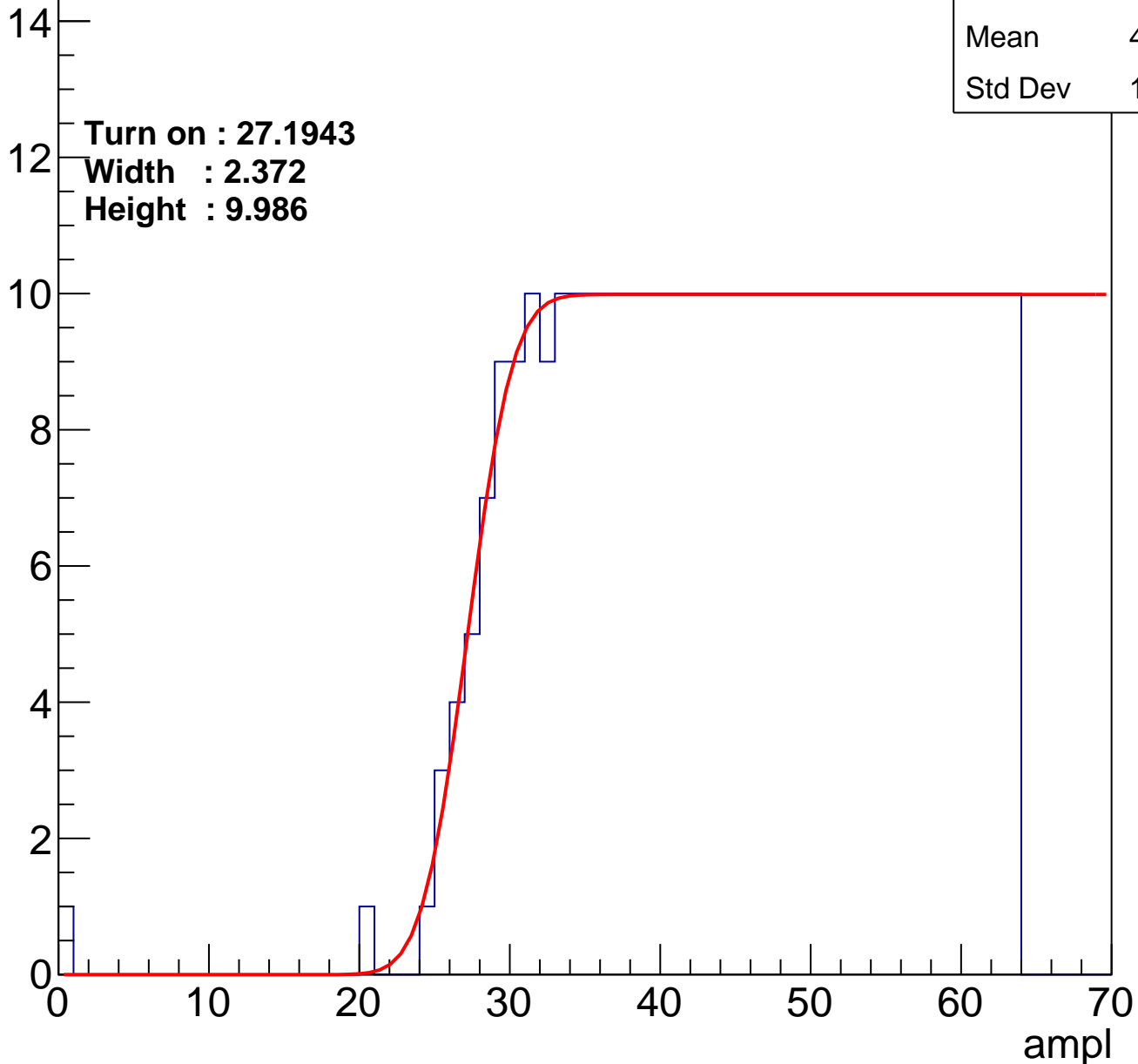
Entries	369
Mean	44.89
Std Dev	11.02

Turn on : 27.1943

Width : 2.372

Height : 9.986

Entry



B1L003S, U21-ch40

calib_packv5_042523_0143.root, FC#13, port D2

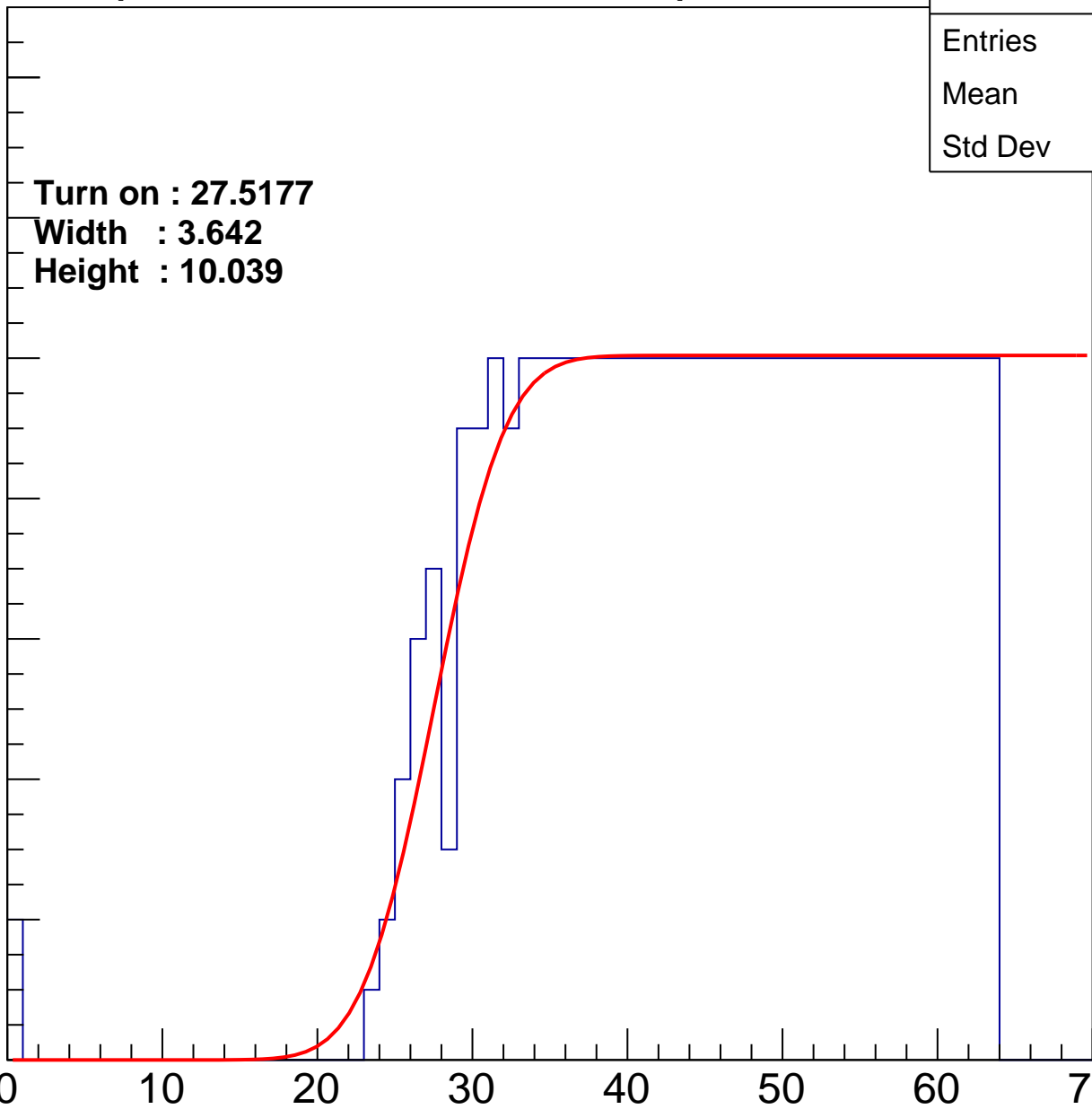
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5177
Width : 3.642
Height : 10.039

Entries	372
Mean	44.65
Std Dev	11.32

ampl



B1L003S, U21-ch41

calib_packv5_042523_0143.root, FC#13, port D2

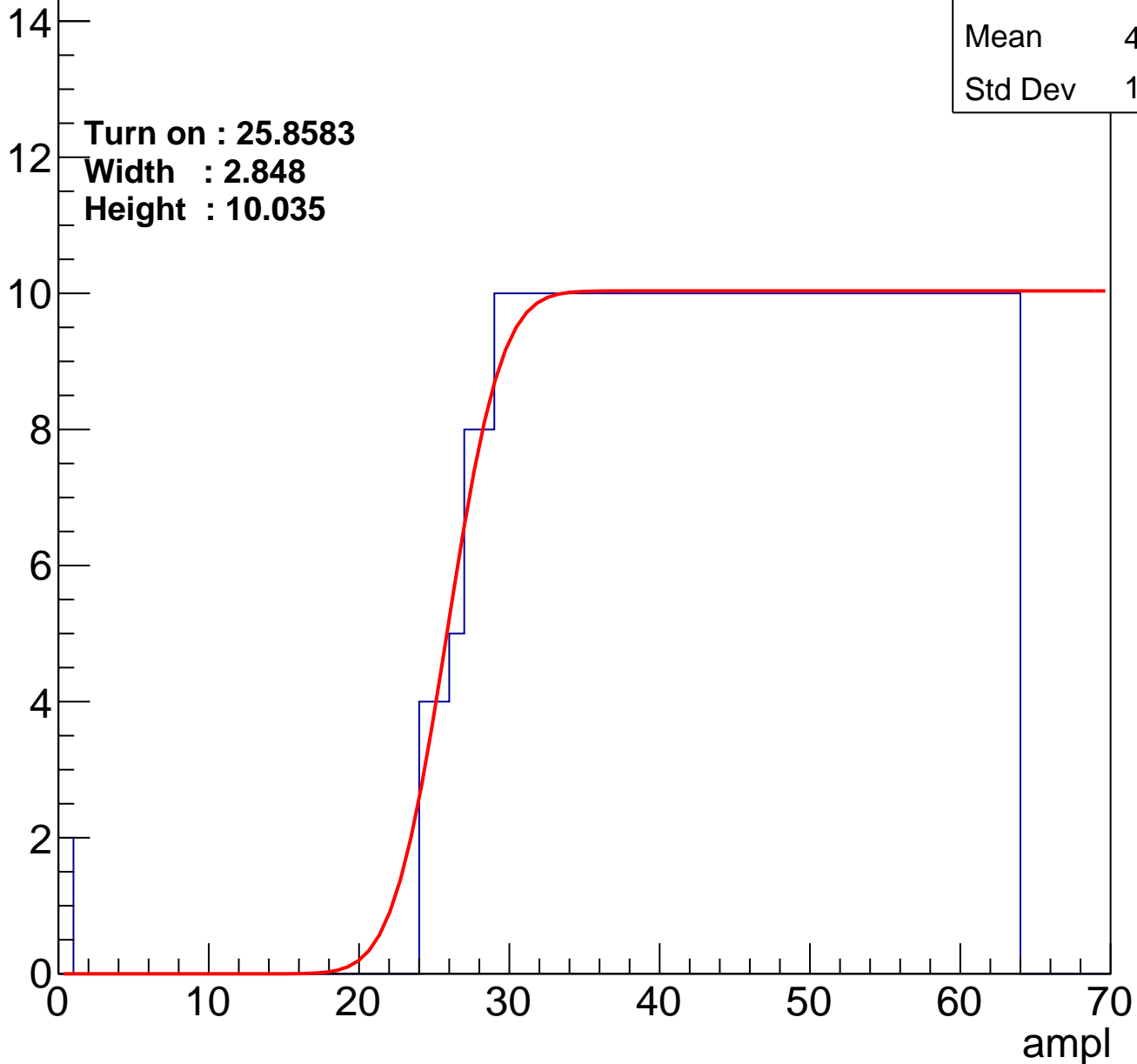
Entries	381
Mean	44.27
Std Dev	11.45

Turn on : 25.8583

Width : 2.848

Height : 10.035

Entry



B1L003S, U21-ch42

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.45
Std Dev	11.6

Turn on : 26.5216

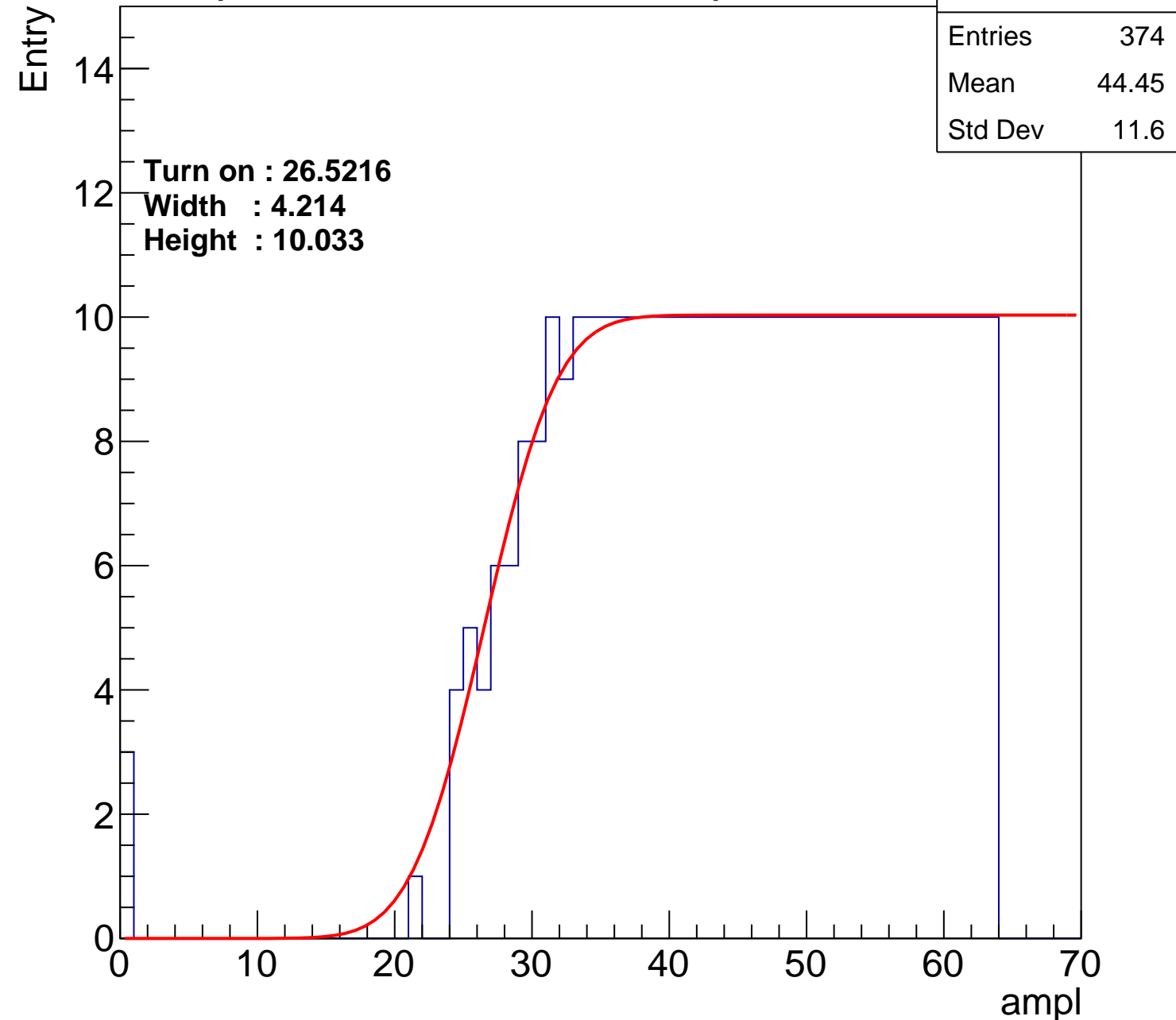
Width : 4.214

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch43

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.89
Std Dev	11.62

Turn on : 25.2776

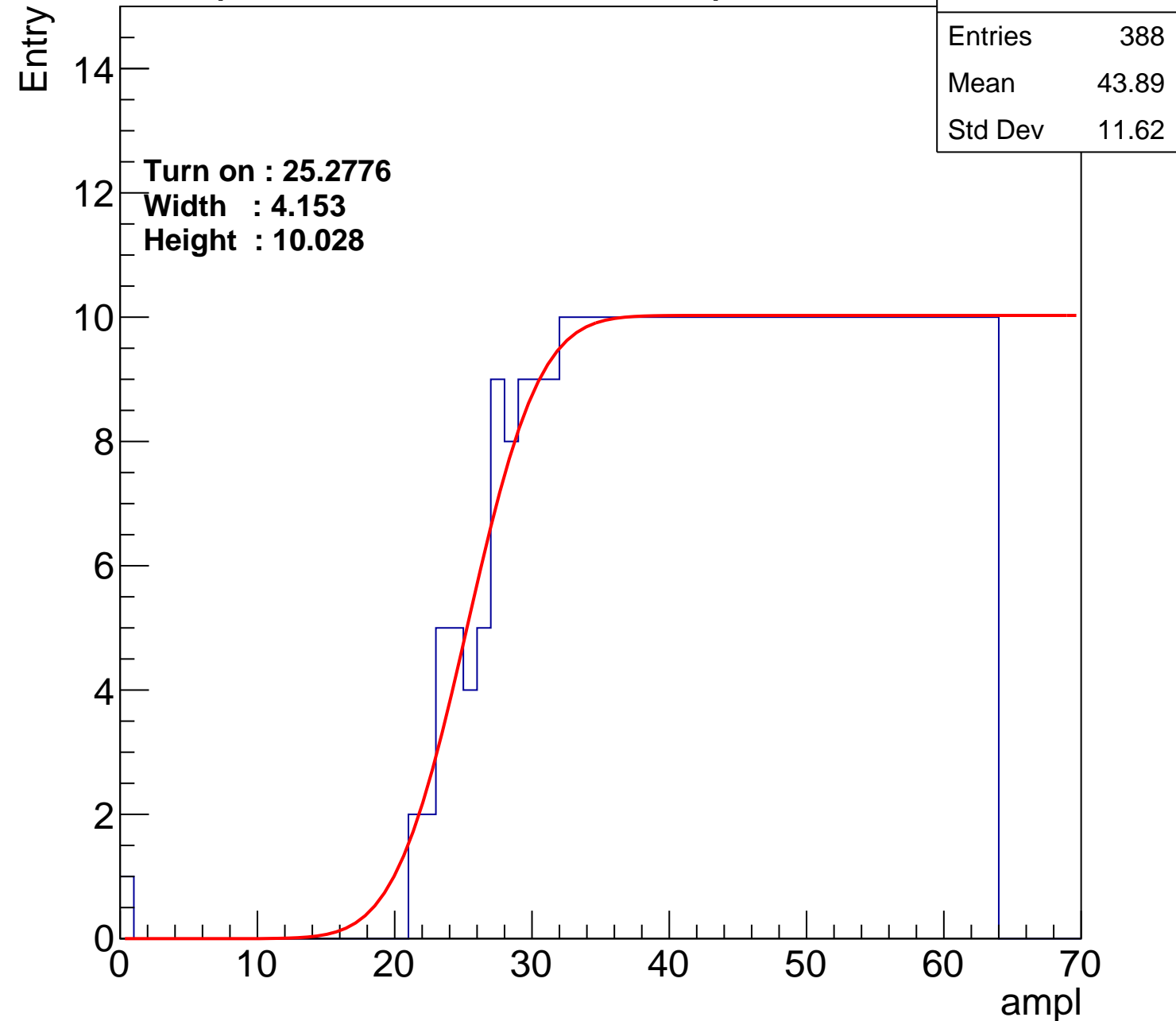
Width : 4.153

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch44

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.07
Std Dev	10.93

Turn on : 27.8194

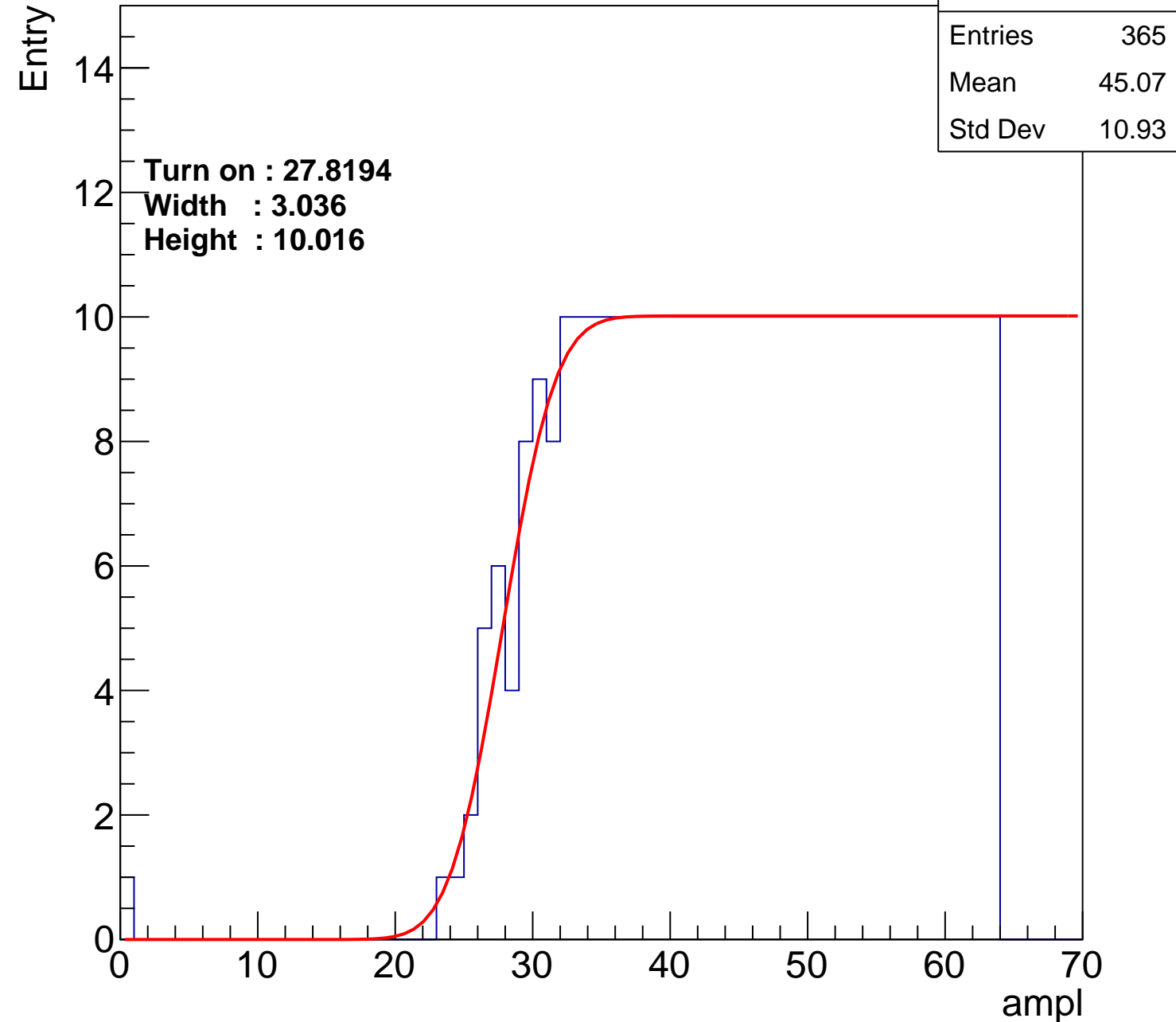
Width : 3.036

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch45

calib_packv5_042523_0143.root, FC#13, port D2

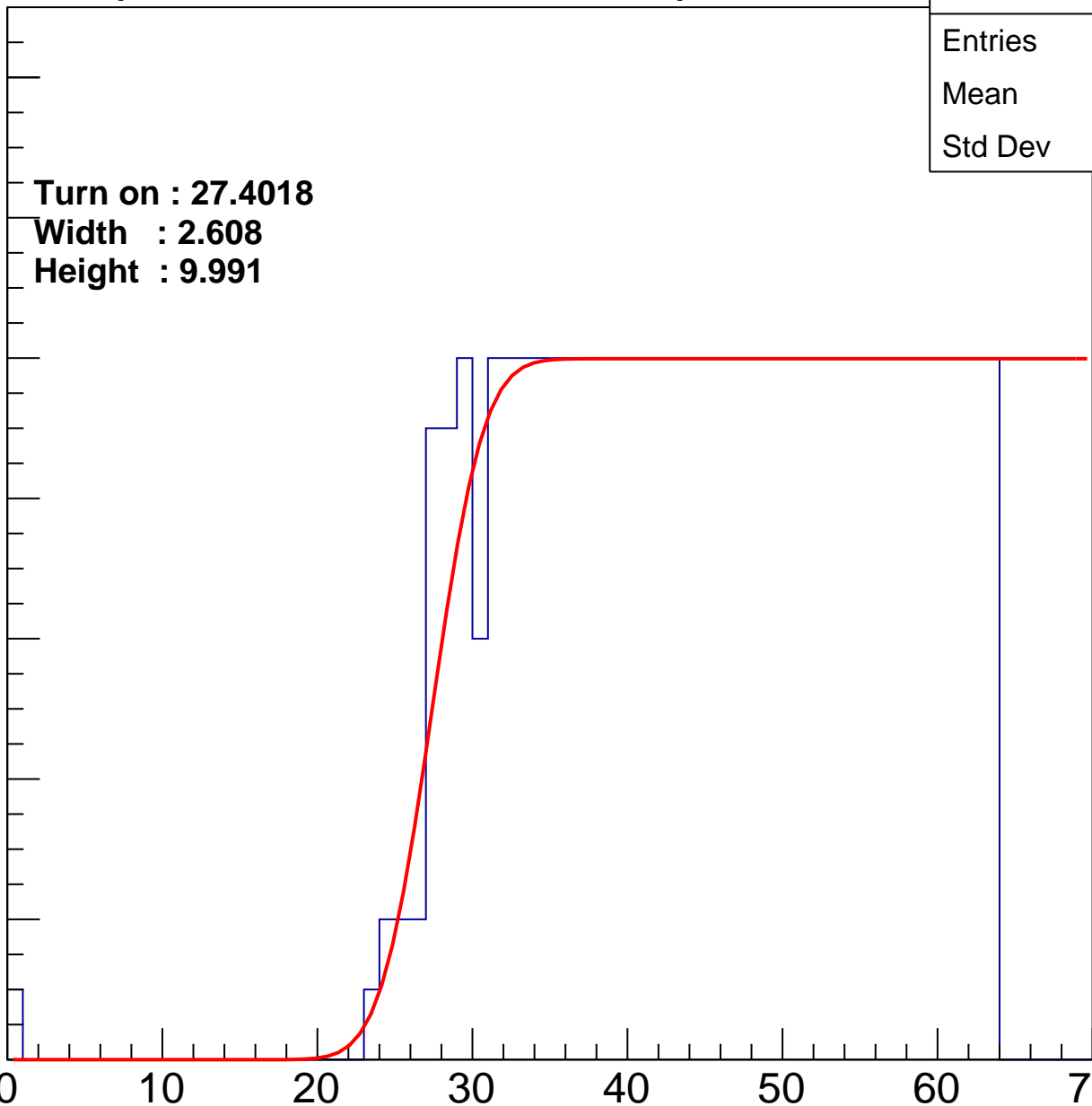
Entry

14
12
10
8
6
4
2
0

Turn on : 27.4018
Width : 2.608
Height : 9.991

Entries	372
Mean	44.75
Std Dev	11.07

ampl



B1L003S, U21-ch46

calib_packv5_042523_0143.root, FC#13, port D2

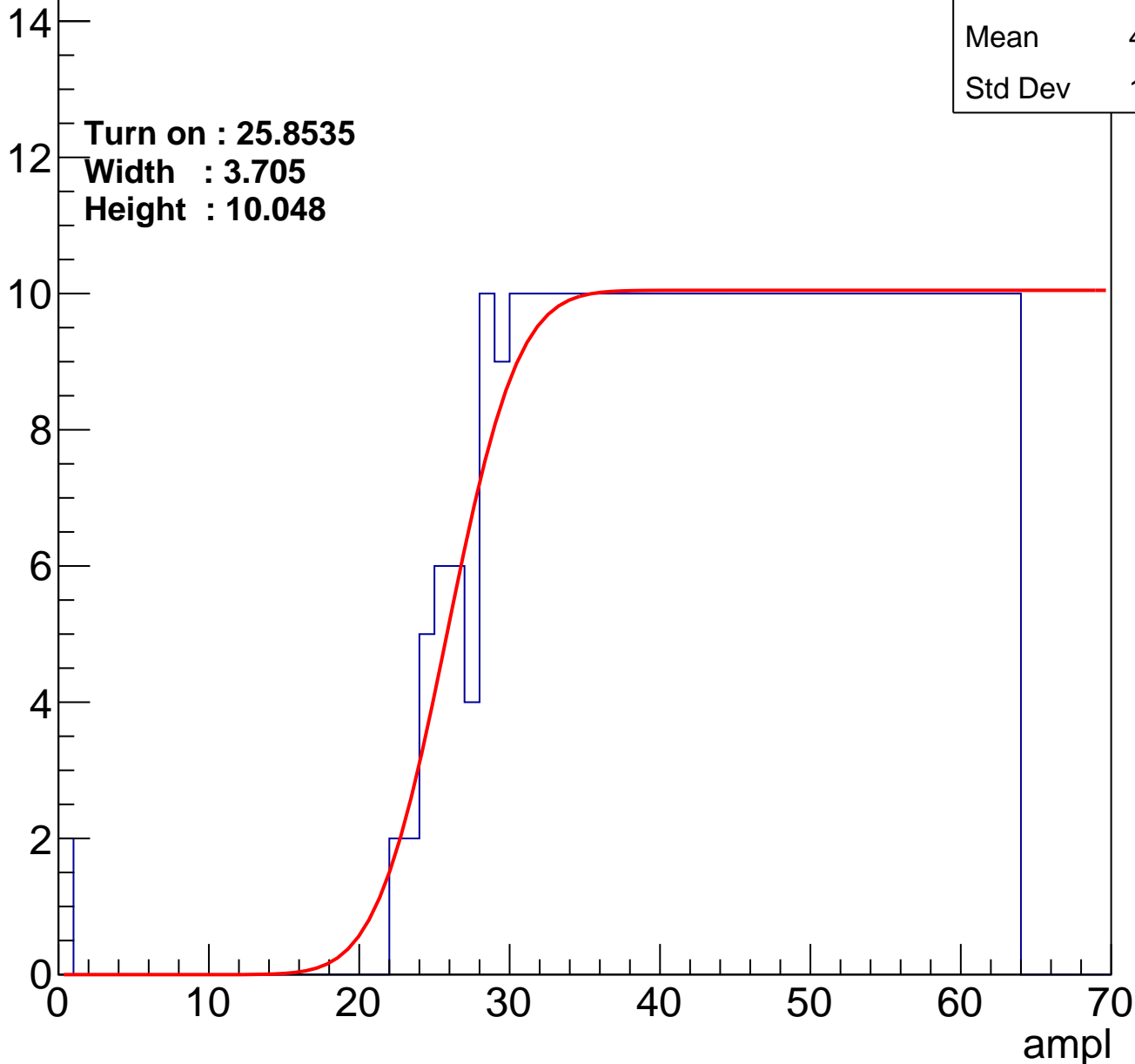
Entries	386
Mean	43.98
Std Dev	11.65

Turn on : 25.8535

Width : 3.705

Height : 10.048

Entry



B1L003S, U21-ch47

calib_packv5_042523_0143.root, FC#13, port D2

Entries	380
Mean	44.06
Std Dev	12.06

Turn on : 26.1887

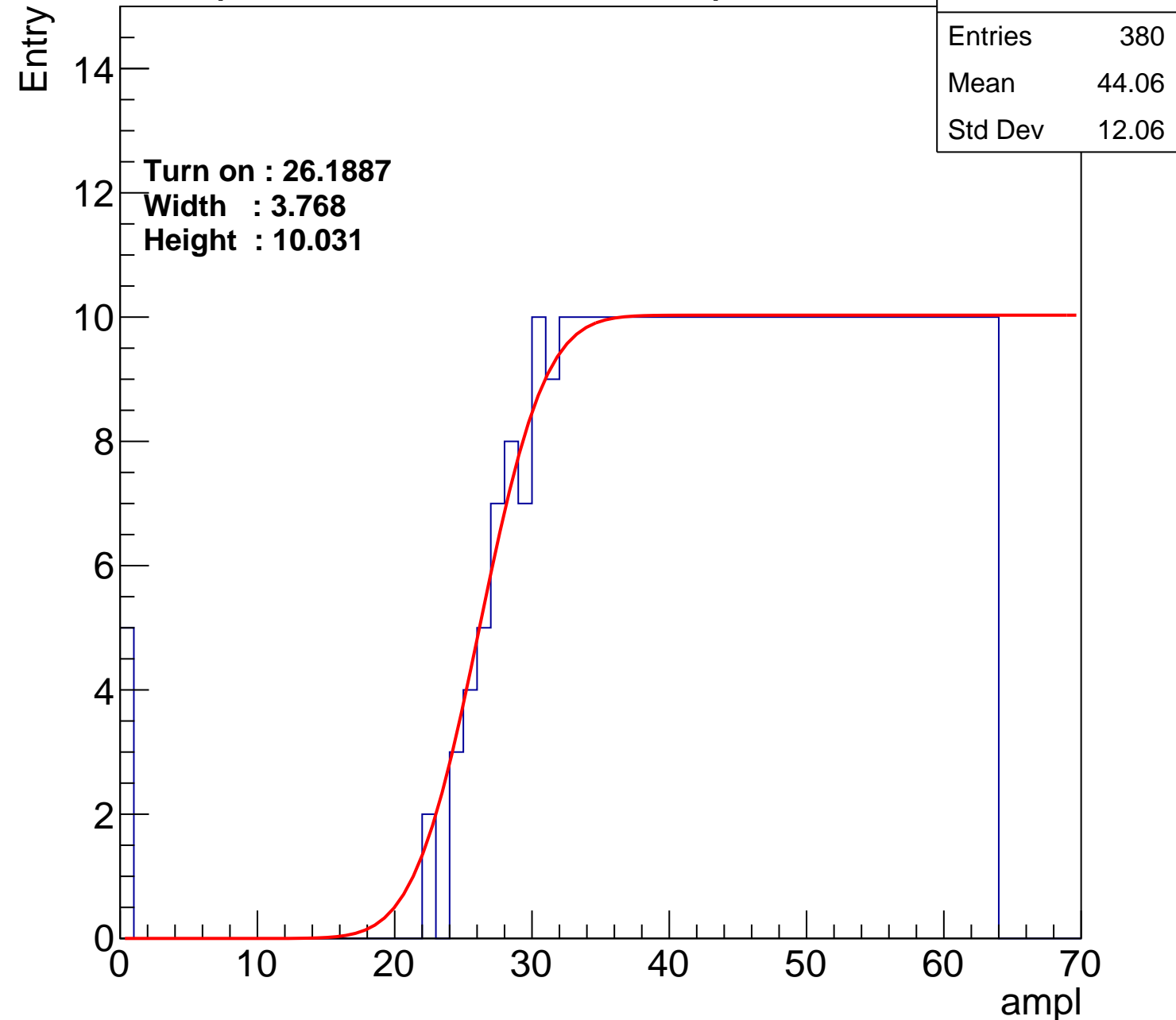
Width : 3.768

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch48

calib_packv5_042523_0143.root, FC#13, port D2

Entries	343
Mean	46.16
Std Dev	10.35

Turn on : 30.2118

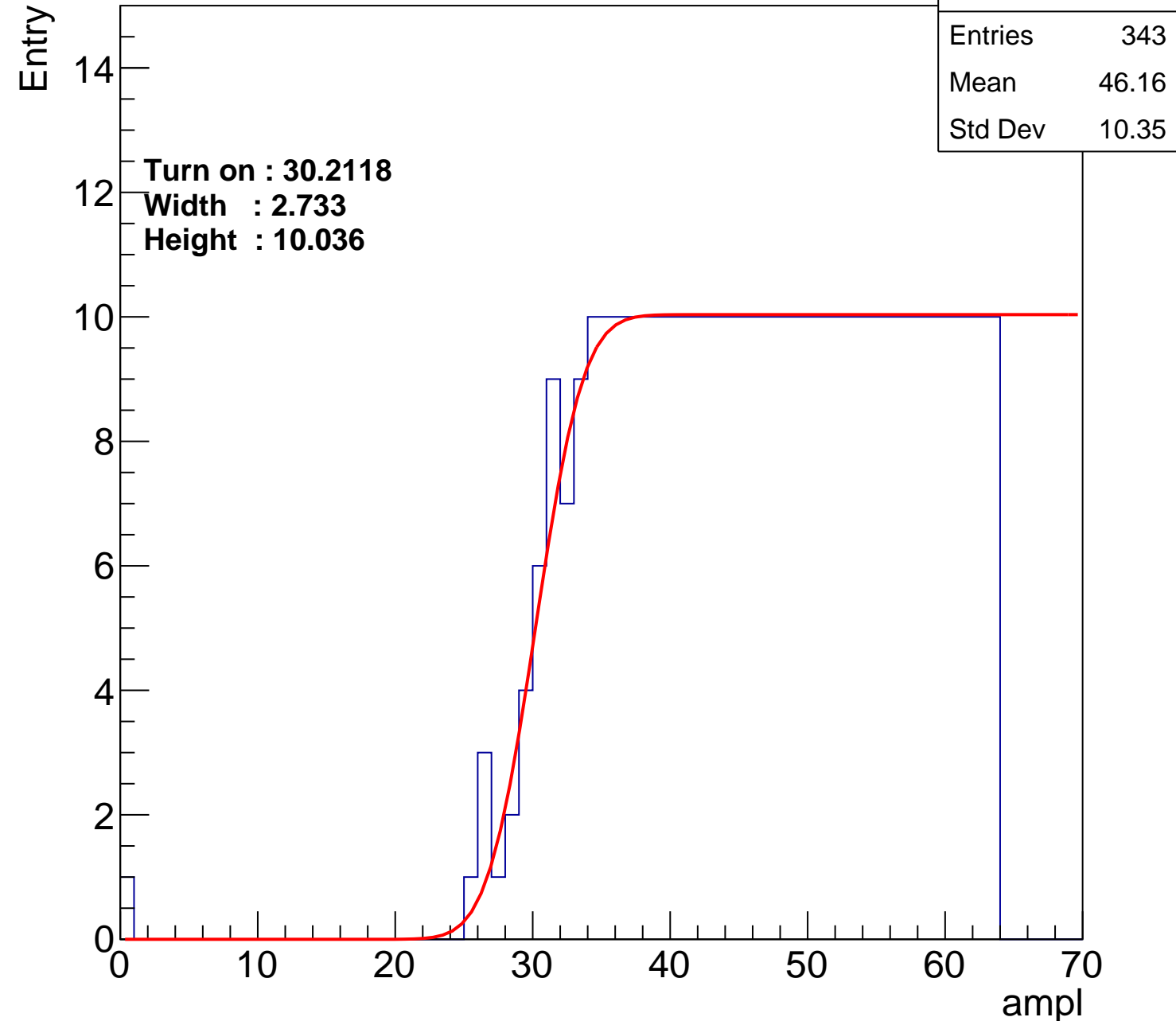
Width : 2.733

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch49

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.33
Std Dev	11.34

Turn on : 29.0984

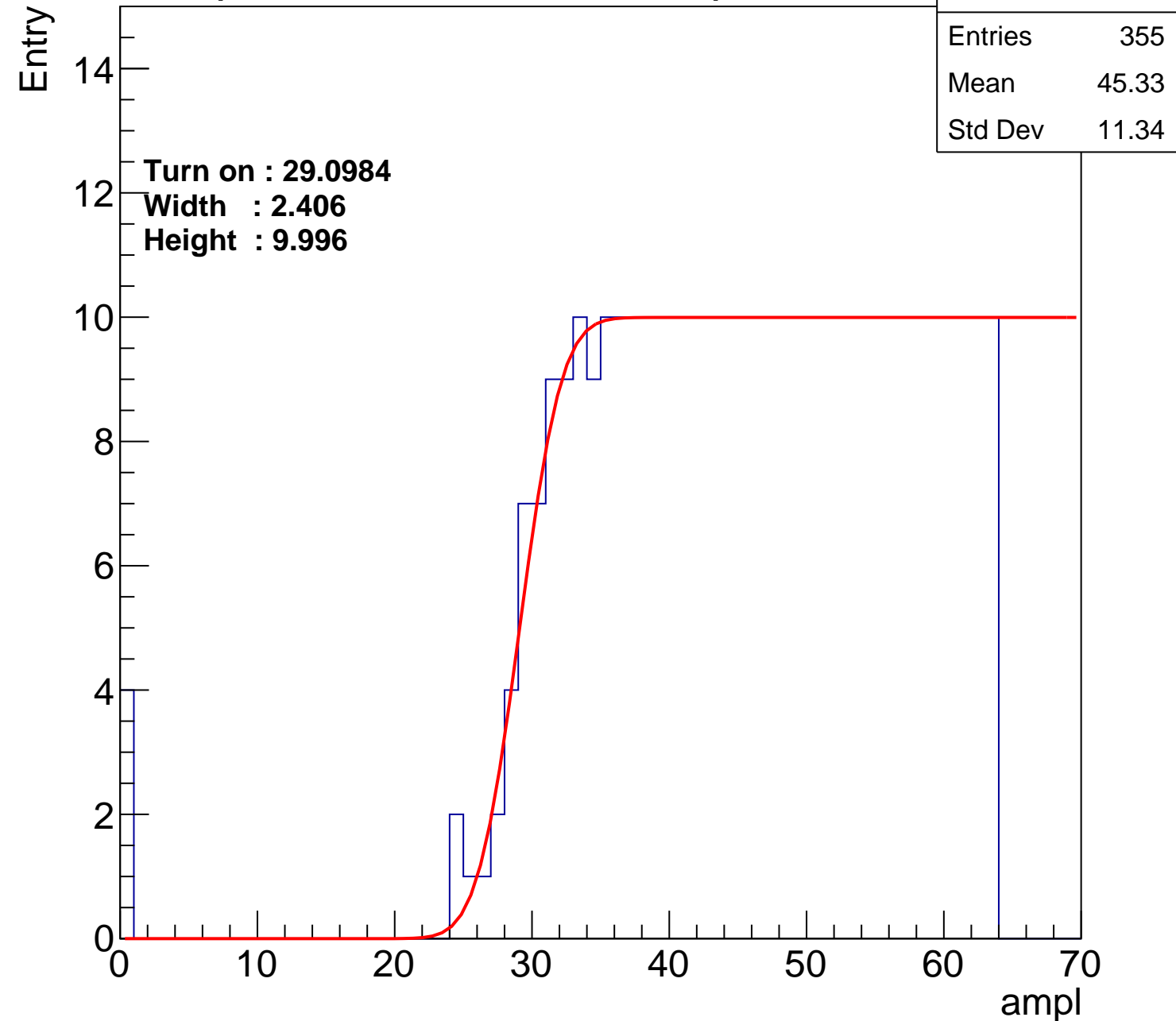
Width : 2.406

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch50

calib_packv5_042523_0143.root, FC#13, port D2

Entries	378
Mean	44.44
Std Dev	11.25

Turn on : 26.1561

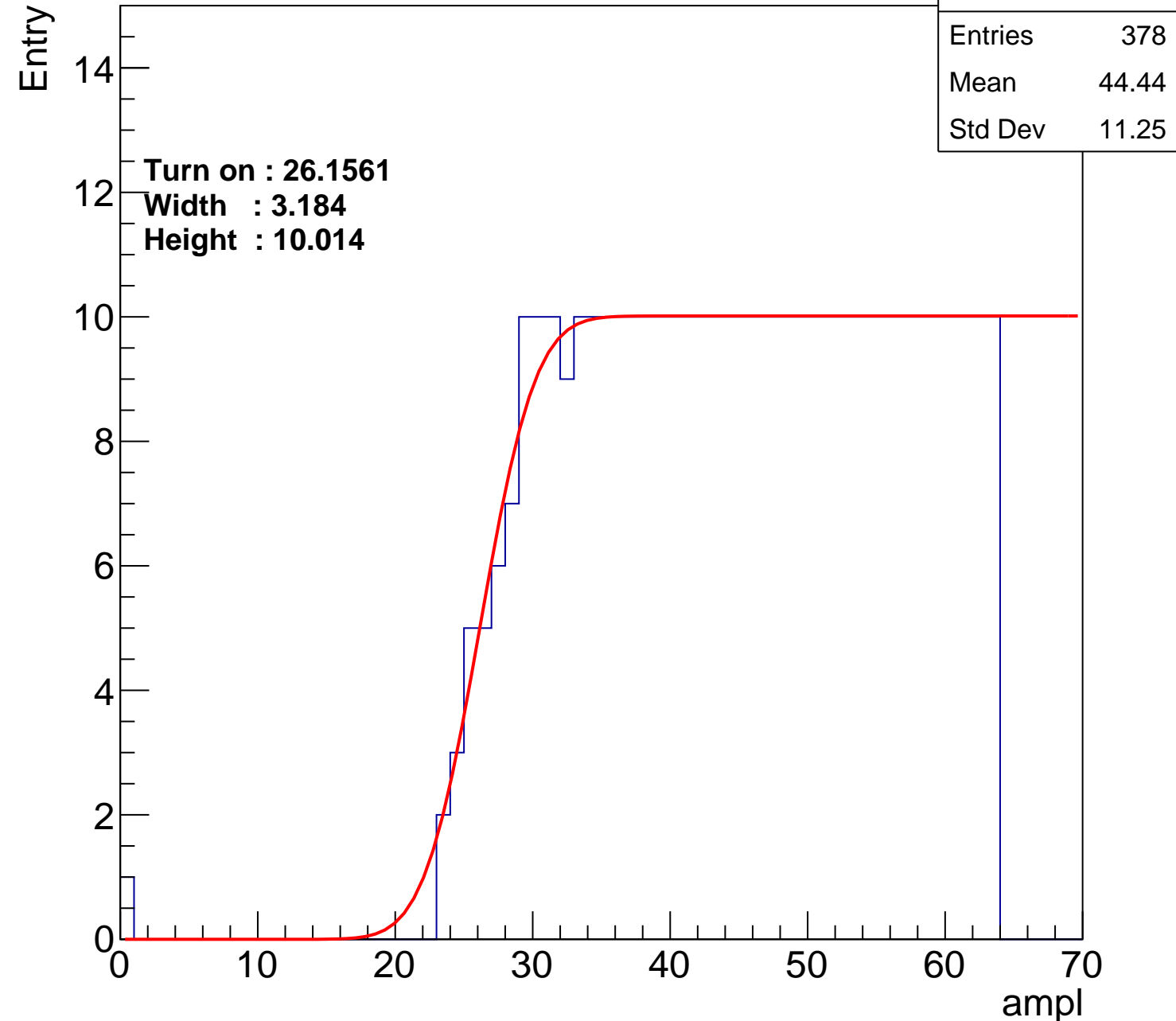
Width : 3.184

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch51

calib_packv5_042523_0143.root, FC#13, port D2

Entries	355
Mean	45.46
Std Dev	10.93

Turn on : 29.3274

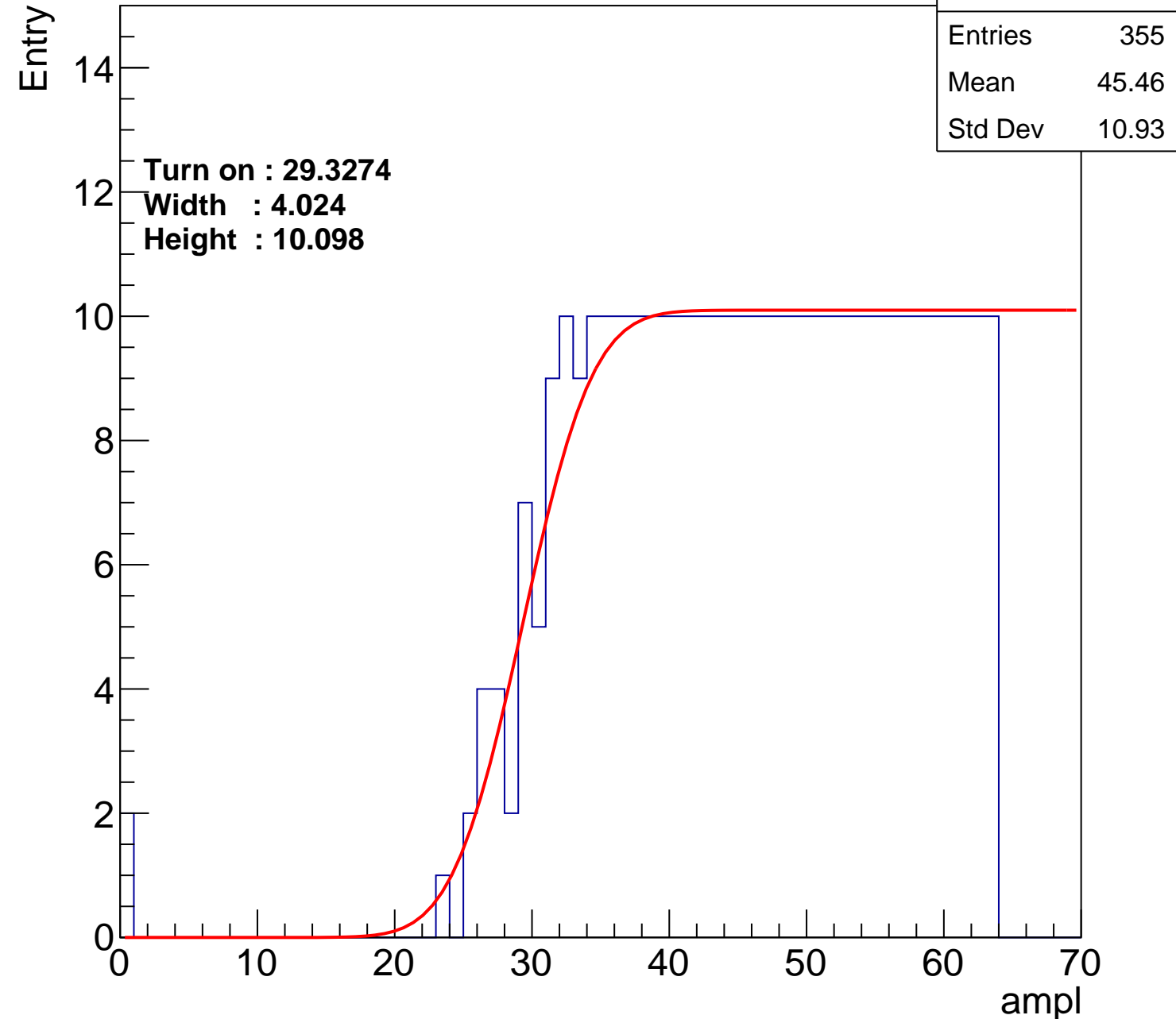
Width : 4.024

Height : 10.098

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch52

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	44.18
Std Dev	11.55

Turn on : 26.0344

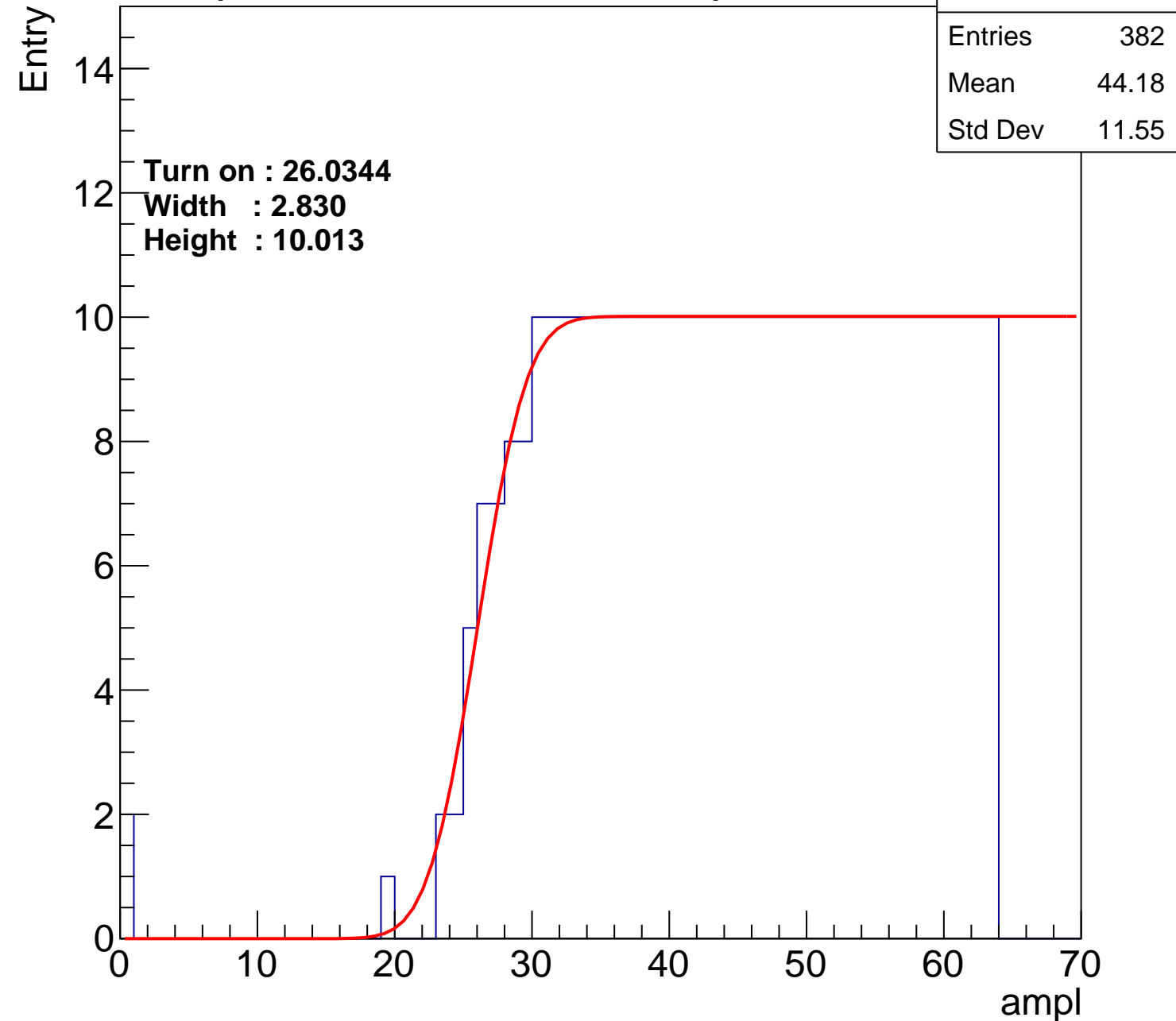
Width : 2.830

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch53

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.39
Std Dev	11.28

Turn on : 26.4436

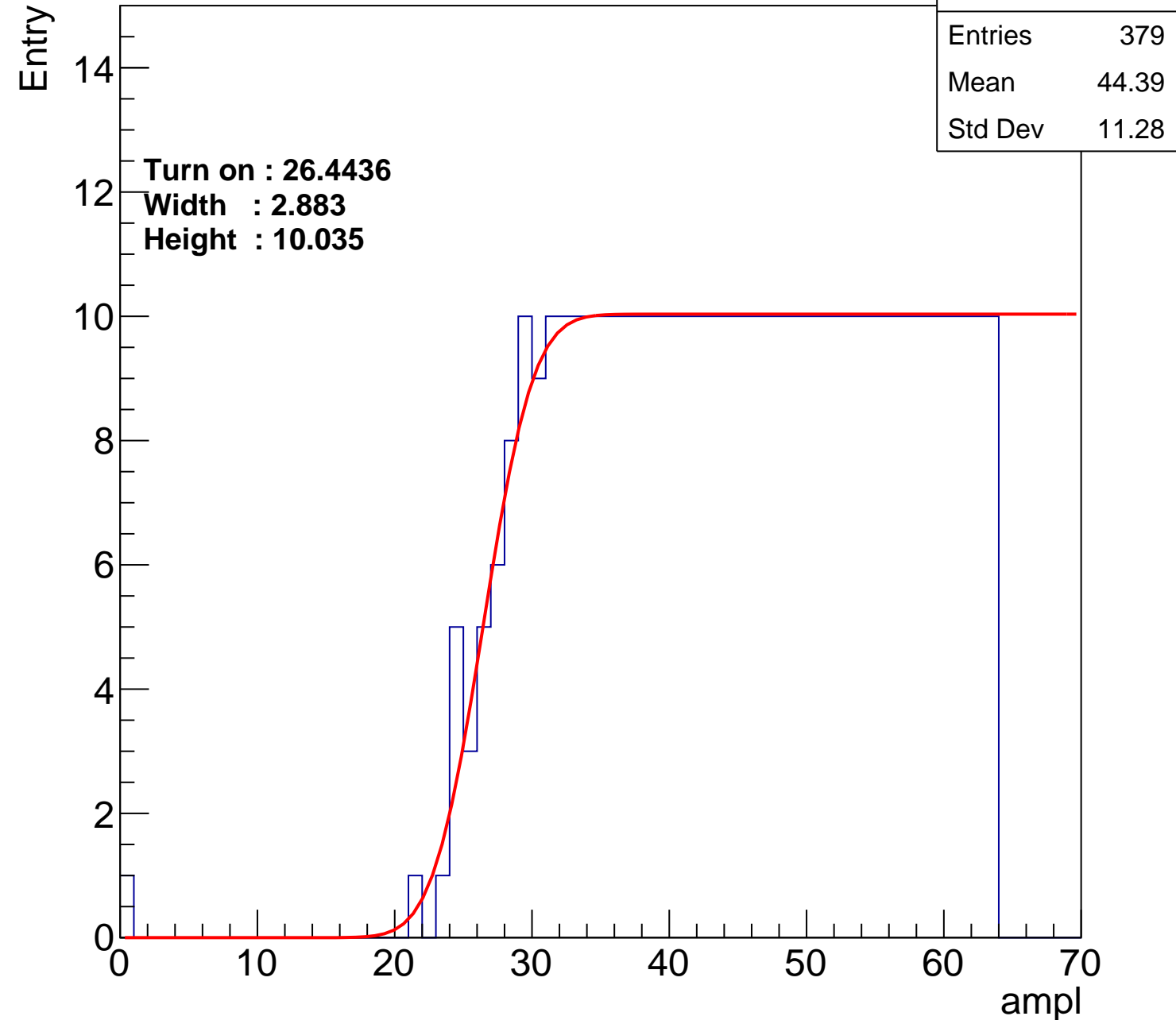
Width : 2.883

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch54

calib_packv5_042523_0143.root, FC#13, port D2

Entries	395
Mean	43.48
Std Dev	12.03

Turn on : 24.8191

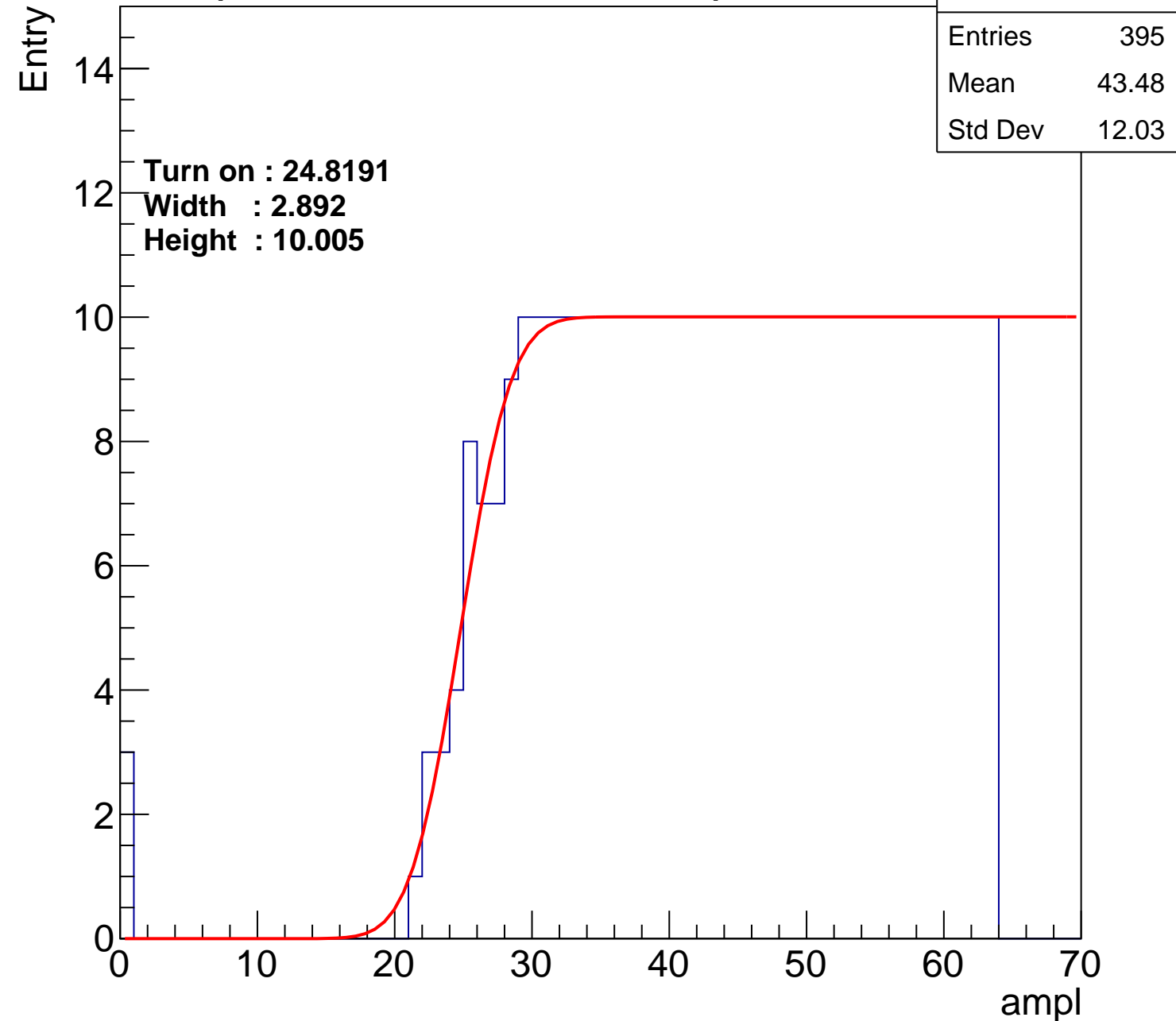
Width : 2.892

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch55

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.53
Std Dev	11.21

Turn on : 26.8099

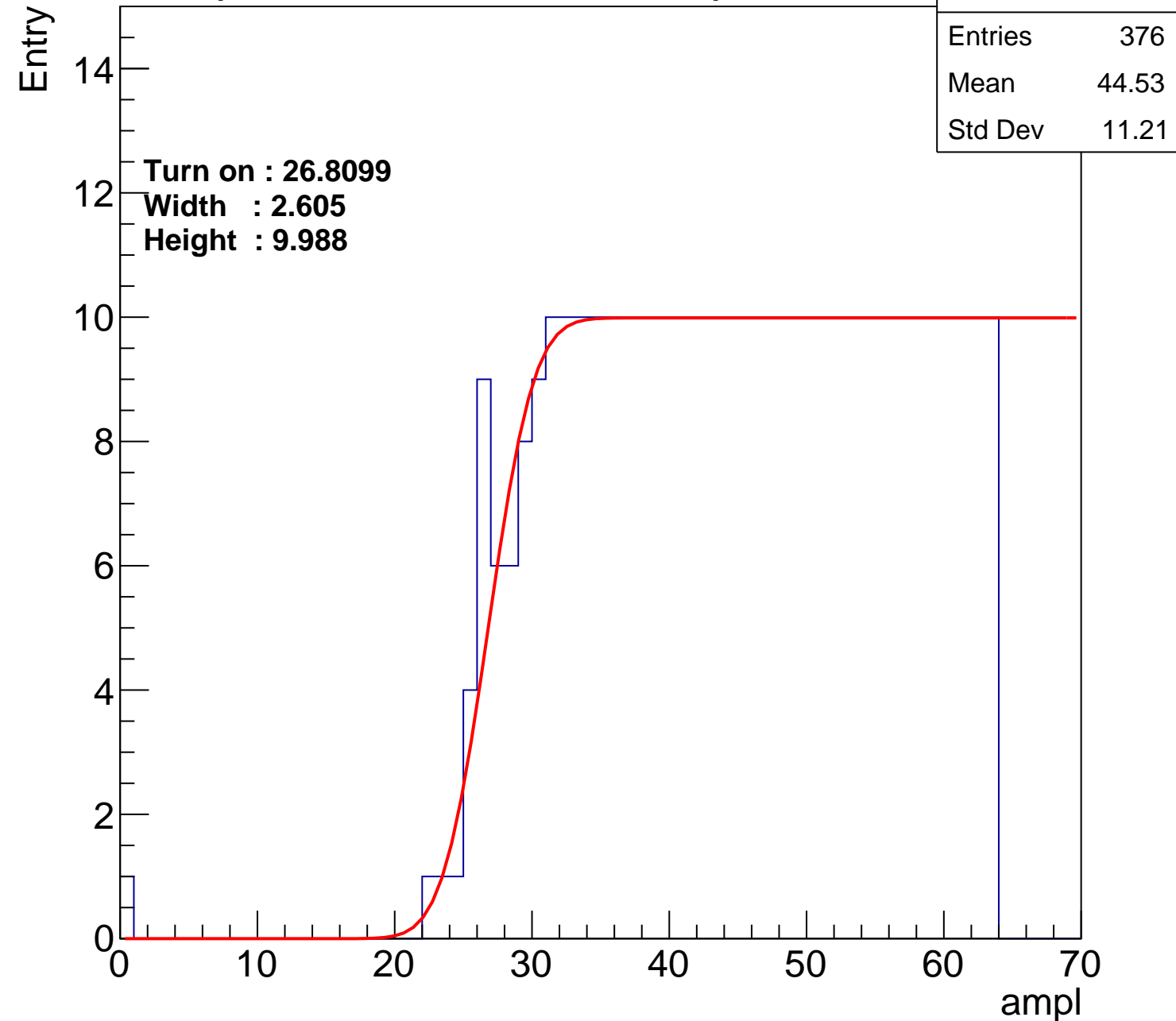
Width : 2.605

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch56

calib_packv5_042523_0143.root, FC#13, port D2

Entries	391
Mean	43.73
Std Dev	11.77

Turn on : 25.3531

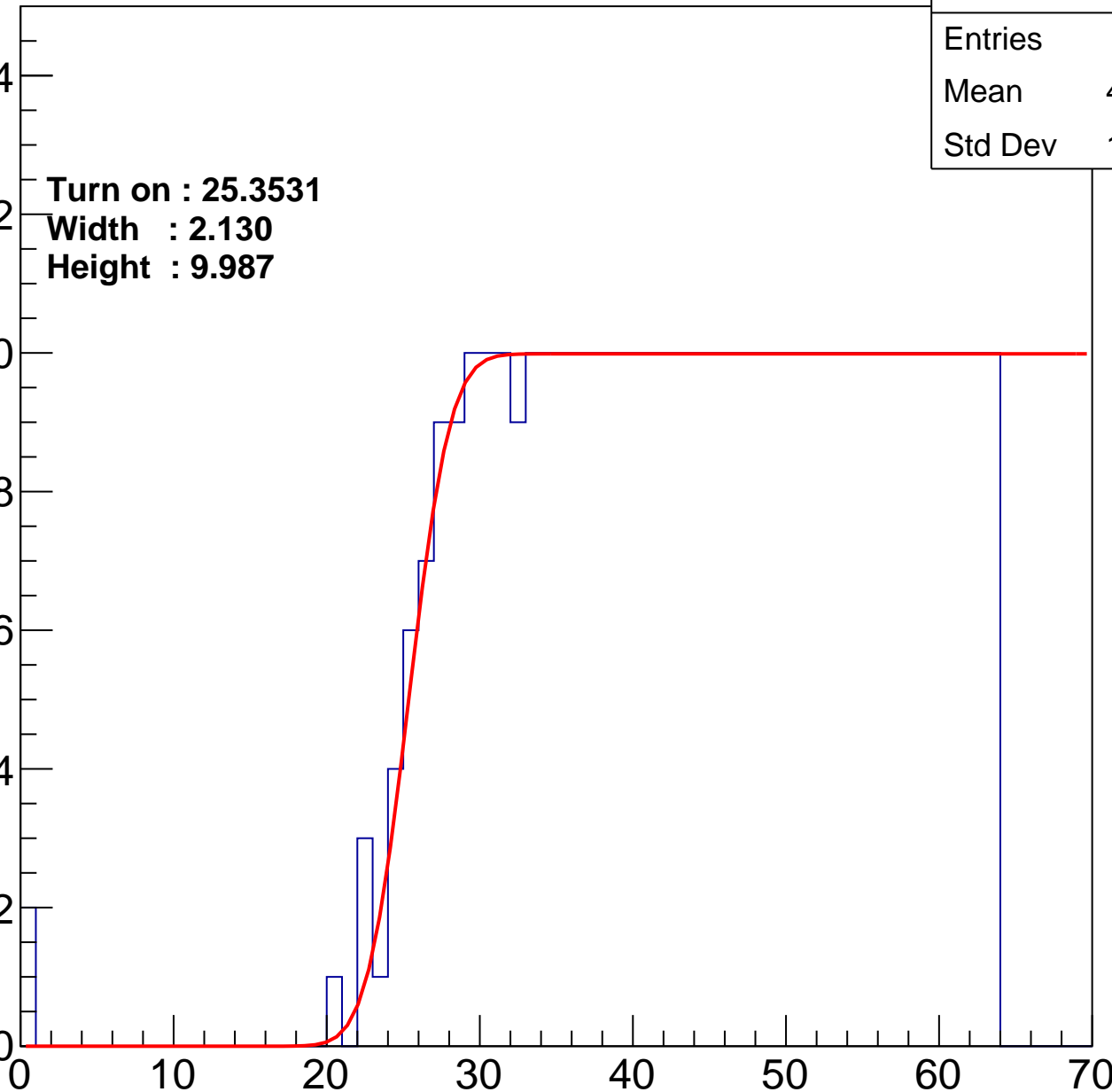
Width : 2.130

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch57

calib_packv5_042523_0143.root, FC#13, port D2

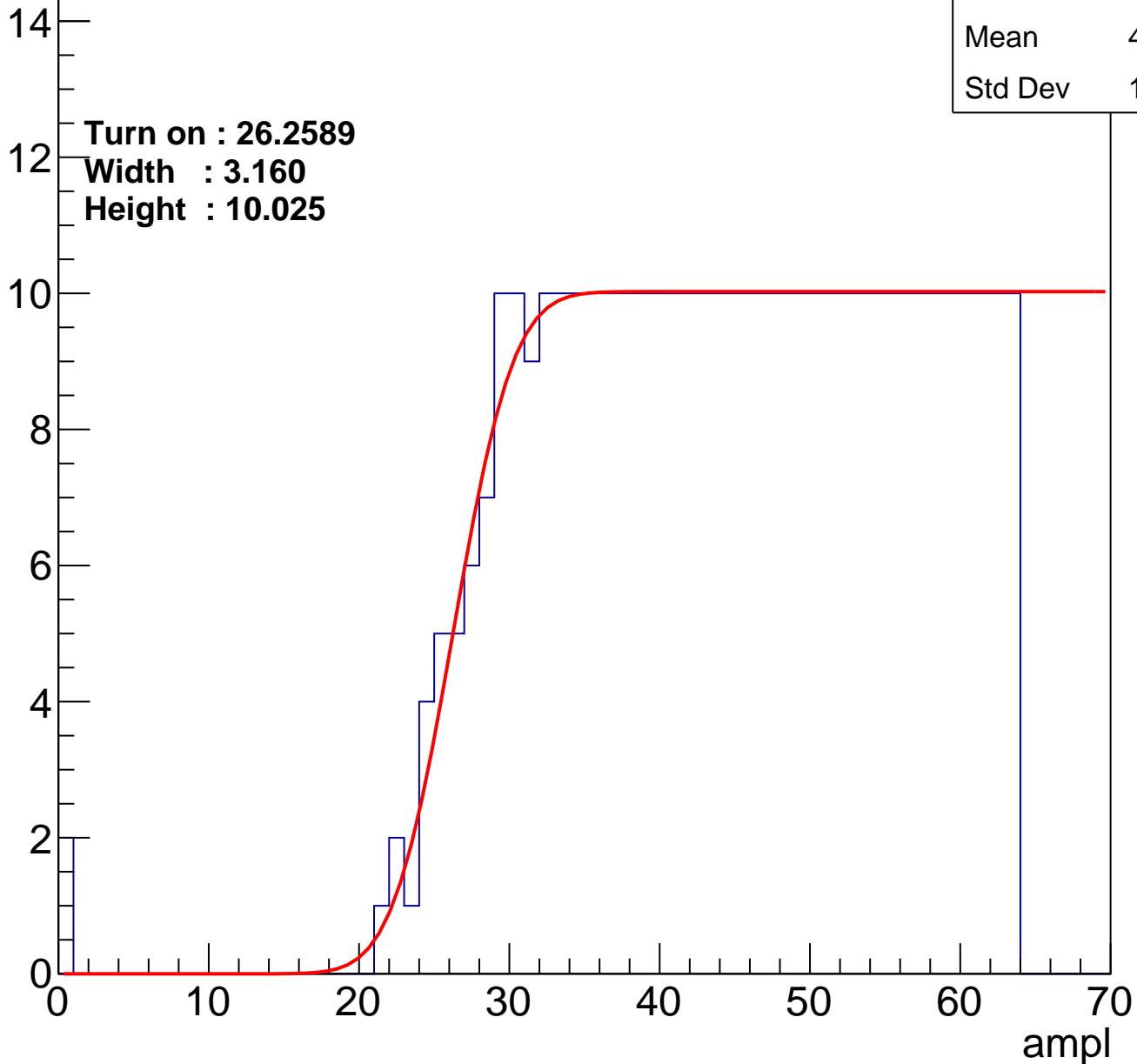
Entries	382
Mean	44.15
Std Dev	11.59

Turn on : 26.2589

Width : 3.160

Height : 10.025

Entry



B1L003S, U21-ch58

calib_packv5_042523_0143.root, FC#13, port D2

Entries	382
Mean	43.9
Std Dev	12.25

Turn on : 26.4505

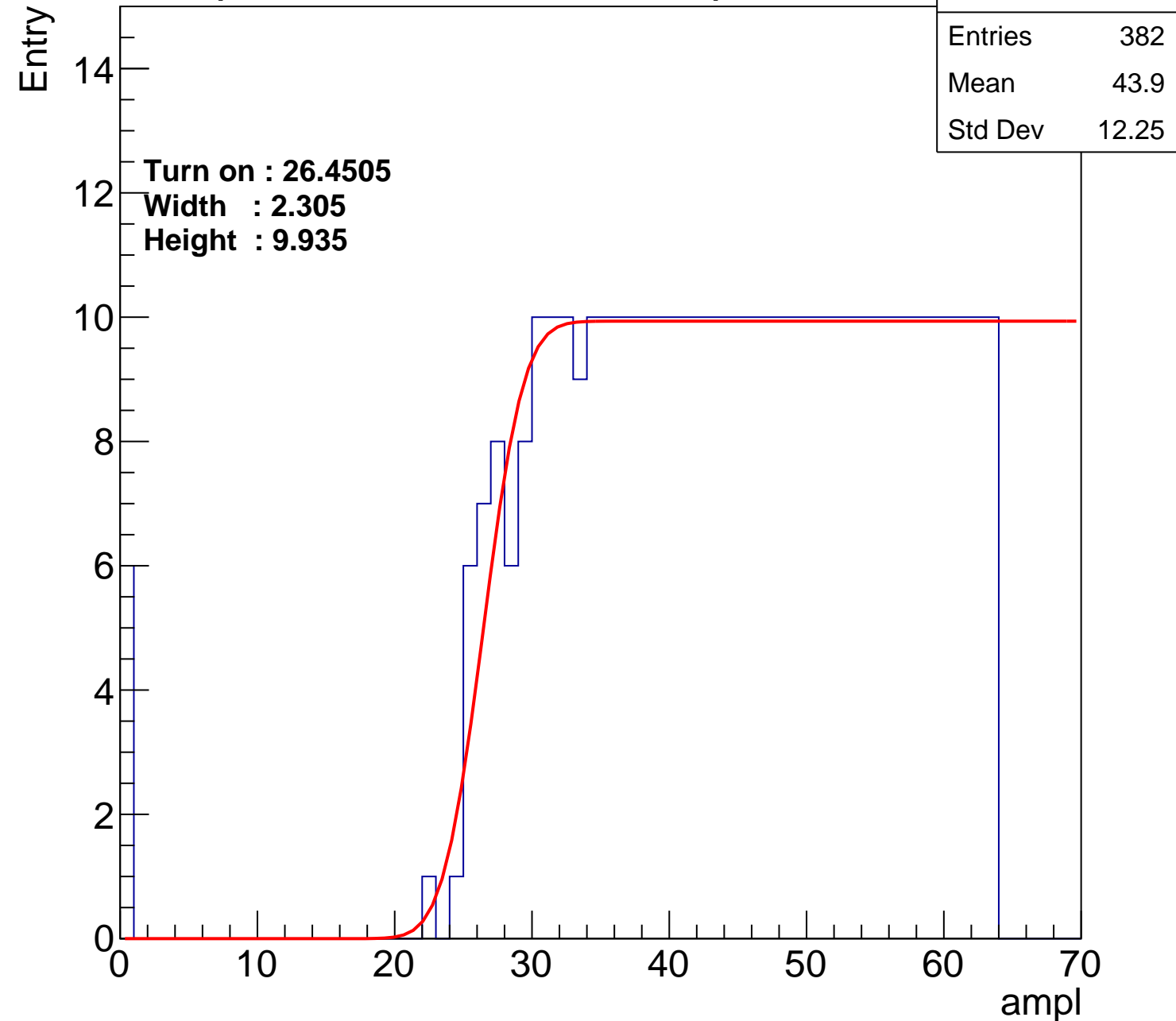
Width : 2.305

Height : 9.935

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch59

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.1
Std Dev	11.87

Turn on : 26.5359

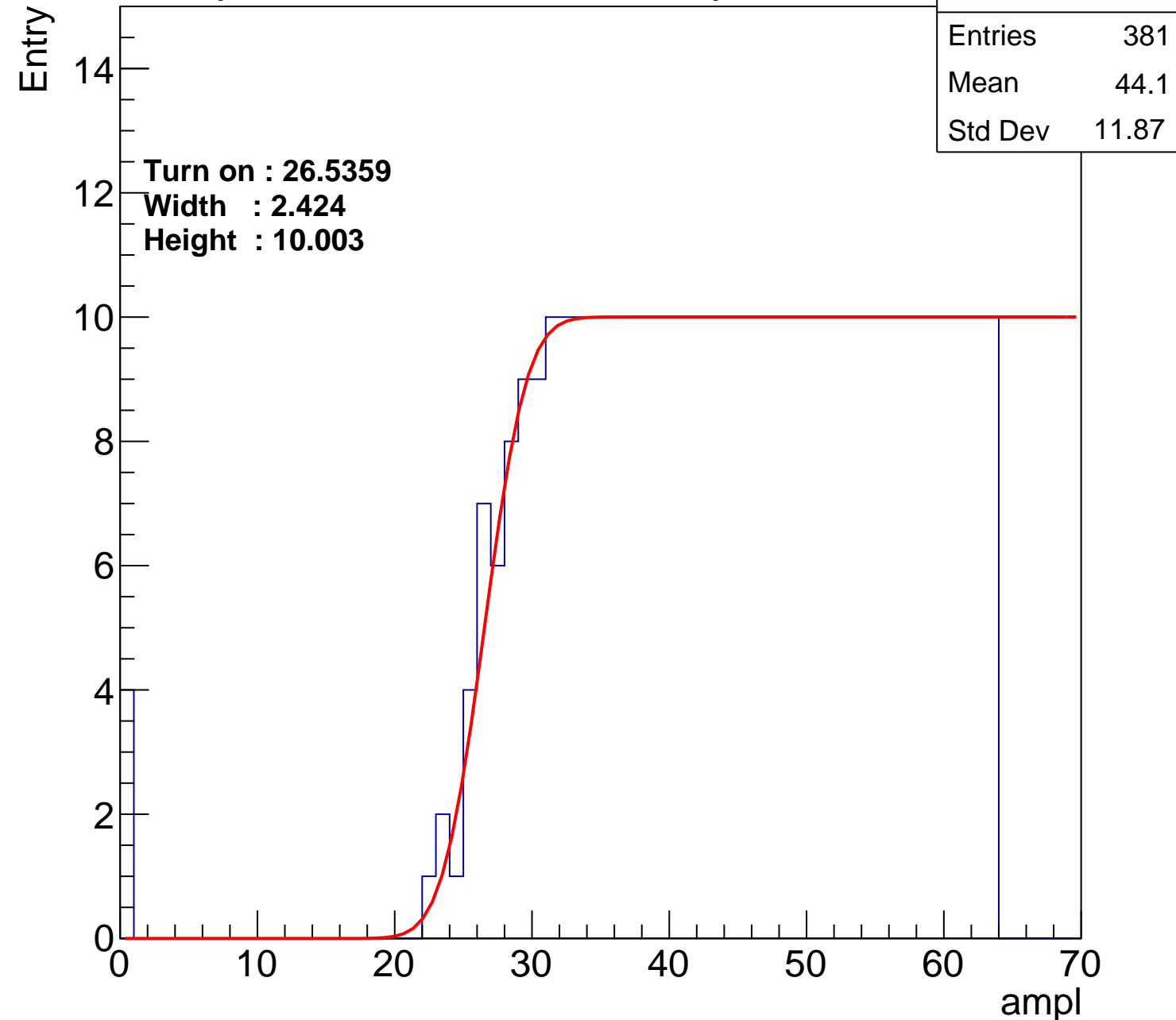
Width : 2.424

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch60

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	43.92
Std Dev	11.99

Turn on : 26.1509

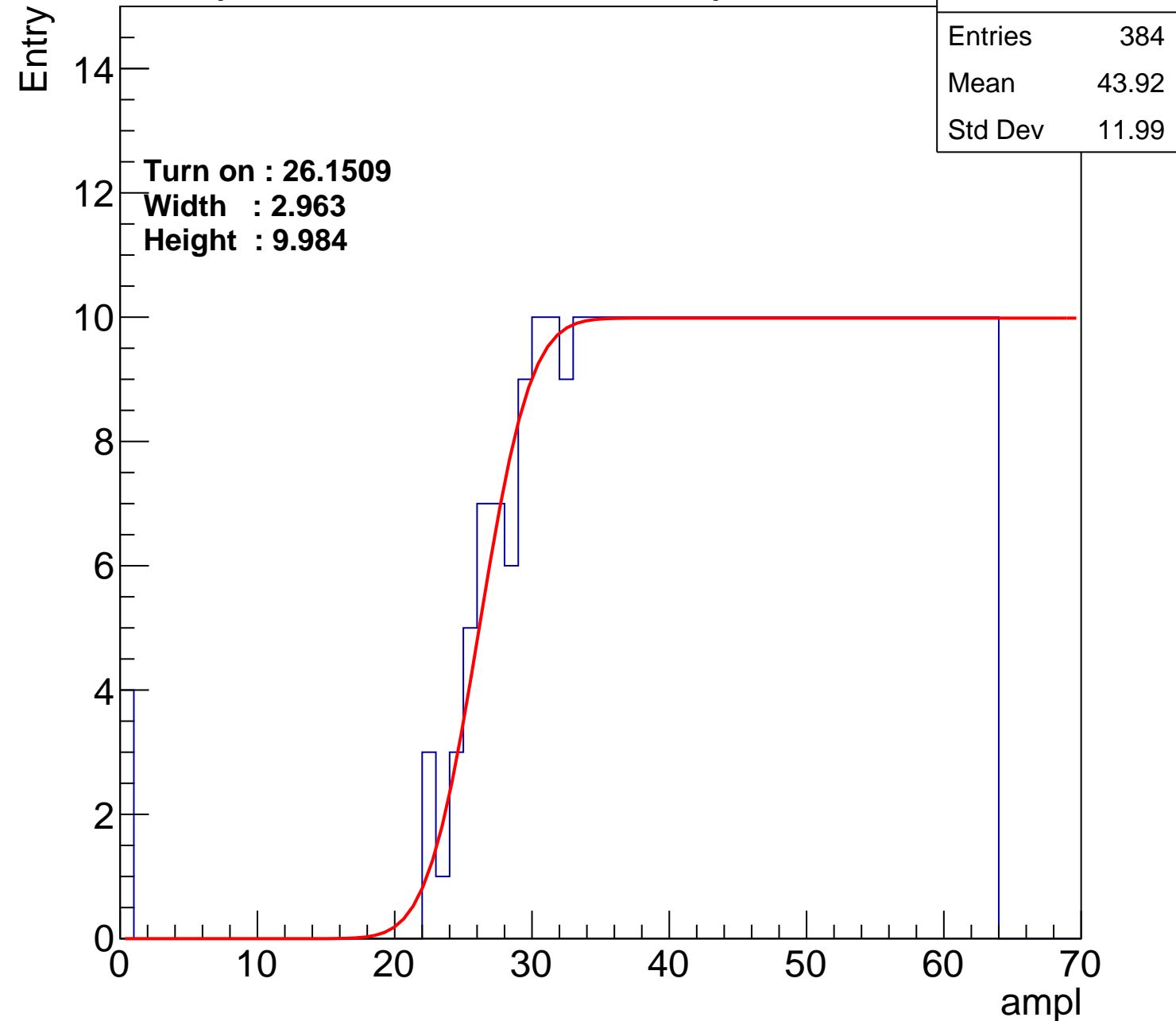
Width : 2.963

Height : 9.984

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch61

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.05
Std Dev	11.06

Turn on : 27.6072

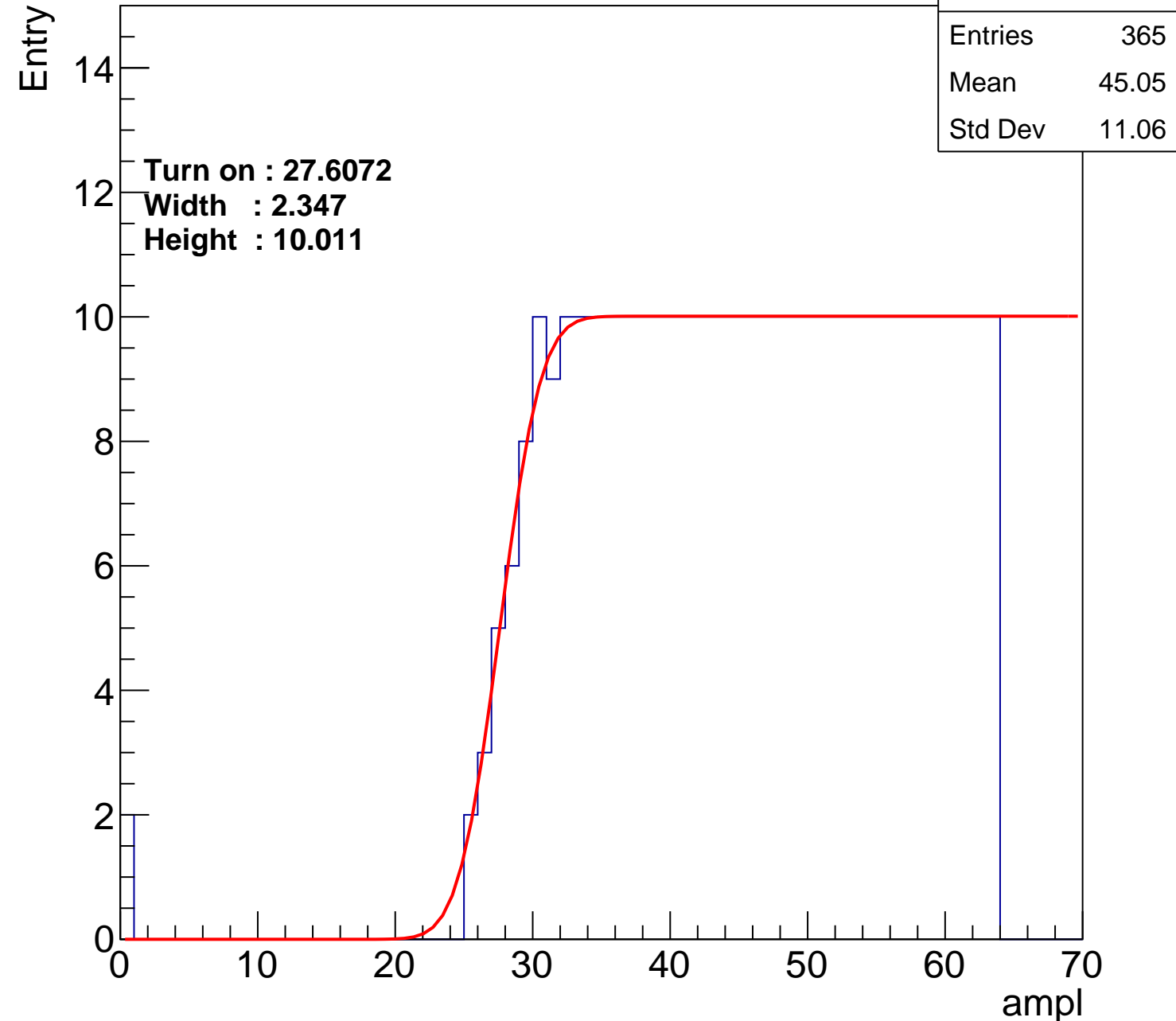
Width : 2.347

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch62

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.37
Std Dev	11.94

Turn on : 26.6718

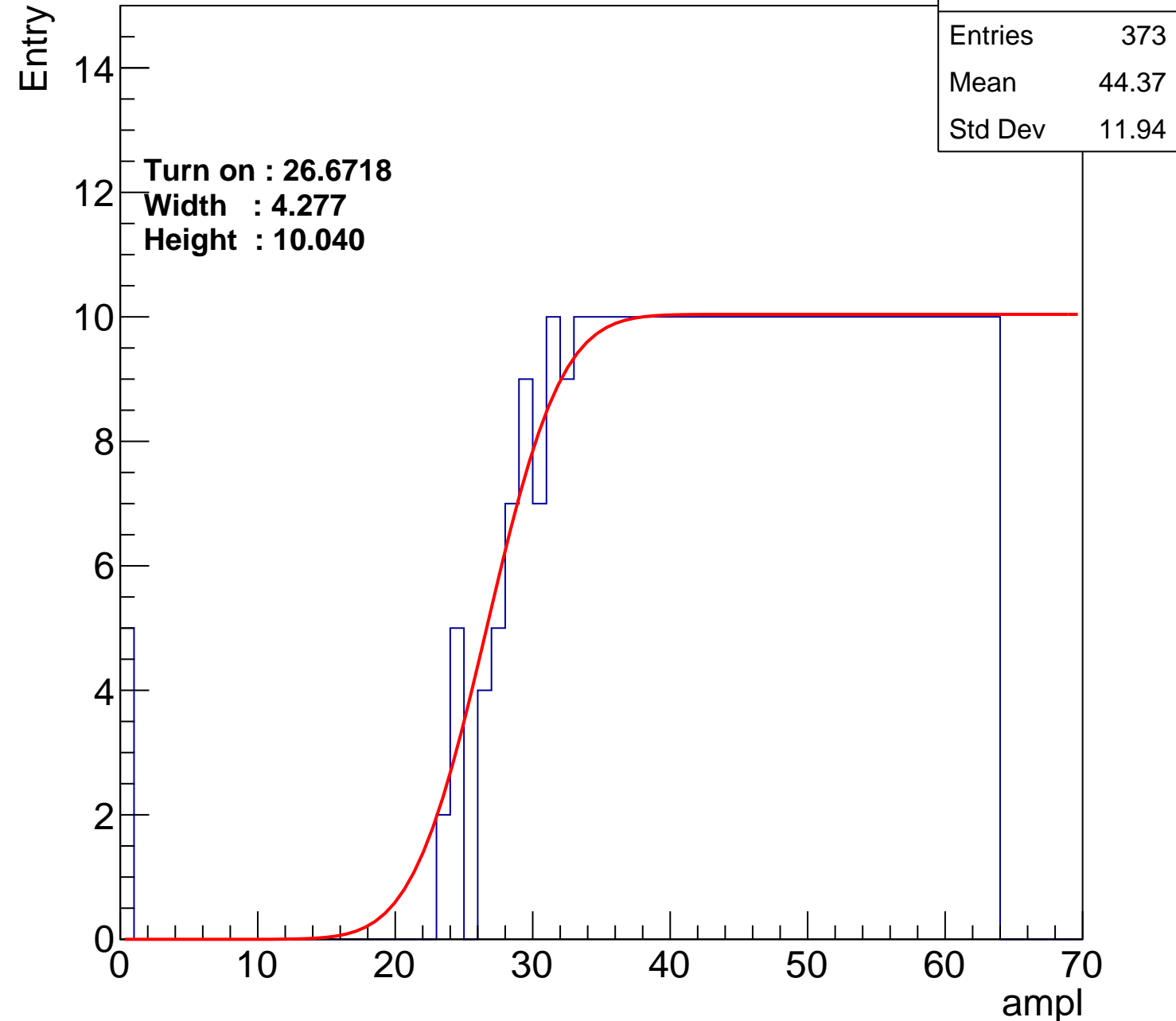
Width : 4.277

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch63

calib_packv5_042523_0143.root, FC#13, port D2

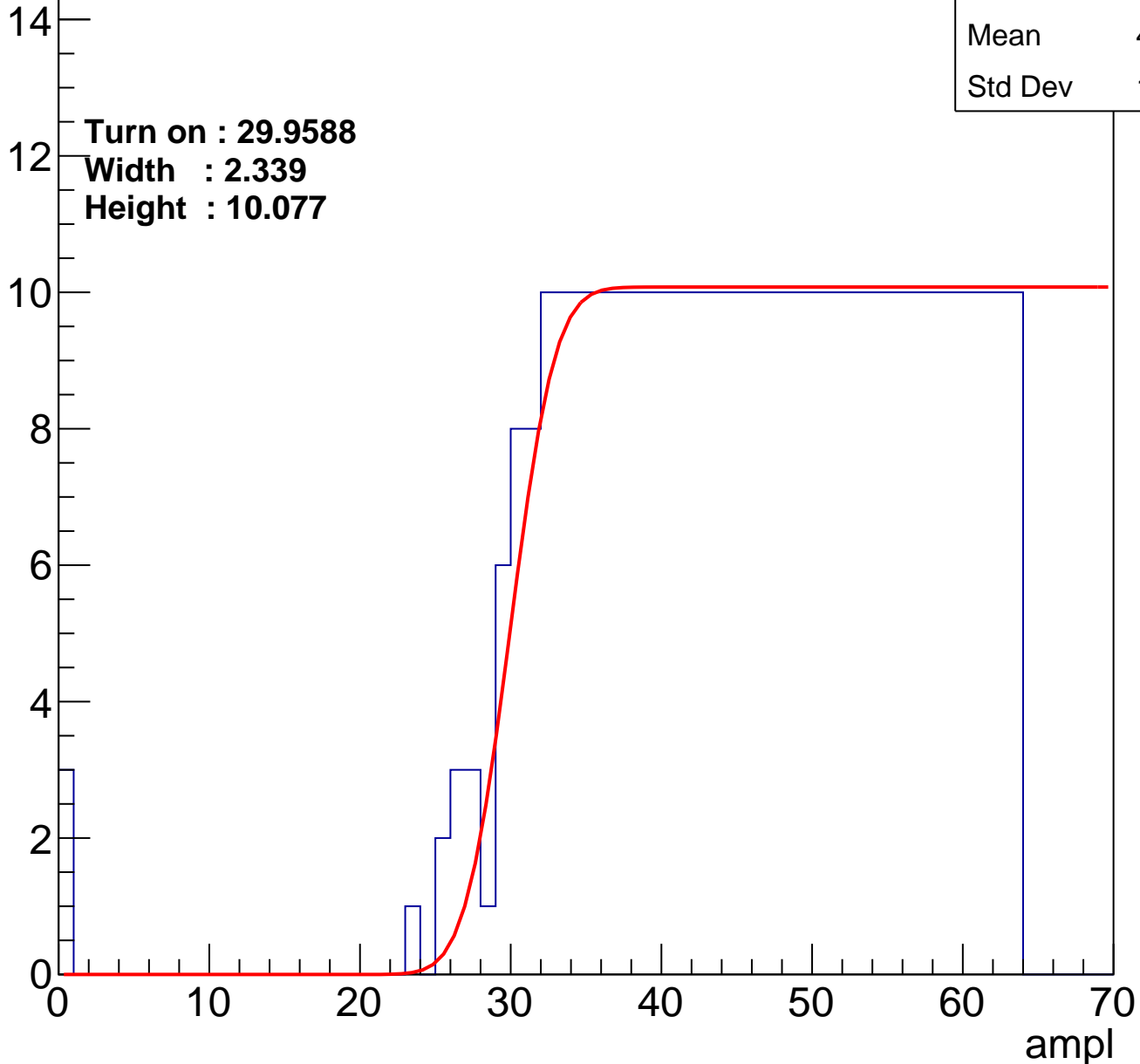
Entries	355
Mean	45.41
Std Dev	11.11

Turn on : 29.9588

Width : 2.339

Height : 10.077

Entry



B1L003S, U21-ch64

calib_packv5_042523_0143.root, FC#13, port D2

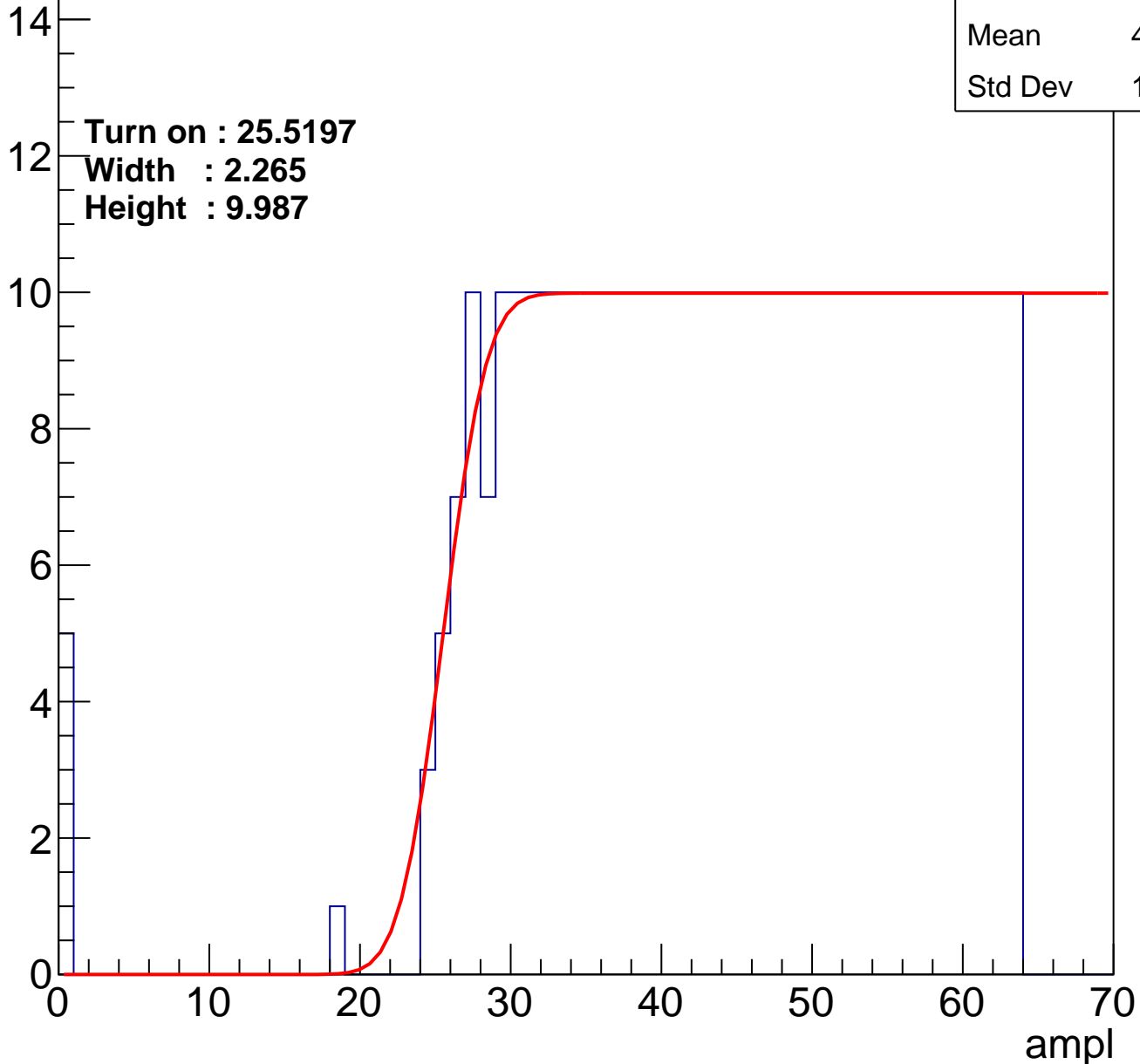
Entries	388
Mean	43.72
Std Dev	12.16

Turn on : 25.5197

Width : 2.265

Height : 9.987

Entry



B1L003S, U21-ch65

calib_packv5_042523_0143.root, FC#13, port D2

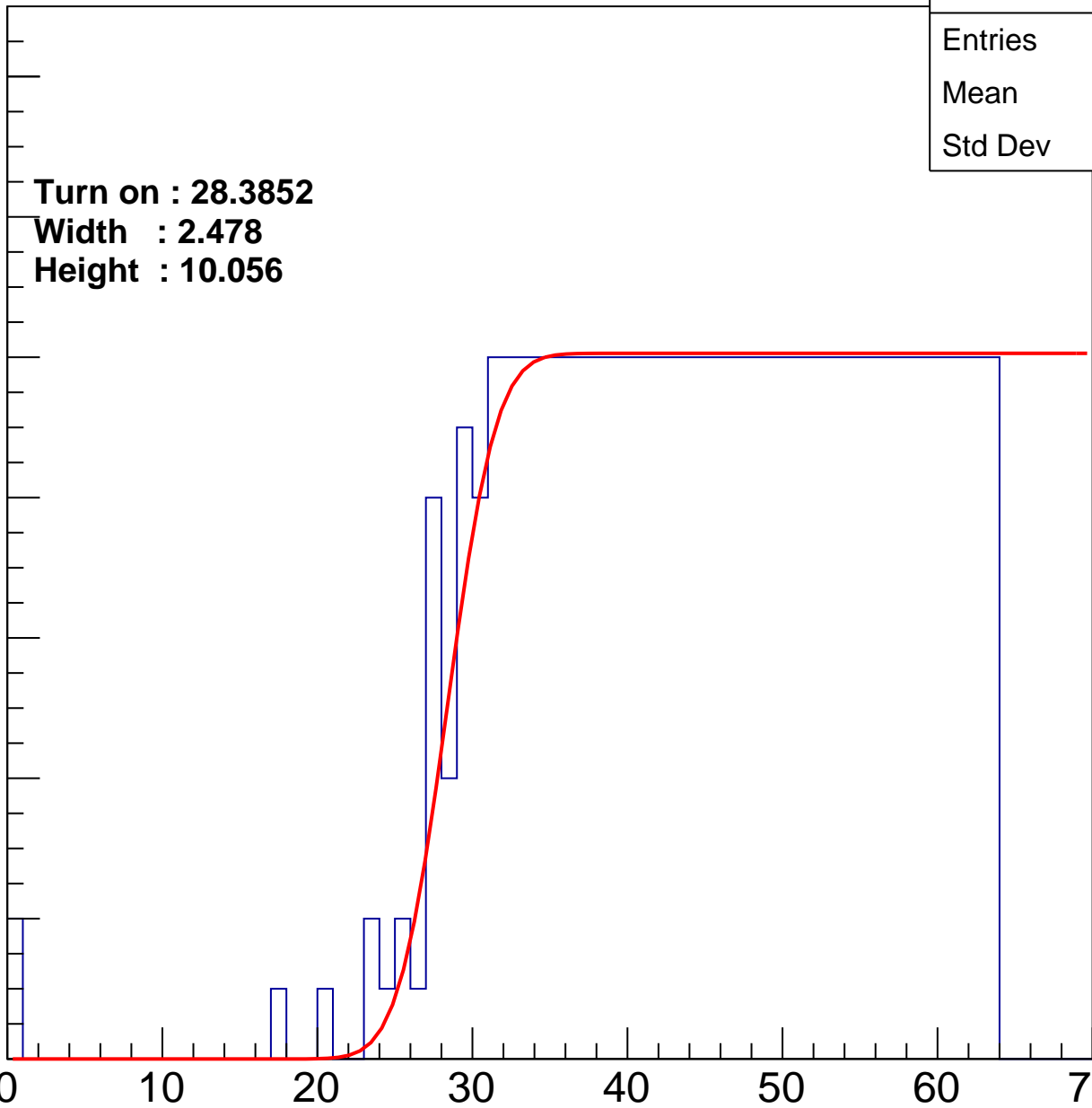
Entry

14
12
10
8
6
4
2
0

Turn on : 28.3852
Width : 2.478
Height : 10.056

Entries	369
Mean	44.78
Std Dev	11.3

ampl



B1L003S, U21-ch66

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	44.82
Std Dev	11.54

Turn on : 28.1014

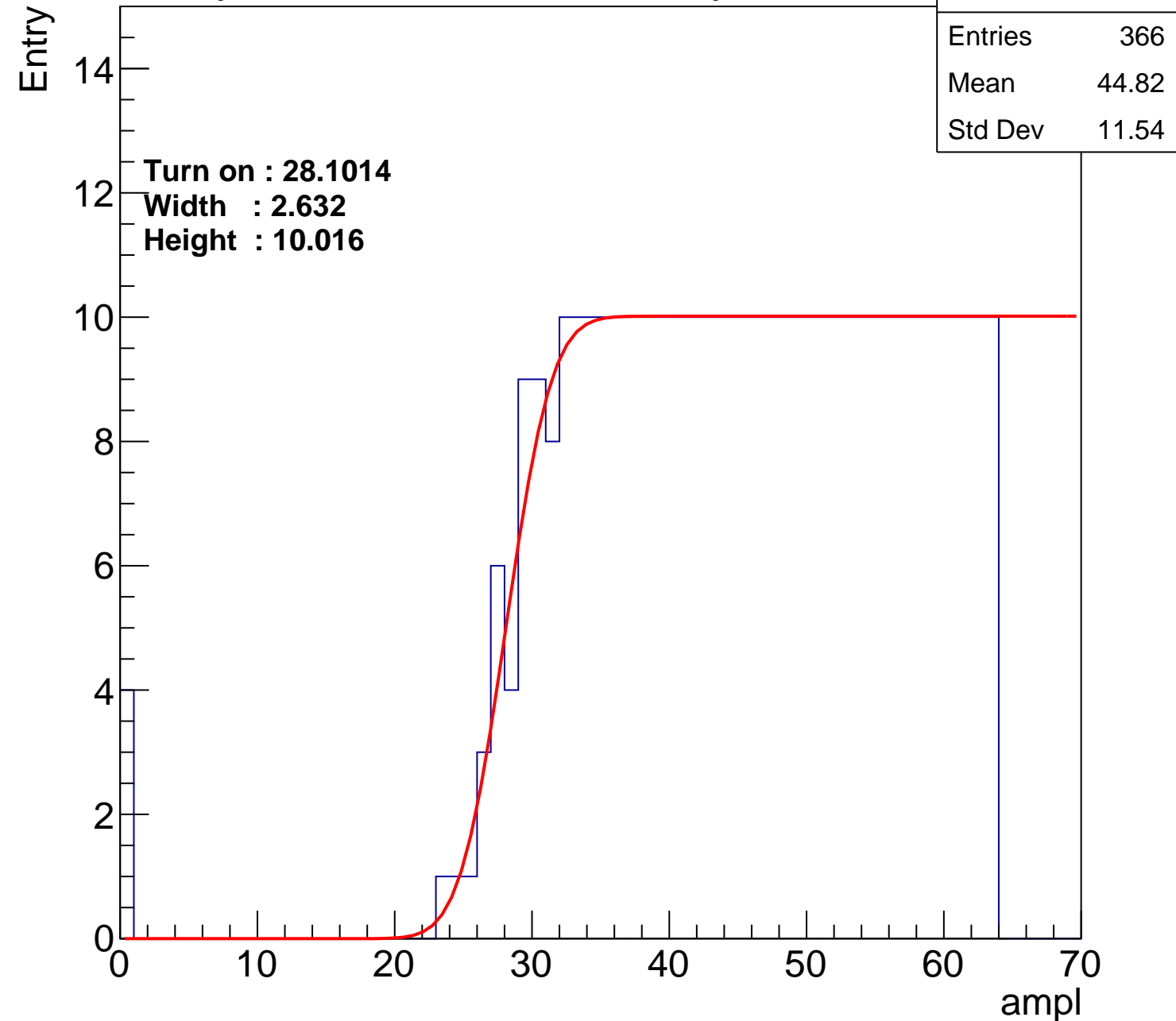
Width : 2.632

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch67

calib_packv5_042523_0143.root, FC#13, port D2

Entries	359
Mean	45.2
Std Dev	11.23

Turn on : 28.5625

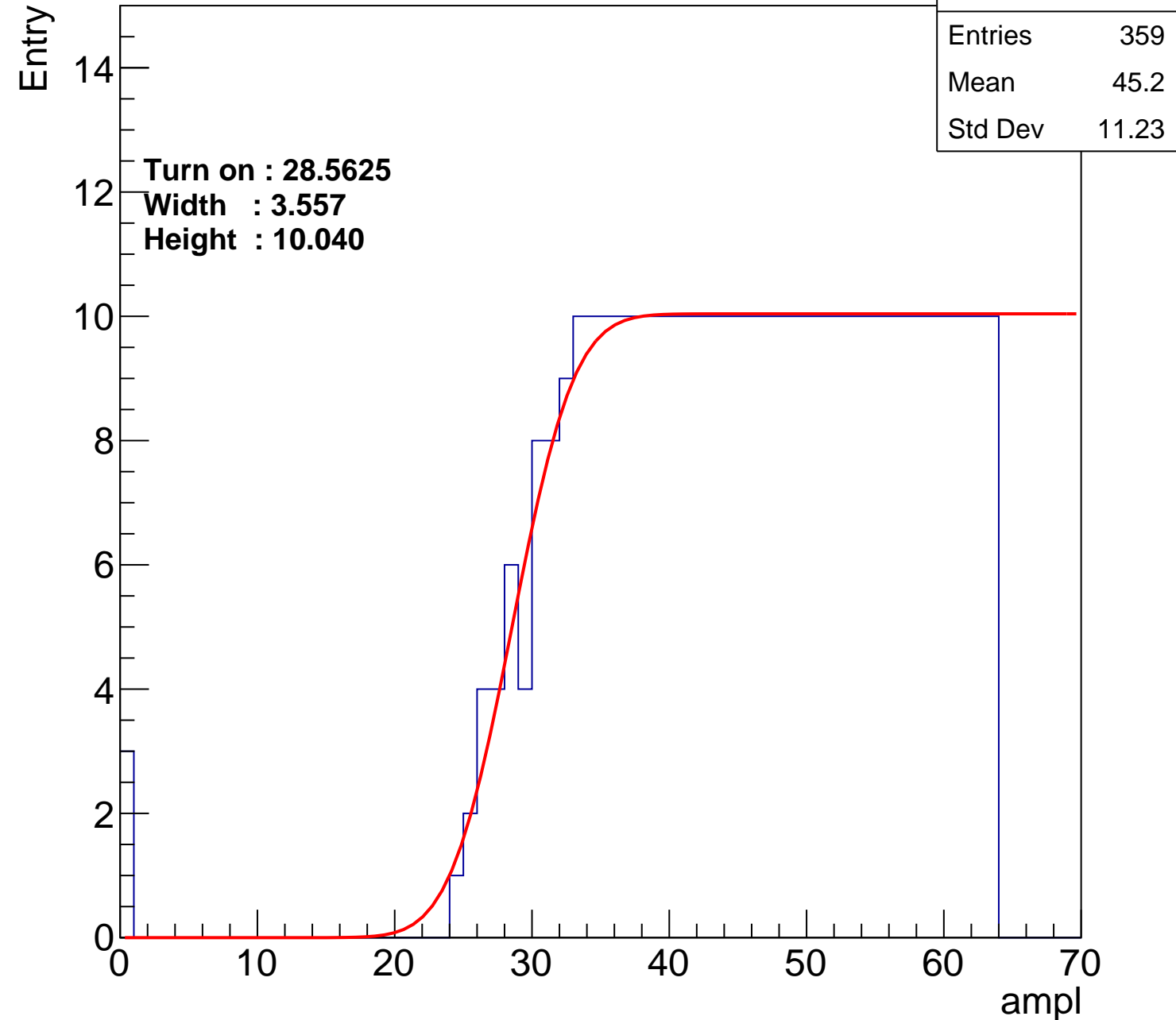
Width : 3.557

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch68

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.17
Std Dev	11.69

Turn on : 26.6788

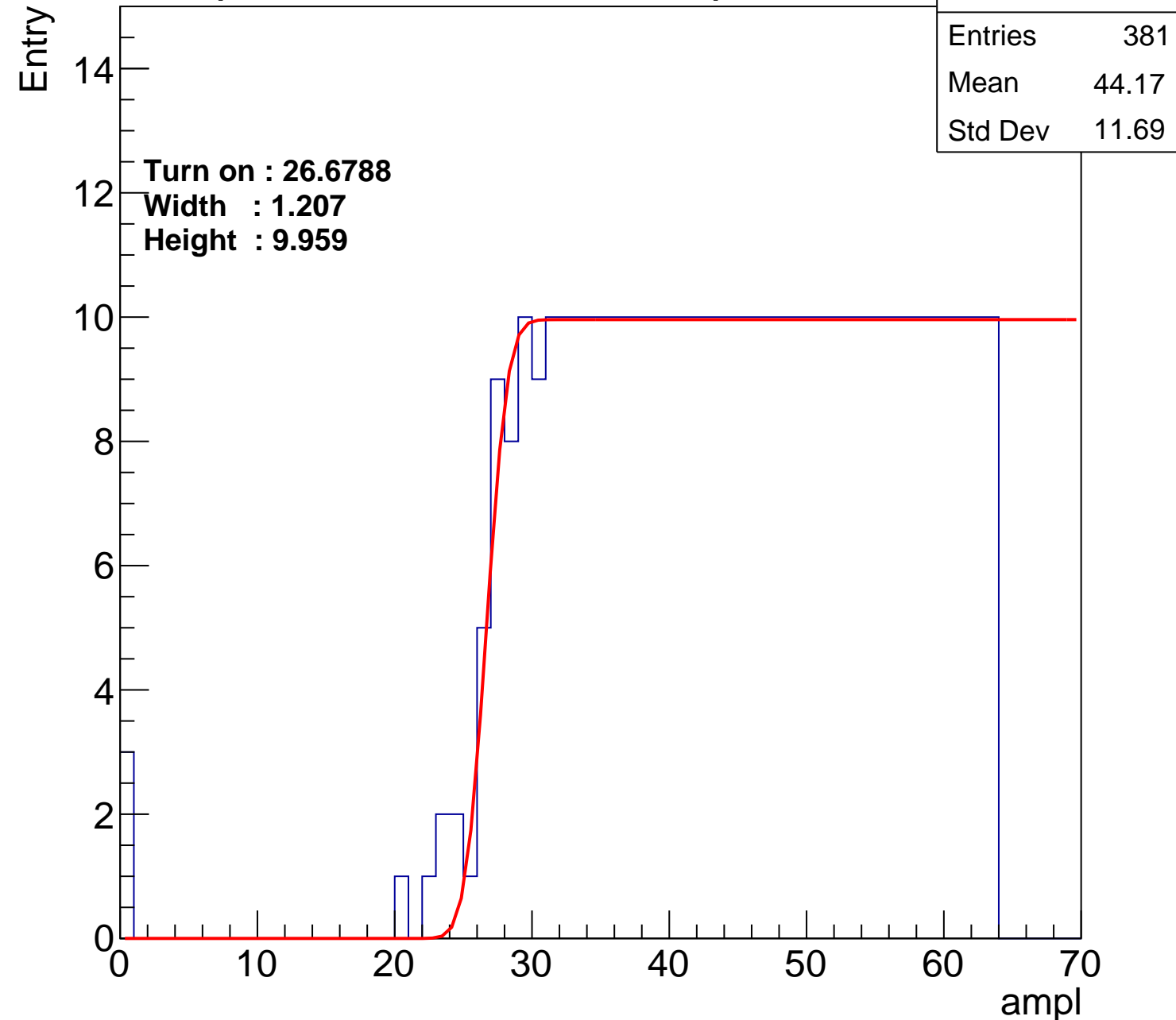
Width : 1.207

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch69

calib_packv5_042523_0143.root, FC#13, port D2

Entries	367
Mean	44.94
Std Dev	11.12

Turn on : 27.3786

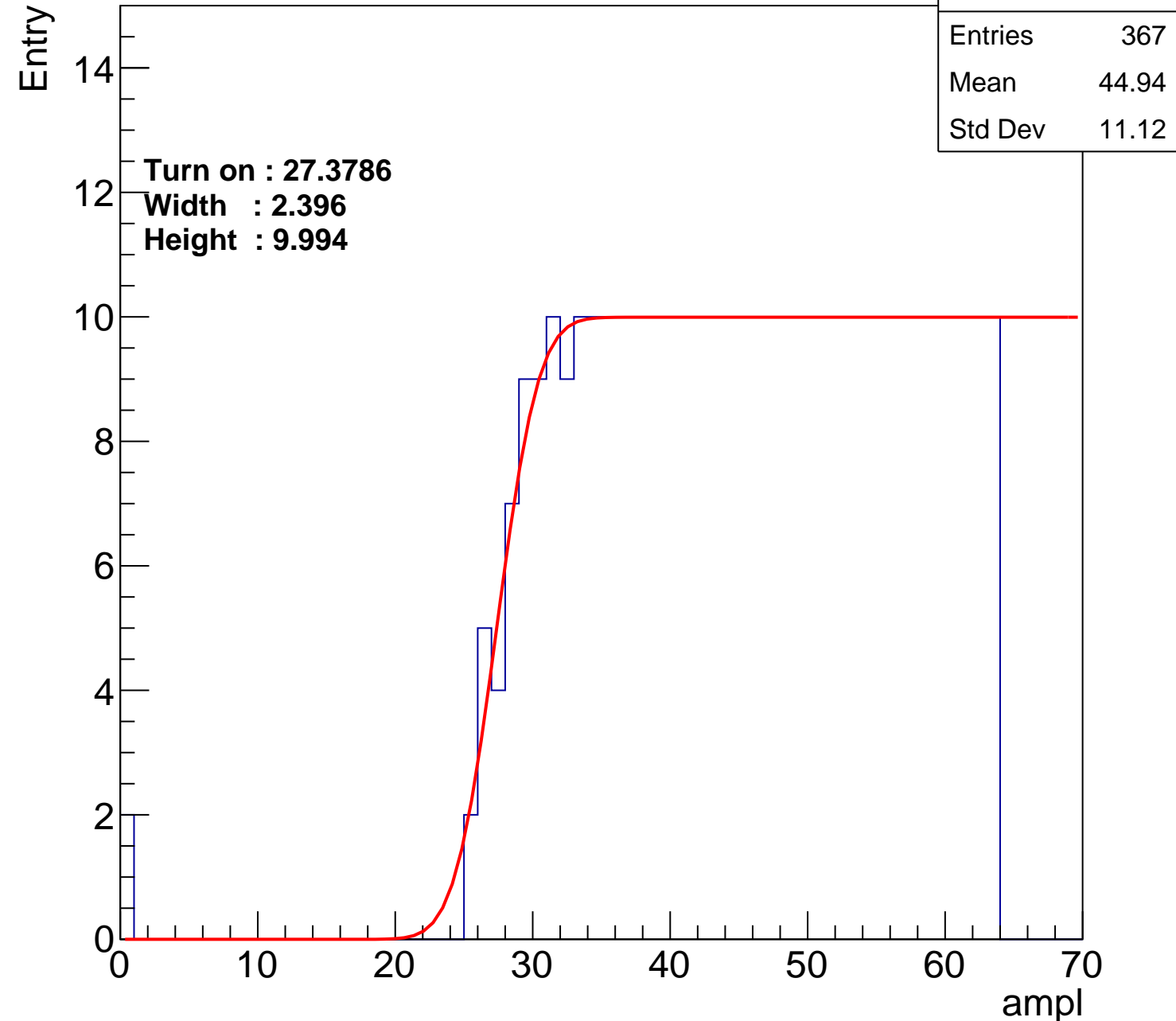
Width : 2.396

Height : 9.994

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch70

calib_packv5_042523_0143.root, FC#13, port D2

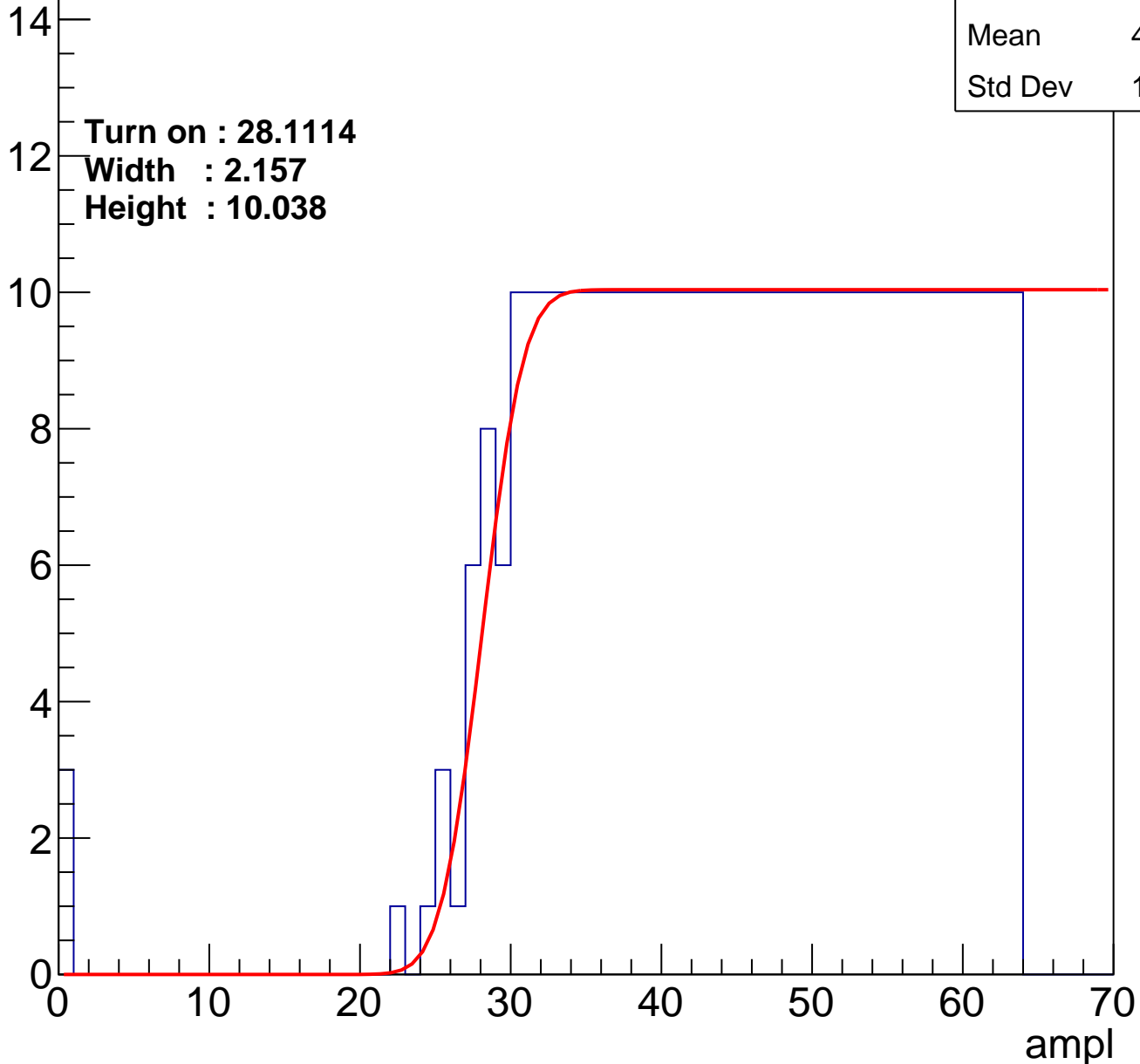
Entries	369
Mean	44.76
Std Dev	11.39

Turn on : 28.1114

Width : 2.157

Height : 10.038

Entry



B1L003S, U21-ch71

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.46
Std Dev	11.73

Turn on : 27.6168

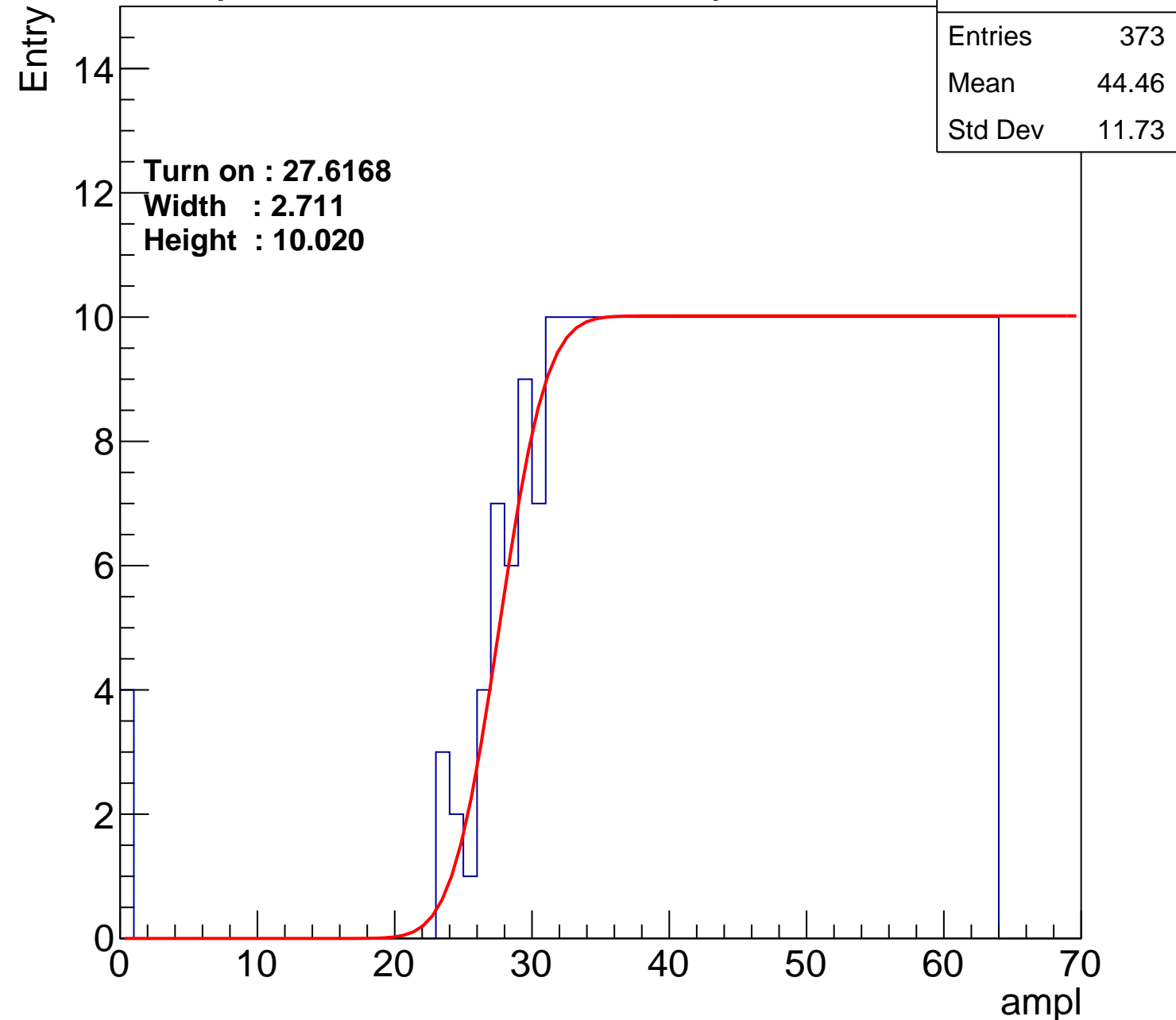
Width : 2.711

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch72

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.97
Std Dev	11.48

Turn on : 25.4949

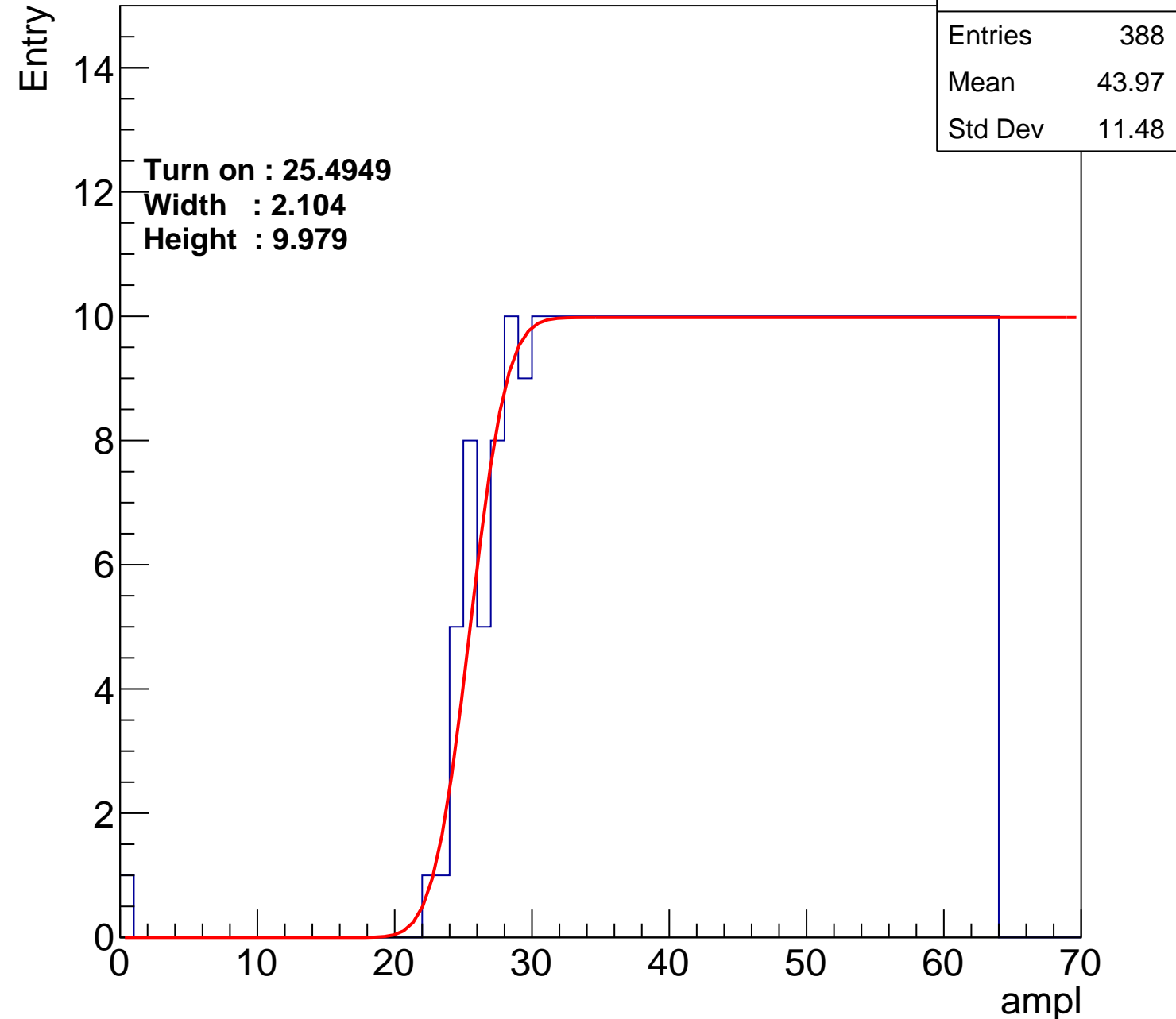
Width : 2.104

Height : 9.979

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch73

calib_packv5_042523_0143.root, FC#13, port D2

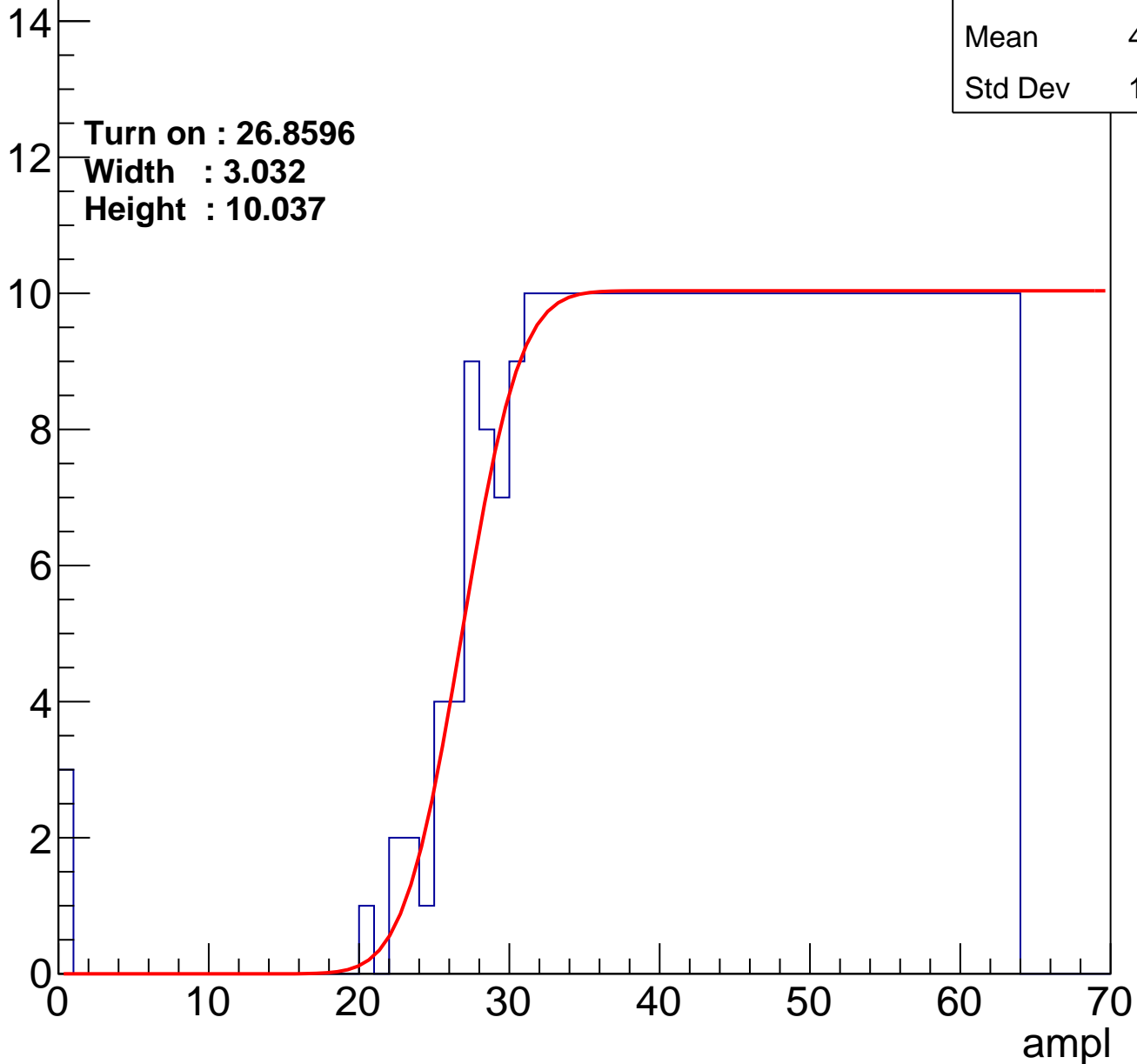
Entries	380
Mean	44.18
Std Dev	11.72

Turn on : 26.8596

Width : 3.032

Height : 10.037

Entry



B1L003S, U21-ch74

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.19
Std Dev	11.83

Turn on : 27.2065

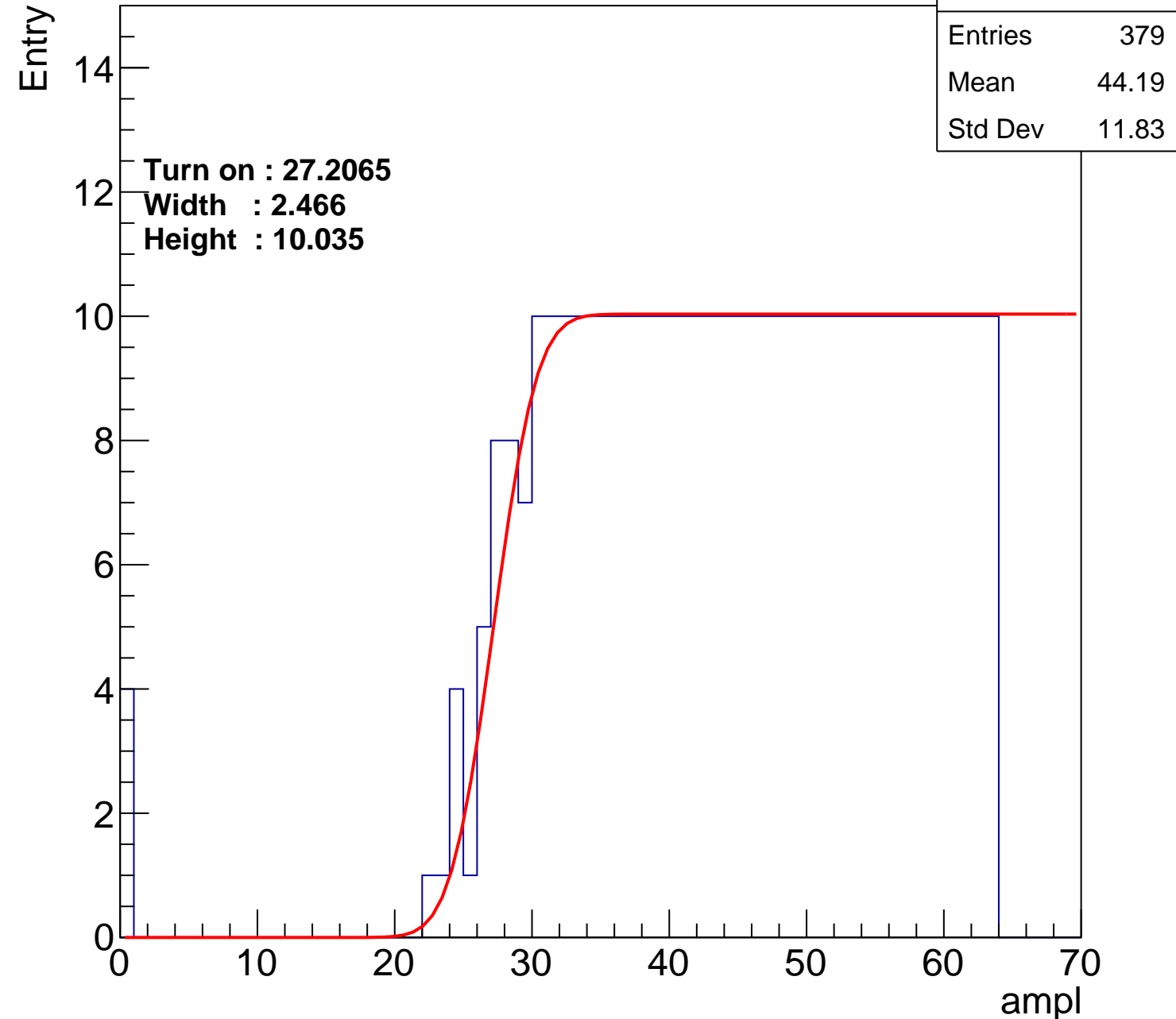
Width : 2.466

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch75

calib_packv5_042523_0143.root, FC#13, port D2

Entries	388
Mean	43.84
Std Dev	11.84

Turn on : 25.7884

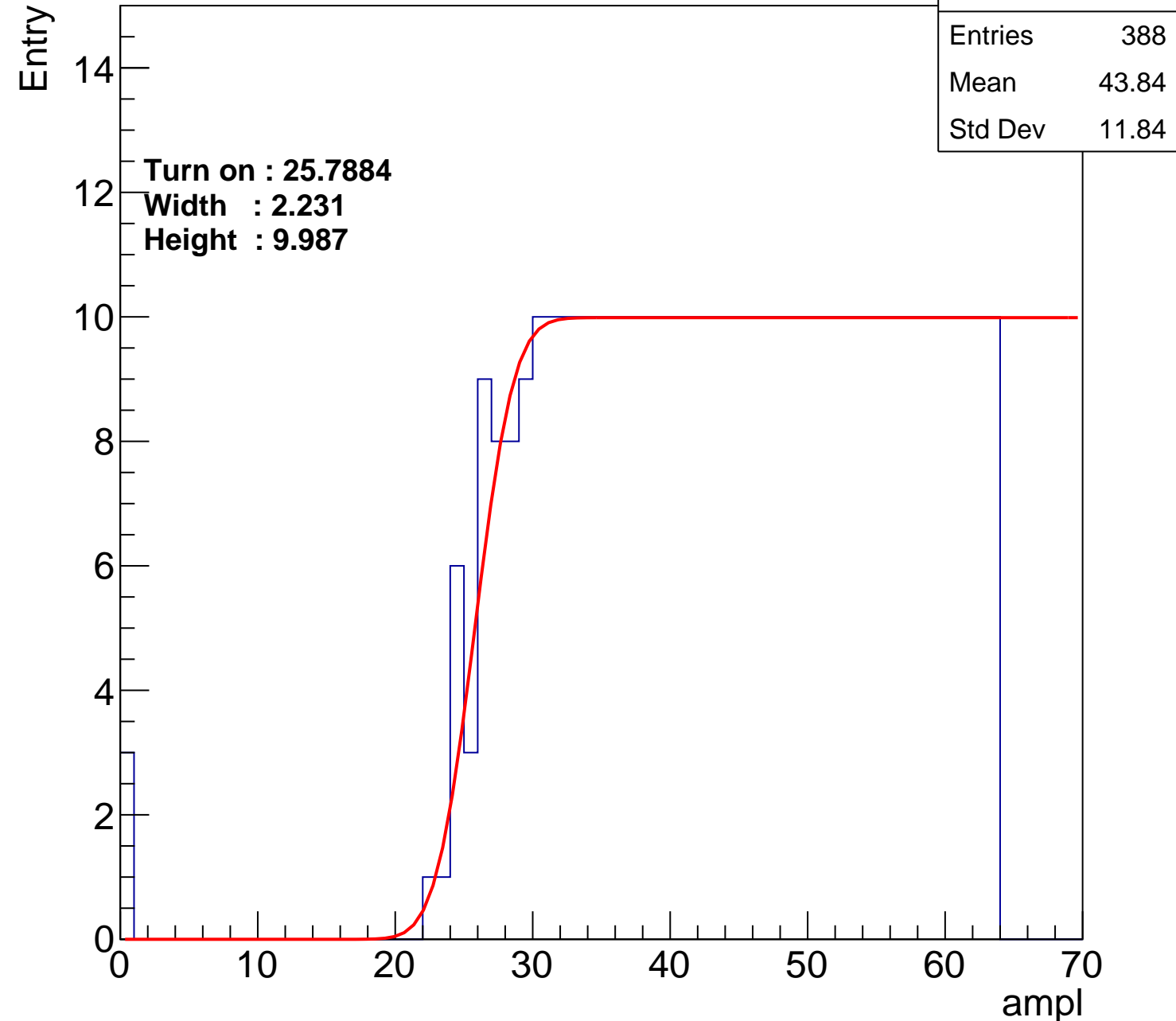
Width : 2.231

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch76

calib_packv5_042523_0143.root, FC#13, port D2

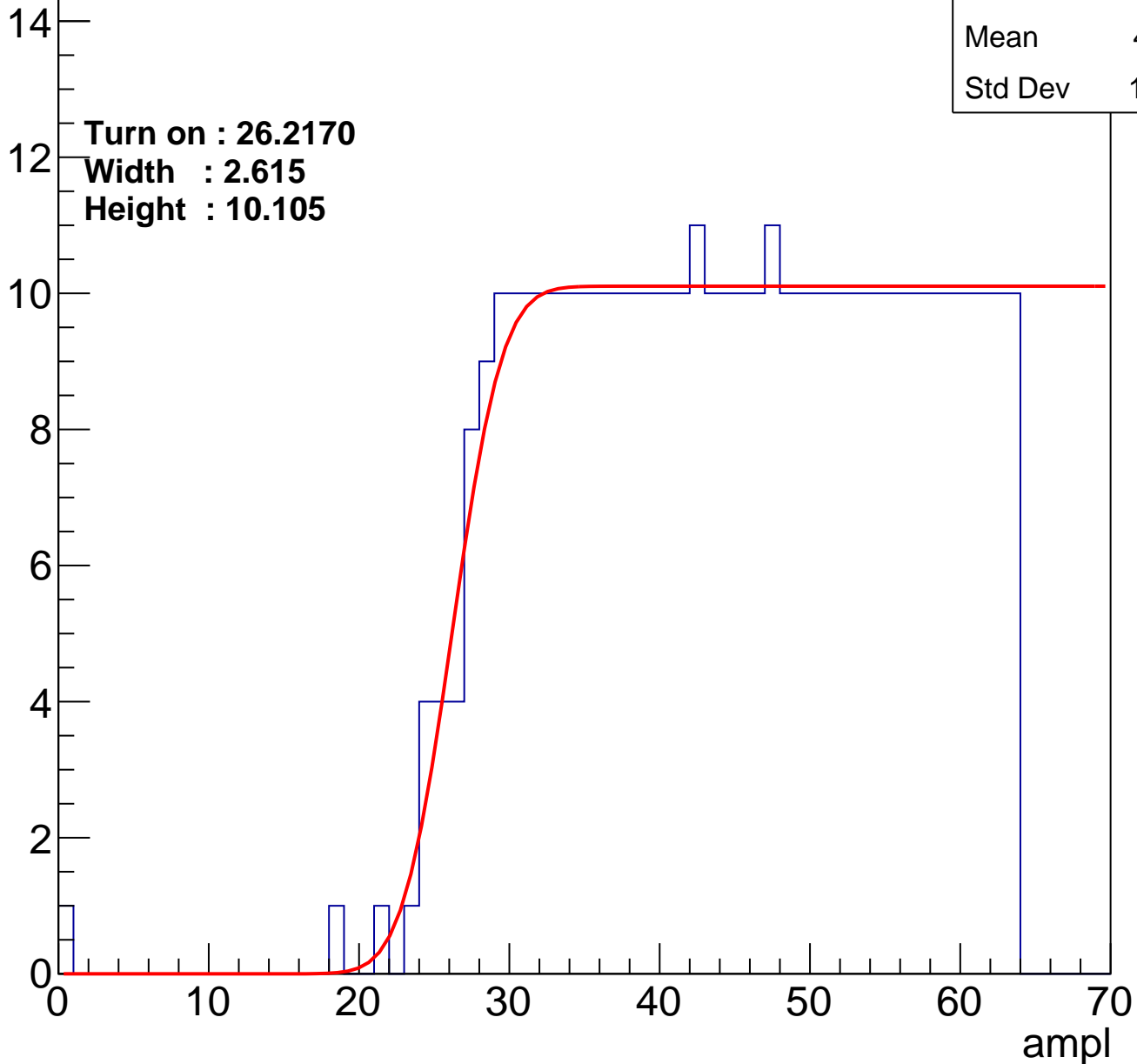
Entries	385
Mean	44.21
Std Dev	11.35

Turn on : 26.2170

Width : 2.615

Height : 10.105

Entry



B1L003S, U21-ch77

calib_packv5_042523_0143.root, FC#13, port D2

Entries	357
Mean	45.37
Std Dev	10.98

Turn on : 29.0470

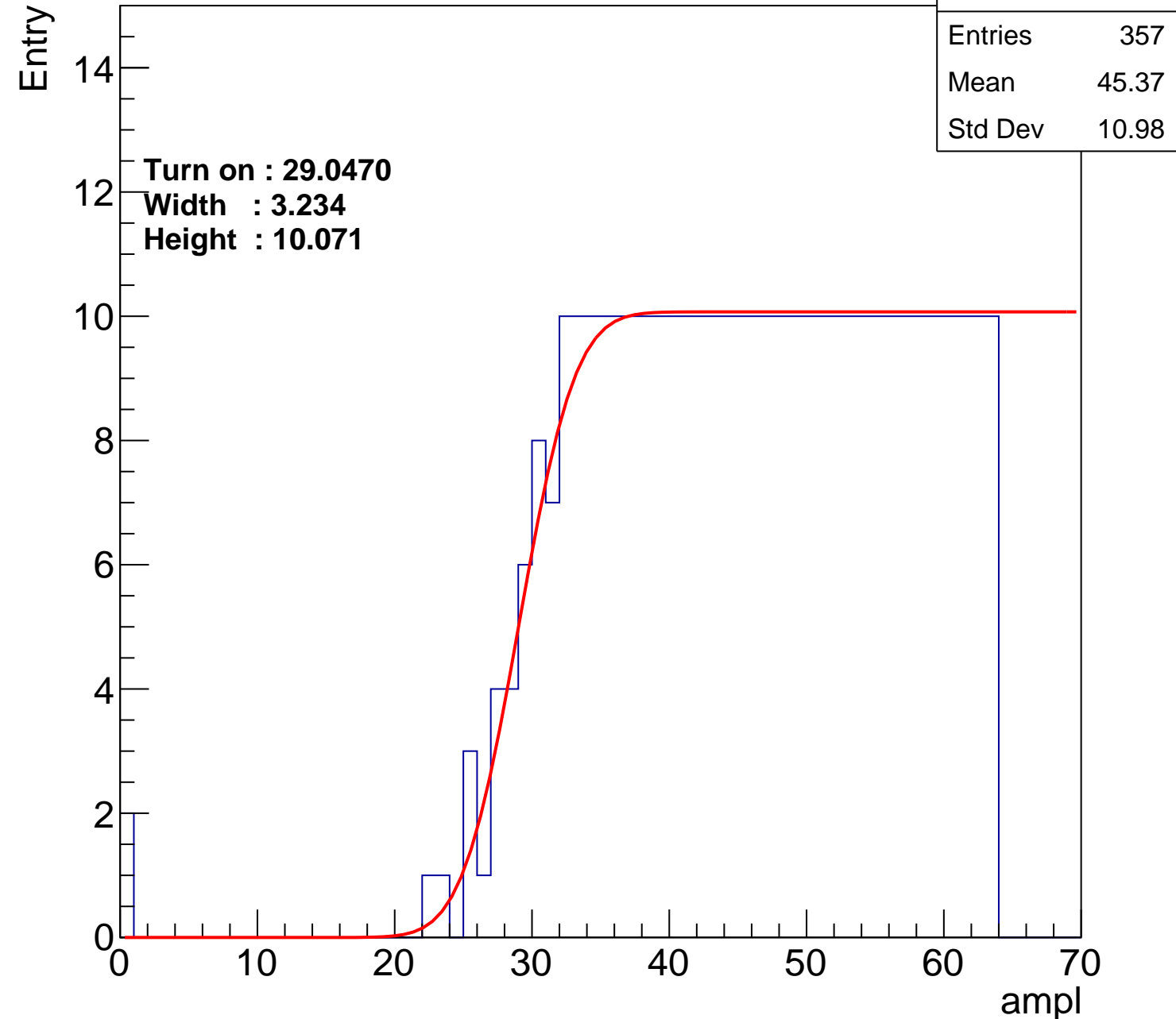
Width : 3.234

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch78

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.26
Std Dev	11.65

Turn on : 26.5272

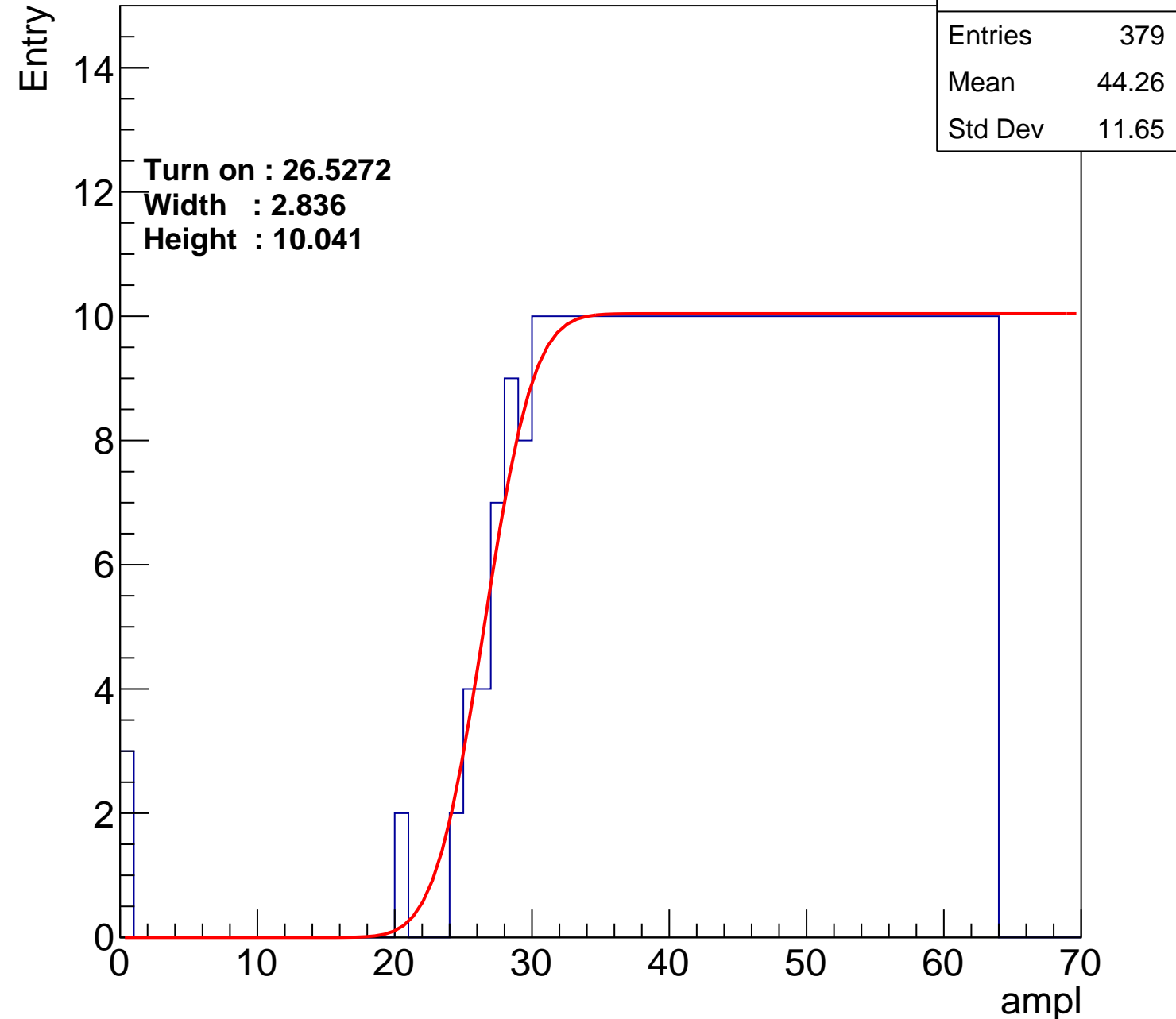
Width : 2.836

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch79

calib_packv5_042523_0143.root, FC#13, port D2

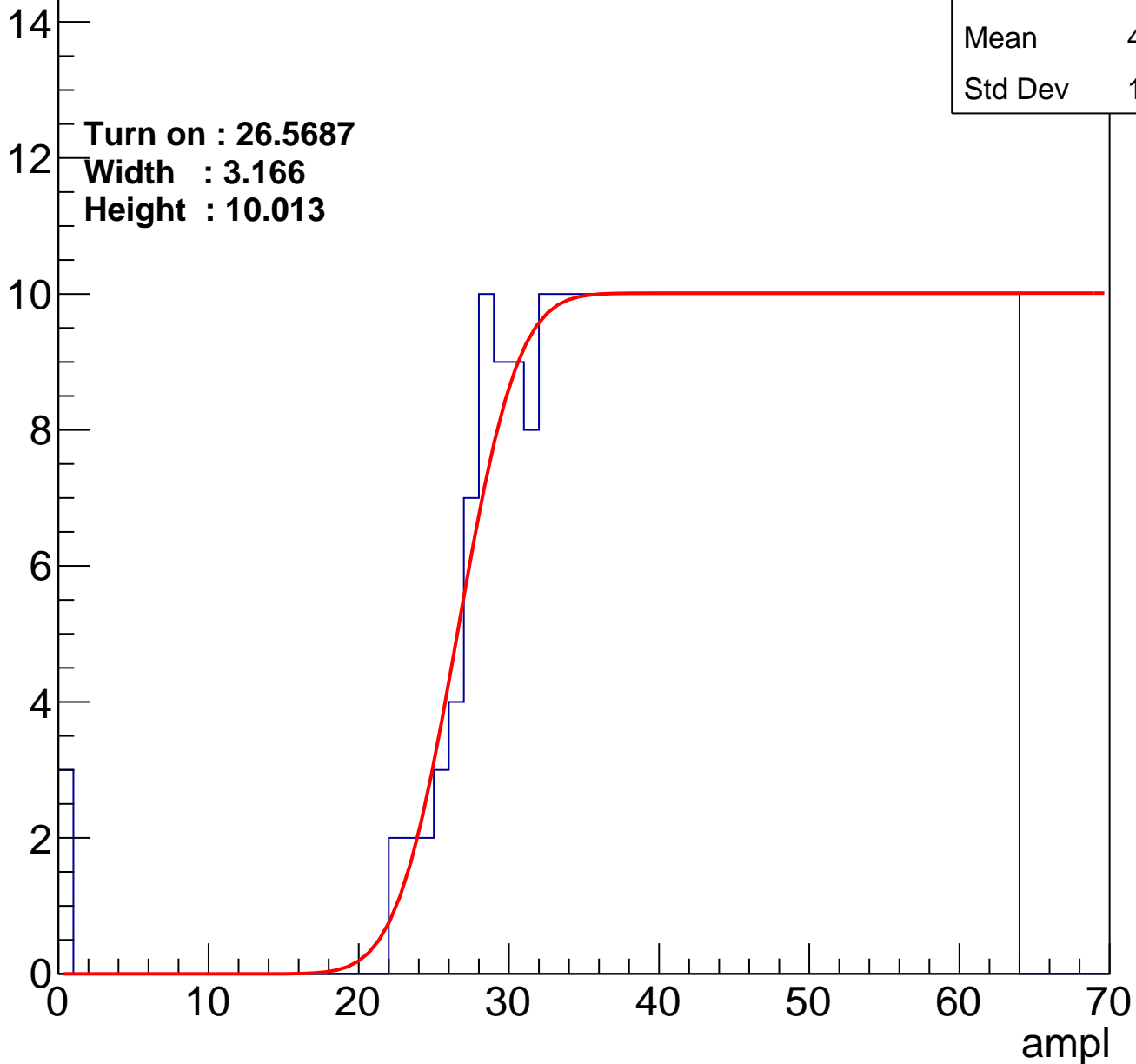
Entries	379
Mean	44.23
Std Dev	11.68

Turn on : 26.5687

Width : 3.166

Height : 10.013

Entry



B1L003S, U21-ch80

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.03
Std Dev	11.08

Turn on : 27.9100

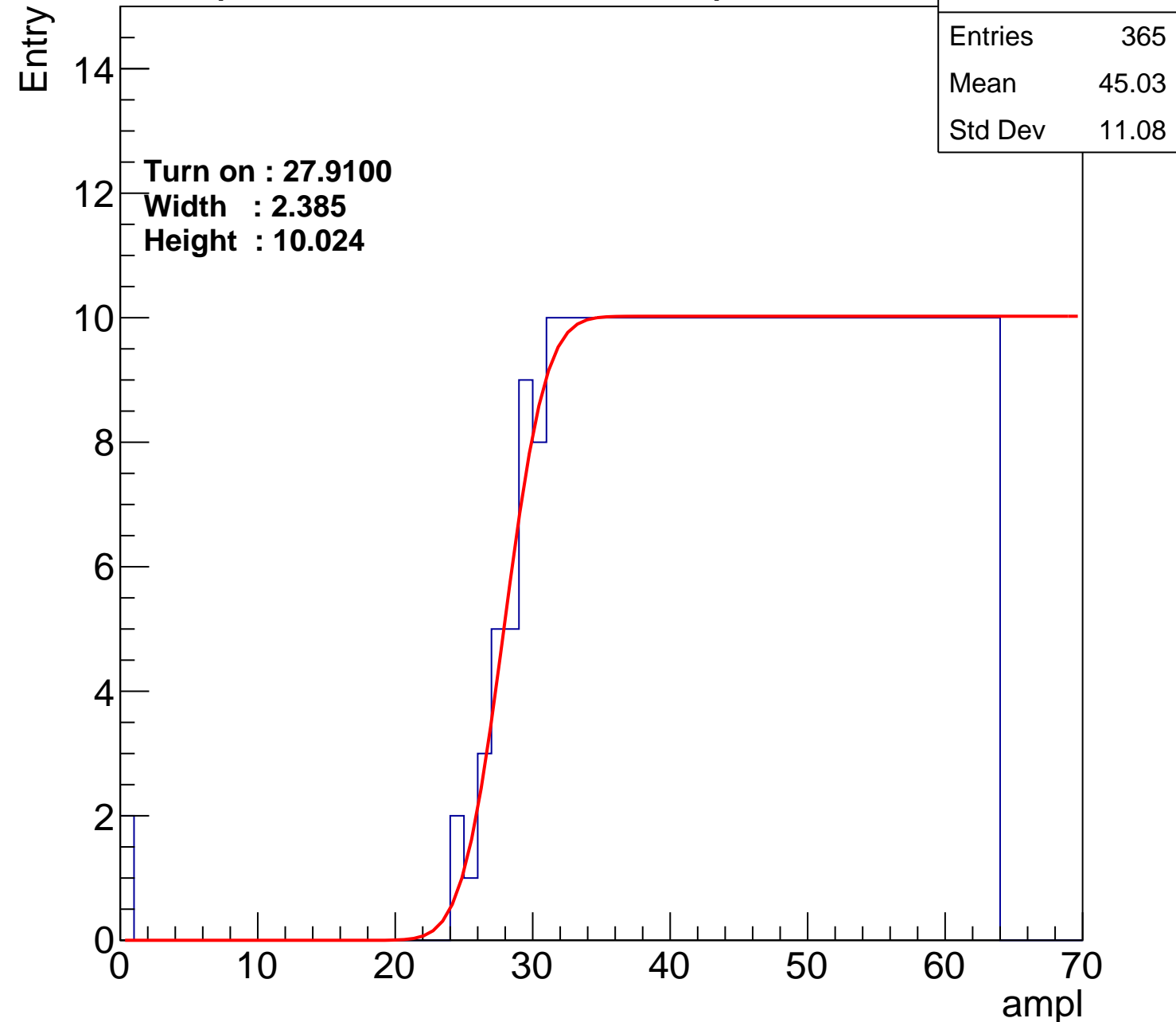
Width : 2.385

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch81

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.75
Std Dev	11.46

Turn on : 28.1239

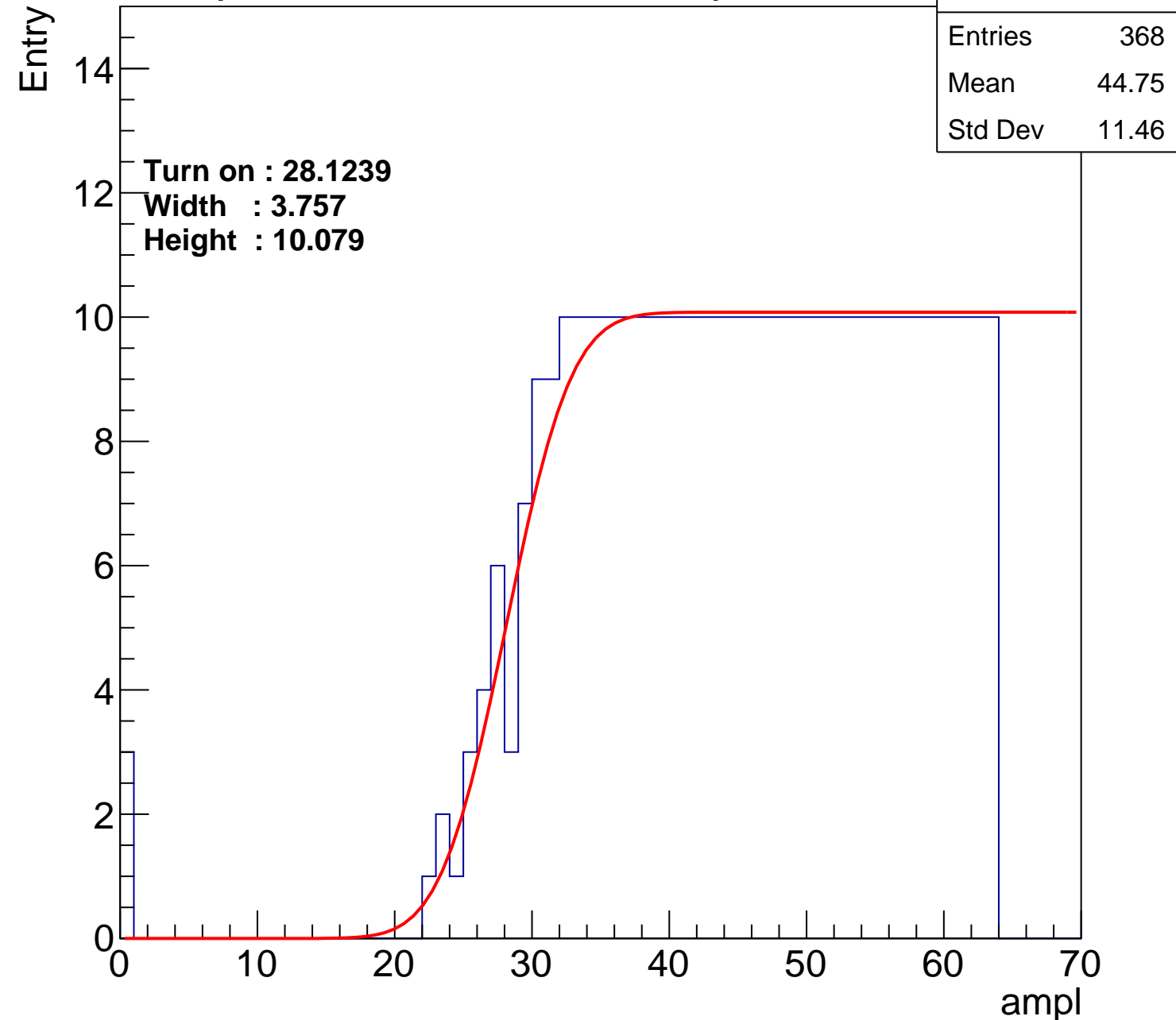
Width : 3.757

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch82

calib_packv5_042523_0143.root, FC#13, port D2

Entries	386
Mean	44.03
Std Dev	11.49

Turn on : 25.7131

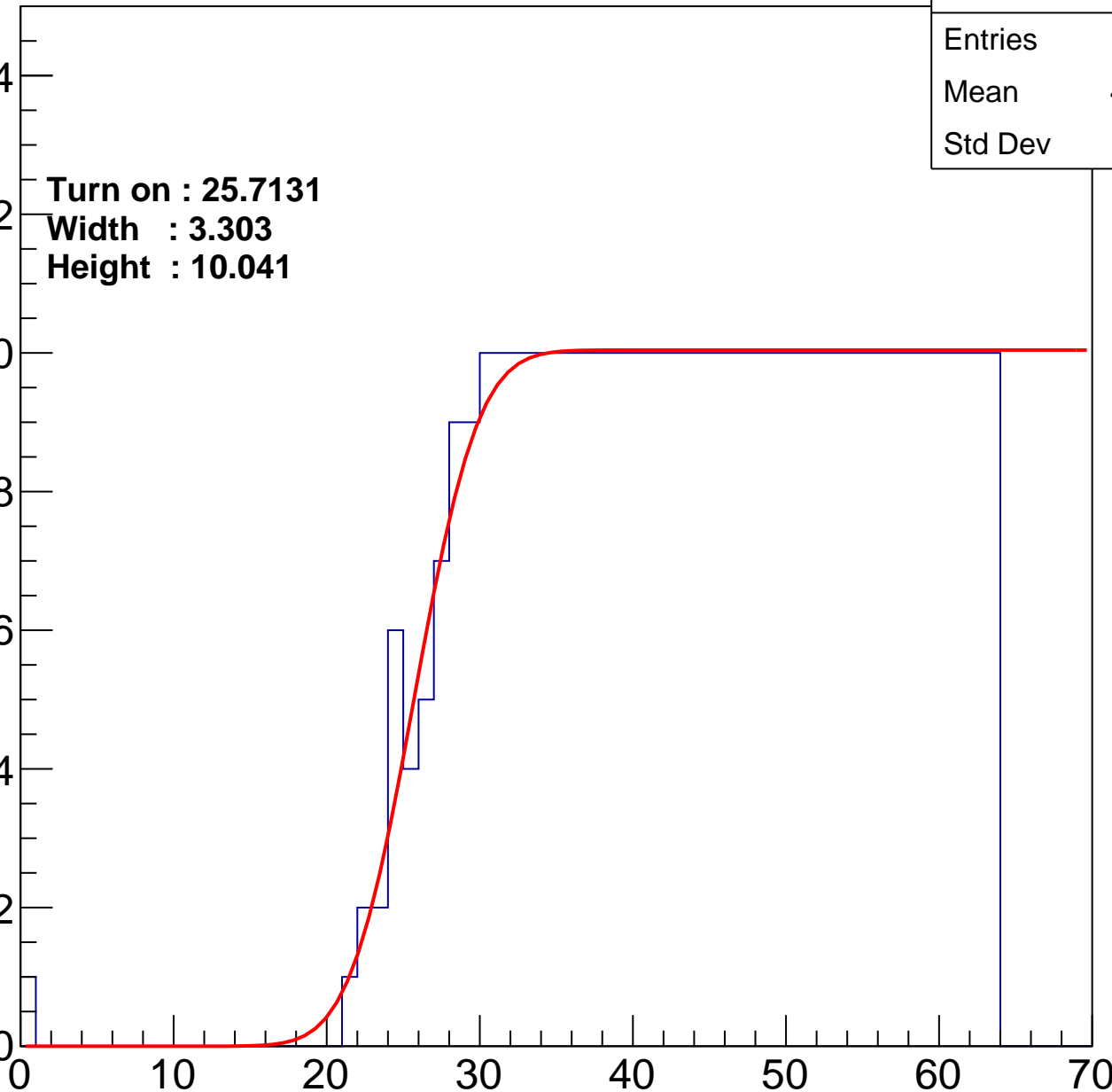
Width : 3.303

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch83

calib_packv5_042523_0143.root, FC#13, port D2

Entries	369
Mean	44.65
Std Dev	11.64

Turn on : 28.1474

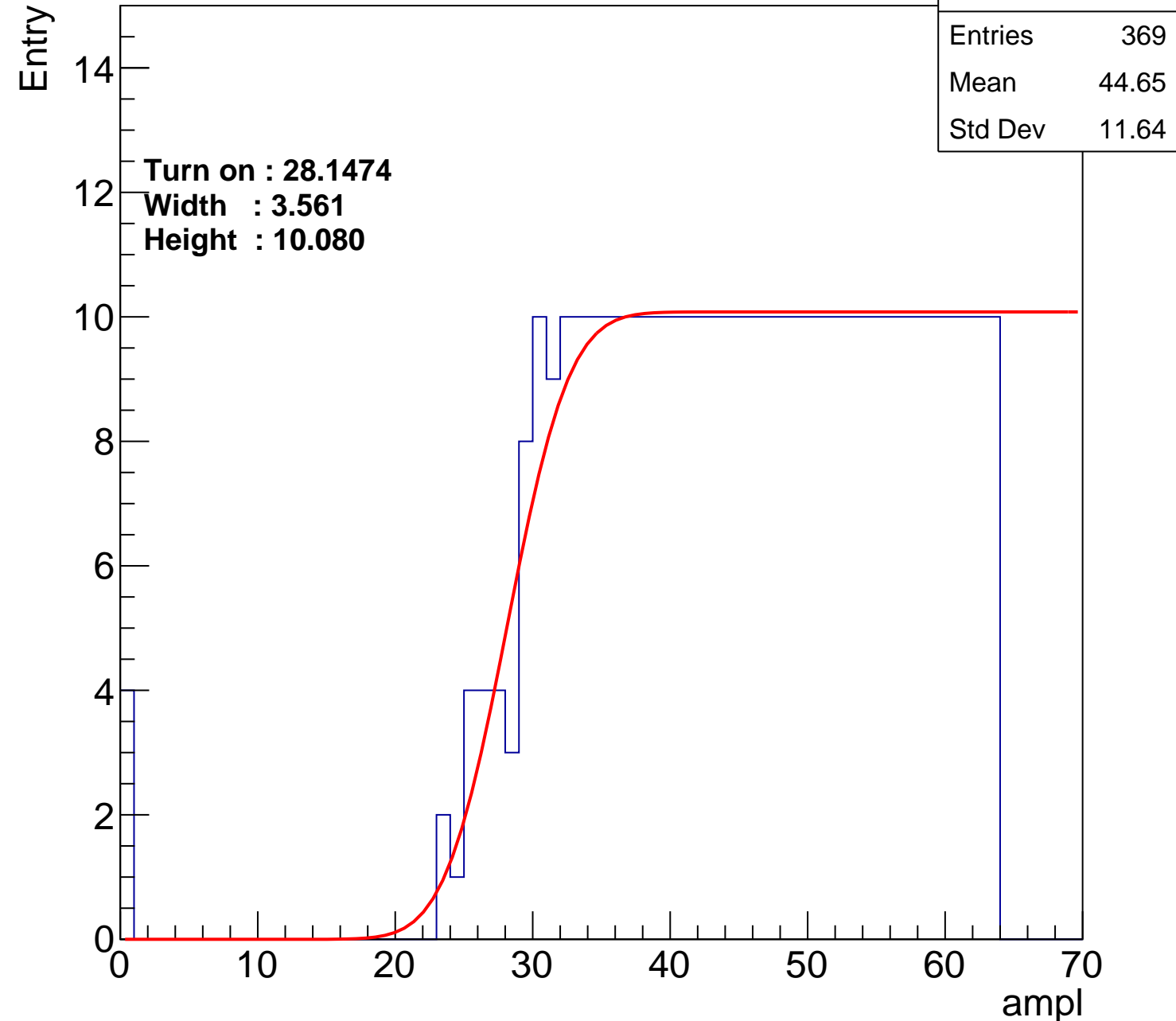
Width : 3.561

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch84

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.38
Std Dev	11.75

Turn on : 26.7863

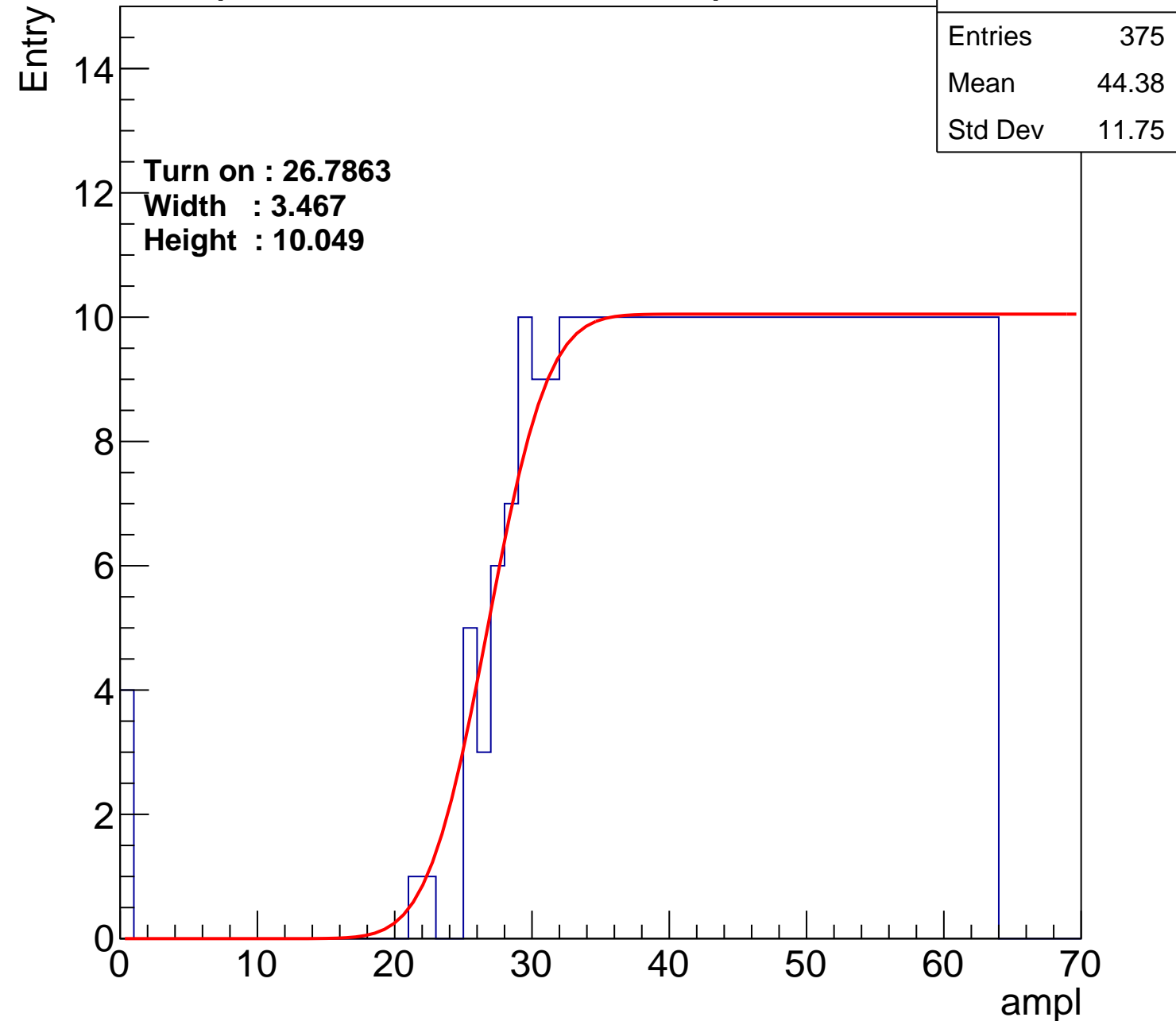
Width : 3.467

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch85

calib_packv5_042523_0143.root, FC#13, port D2

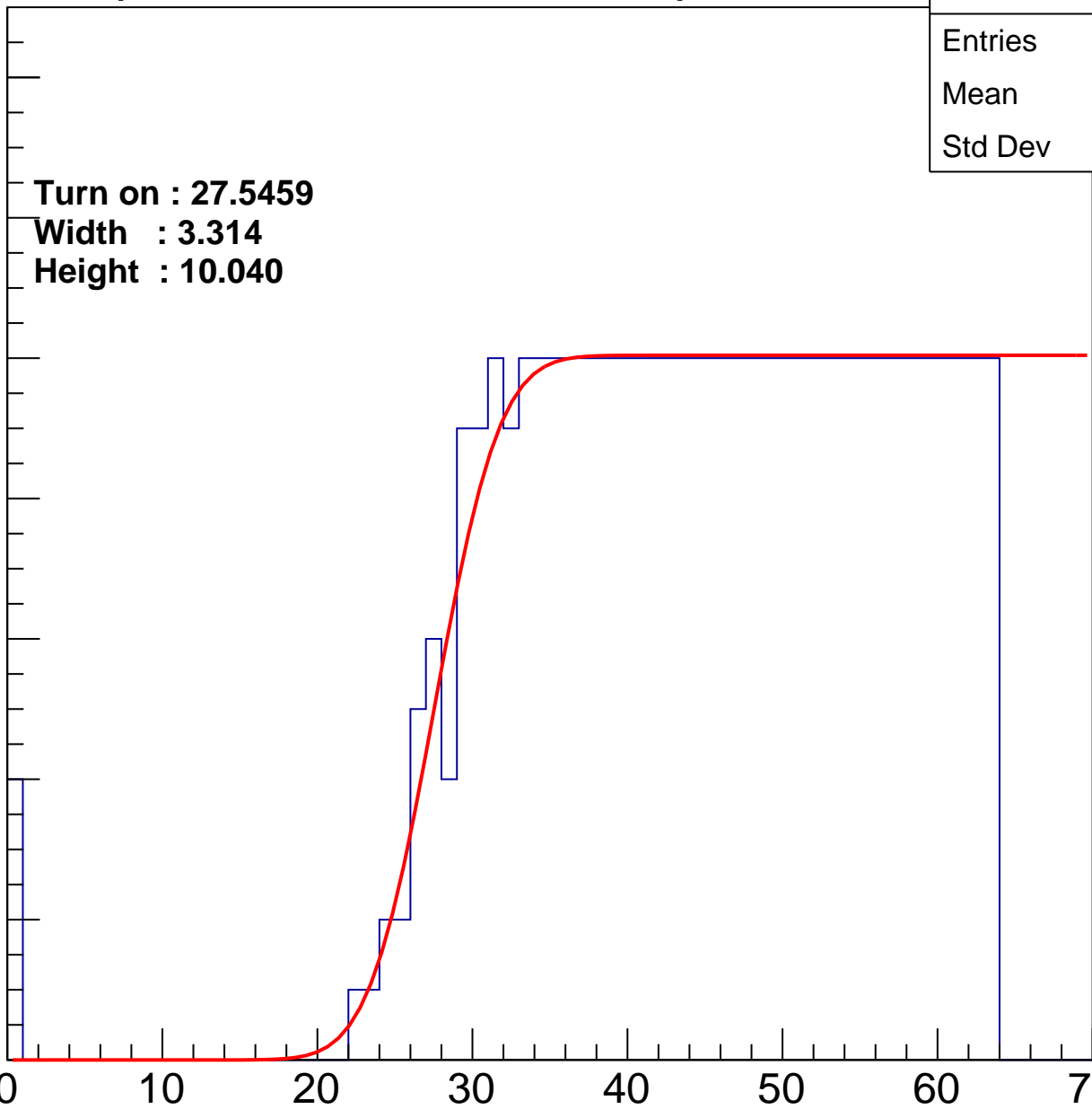
Entry

14
12
10
8
6
4
2
0

Turn on : 27.5459
Width : 3.314
Height : 10.040

Entries	372
Mean	44.51
Std Dev	11.71

ampl



B1L003S, U21-ch86

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	44.09
Std Dev	11.45

Turn on : 25.5295

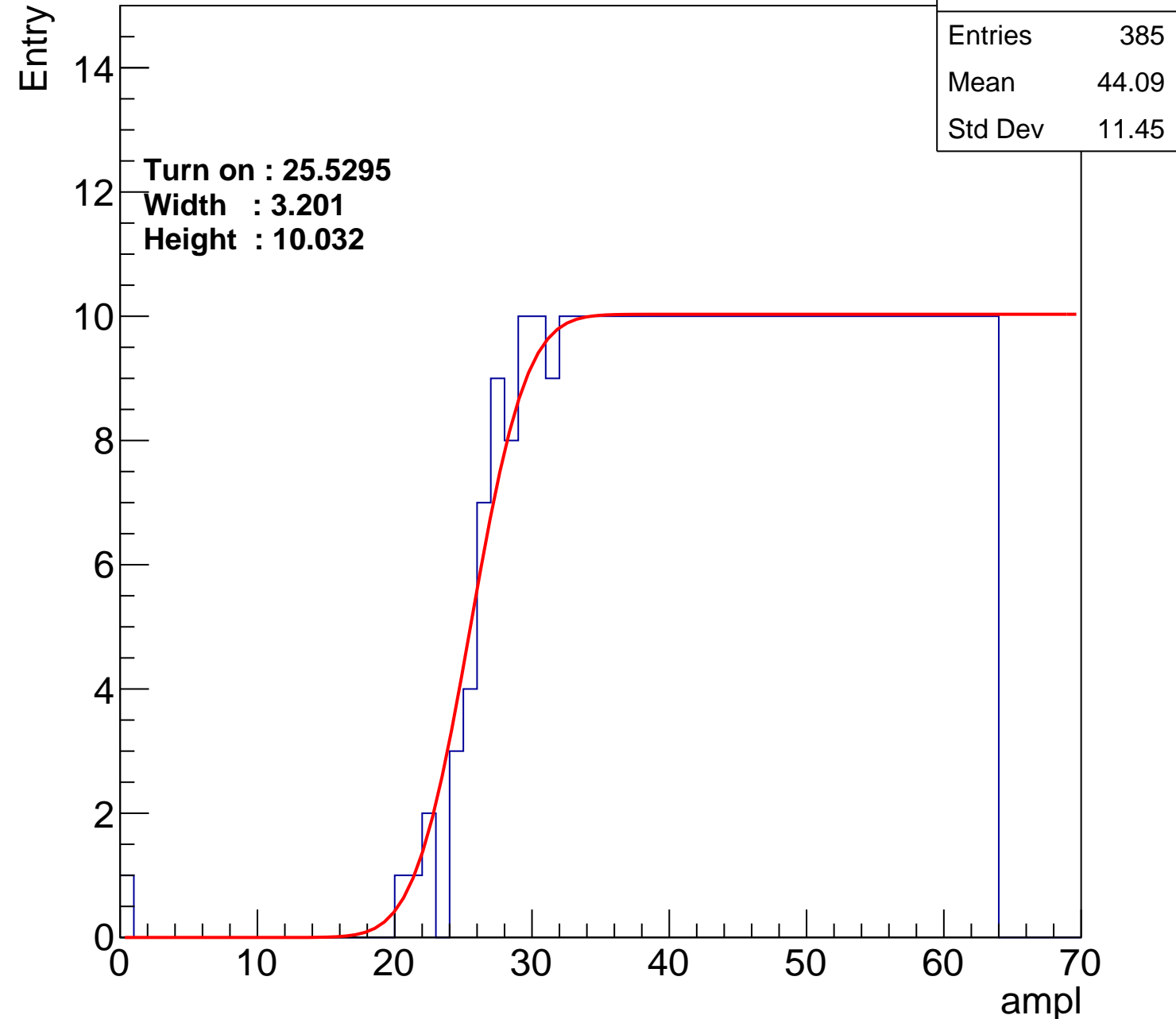
Width : 3.201

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch87

calib_packv5_042523_0143.root, FC#13, port D2

Entries	362
Mean	45.08
Std Dev	11.42

Turn on : 28.9000

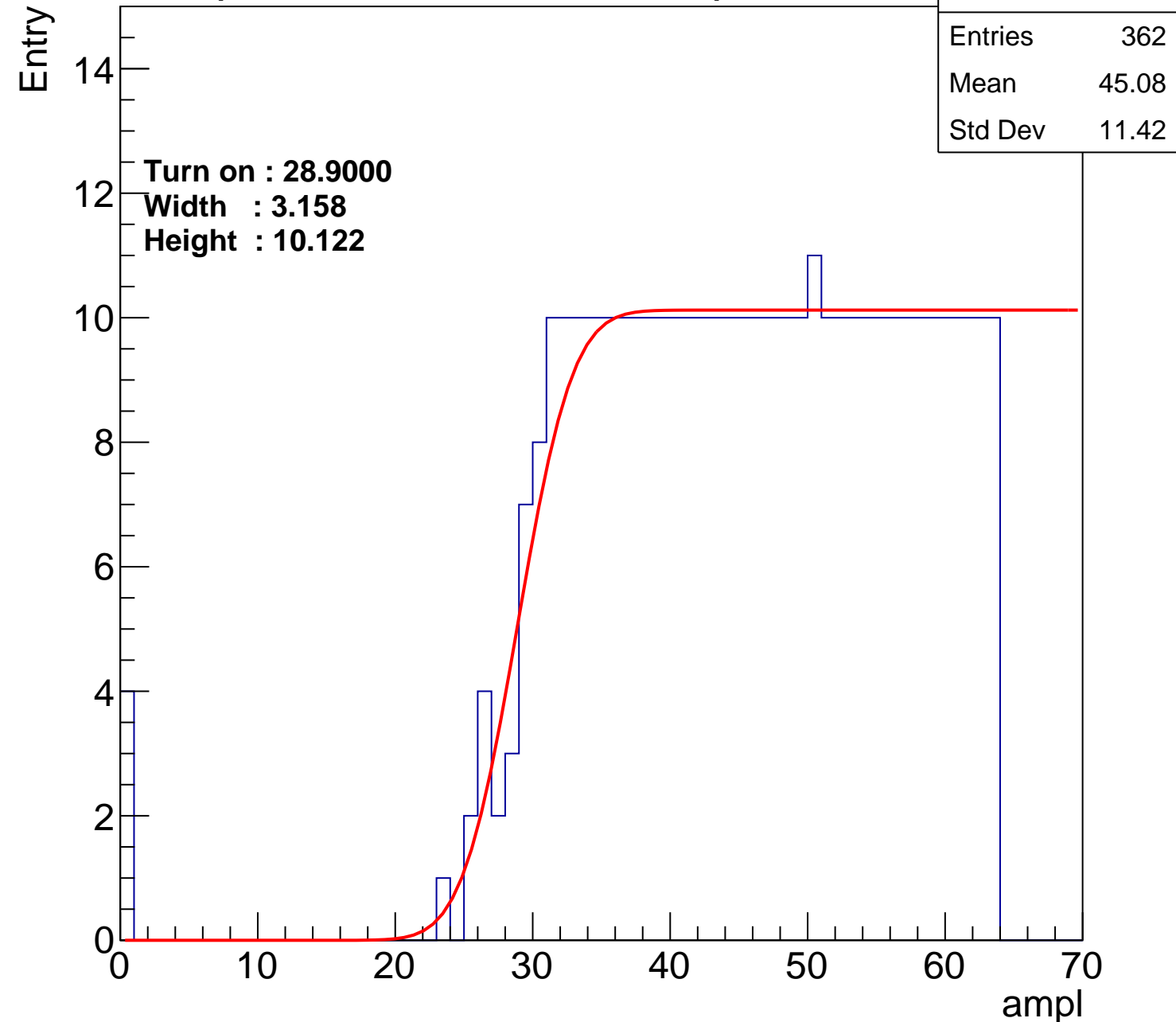
Width : 3.158

Height : 10.122

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch88

calib_packv5_042523_0143.root, FC#13, port D2

Entries	368
Mean	44.62
Std Dev	11.82

Turn on : 28.0514

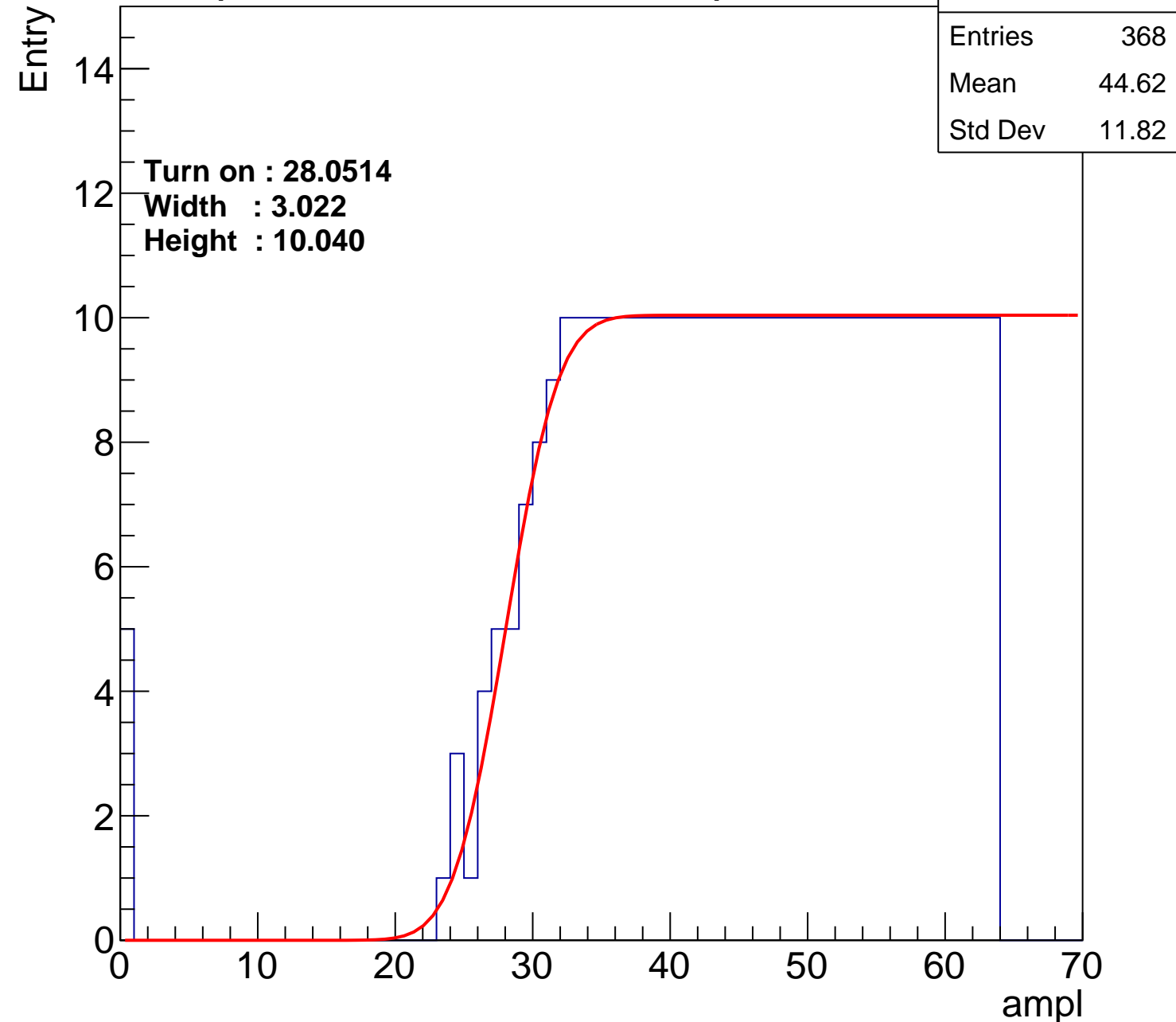
Width : 3.022

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch89

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.99
Std Dev	11.35

Turn on : 28.4012

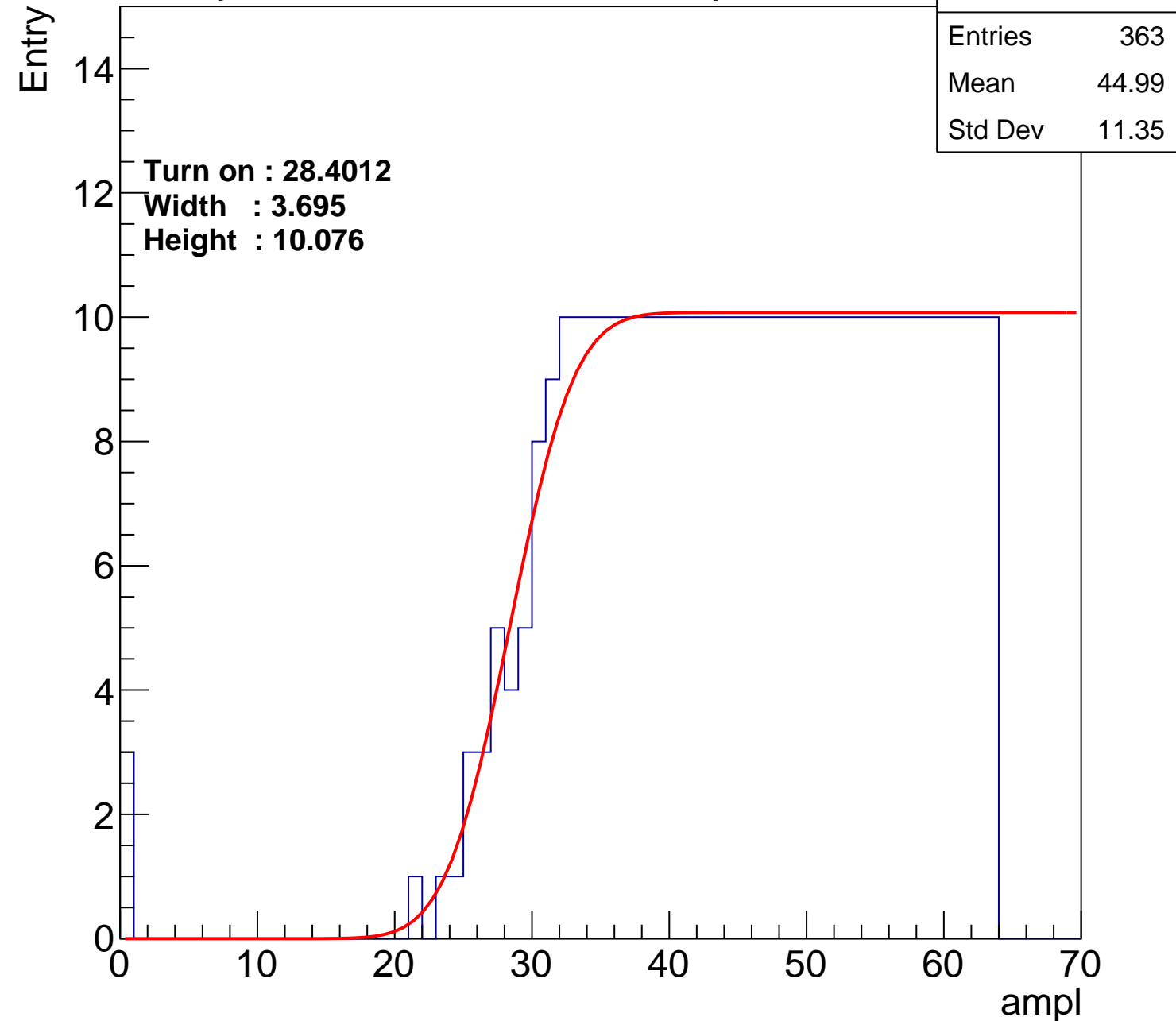
Width : 3.695

Height : 10.076

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch90

calib_packv5_042523_0143.root, FC#13, port D2

Entries	356
Mean	45.37
Std Dev	11.12

Turn on : 28.9224

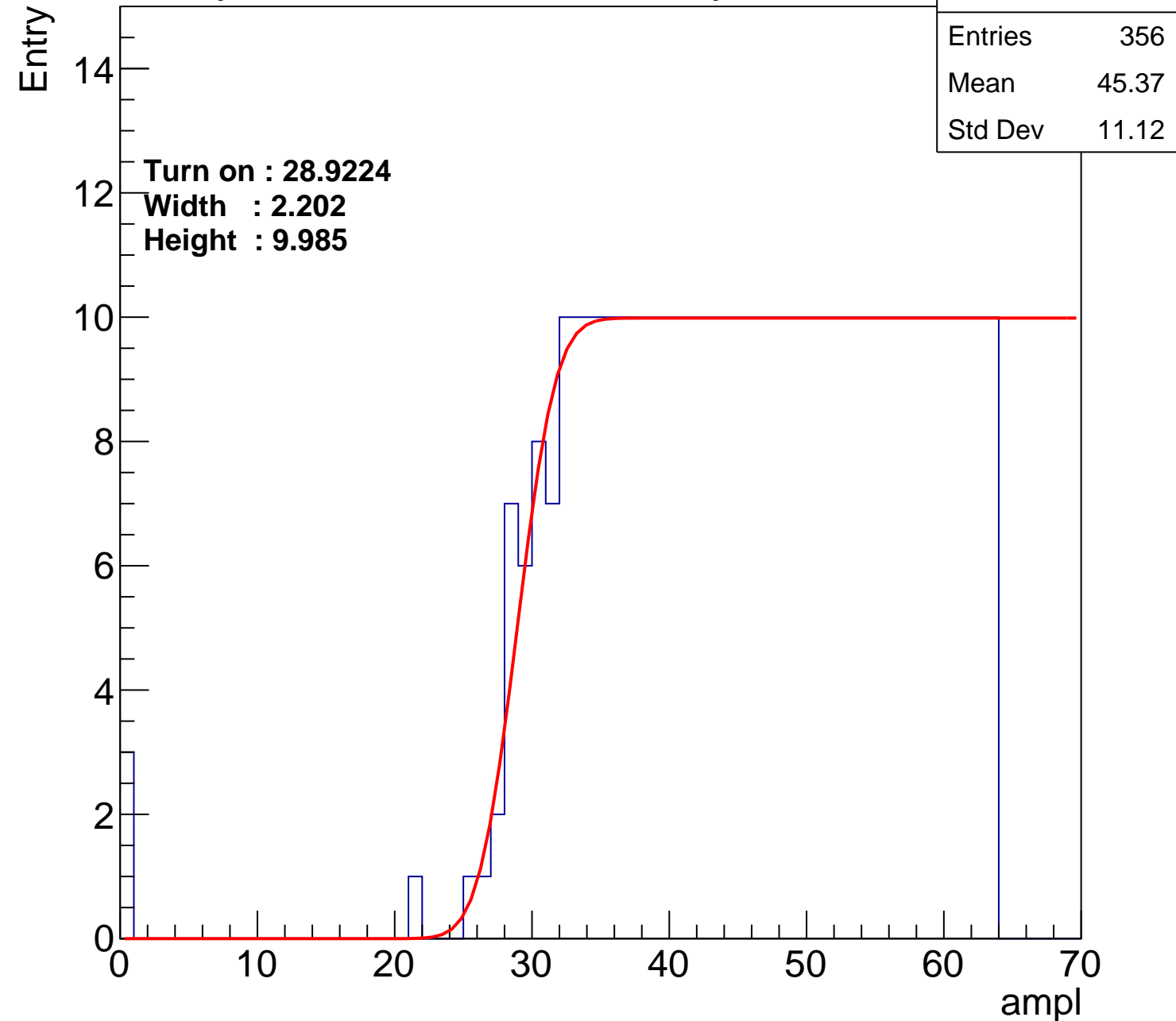
Width : 2.202

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch91

calib_packv5_042523_0143.root, FC#13, port D2

Entries	377
Mean	44.26
Std Dev	11.83

Turn on : 26.9928

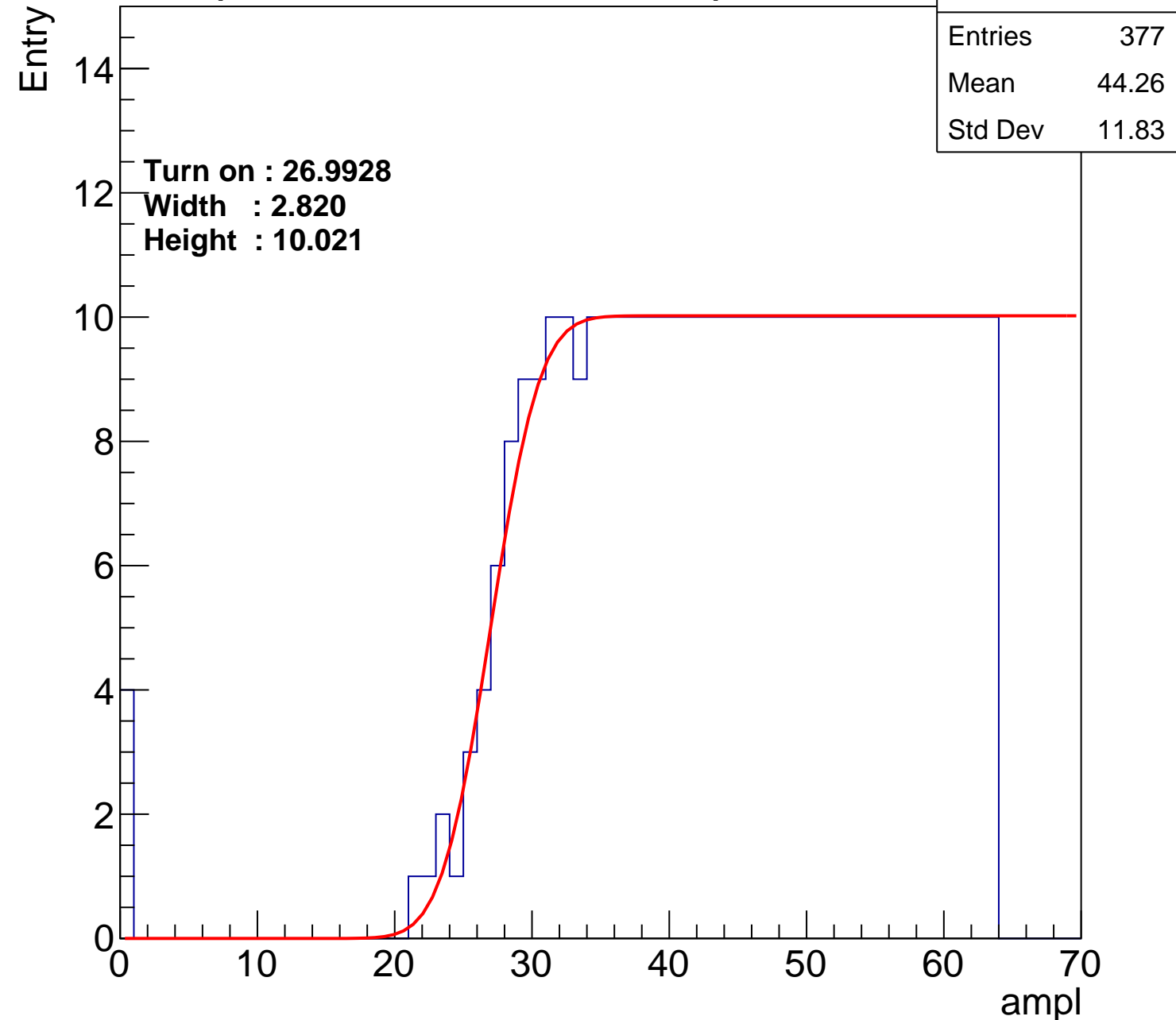
Width : 2.820

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch92

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.47
Std Dev	11.58

Turn on : 27.0984

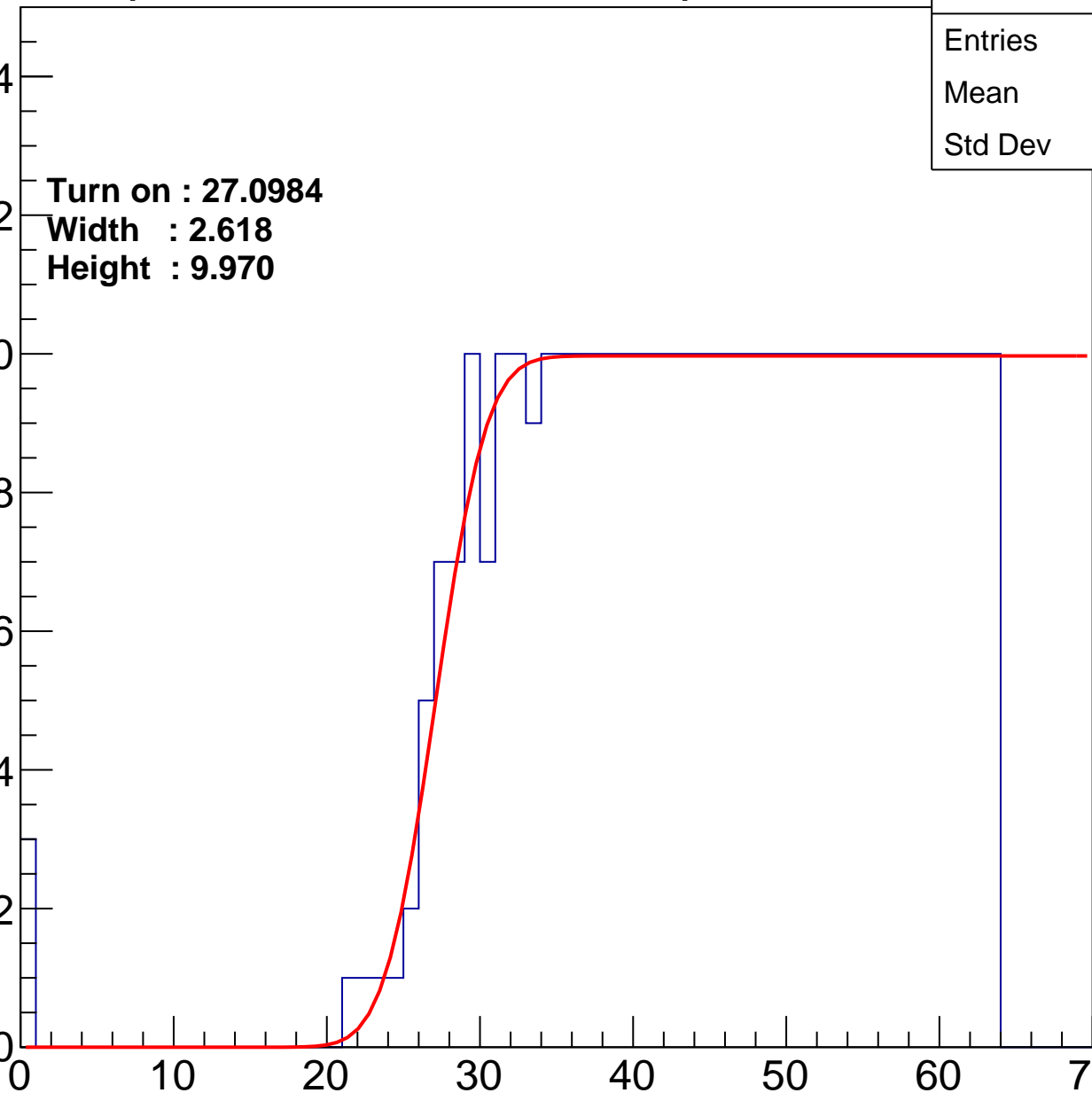
Width : 2.618

Height : 9.970

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch93

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.36
Std Dev	11.91

Turn on : 26.6618

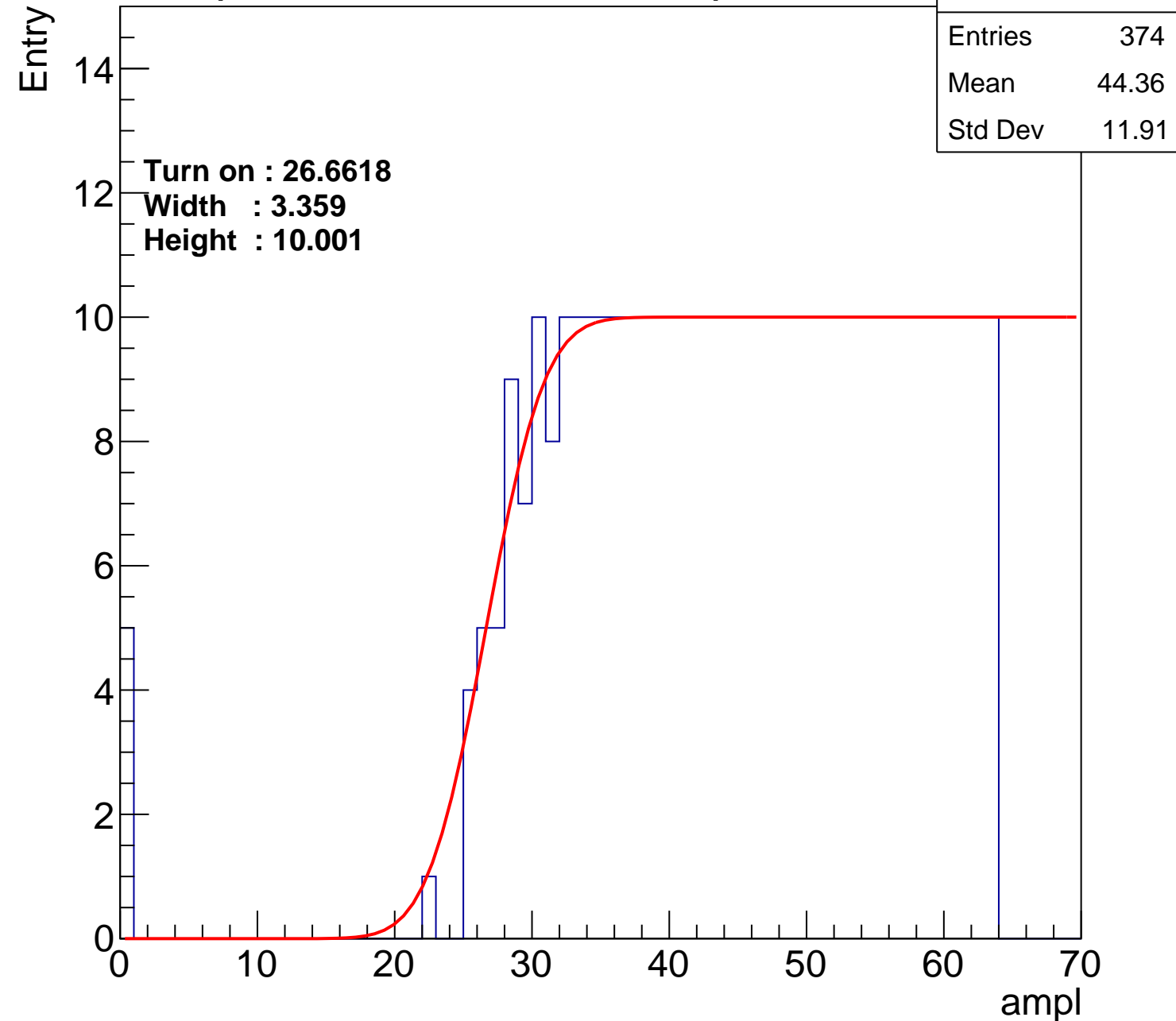
Width : 3.359

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch94

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.45
Std Dev	11.38

Turn on : 26.5714

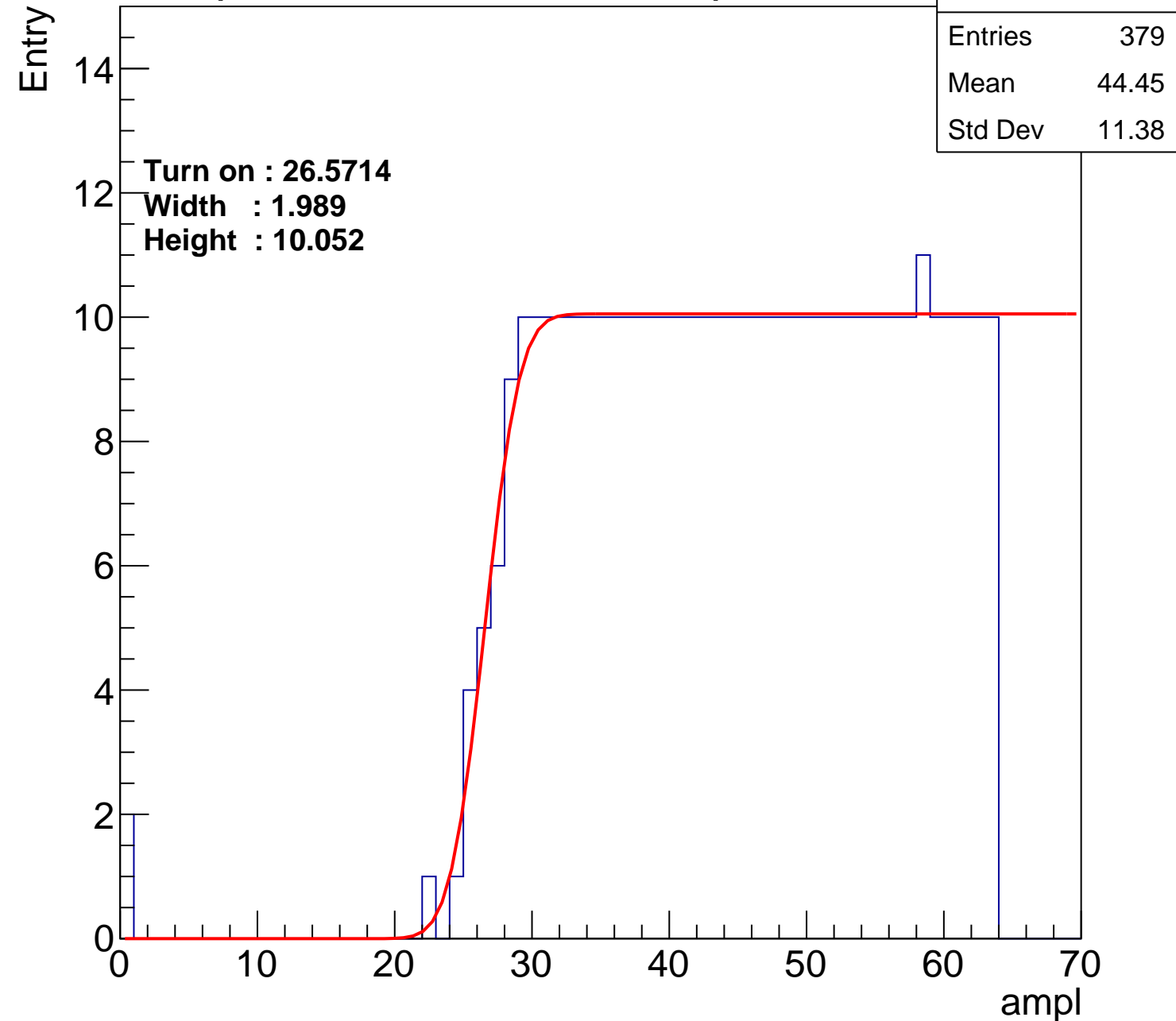
Width : 1.989

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch95

calib_packv5_042523_0143.root, FC#13, port D2

Entries	364
Mean	44.95
Std Dev	11.36

Turn on : 27.9354

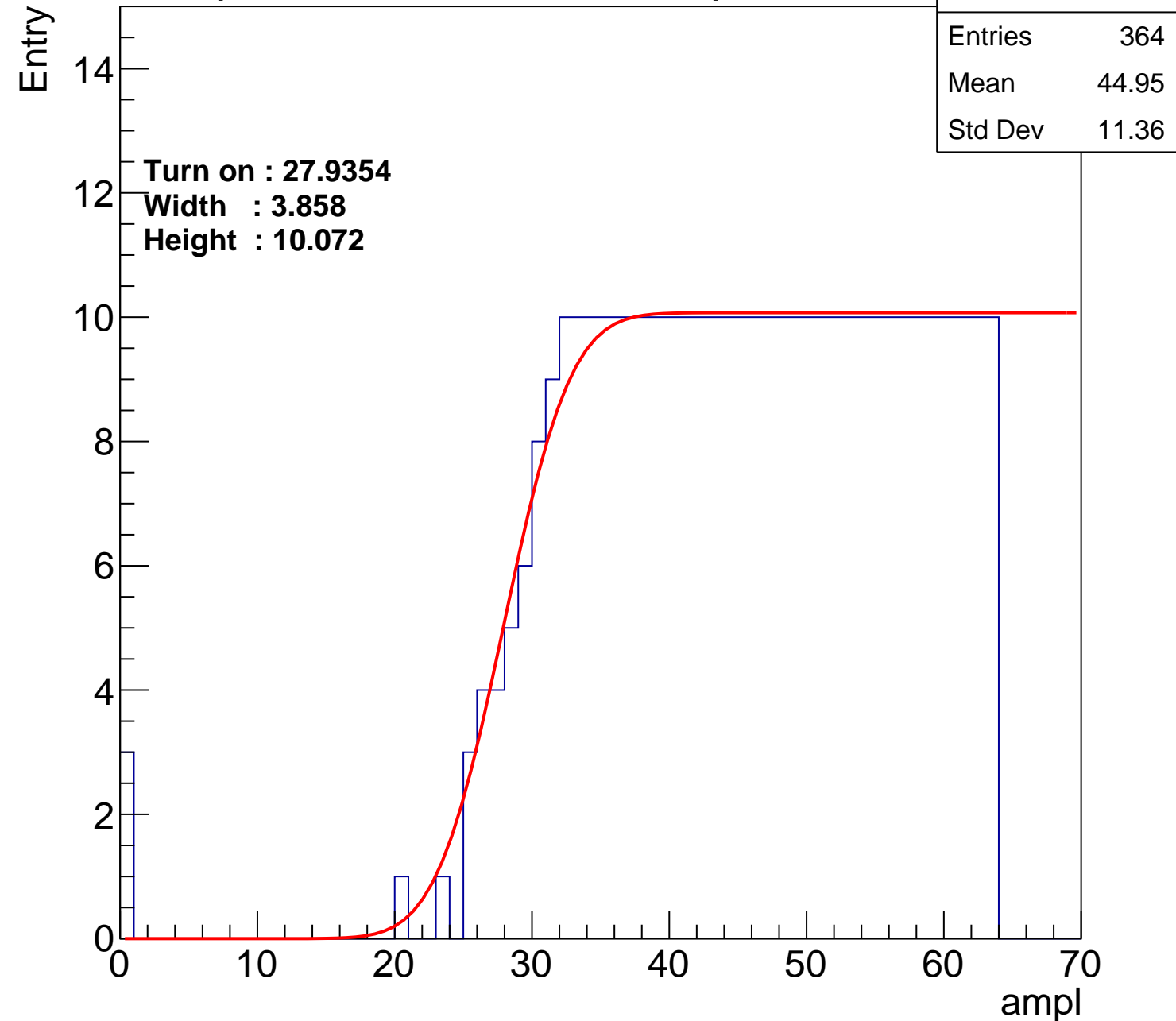
Width : 3.858

Height : 10.072

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch96

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.28
Std Dev	11.55

Turn on : 26.5262

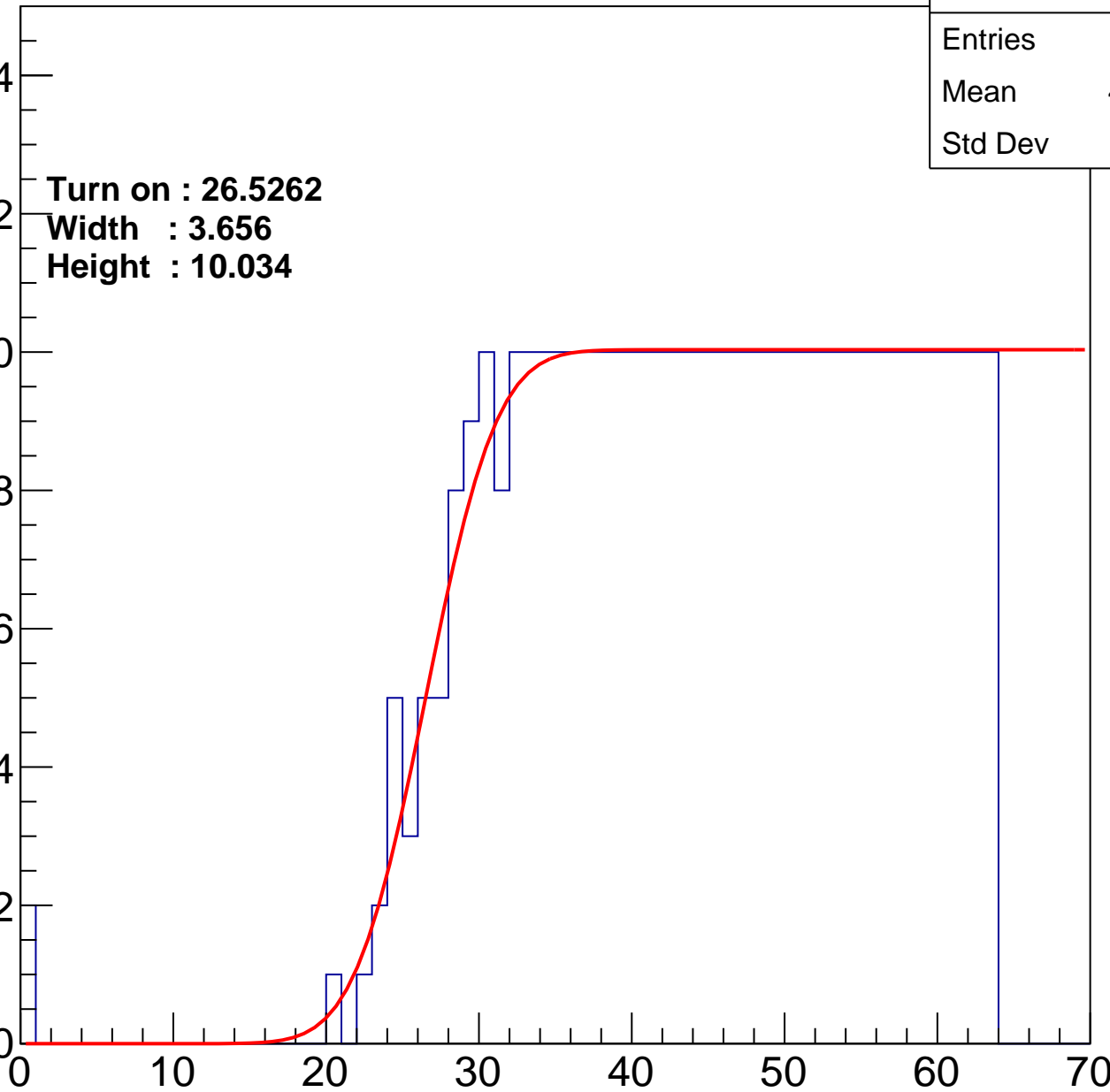
Width : 3.656

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch97

calib_packv5_042523_0143.root, FC#13, port D2

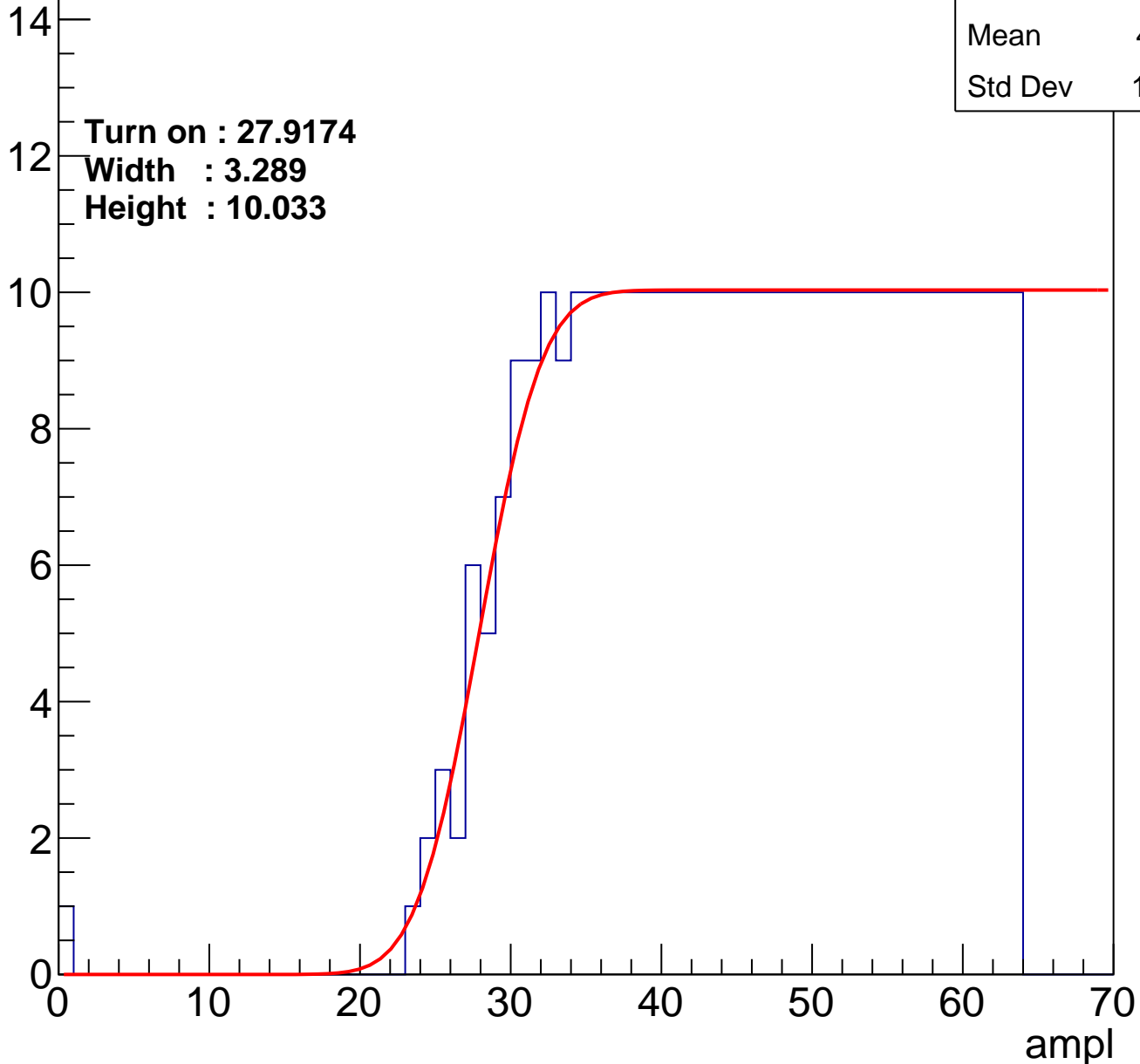
Entries	364
Mean	45.11
Std Dev	10.92

Turn on : 27.9174

Width : 3.289

Height : 10.033

Entry



B1L003S, U21-ch98

calib_packv5_042523_0143.root, FC#13, port D2

Entries	374
Mean	44.63
Std Dev	11.17

Turn on : 26.9742

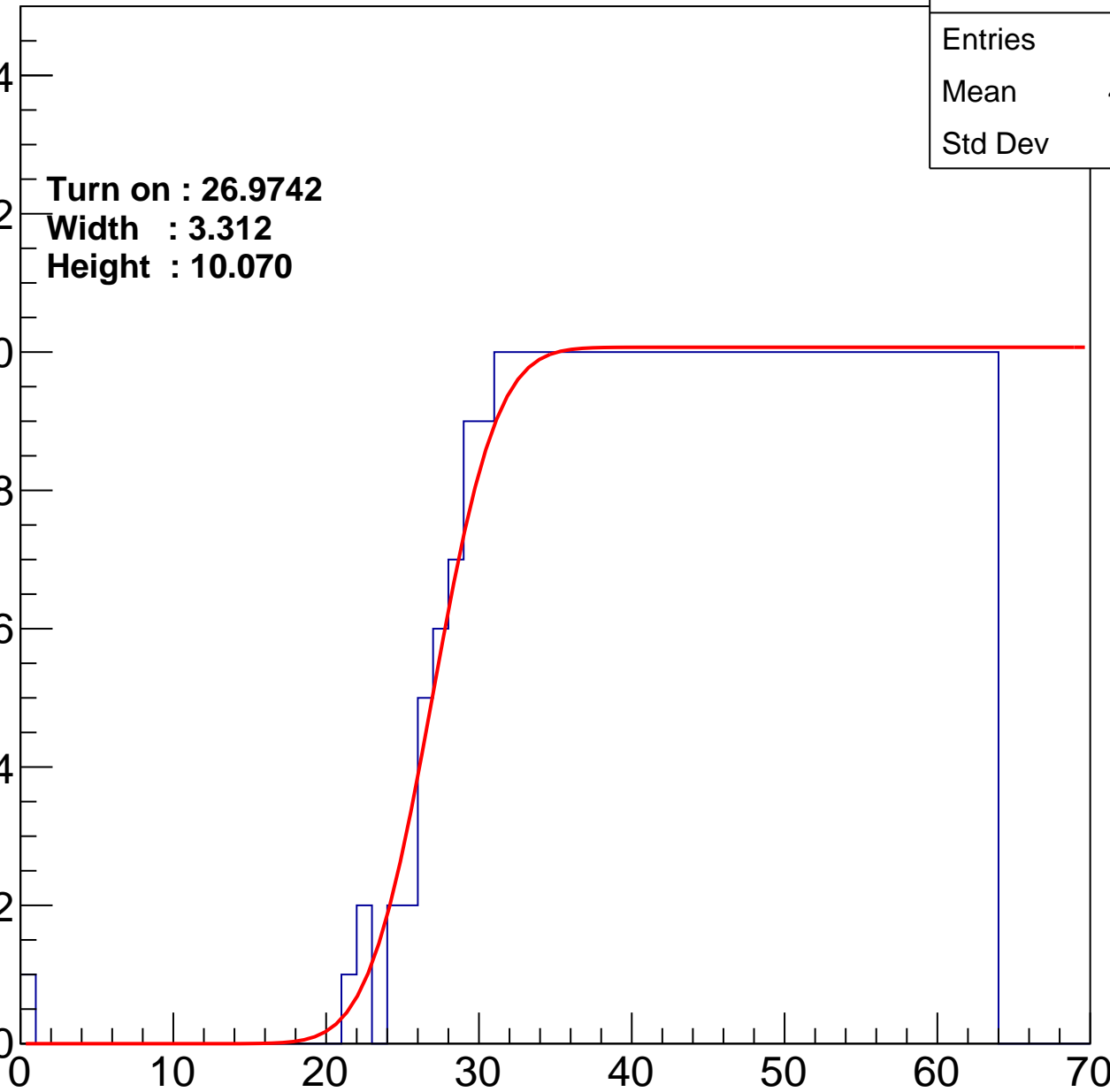
Width : 3.312

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch99

calib_packv5_042523_0143.root, FC#13, port D2

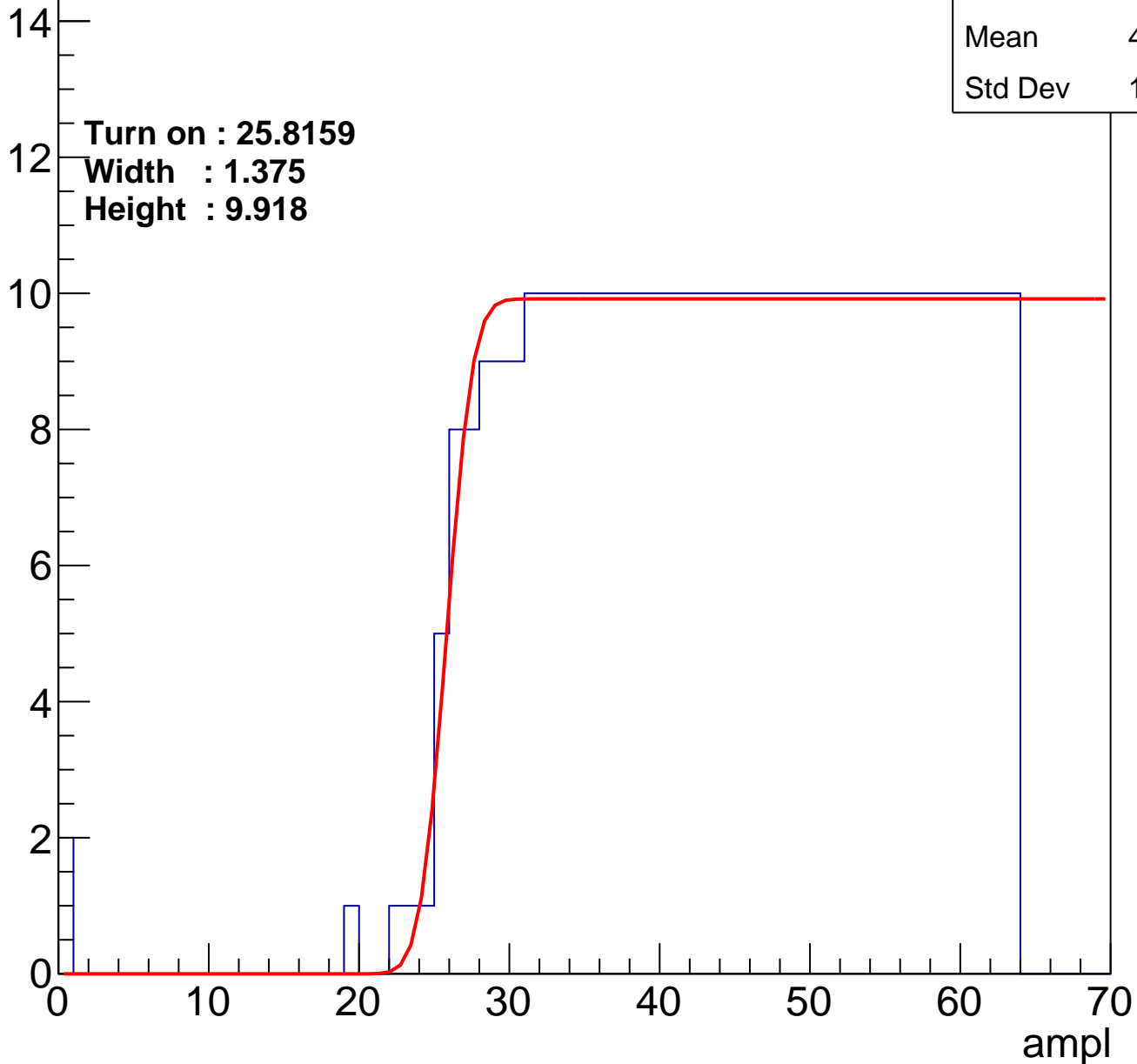
Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.8159

Width : 1.375

Height : 9.918

Entry



B1L003S, U21-ch100

calib_packv5_042523_0143.root, FC#13, port D2

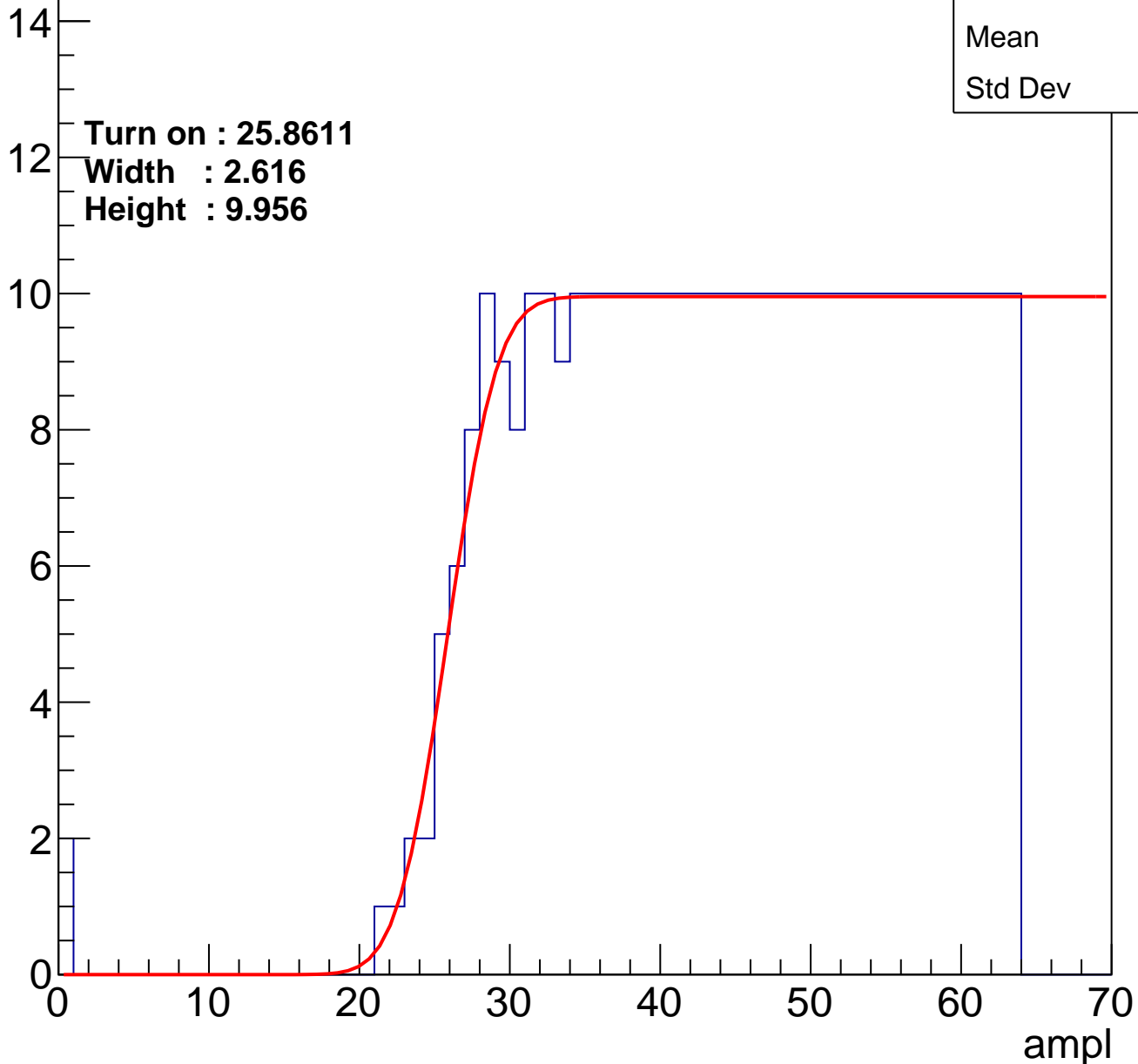
Entries	383
Mean	44.1
Std Dev	11.6

Turn on : 25.8611

Width : 2.616

Height : 9.956

Entry



B1L003S, U21-ch101

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.54
Std Dev	11.66

Turn on : 27.6737

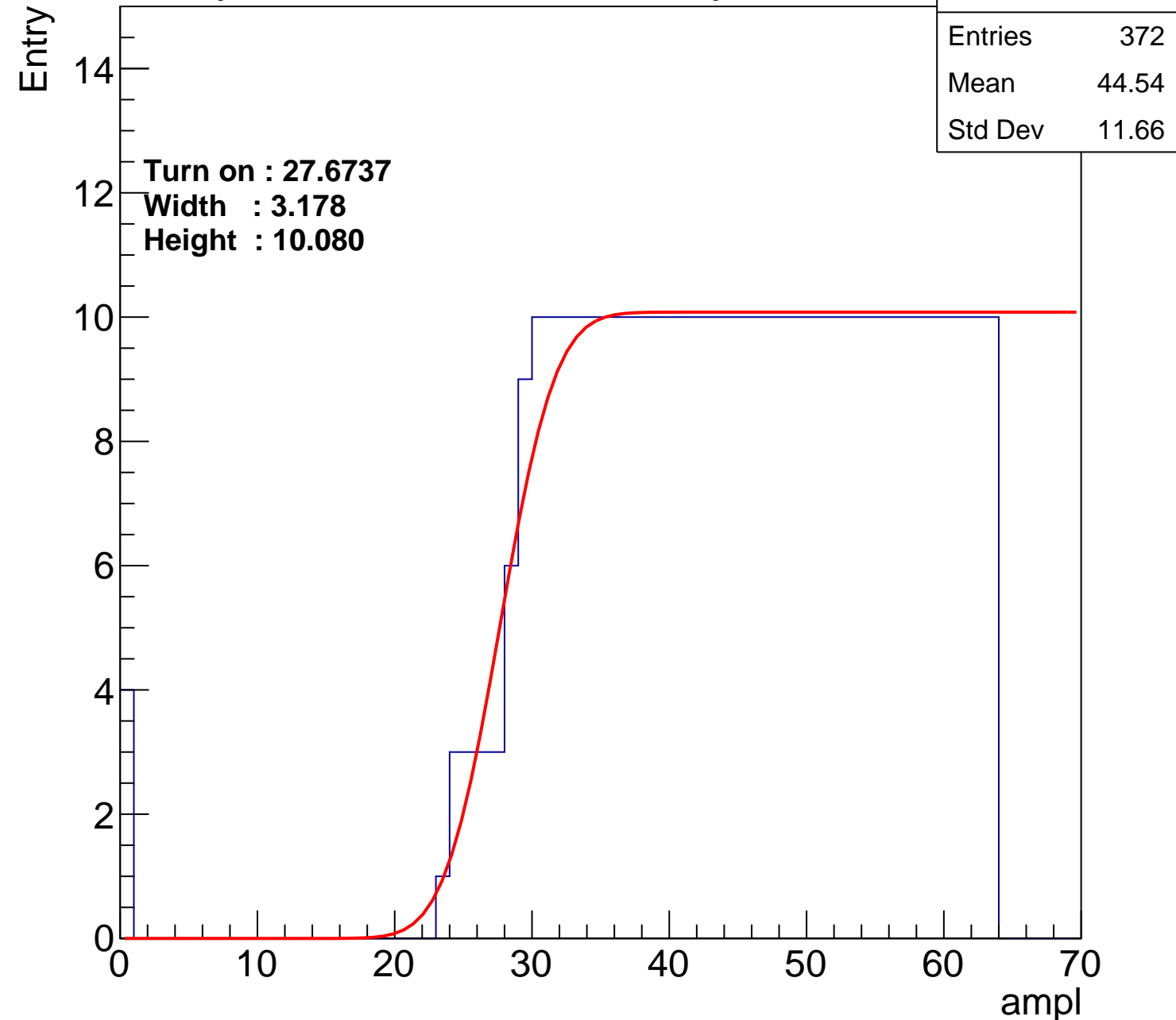
Width : 3.178

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch102

calib_packv5_042523_0143.root, FC#13, port D2

Entries	371
Mean	44.46
Std Dev	11.91

Turn on : 27.7215

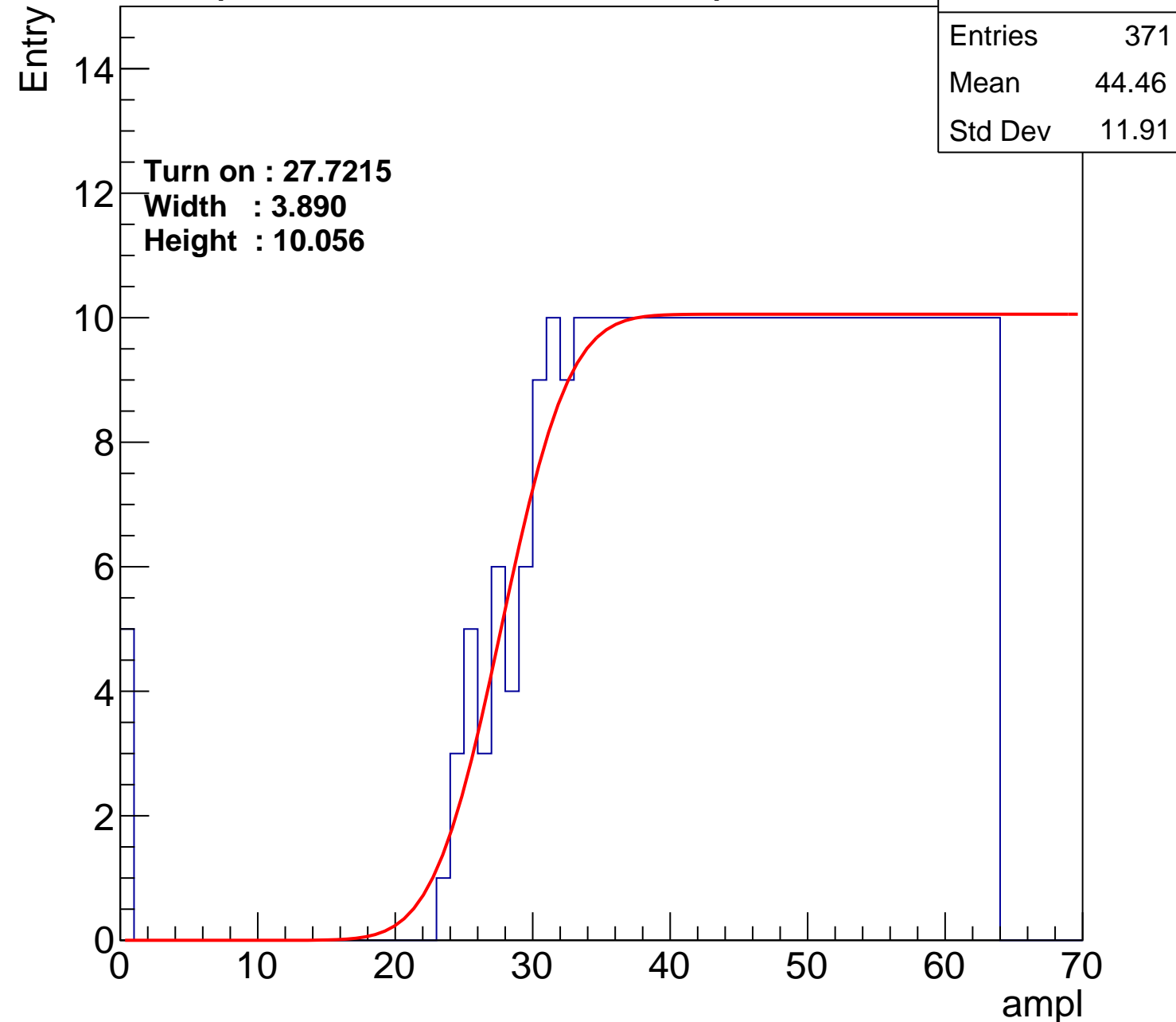
Width : 3.890

Height : 10.056

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch103

calib_packv5_042523_0143.root, FC#13, port D2

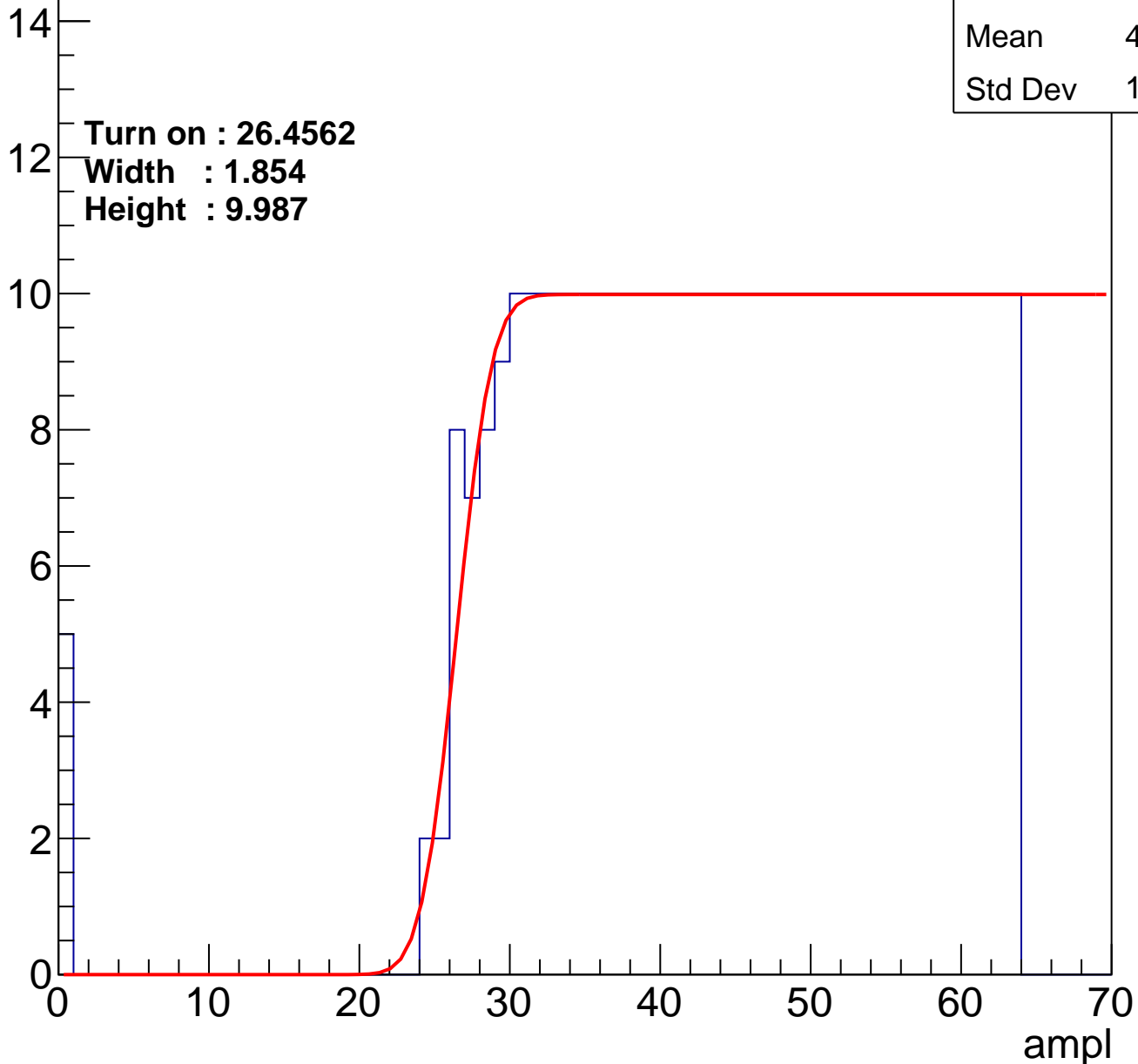
Entries	381
Mean	44.07
Std Dev	11.99

Turn on : 26.4562

Width : 1.854

Height : 9.987

Entry



B1L003S, U21-ch104

calib_packv5_042523_0143.root, FC#13, port D2

Entries	384
Mean	44.07
Std Dev	11.6

Turn on : 25.6675

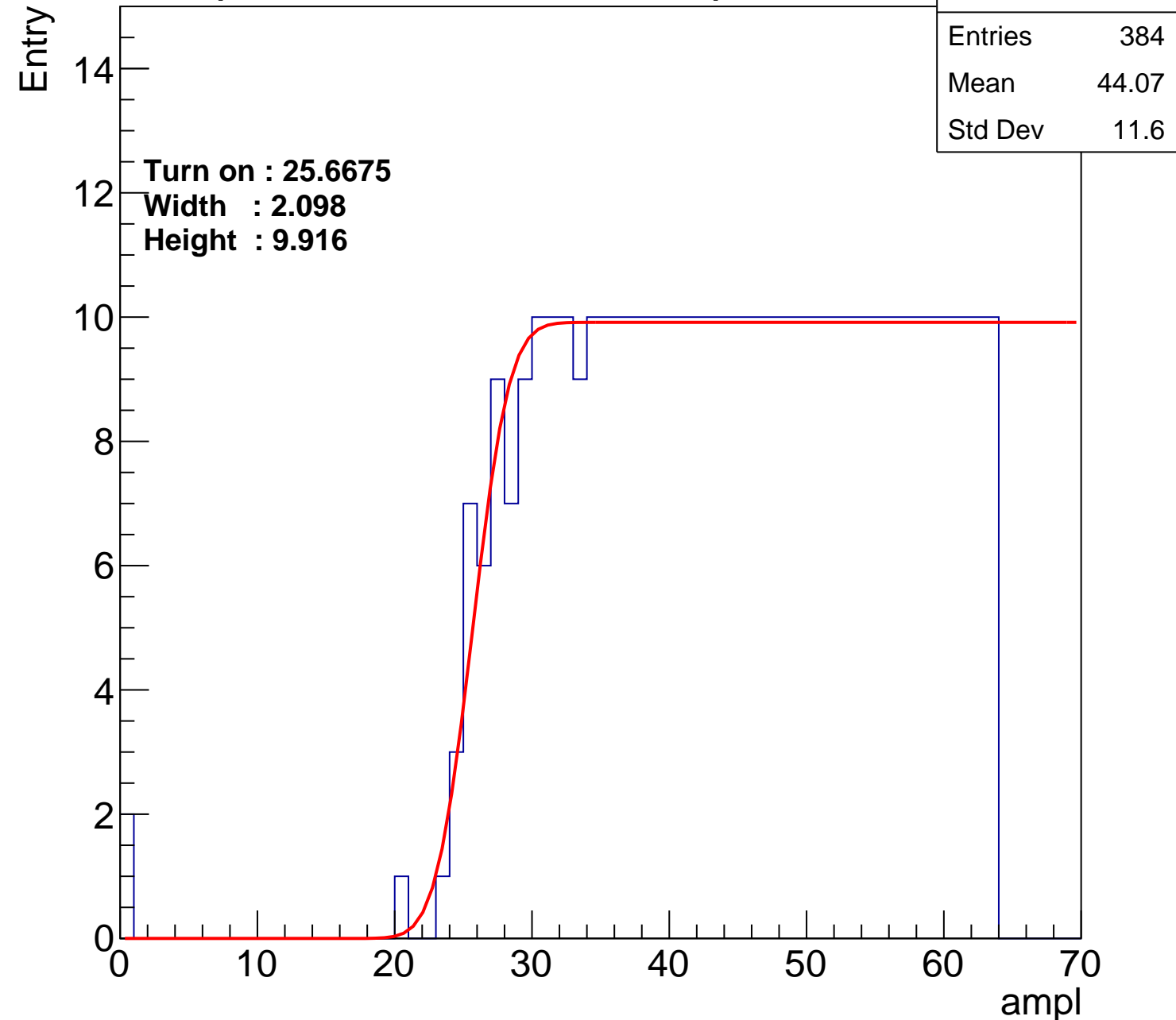
Width : 2.098

Height : 9.916

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch105

calib_packv5_042523_0143.root, FC#13, port D2

Entries	385
Mean	43.95
Std Dev	11.82

Turn on : 26.5245

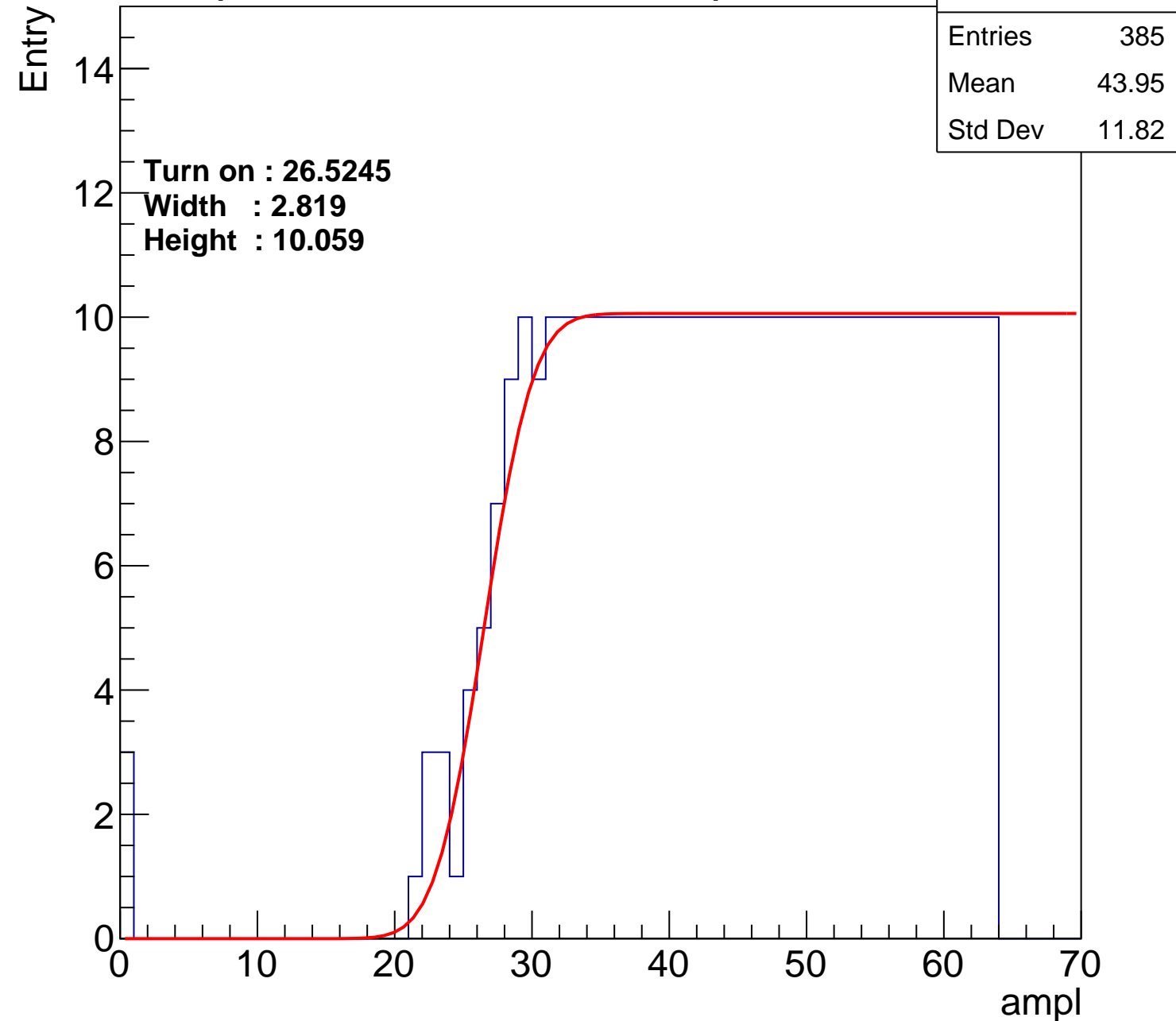
Width : 2.819

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch106

calib_packv5_042523_0143.root, FC#13, port D2

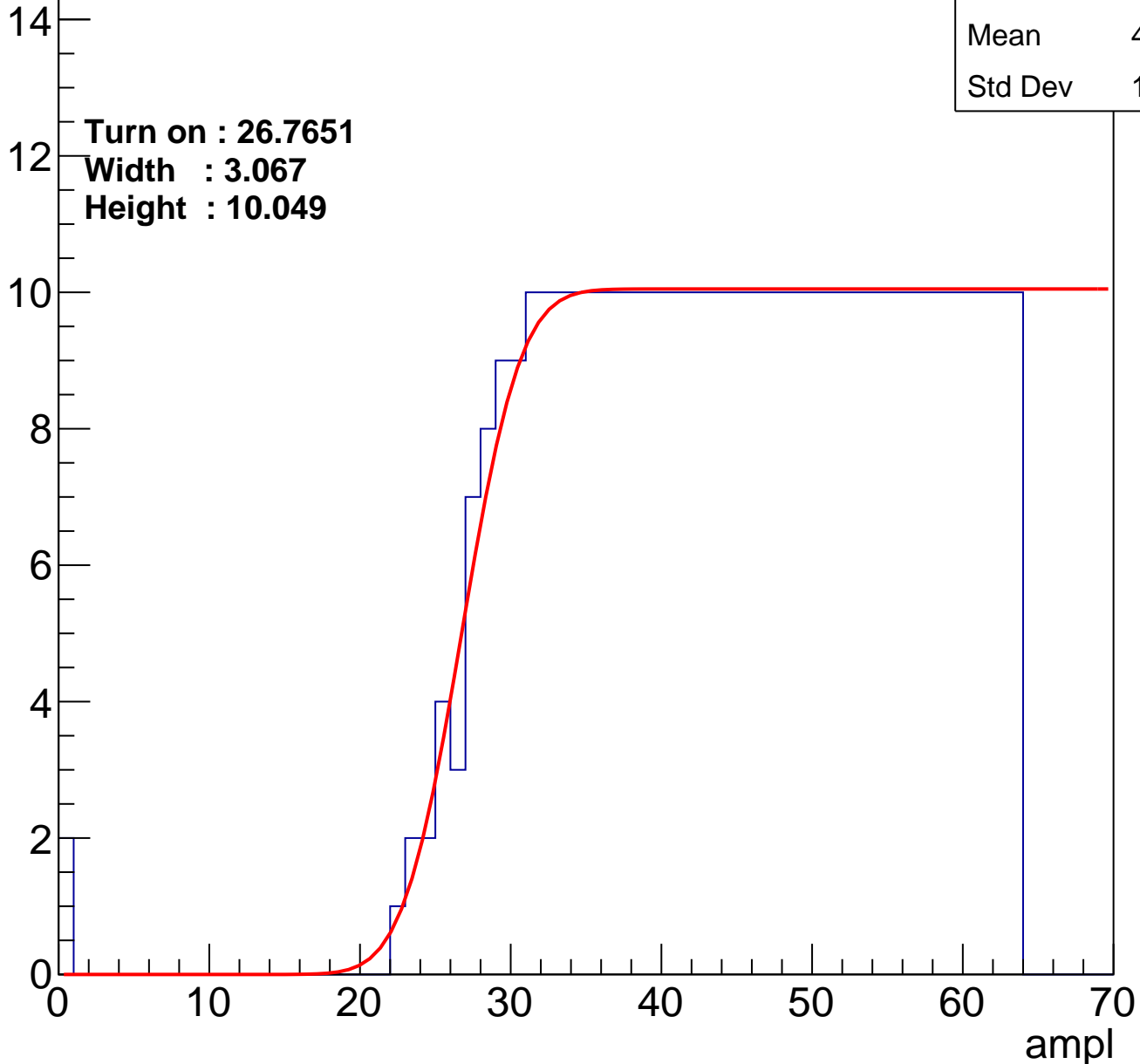
Entries	377
Mean	44.42
Std Dev	11.42

Turn on : 26.7651

Width : 3.067

Height : 10.049

Entry



B1L003S, U21-ch107

calib_packv5_042523_0143.root, FC#13, port D2

Entries	376
Mean	44.32
Std Dev	11.78

Turn on : 26.7586

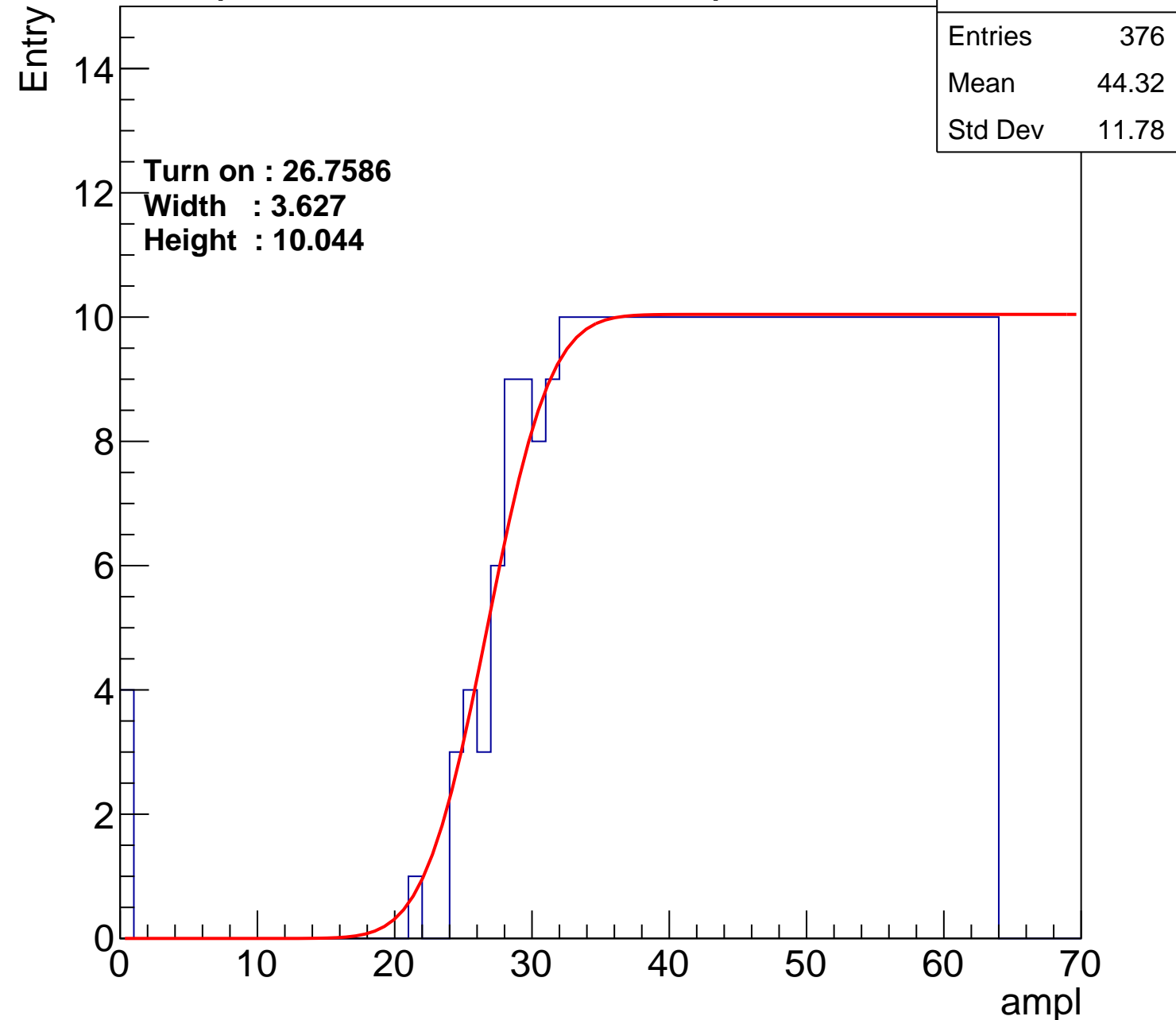
Width : 3.627

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch108

calib_packv5_042523_0143.root, FC#13, port D2

Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 27.4903

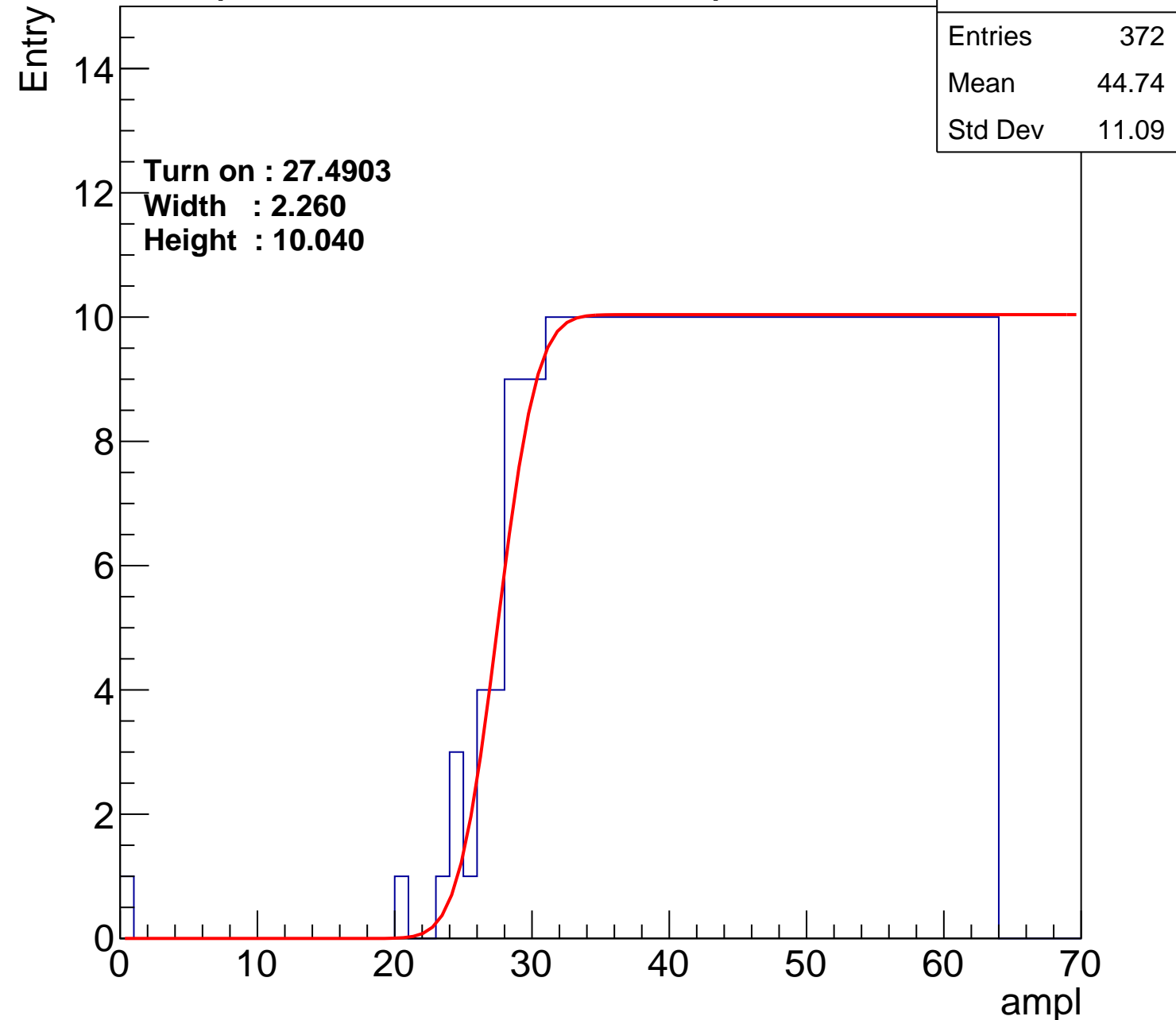
Width : 2.260

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch109

calib_packv5_042523_0143.root, FC#13, port D2

Entries	379
Mean	44.16
Std Dev	11.88

Turn on : 26.6278

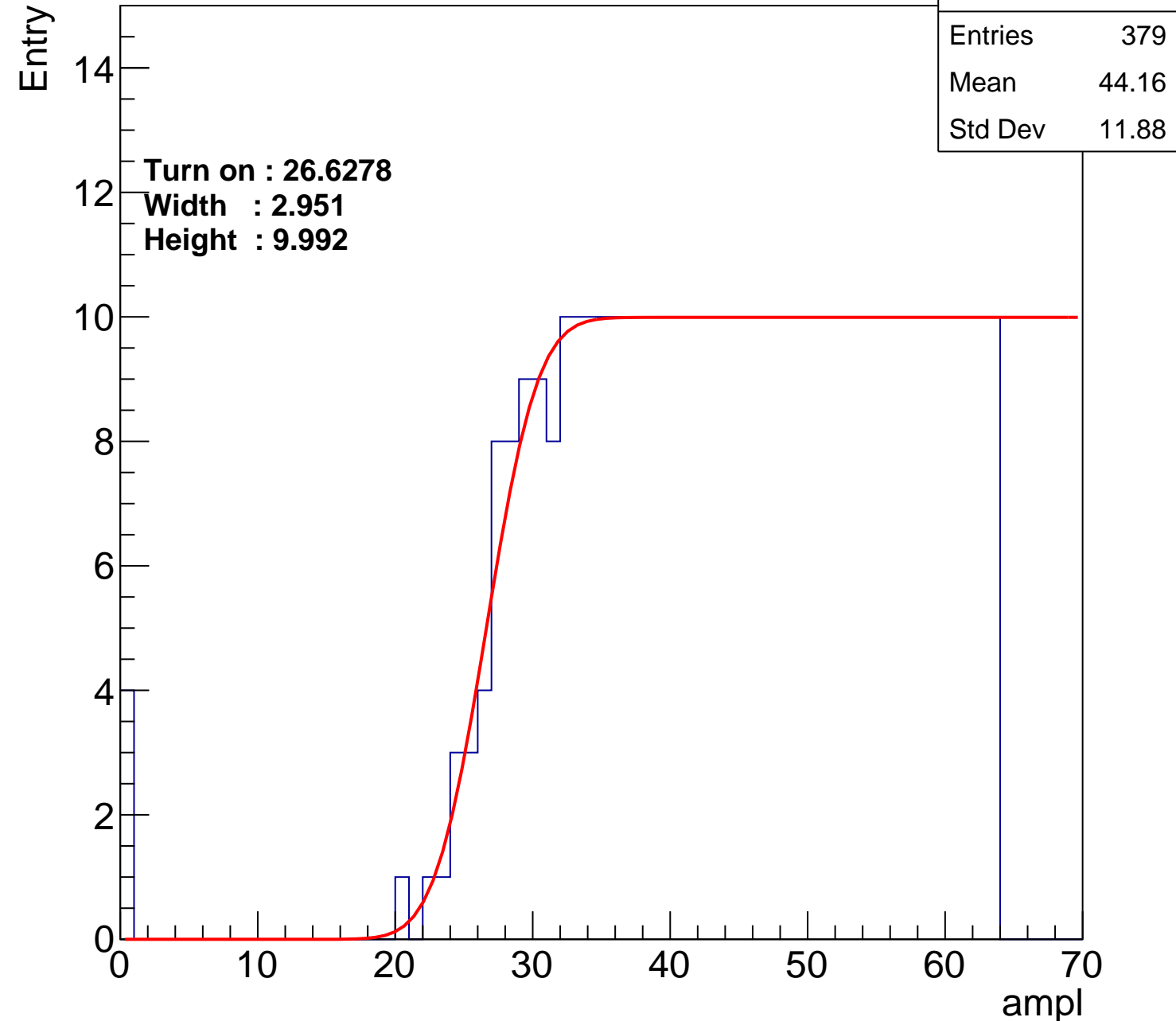
Width : 2.951

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch110

calib_packv5_042523_0143.root, FC#13, port D2

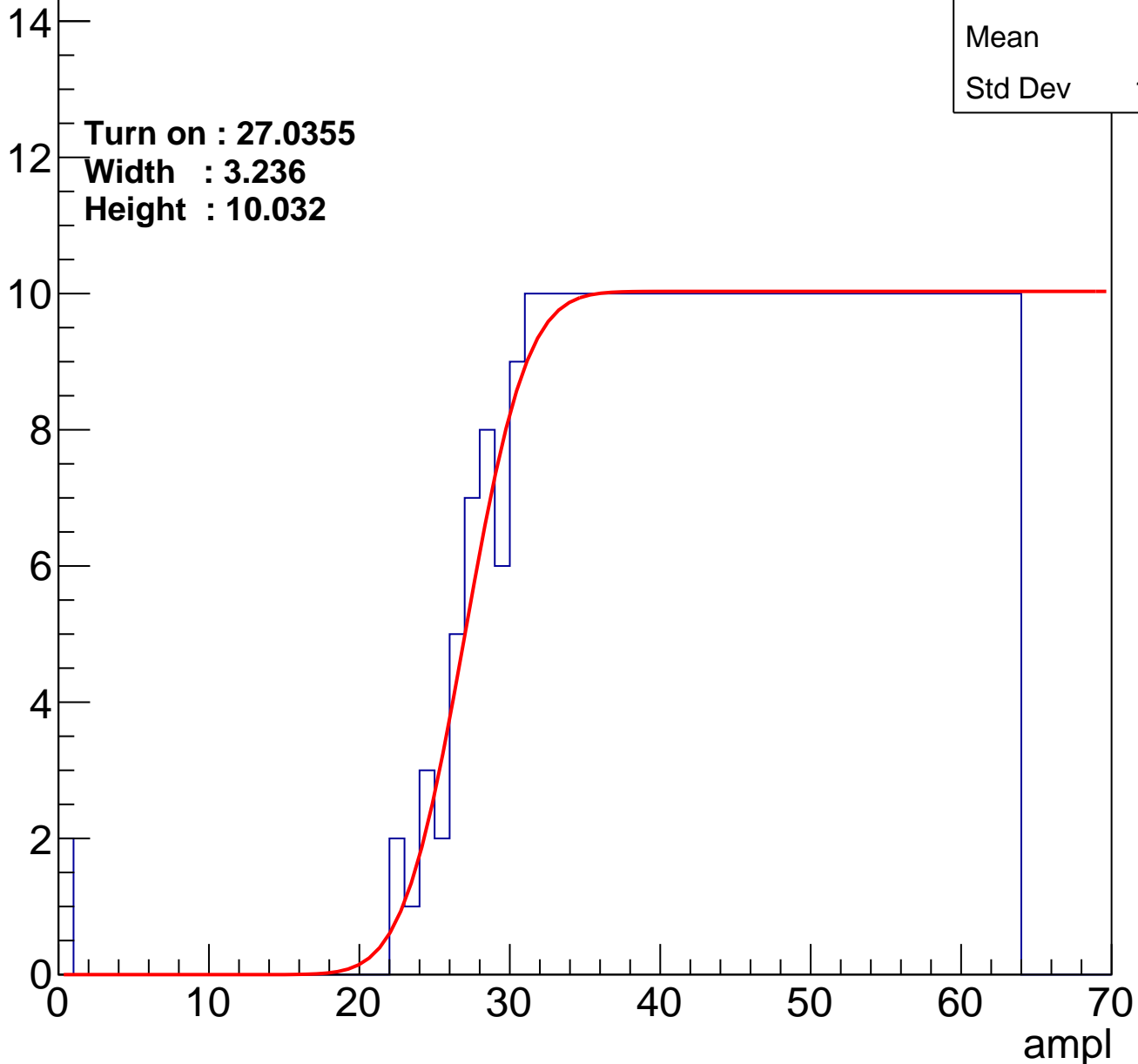
Entries	375
Mean	44.5
Std Dev	11.41

Turn on : 27.0355

Width : 3.236

Height : 10.032

Entry



B1L003S, U21-ch111

calib_packv5_042523_0143.root, FC#13, port D2

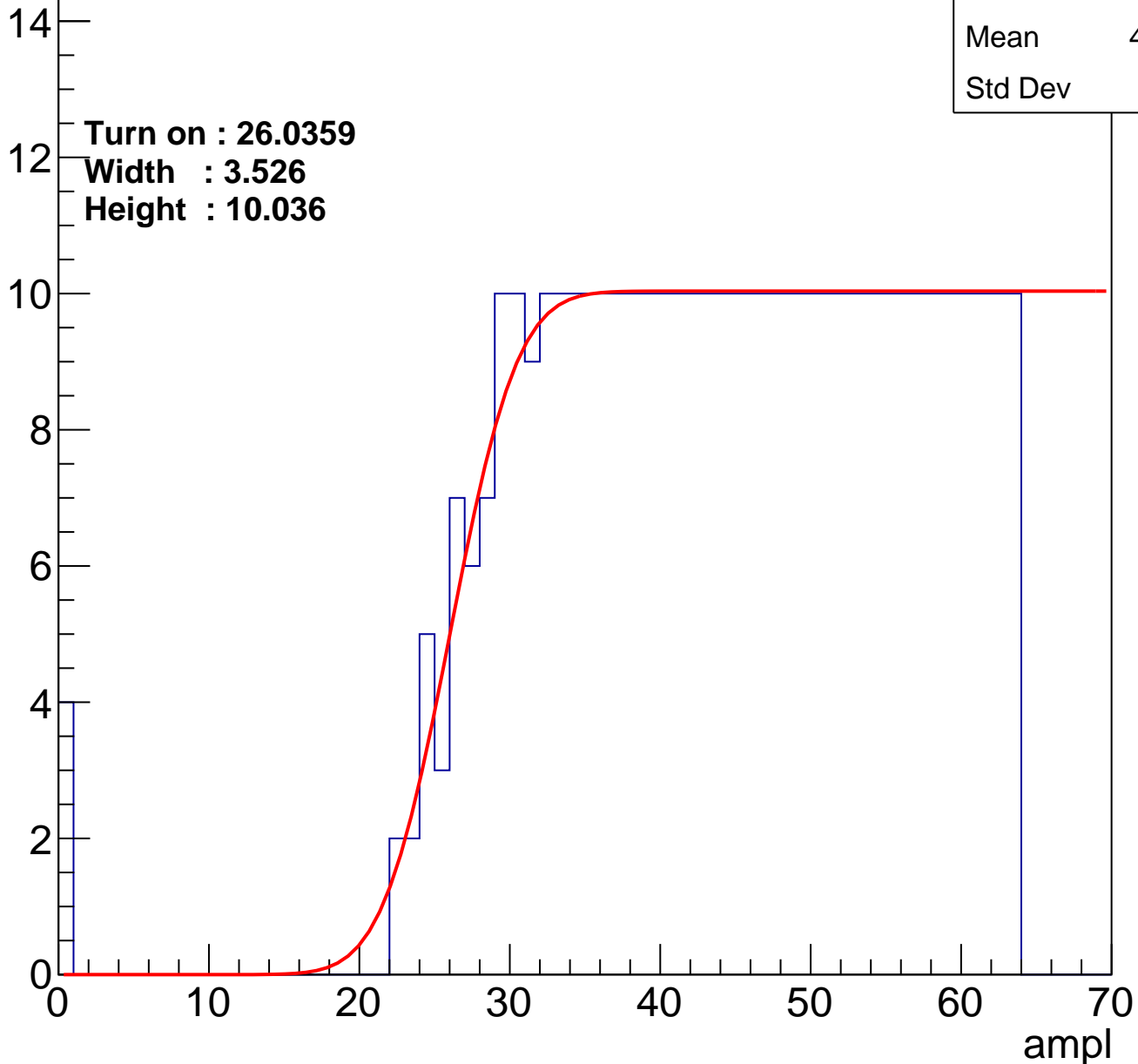
Entries	385
Mean	43.88
Std Dev	12

Turn on : 26.0359

Width : 3.526

Height : 10.036

Entry



B1L003S, U21-ch112

calib_packv5_042523_0143.root, FC#13, port D2

Entries	366
Mean	45.04
Std Dev	10.93

Turn on : 27.1765

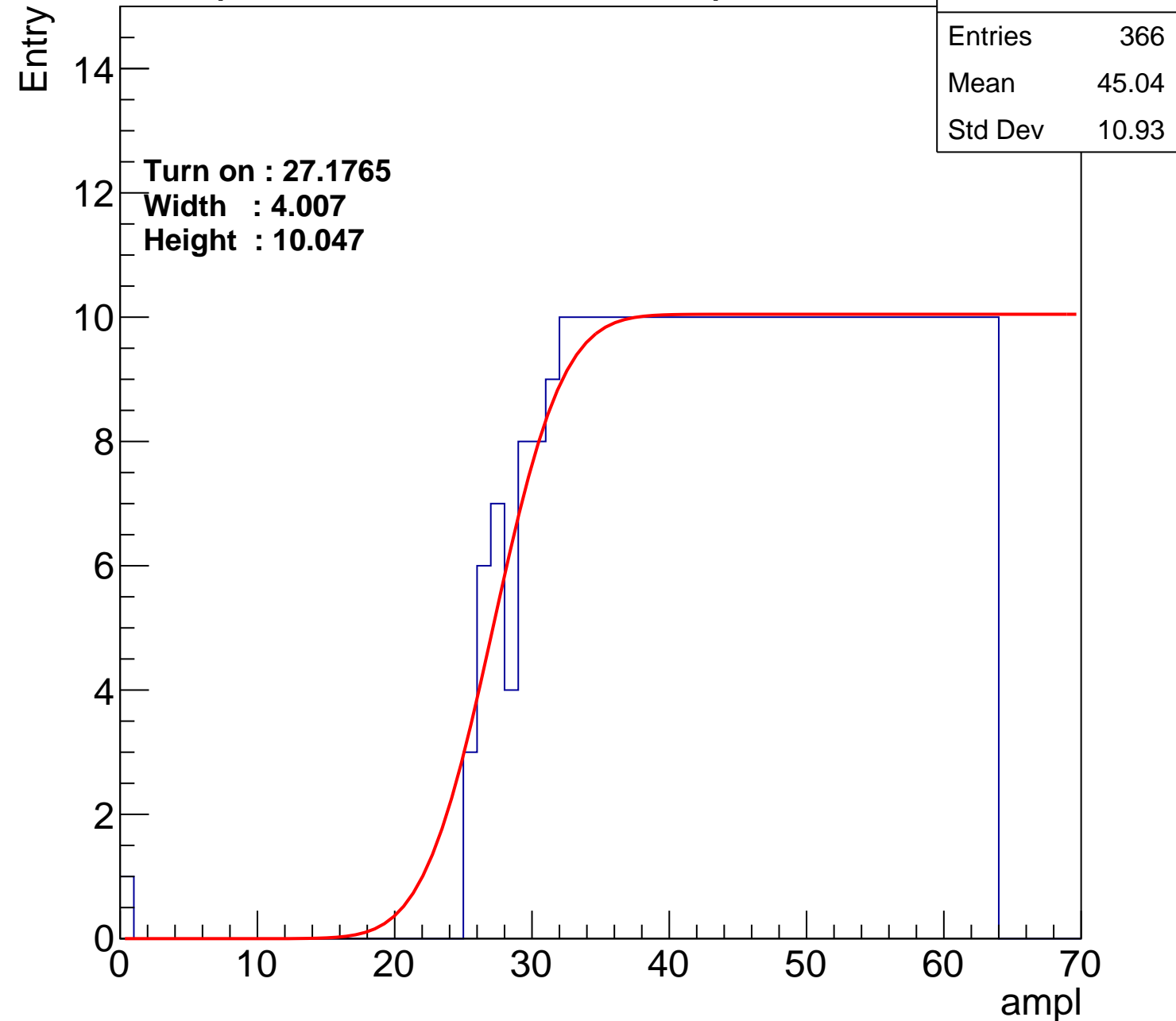
Width : 4.007

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch113

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.46
Std Dev	11.54

Turn on : 26.7851

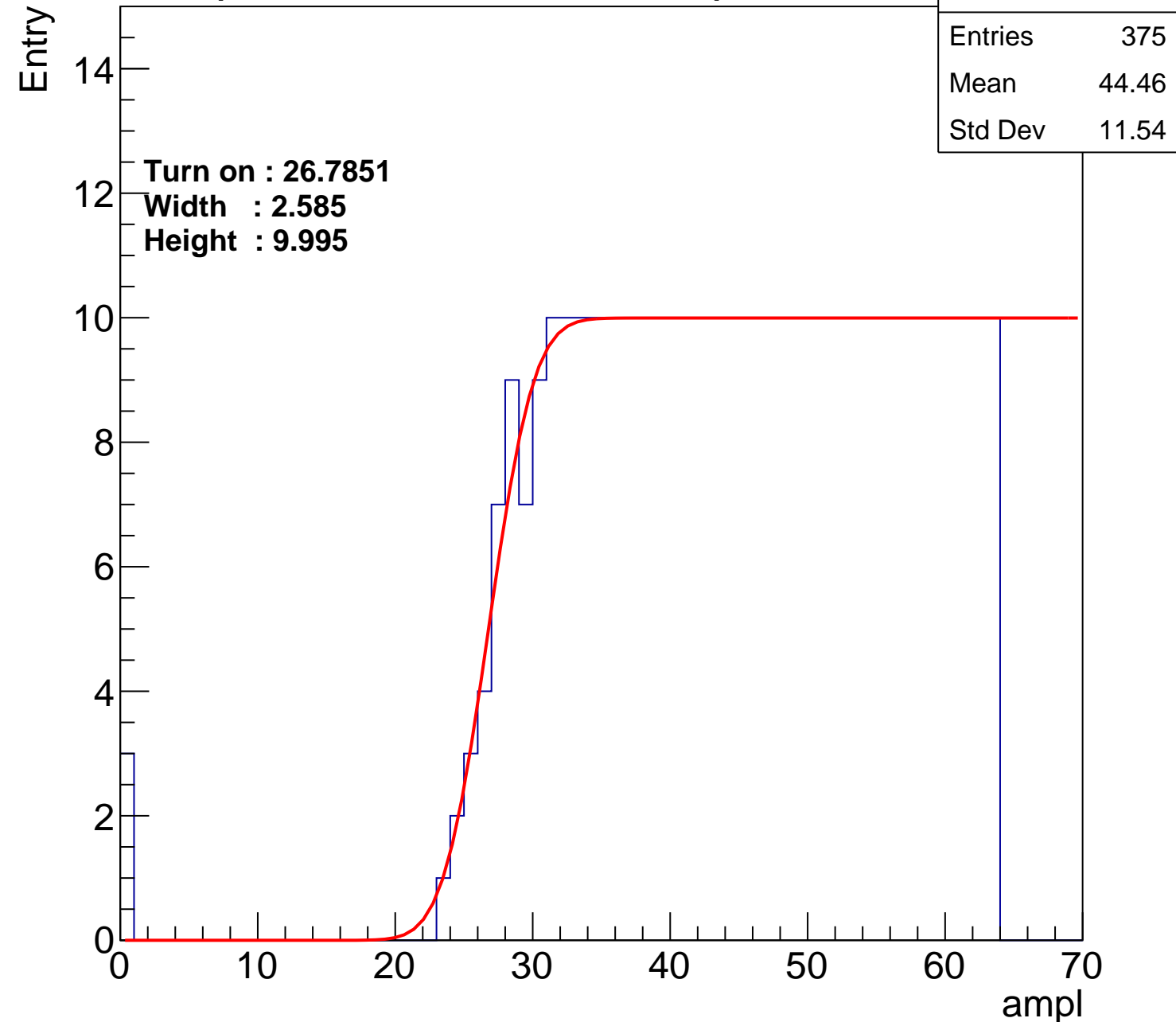
Width : 2.585

Height : 9.995

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch114

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.2119

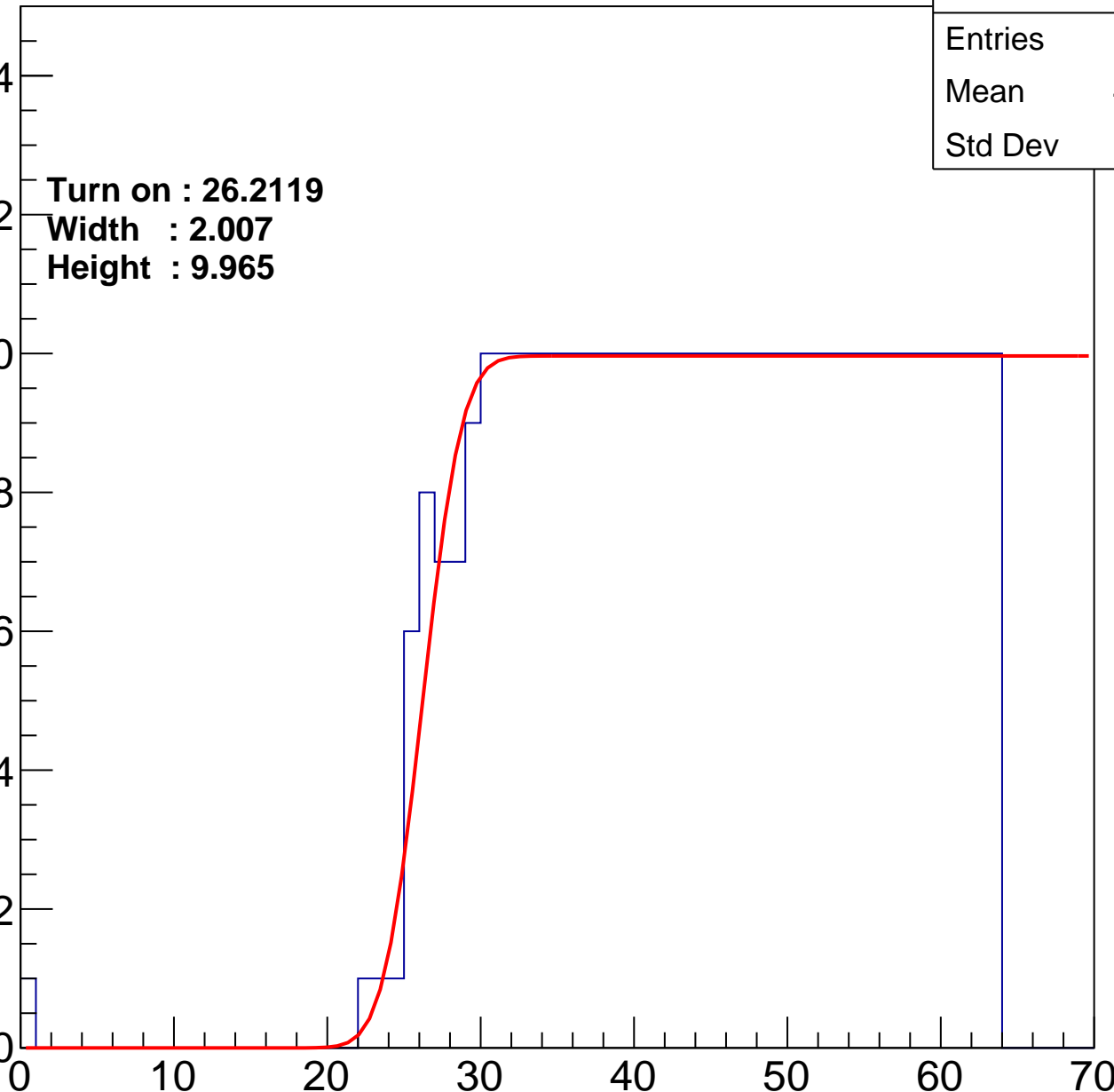
Width : 2.007

Height : 9.965

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch115

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.09
Std Dev	11.88

Turn on : 26.7593

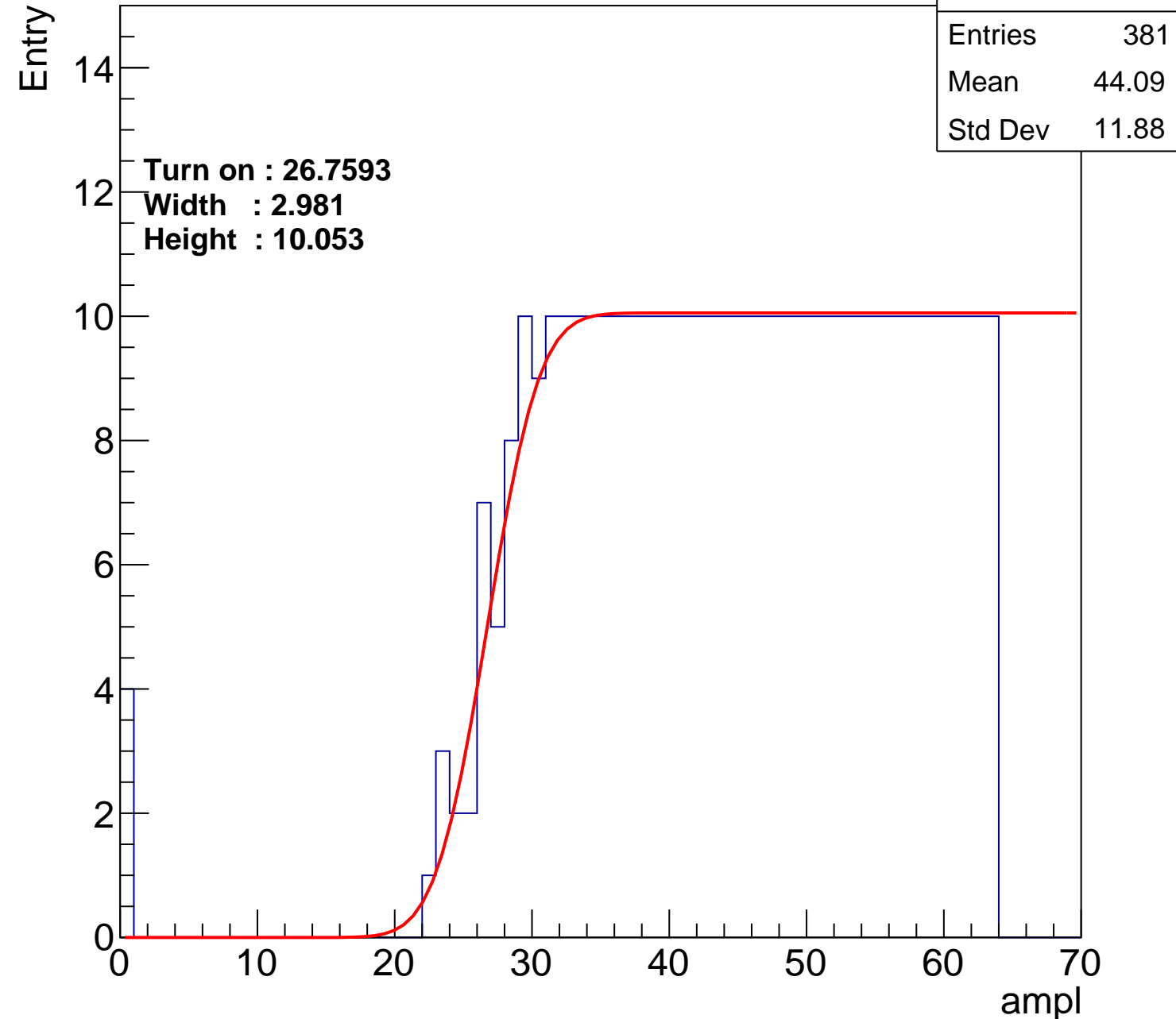
Width : 2.981

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch116

calib_packv5_042523_0143.root, FC#13, port D2

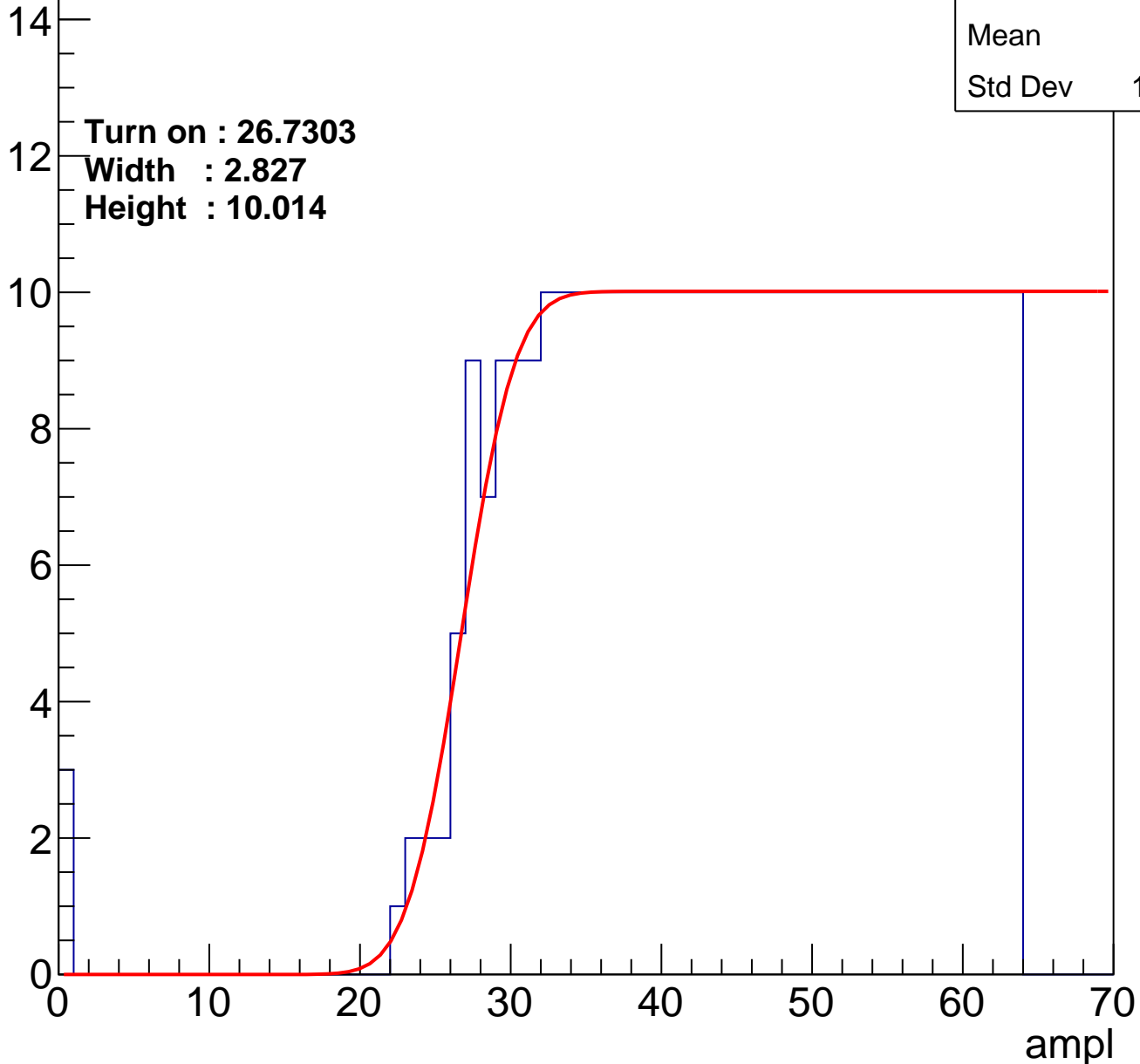
Entries	378
Mean	44.3
Std Dev	11.64

Turn on : 26.7303

Width : 2.827

Height : 10.014

Entry



B1L003S, U21-ch117

calib_packv5_042523_0143.root, FC#13, port D2

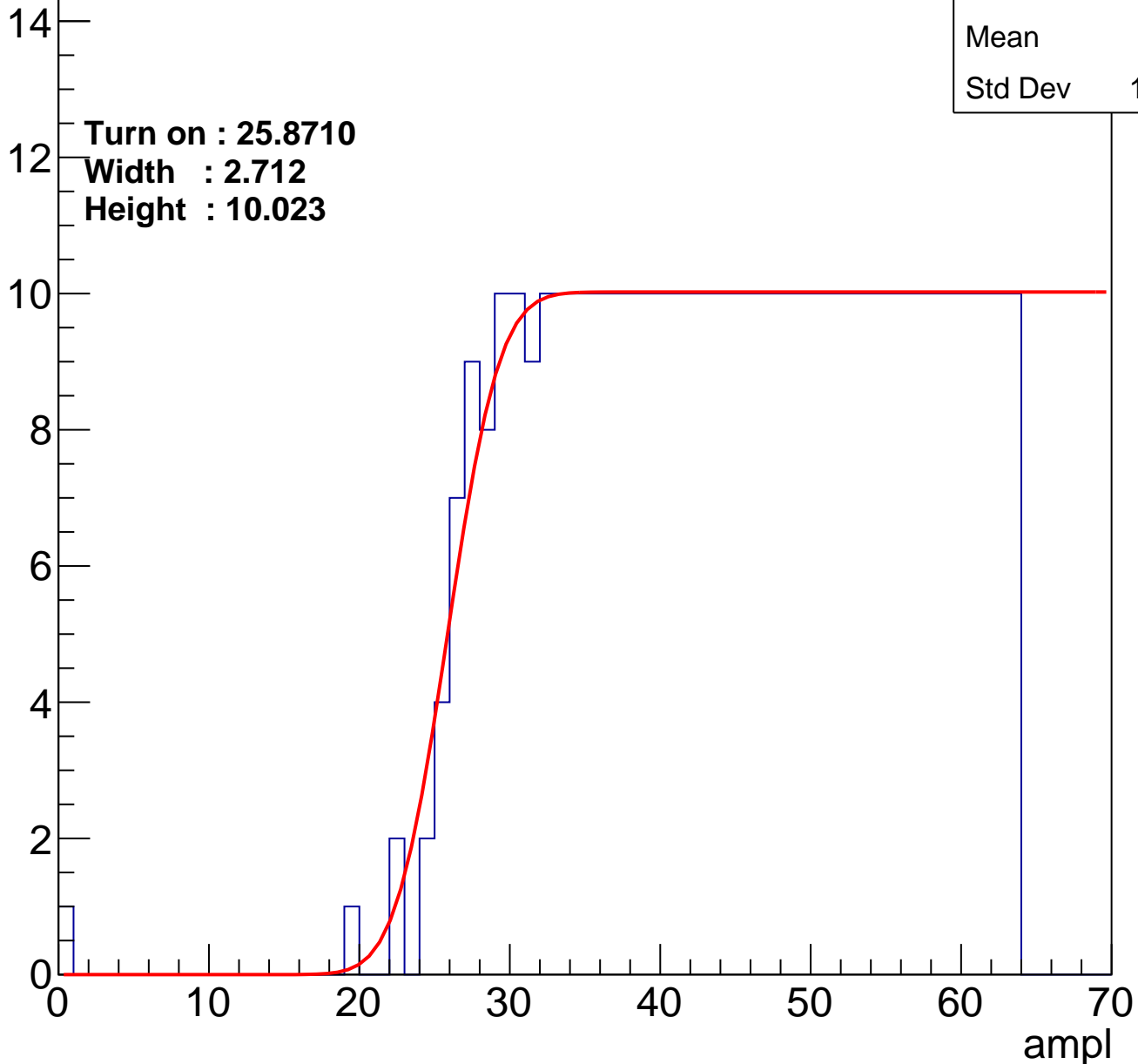
Entries	383
Mean	44.2
Std Dev	11.38

Turn on : 25.8710

Width : 2.712

Height : 10.023

Entry



B1L003S, U21-ch118

calib_packv5_042523_0143.root, FC#13, port D2

Entries	363
Mean	44.97
Std Dev	11.46

Turn on : 27.9929

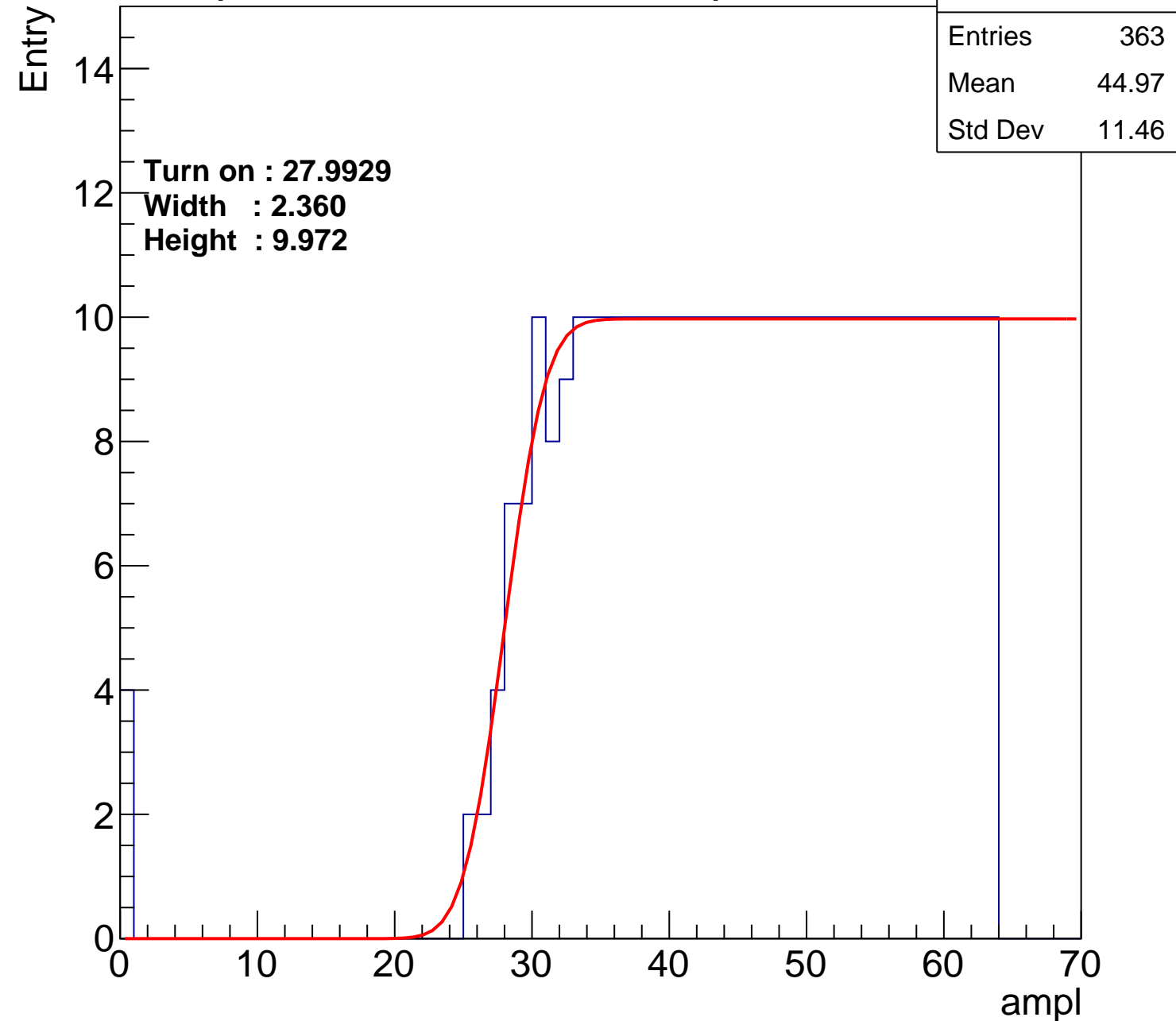
Width : 2.360

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch119

calib_packv5_042523_0143.root, FC#13, port D2

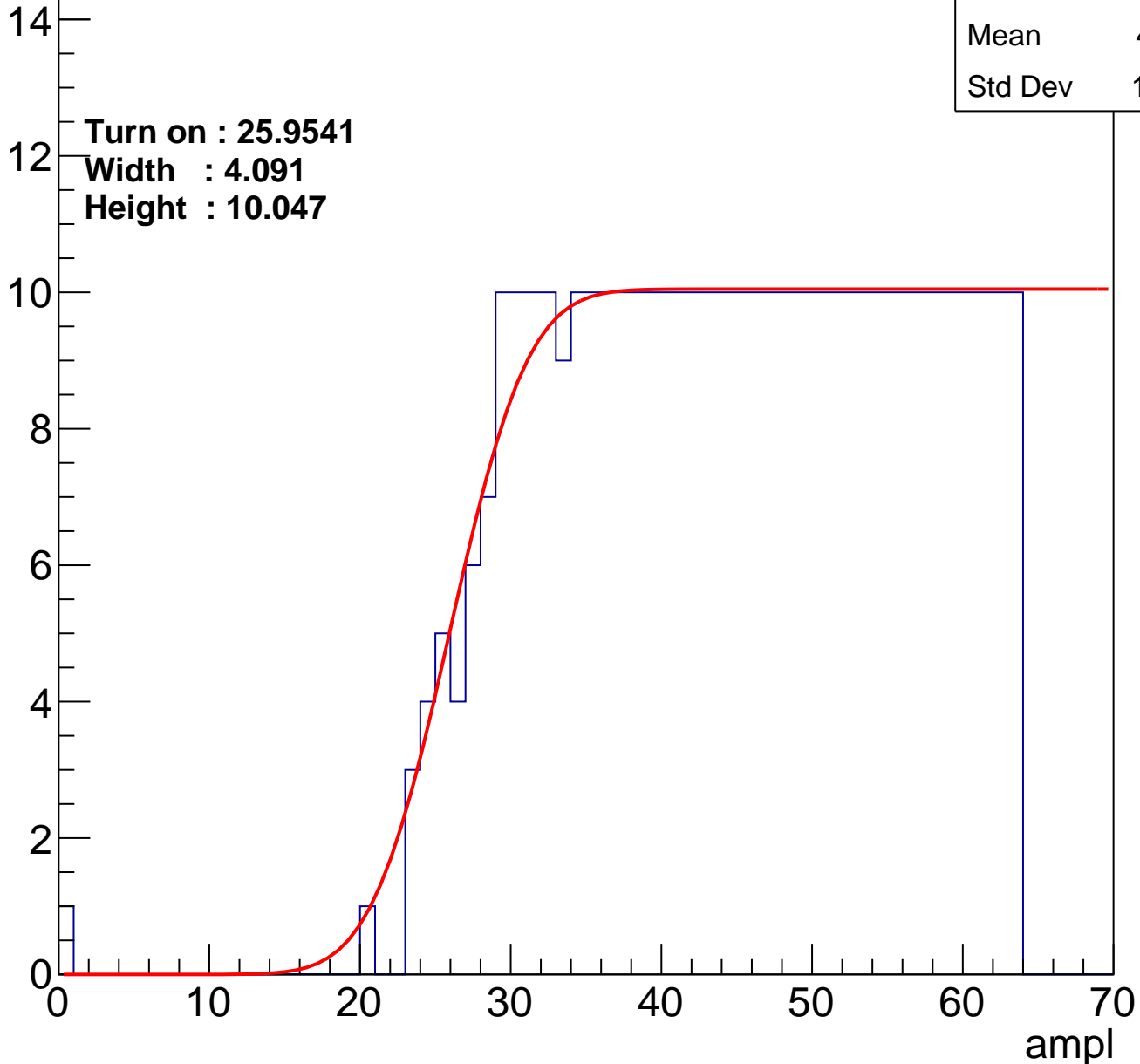
Entries	380
Mean	44.31
Std Dev	11.36

Turn on : 25.9541

Width : 4.091

Height : 10.047

Entry



B1L003S, U21-ch120

calib_packv5_042523_0143.root, FC#13, port D2

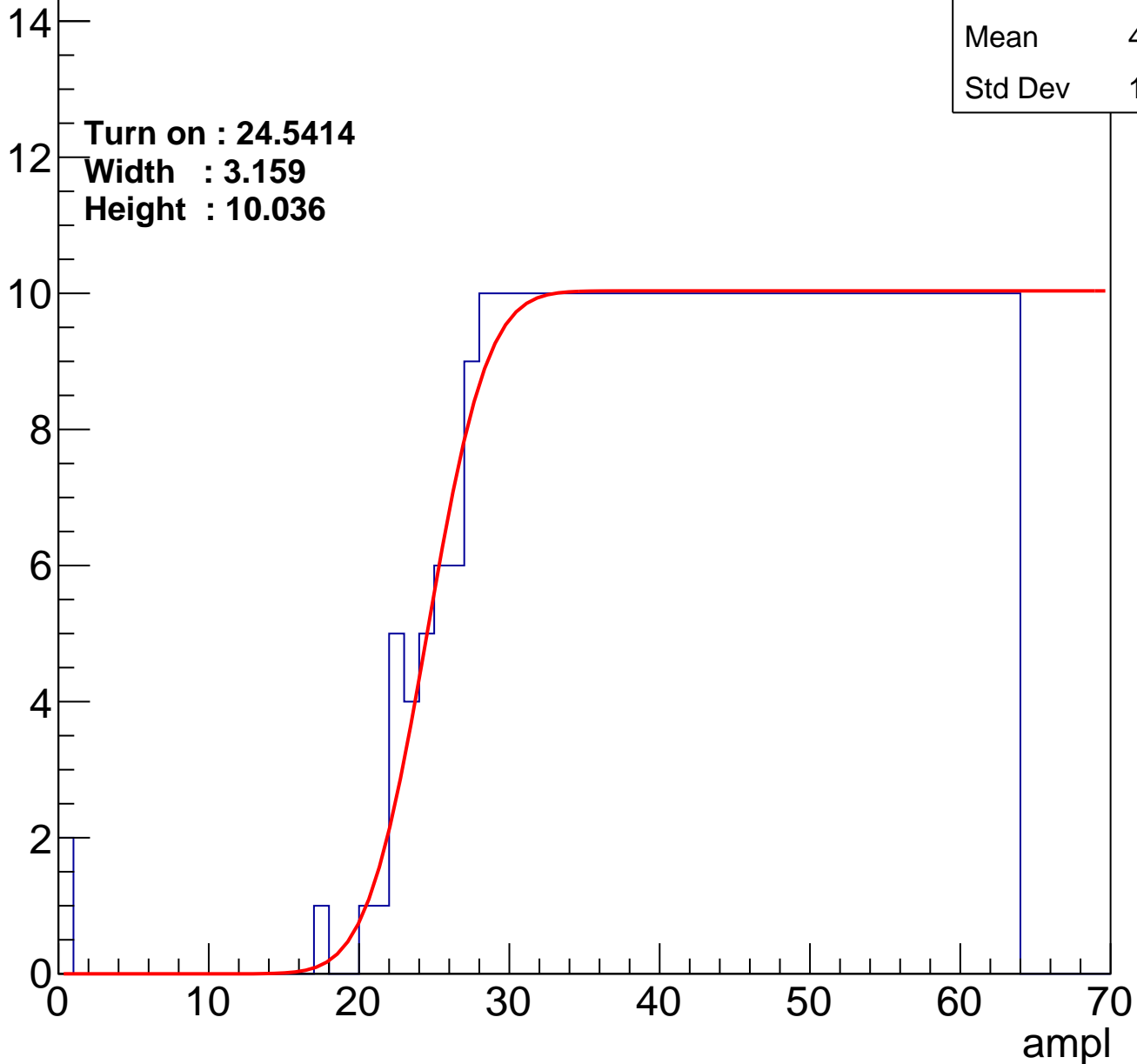
Entries	400
Mean	43.27
Std Dev	12.05

Turn on : 24.5414

Width : 3.159

Height : 10.036

Entry



B1L003S, U21-ch121

calib_packv5_042523_0143.root, FC#13, port D2

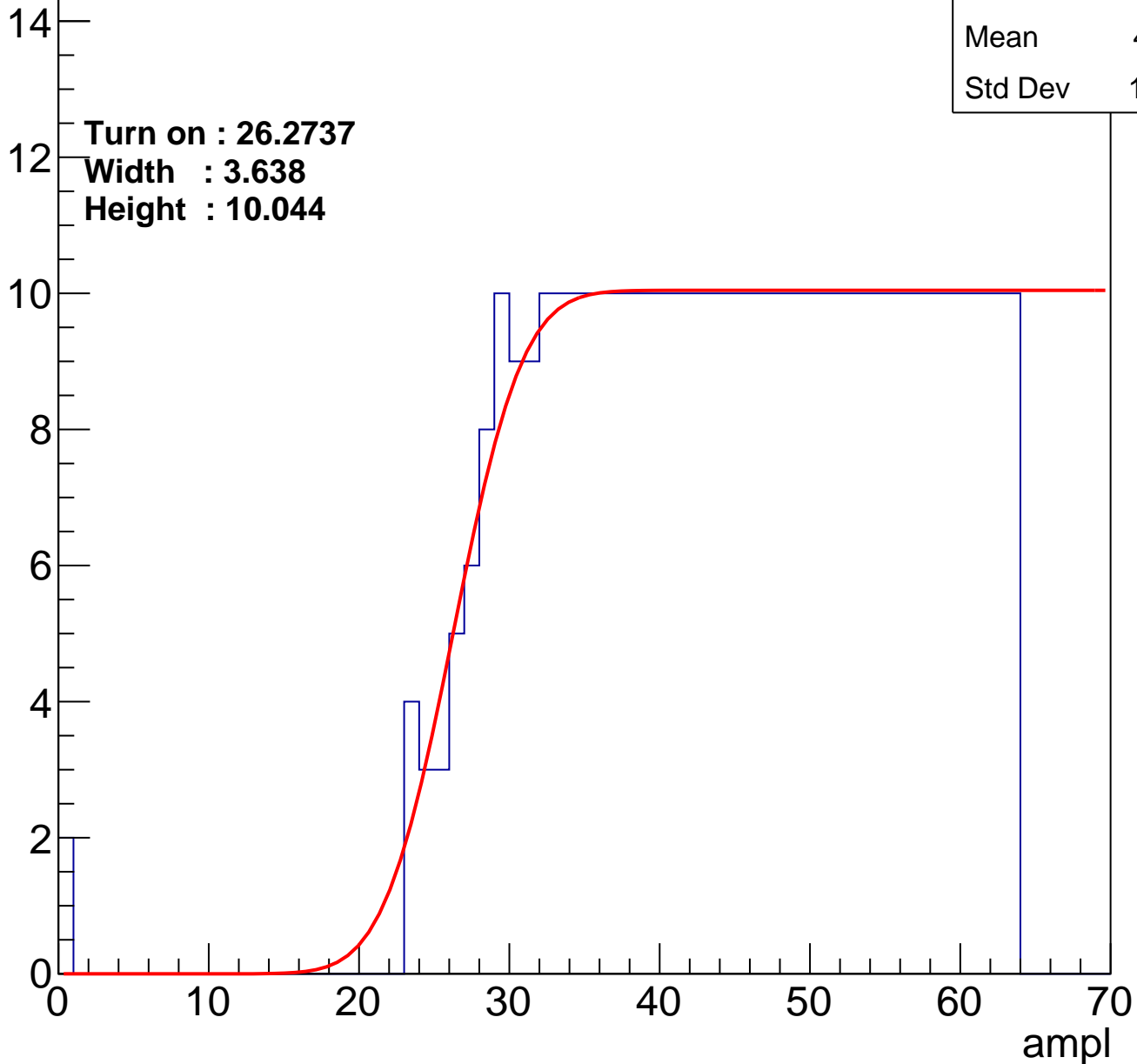
Entries	379
Mean	44.31
Std Dev	11.49

Turn on : 26.2737

Width : 3.638

Height : 10.044

Entry



B1L003S, U21-ch122

calib_packv5_042523_0143.root, FC#13, port D2

Entries	373
Mean	44.54
Std Dev	11.53

Turn on : 27.4853

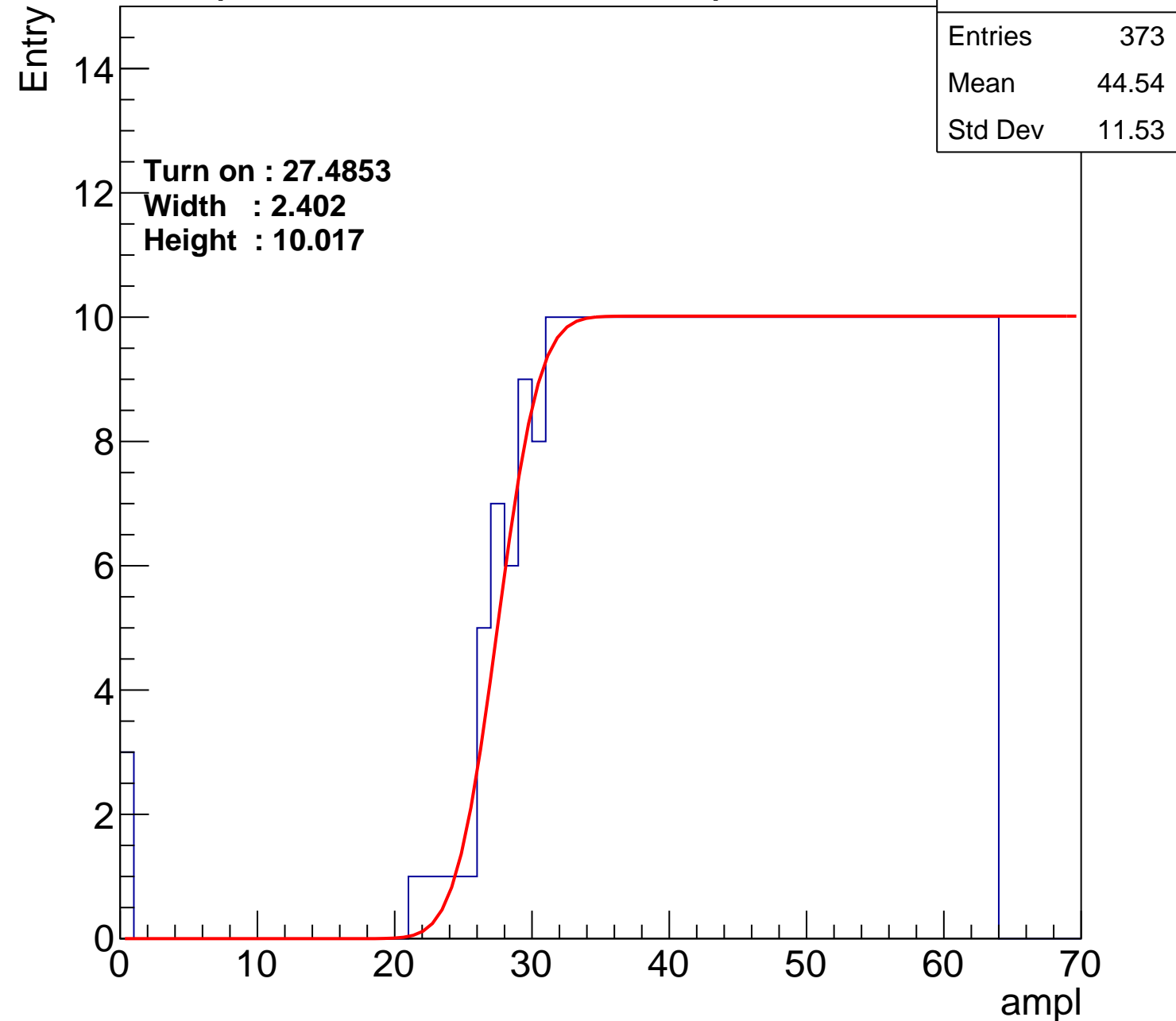
Width : 2.402

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch123

calib_packv5_042523_0143.root, FC#13, port D2

Entries	387
Mean	43.71
Std Dev	12.23

Turn on : 26.3107

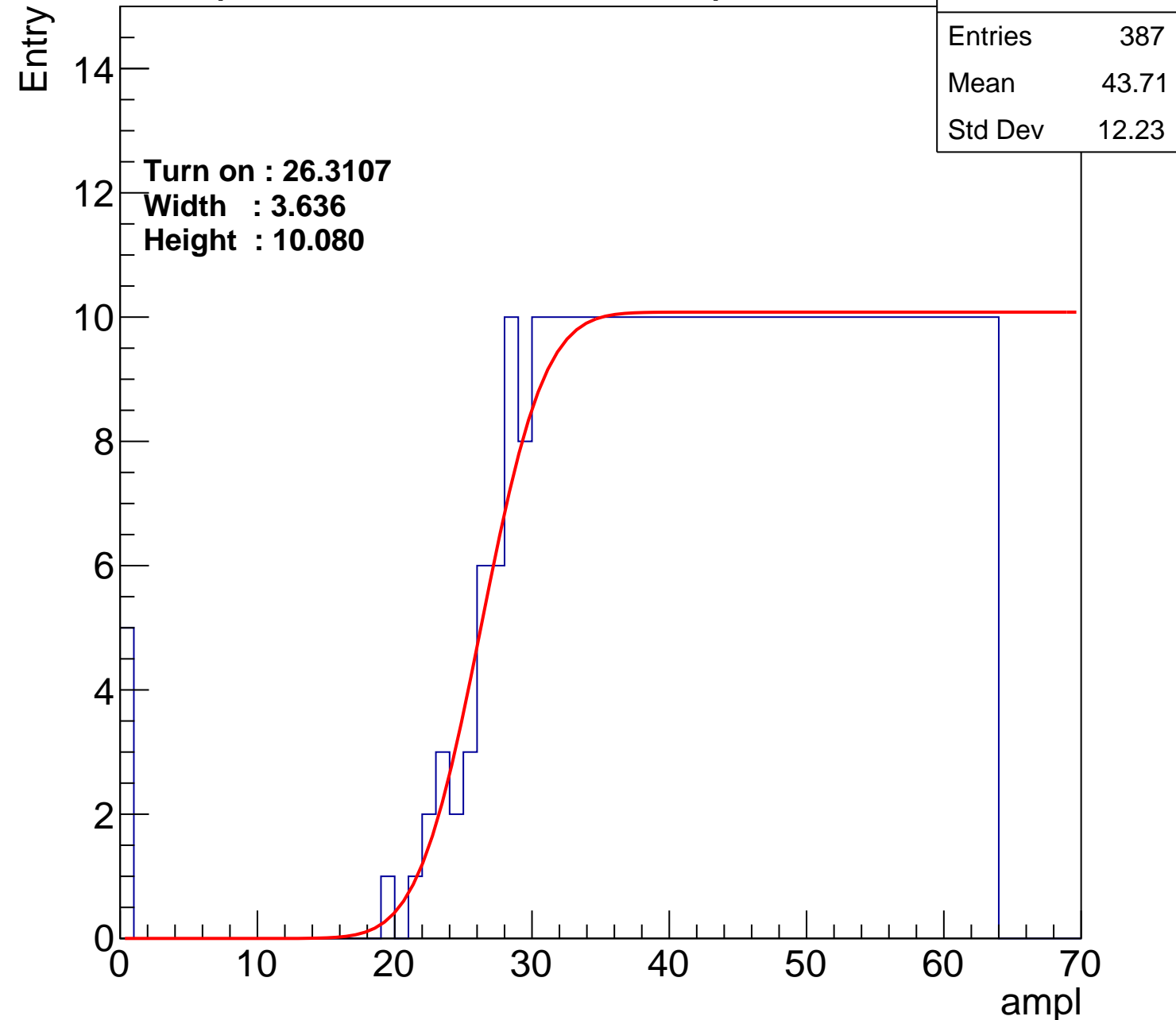
Width : 3.636

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch124

calib_packv5_042523_0143.root, FC#13, port D2

Entries	365
Mean	45.08
Std Dev	10.91

Turn on : 27.5248

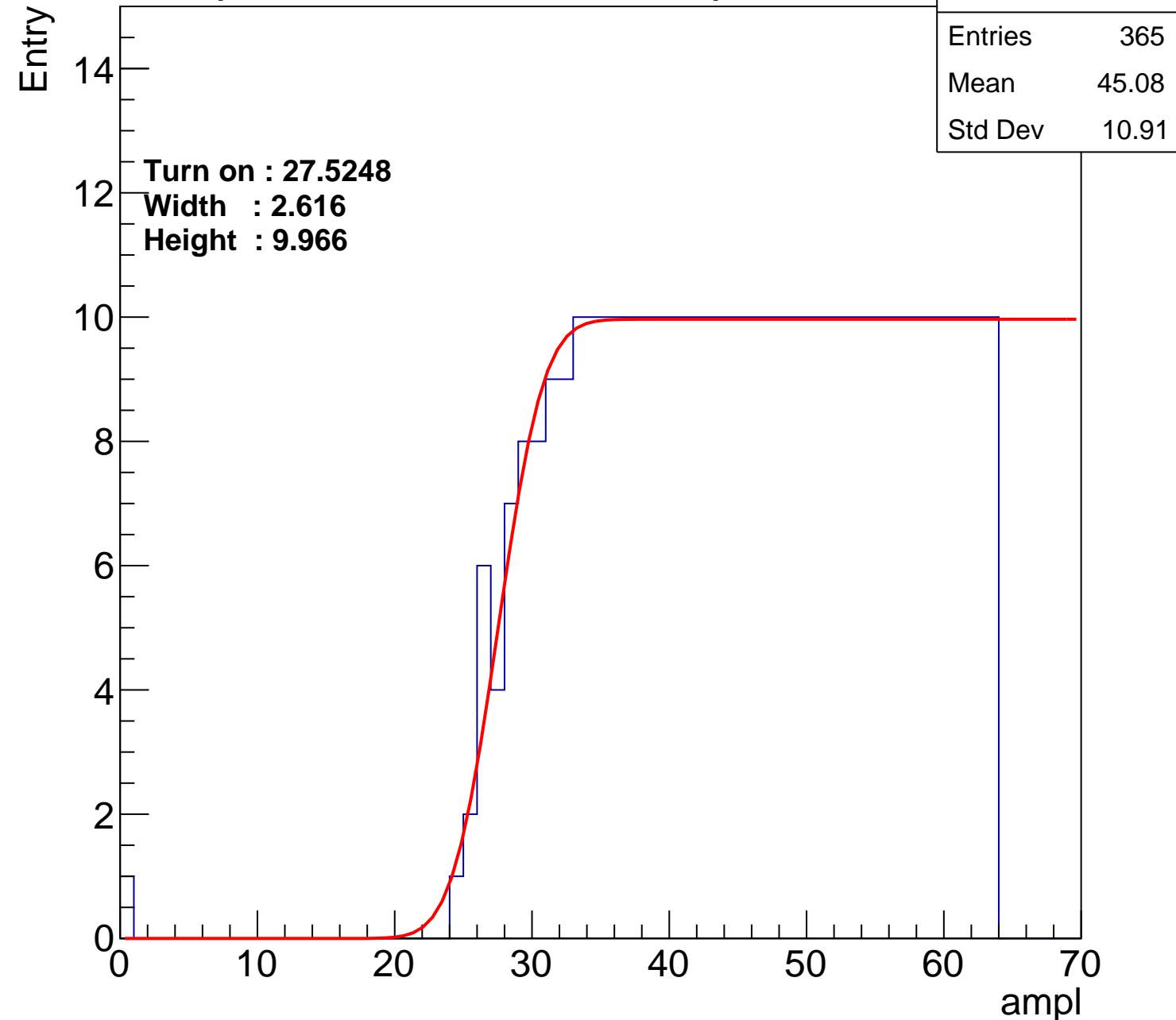
Width : 2.616

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch125

calib_packv5_042523_0143.root, FC#13, port D2

Entries	381
Mean	44.24
Std Dev	11.5

Turn on : 25.7181

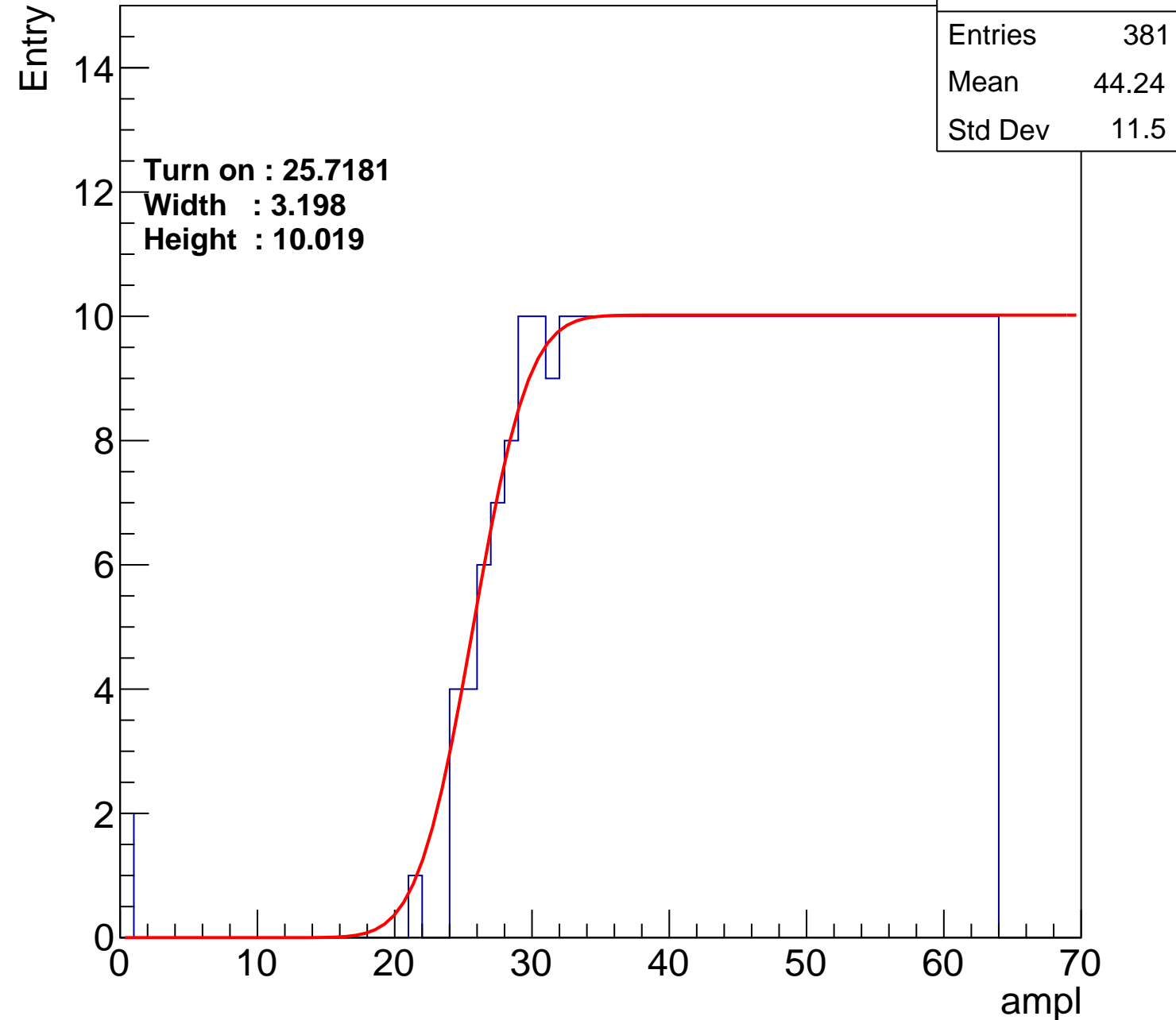
Width : 3.198

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch126

calib_packv5_042523_0143.root, FC#13, port D2

Entries	390
Mean	43.83
Std Dev	11.68

Turn on : 25.4536

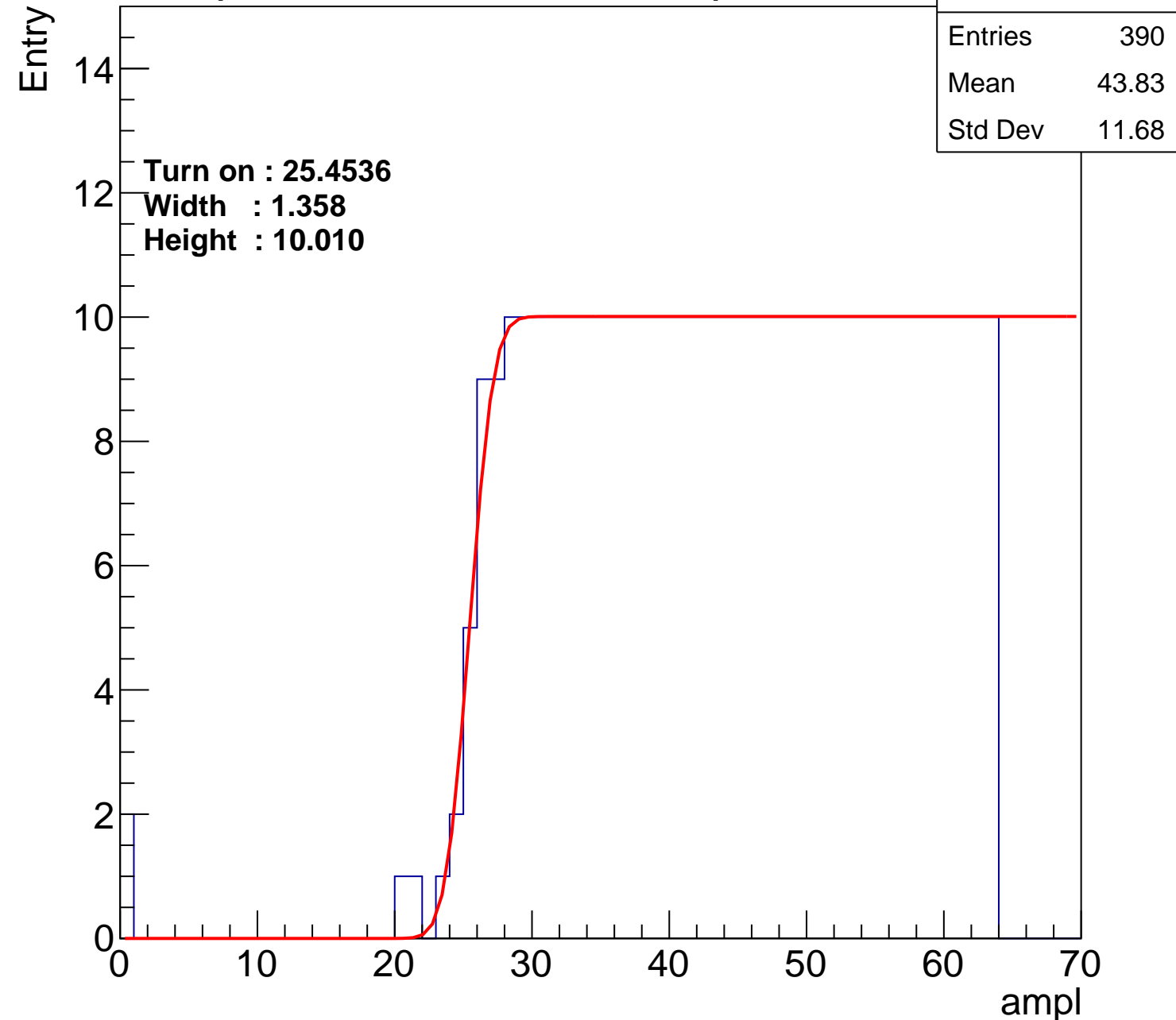
Width : 1.358

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.43
Std Dev	11.6

Turn on : 27.1776

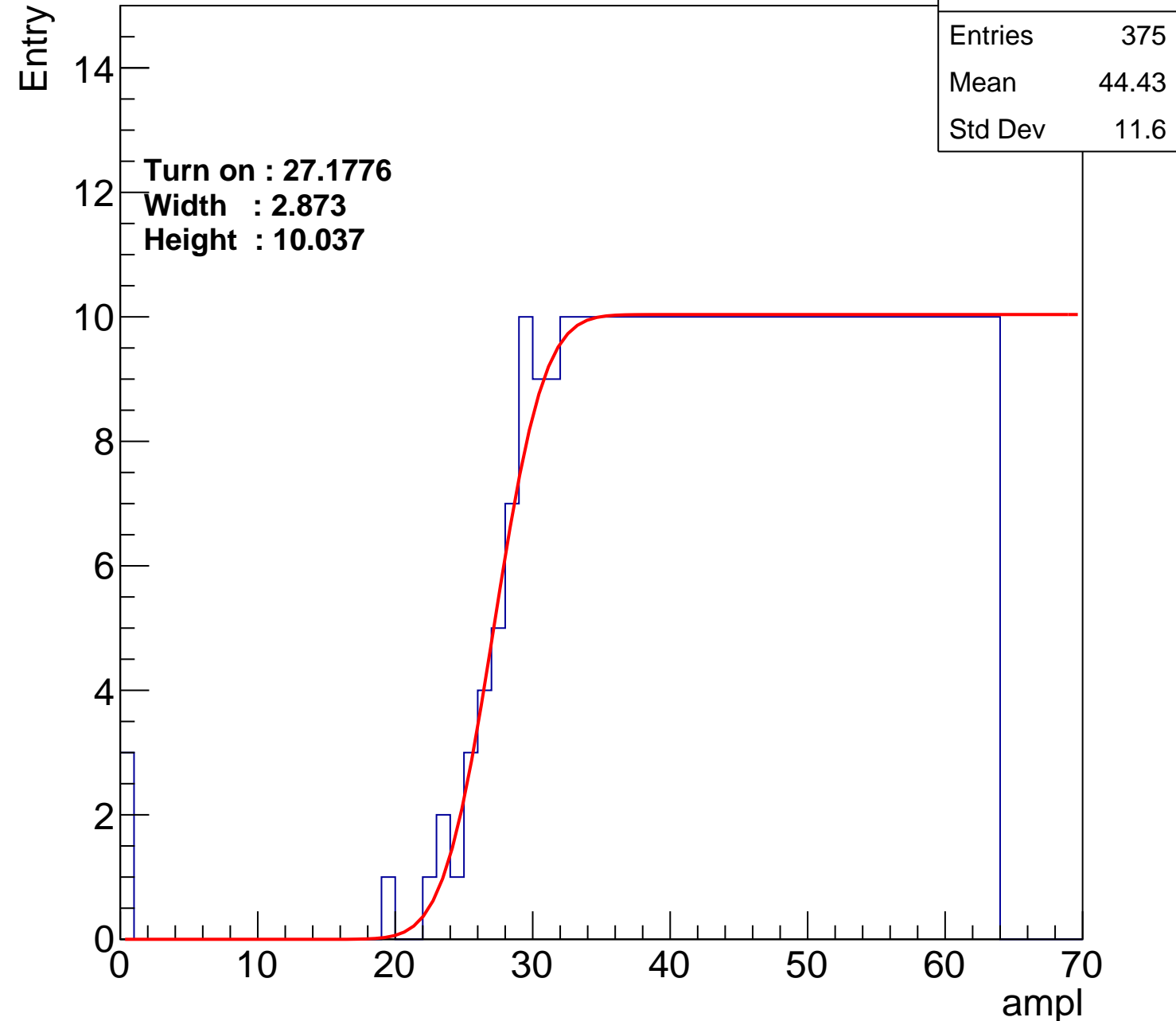
Width : 2.873

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L003S, U21-ch127

calib_packv5_042523_0143.root, FC#13, port D2

Entries	375
Mean	44.43
Std Dev	11.6

Turn on : 27.1776

Width : 2.873

Height : 10.037

Entry

