

B1L100S, U1-ch0, adc0

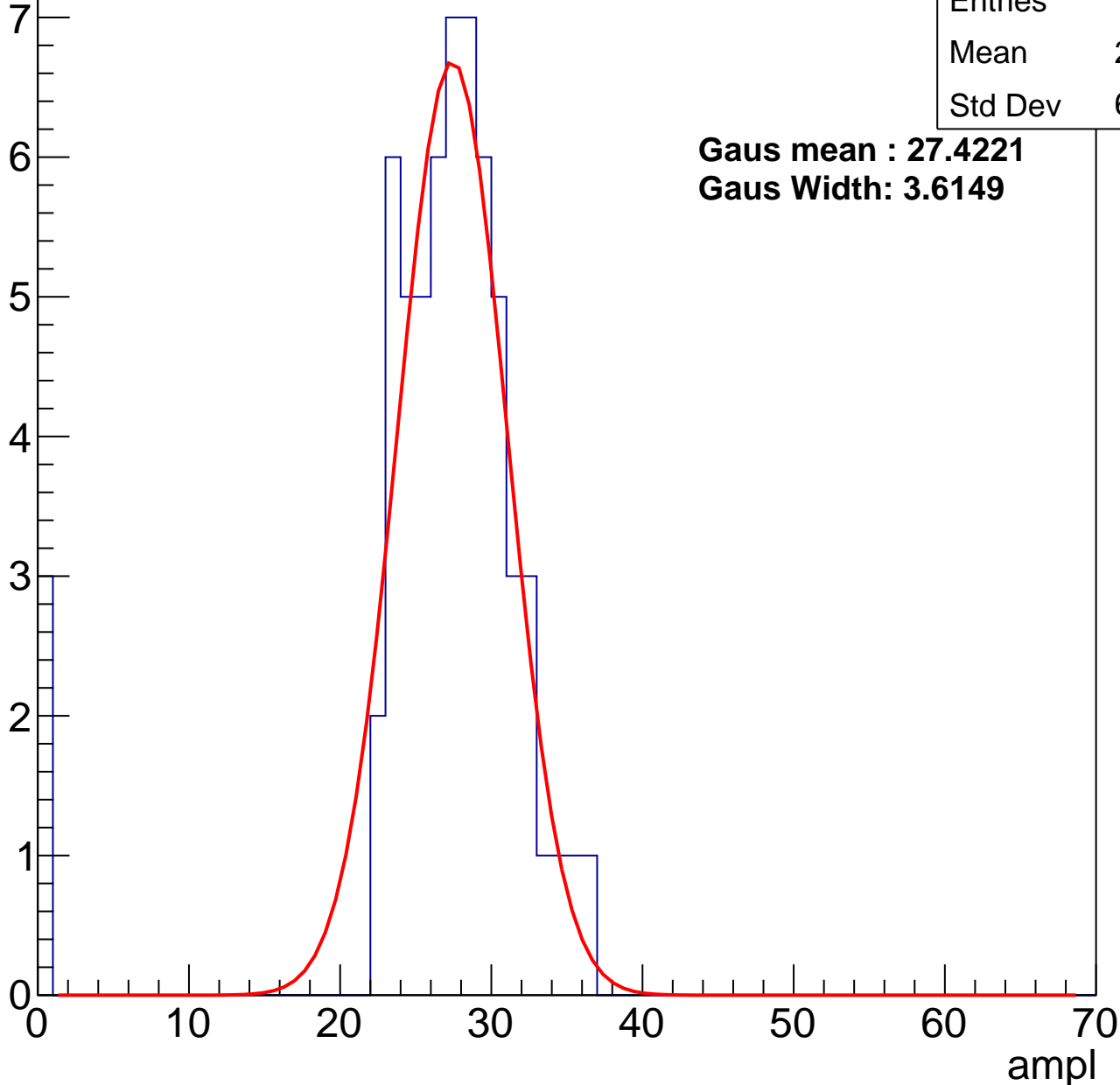
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	26.11
Std Dev	6.701

Gaus mean : 27.4221

Gaus Width: 3.6149



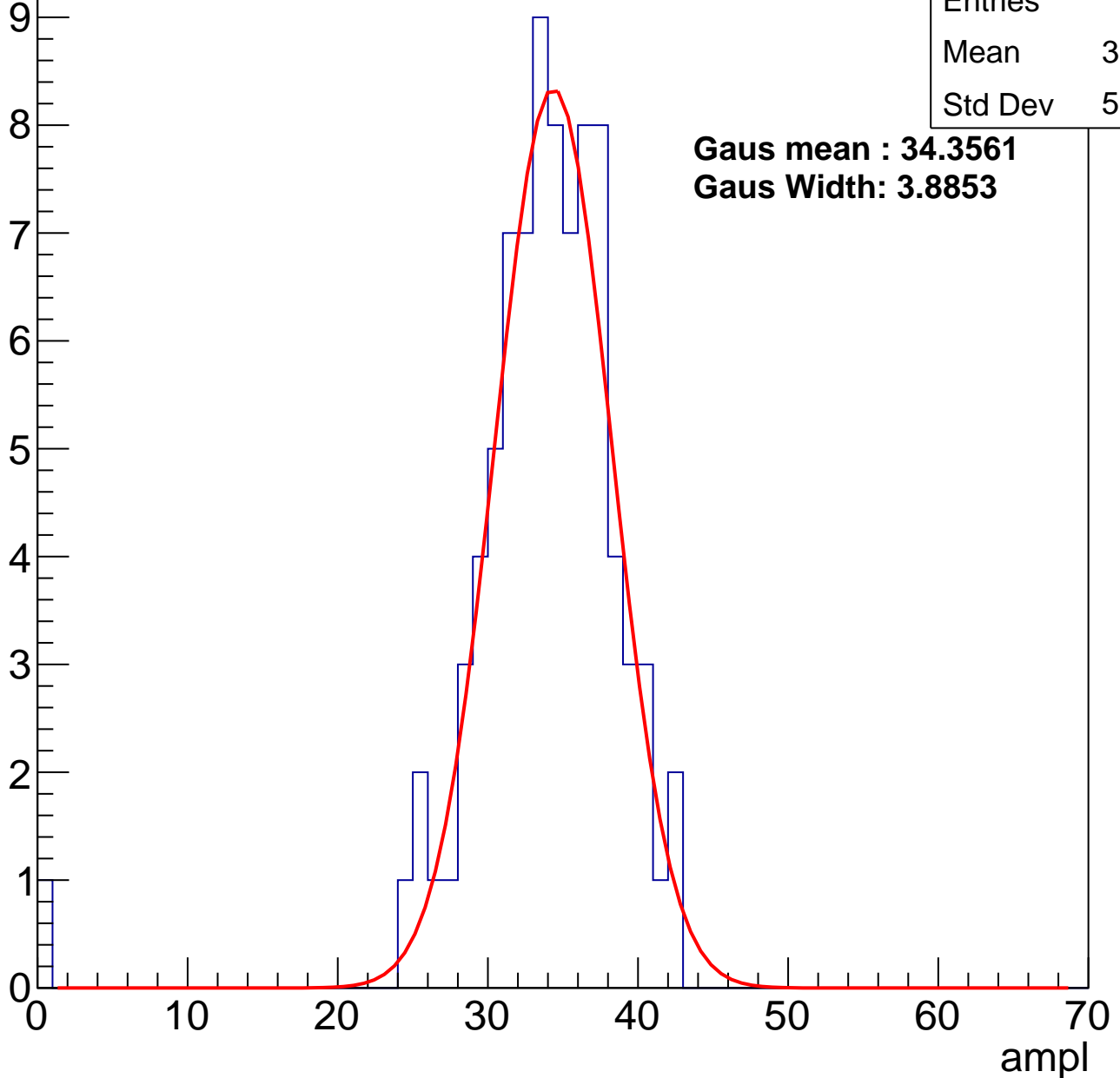
B1L100S, U1-ch0, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	33.29
Std Dev	5.327

Gaus mean : 34.3561
Gaus Width: 3.8853



B1L100S, U1-ch0, adc2

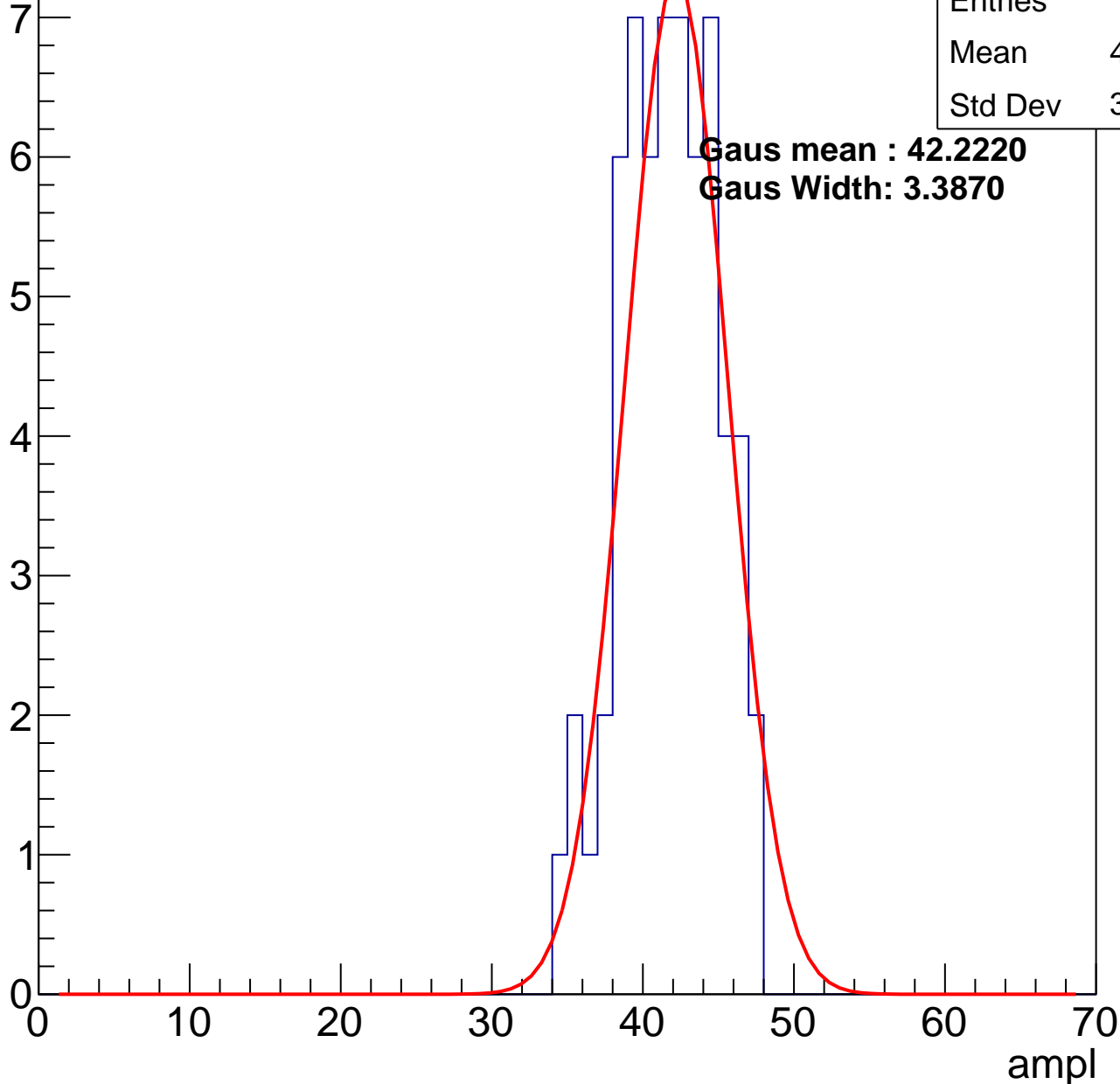
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.29
Std Dev	3.087

Gaus mean : 42.2220

Gaus Width: 3.3870

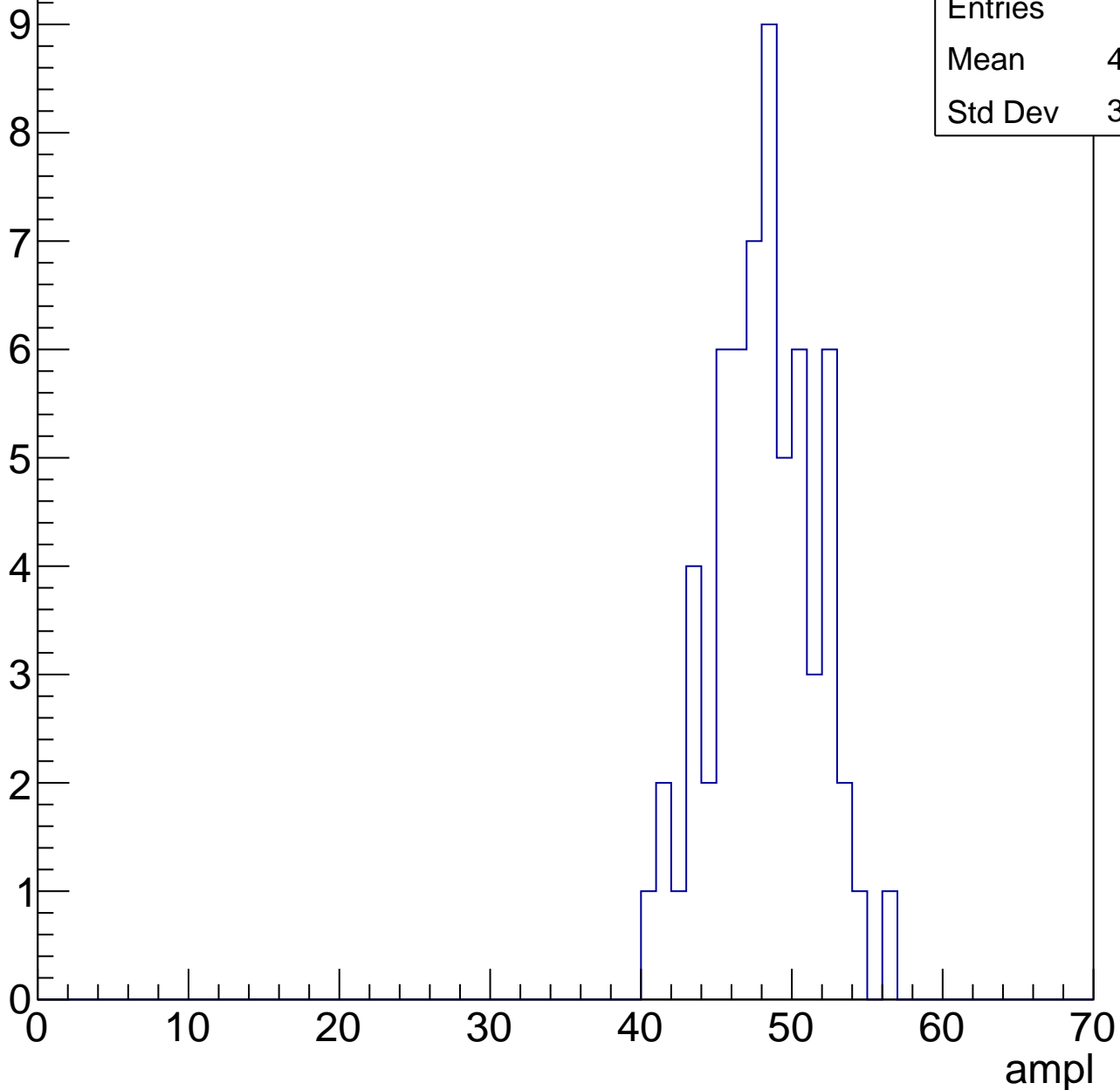


B1L100S, U1-ch0, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	47.69
Std Dev	3.387

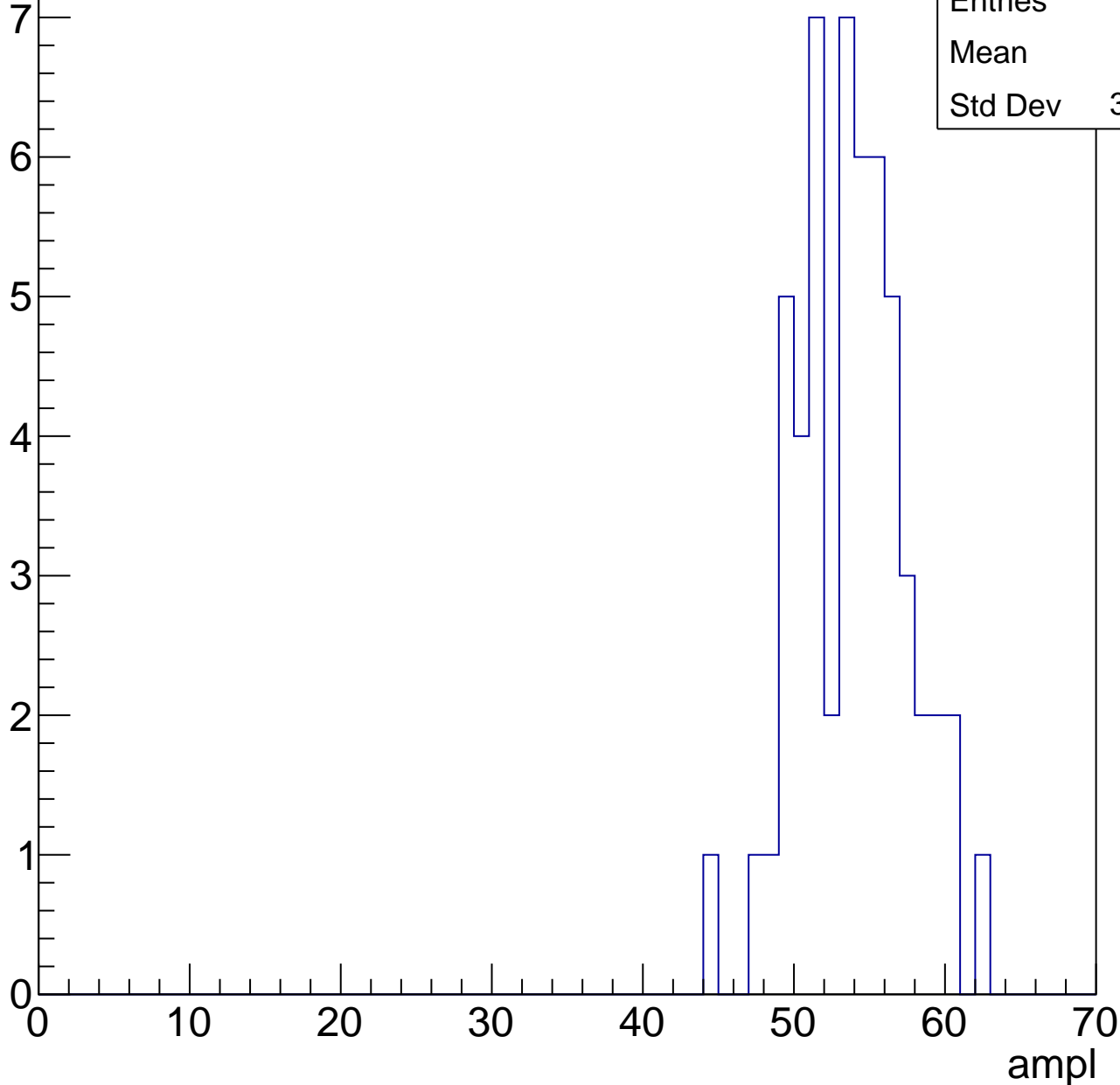


B1L100S, U1-ch0, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	53.4
Std Dev	3.576

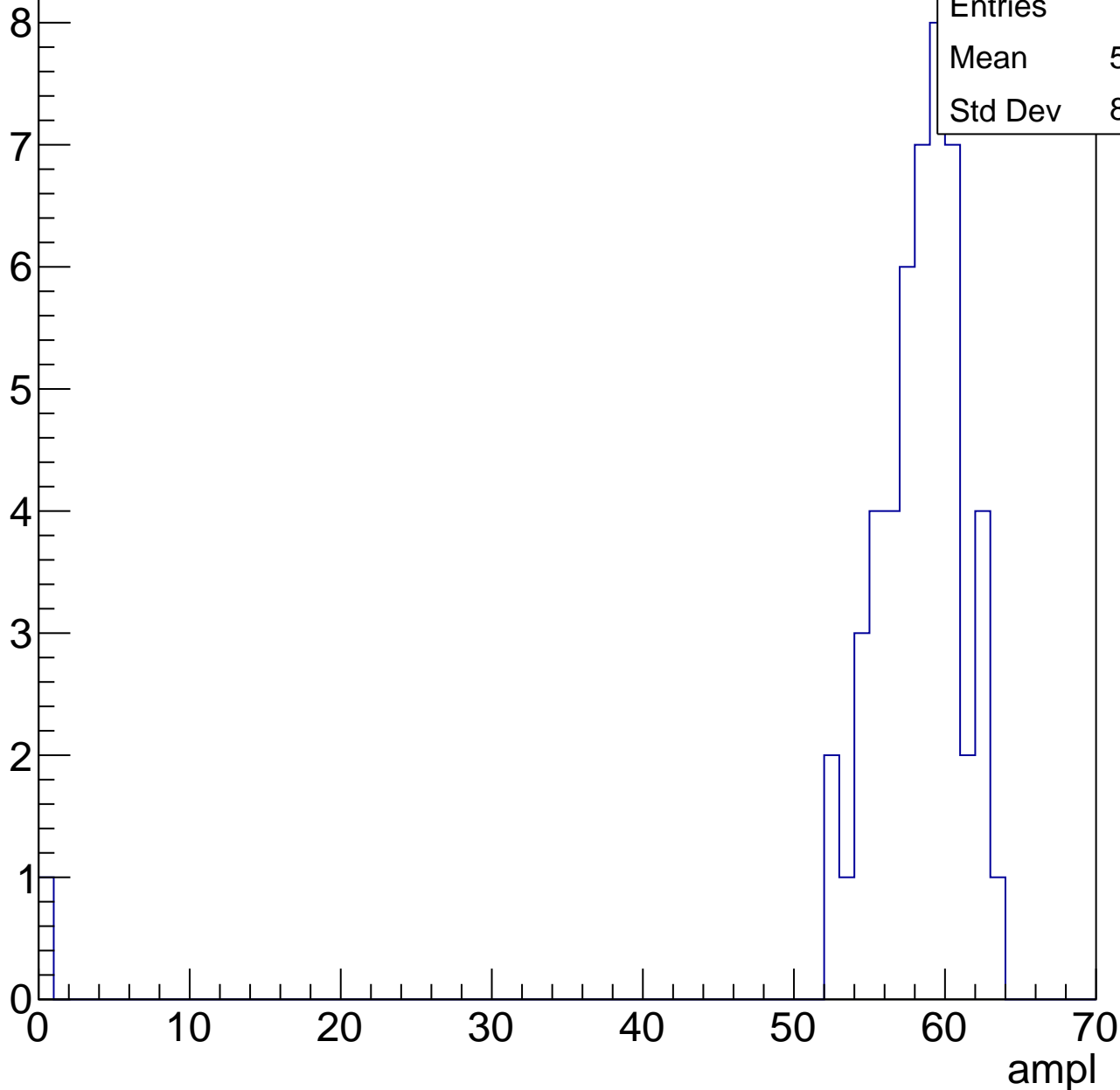


B1L100S, U1-ch0, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

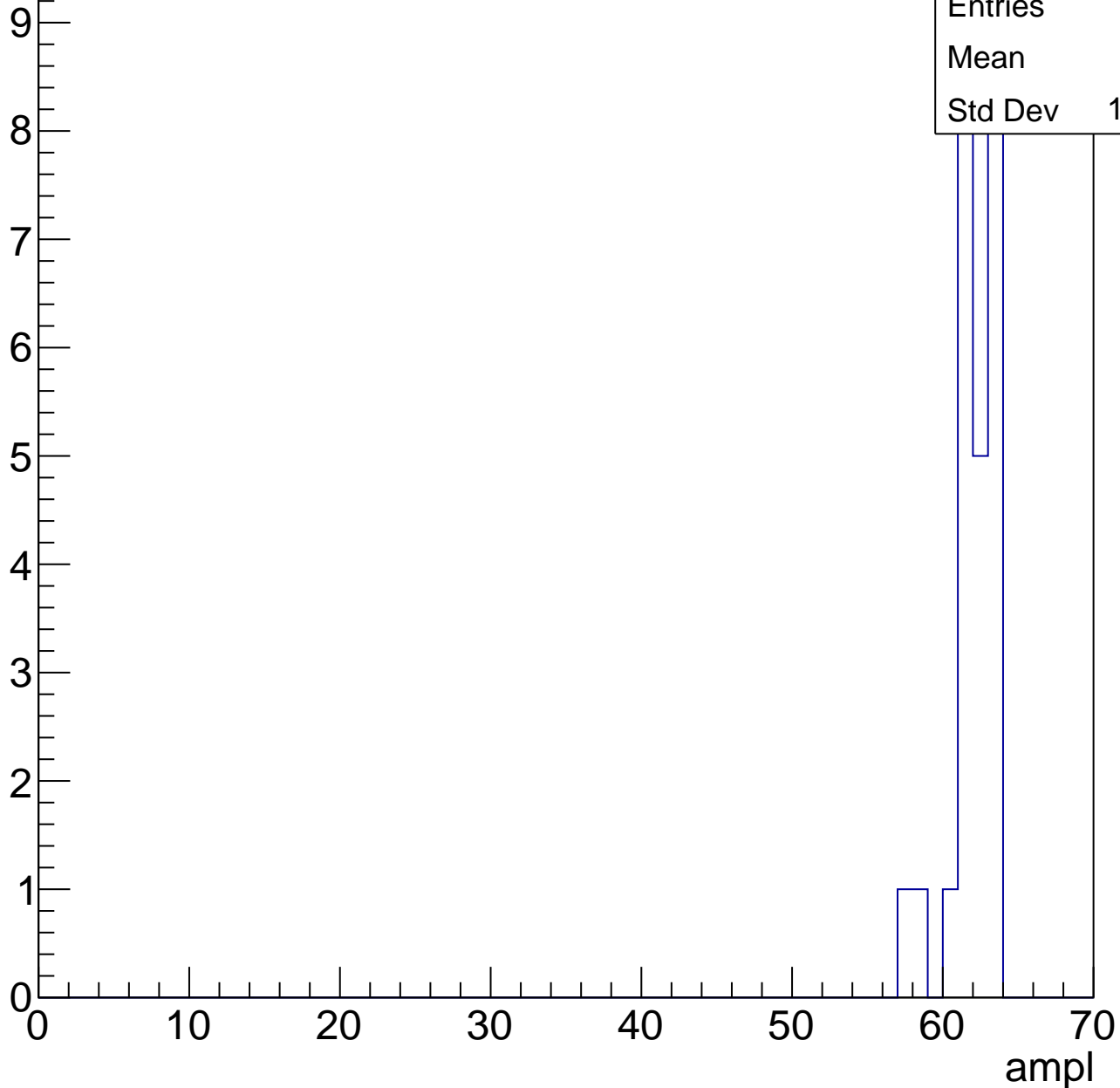
Entries	50
Mean	56.72
Std Dev	8.518



B1L100S, U1-ch0, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch0, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch1, adc0

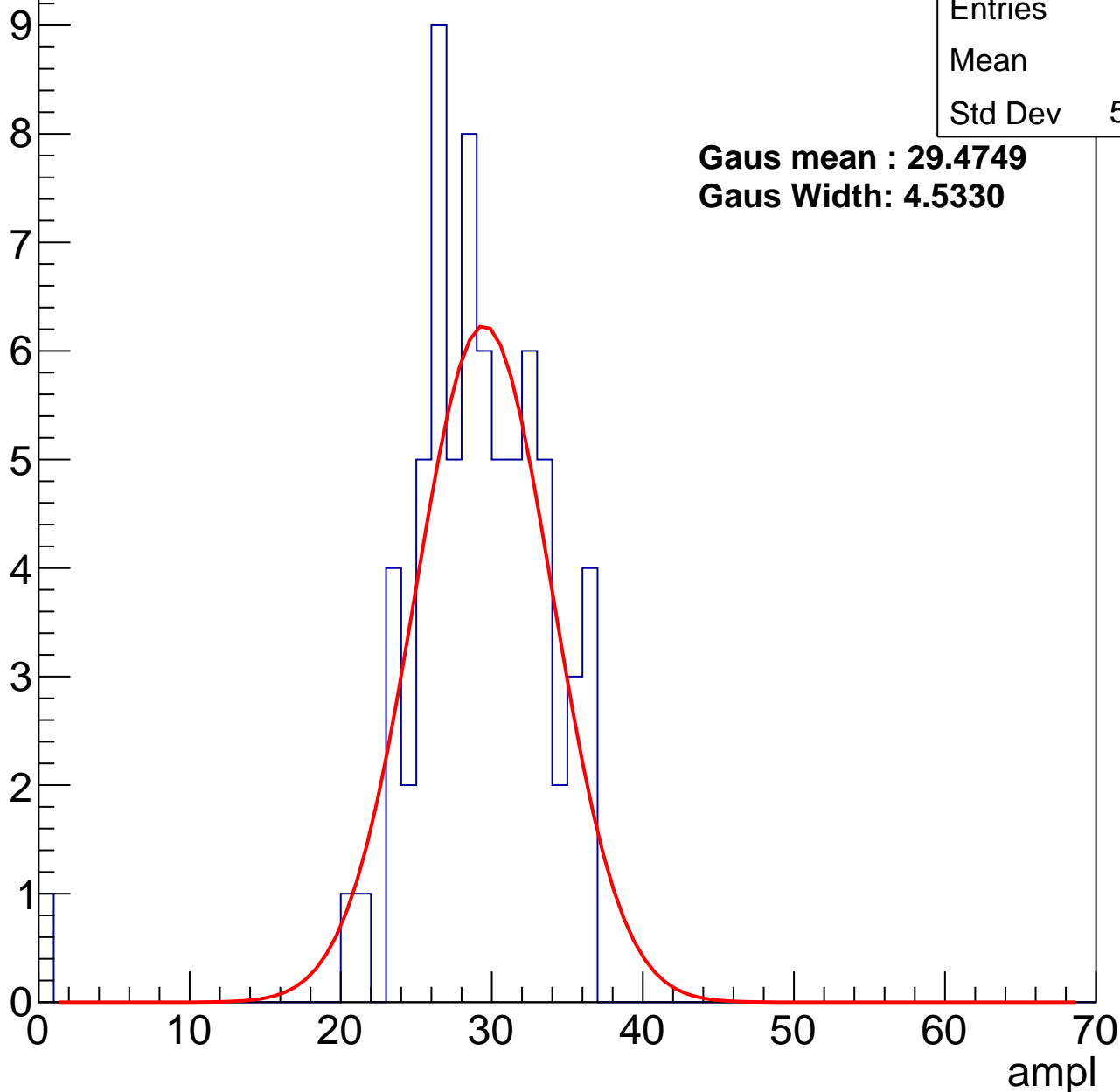
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.5
Std Dev	5.088

Gaus mean : 29.4749

Gaus Width: 4.5330



B1L100S, U1-ch1, adc1

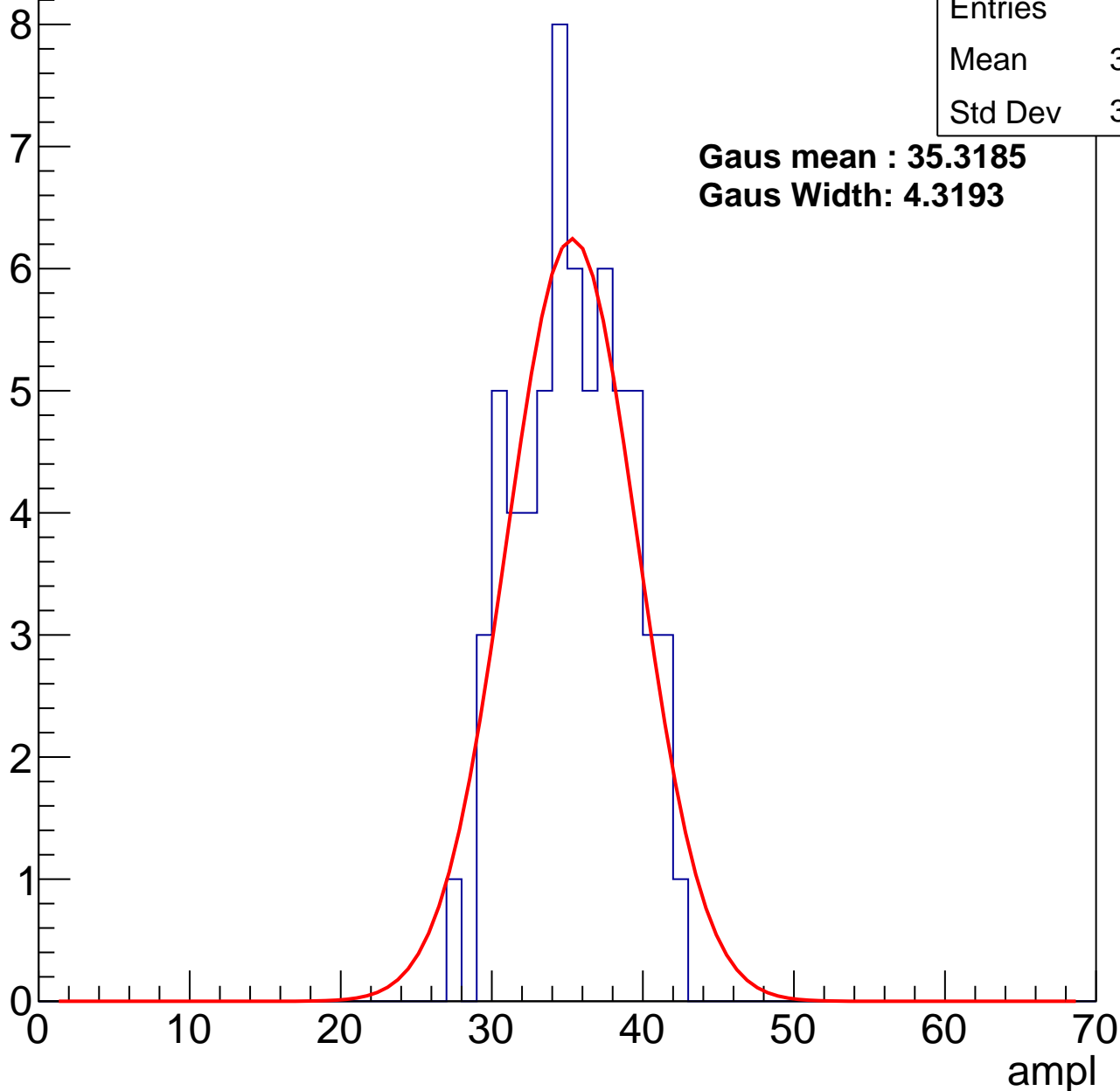
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	34.92
Std Dev	3.559

Gaus mean : 35.3185

Gaus Width: 4.3193



B1L100S, U1-ch1, adc2

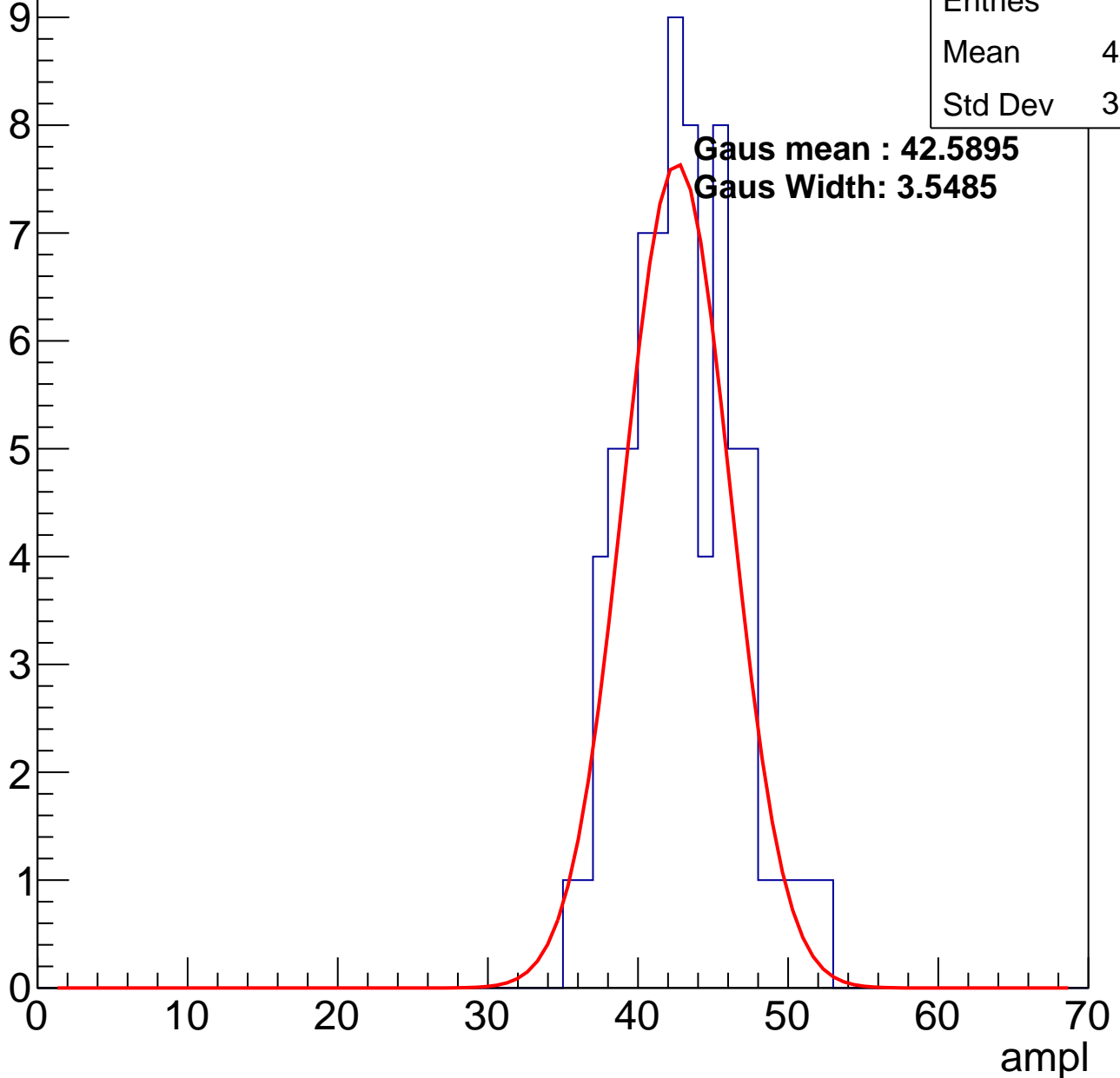
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	42.49
Std Dev	3.599

Gaus mean : 42.5895

Gaus Width: 3.5485

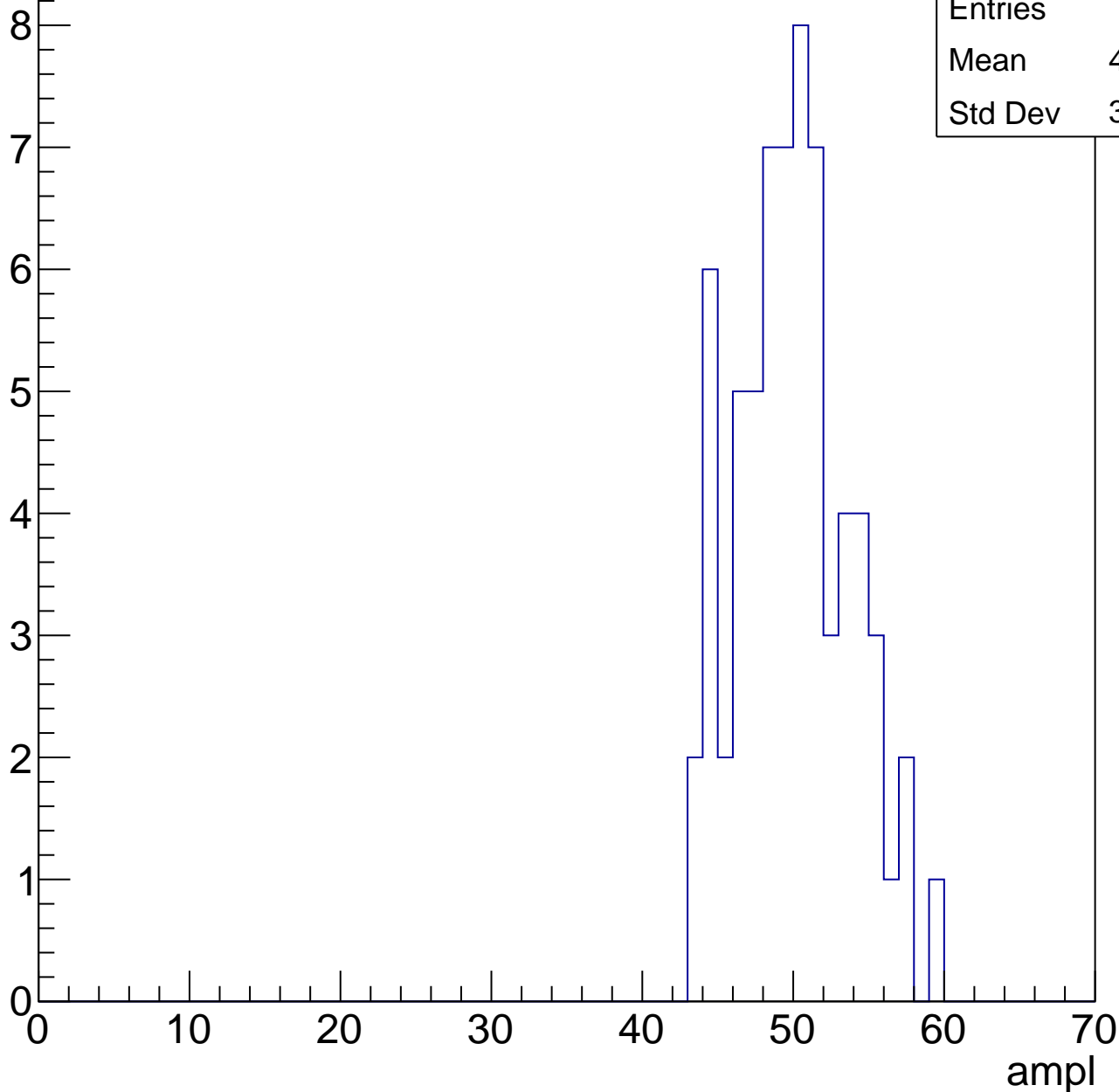


B1L100S, U1-ch1, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

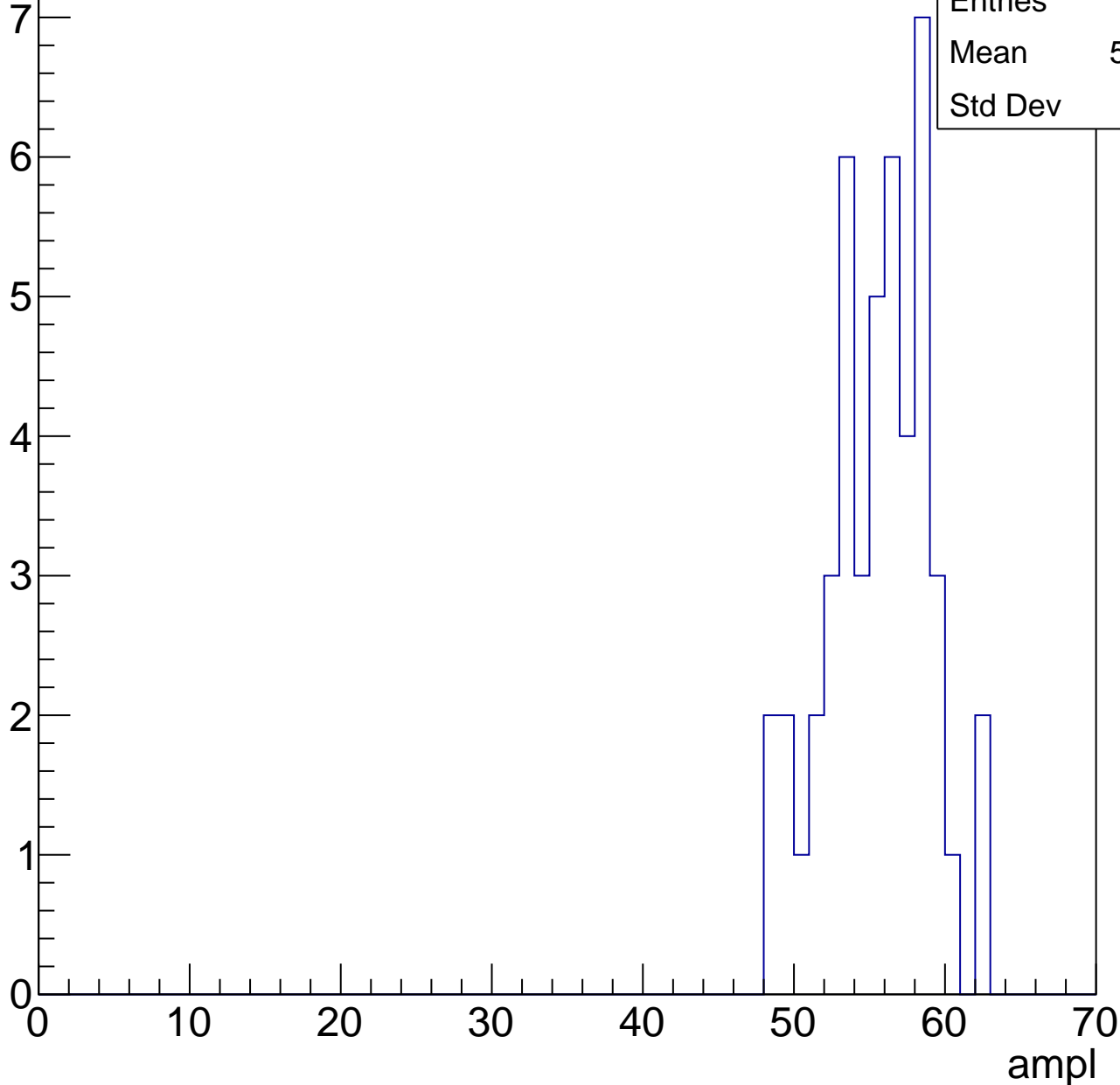
Entries	67
Mean	49.54
Std Dev	3.703



B1L100S, U1-ch1, adc4

calib_packv5_042523_0143.root, FC#4, port A2

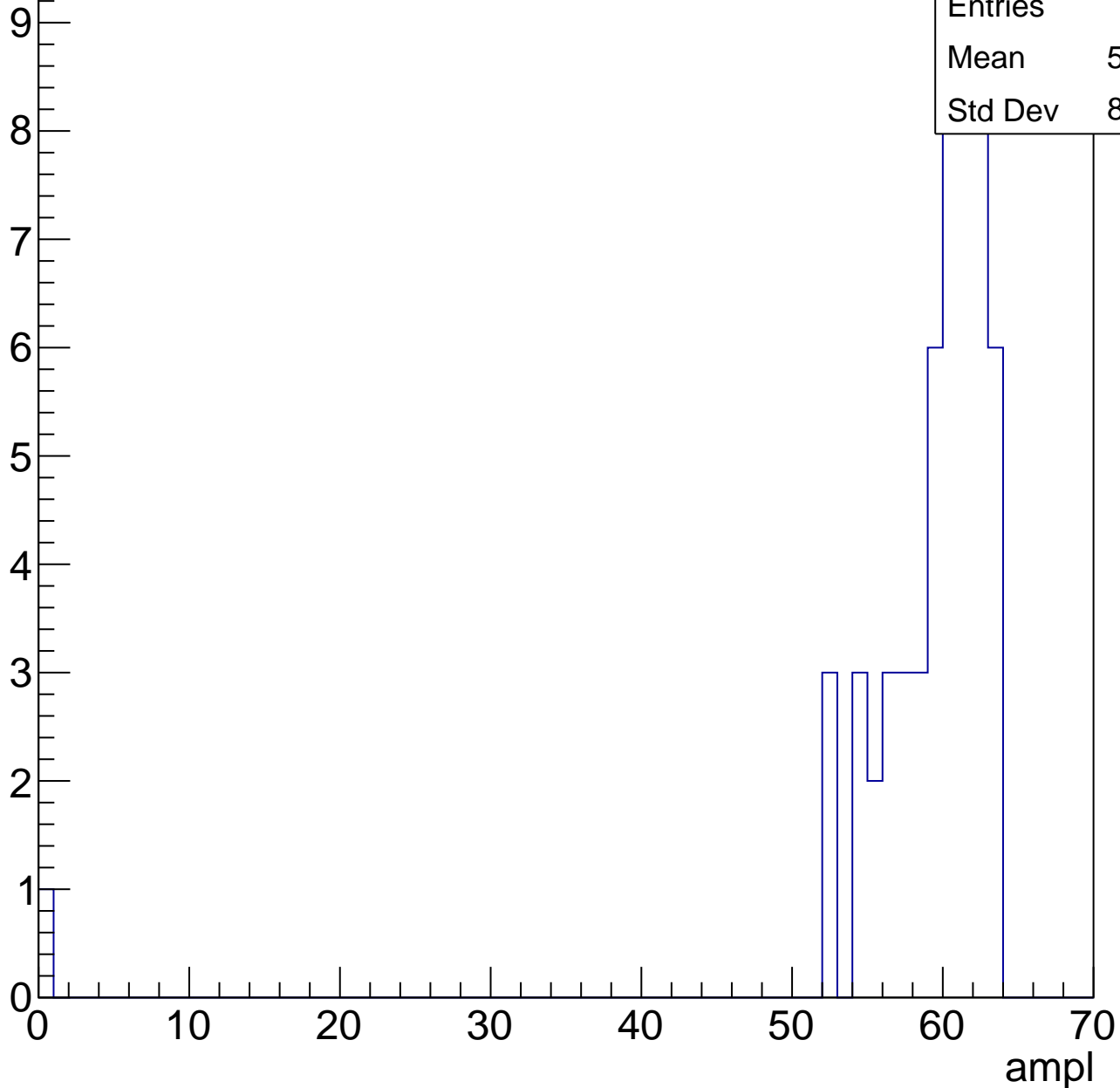
Entry



B1L100S, U1-ch1, adc5

calib_packv5_042523_0143.root, FC#4, port A2

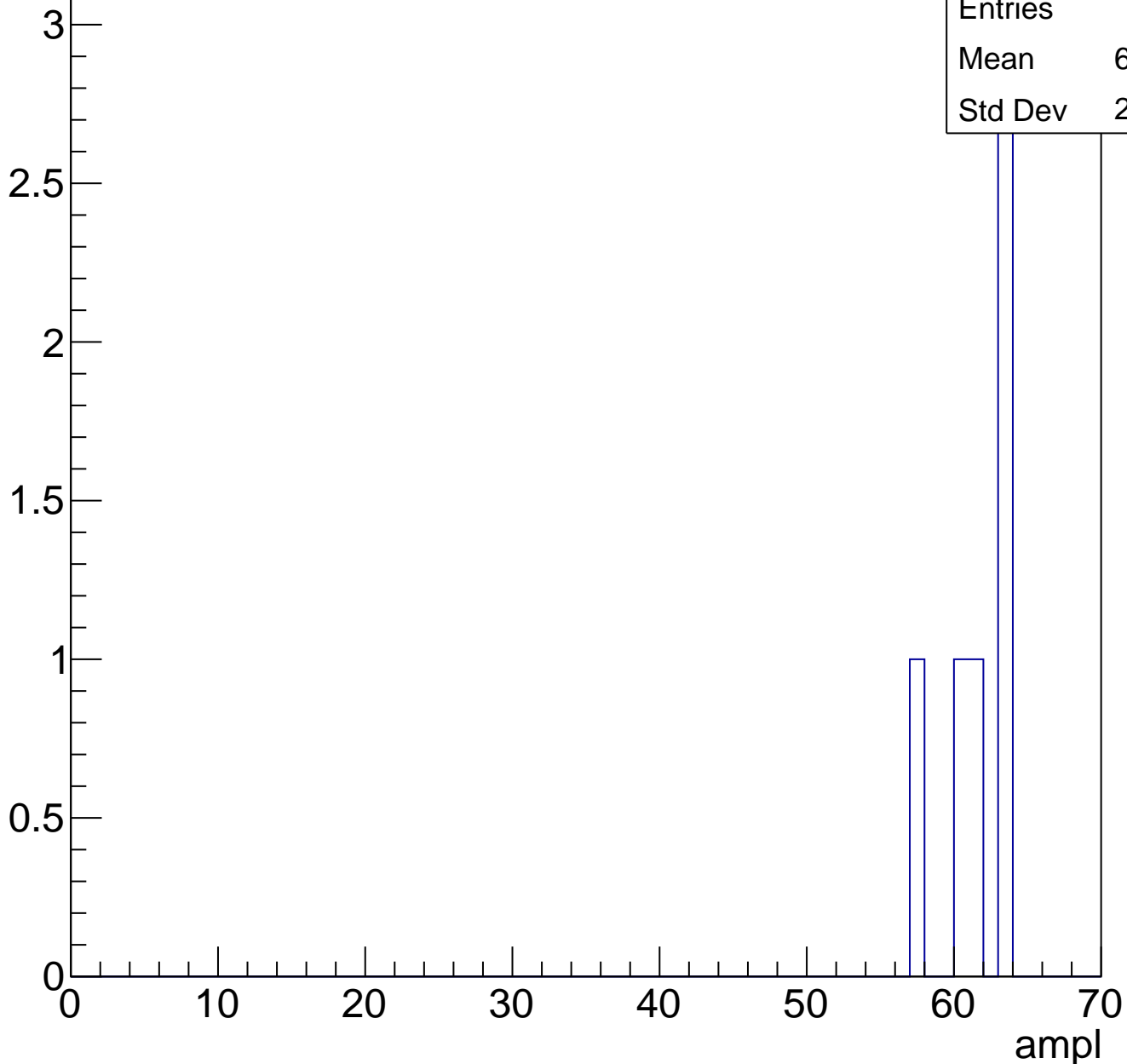
Entry



B1L100S, U1-ch1, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch1, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch2, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	61
Mean	28.07
Std Dev	5.111

Gaus mean : 29.5894

Gaus Width: 4.4781

Entry

10
8
6
4
2
0

ampl

0

10

20

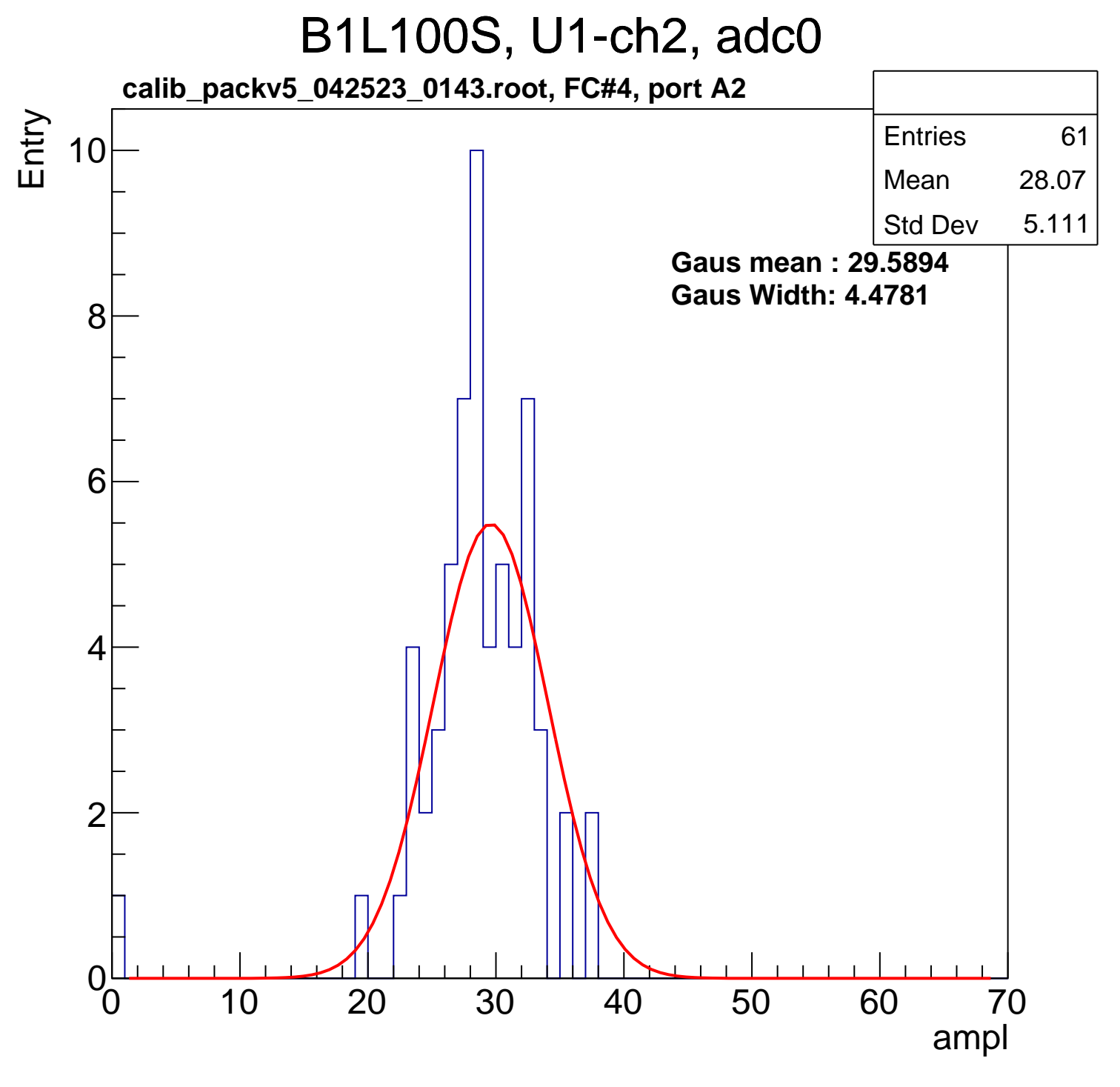
30

40

50

60

70



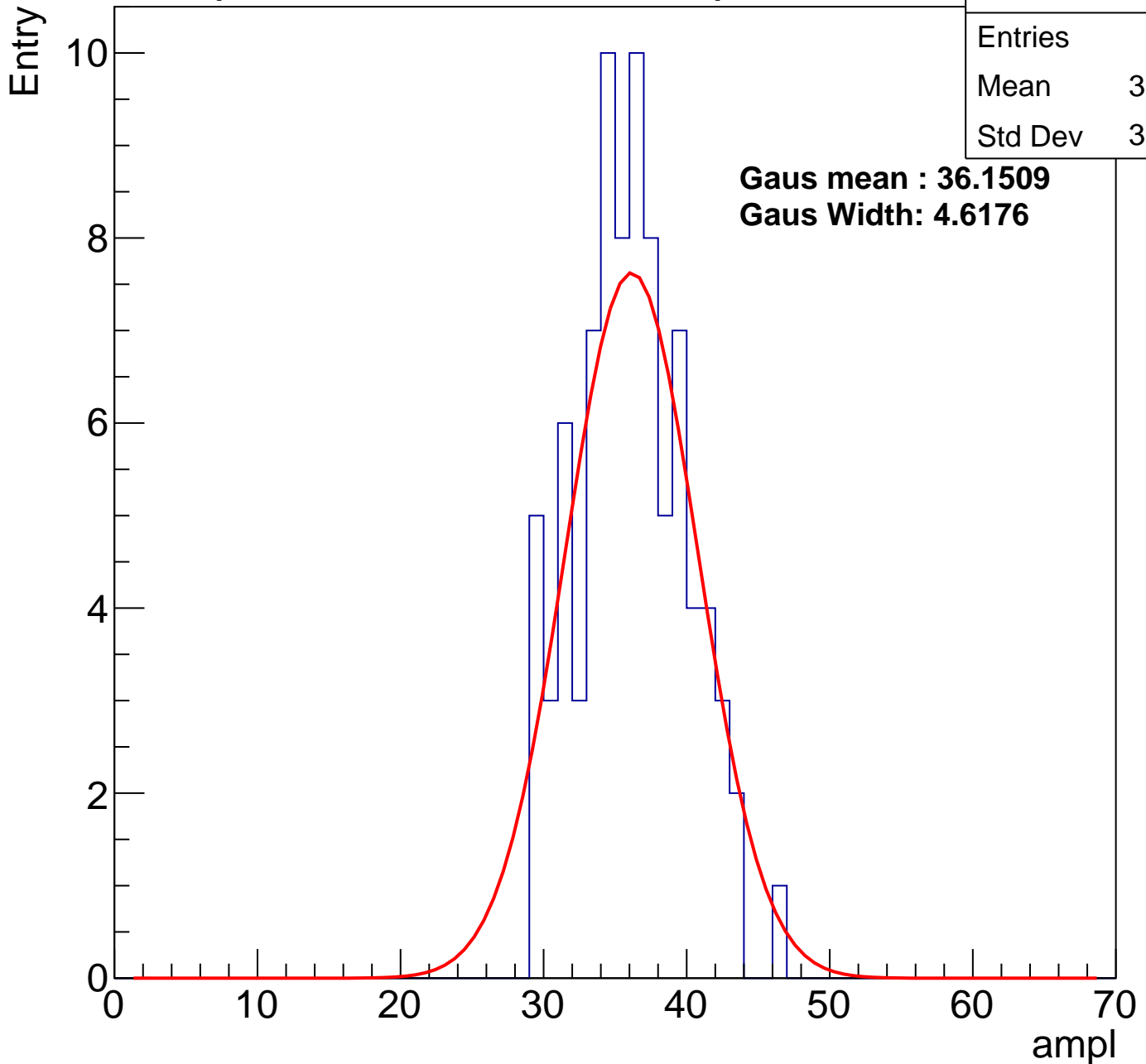
B1L100S, U1-ch2, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	86
Mean	35.69
Std Dev	3.758

Gaus mean : 36.1509

Gaus Width: 4.6176



B1L100S, U1-ch2, adc2

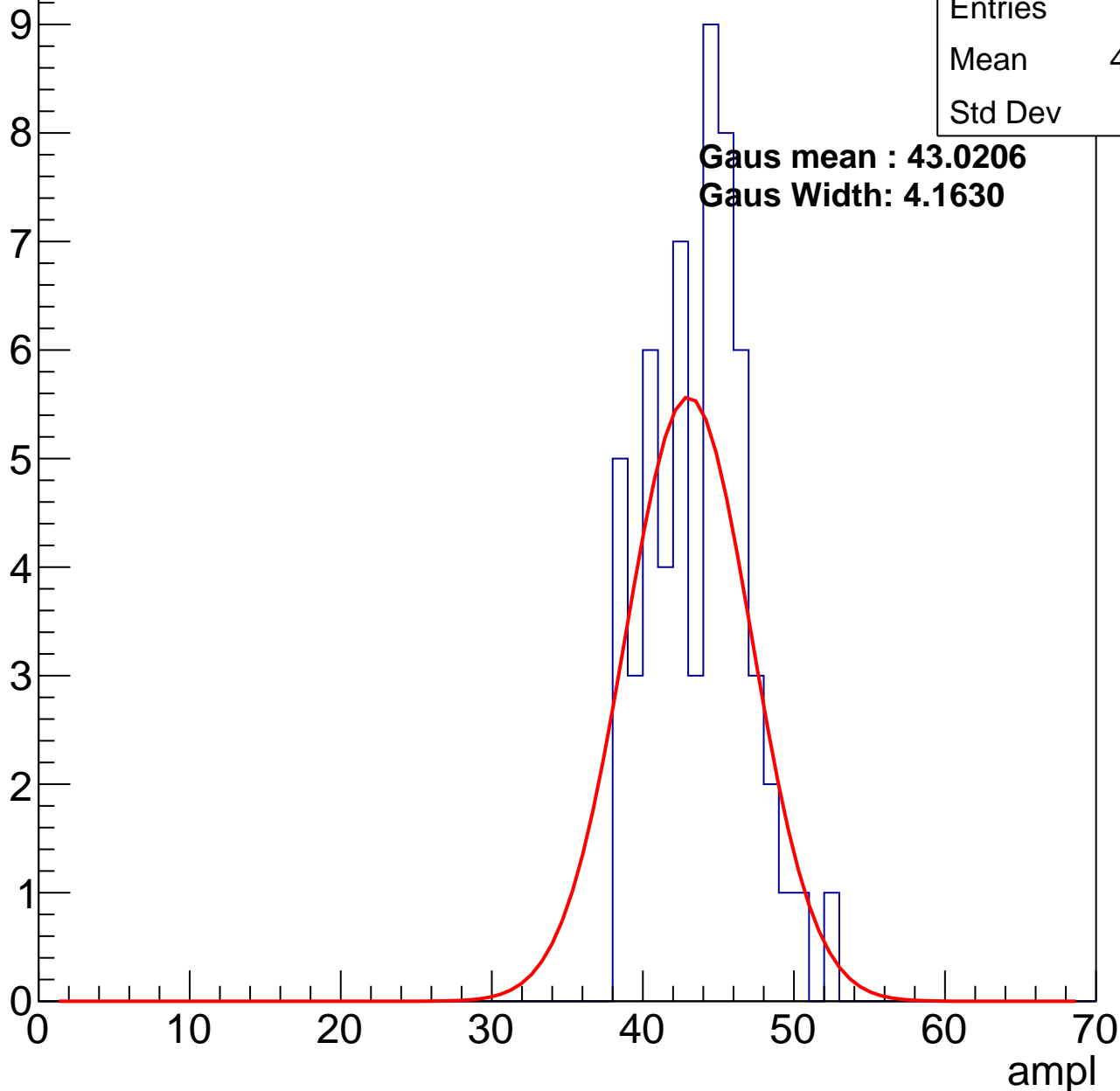
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	43.29
Std Dev	3.2

Gaus mean : 43.0206

Gaus Width: 4.1630

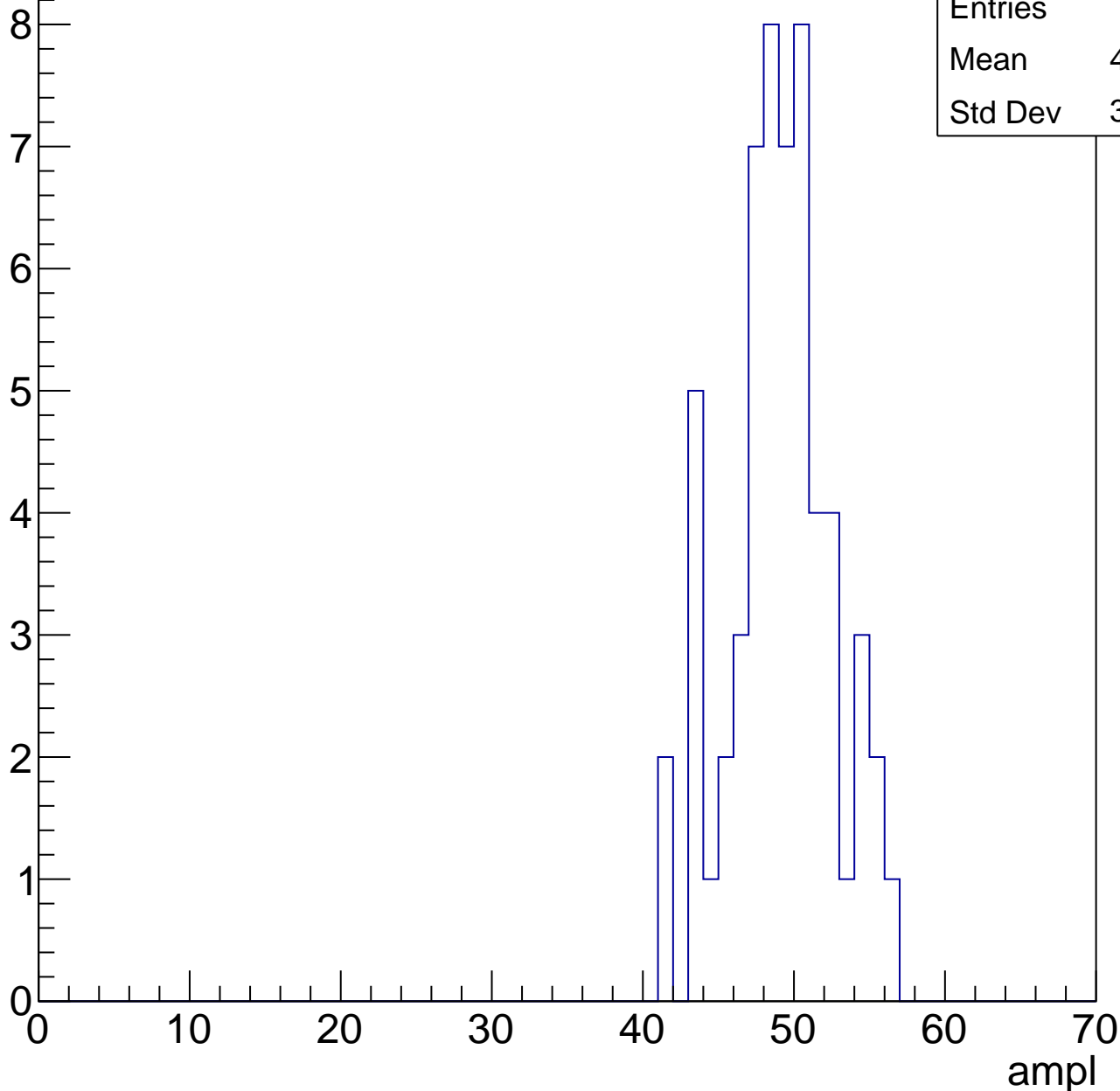


B1L100S, U1-ch2, adc3

calib_packv5_042523_0143.root, FC#4, port A2

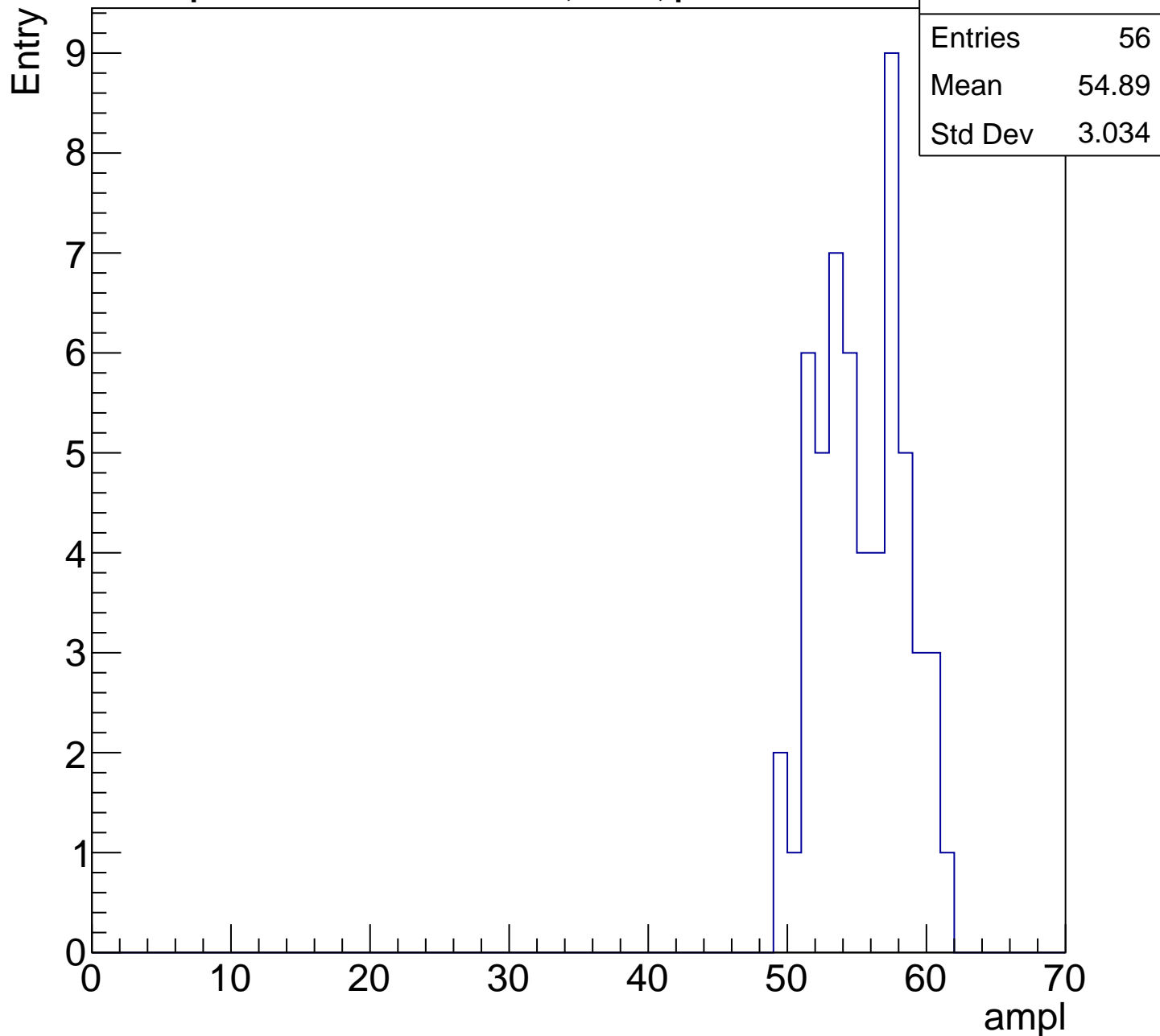
Entry

Entries	58
Mean	48.59
Std Dev	3.449



B1L100S, U1-ch2, adc4

calib_packv5_042523_0143.root, FC#4, port A2

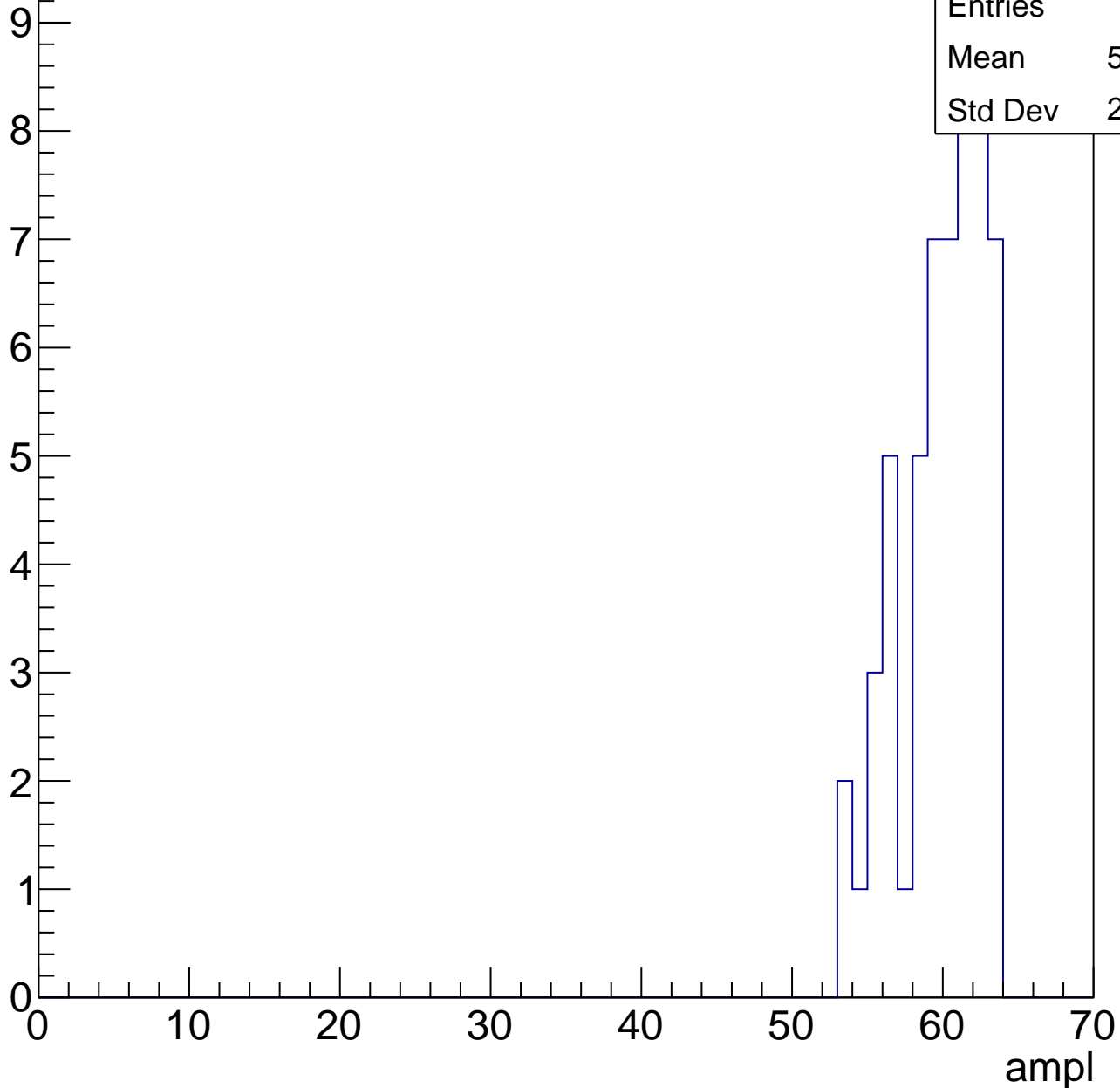


B1L100S, U1-ch2, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

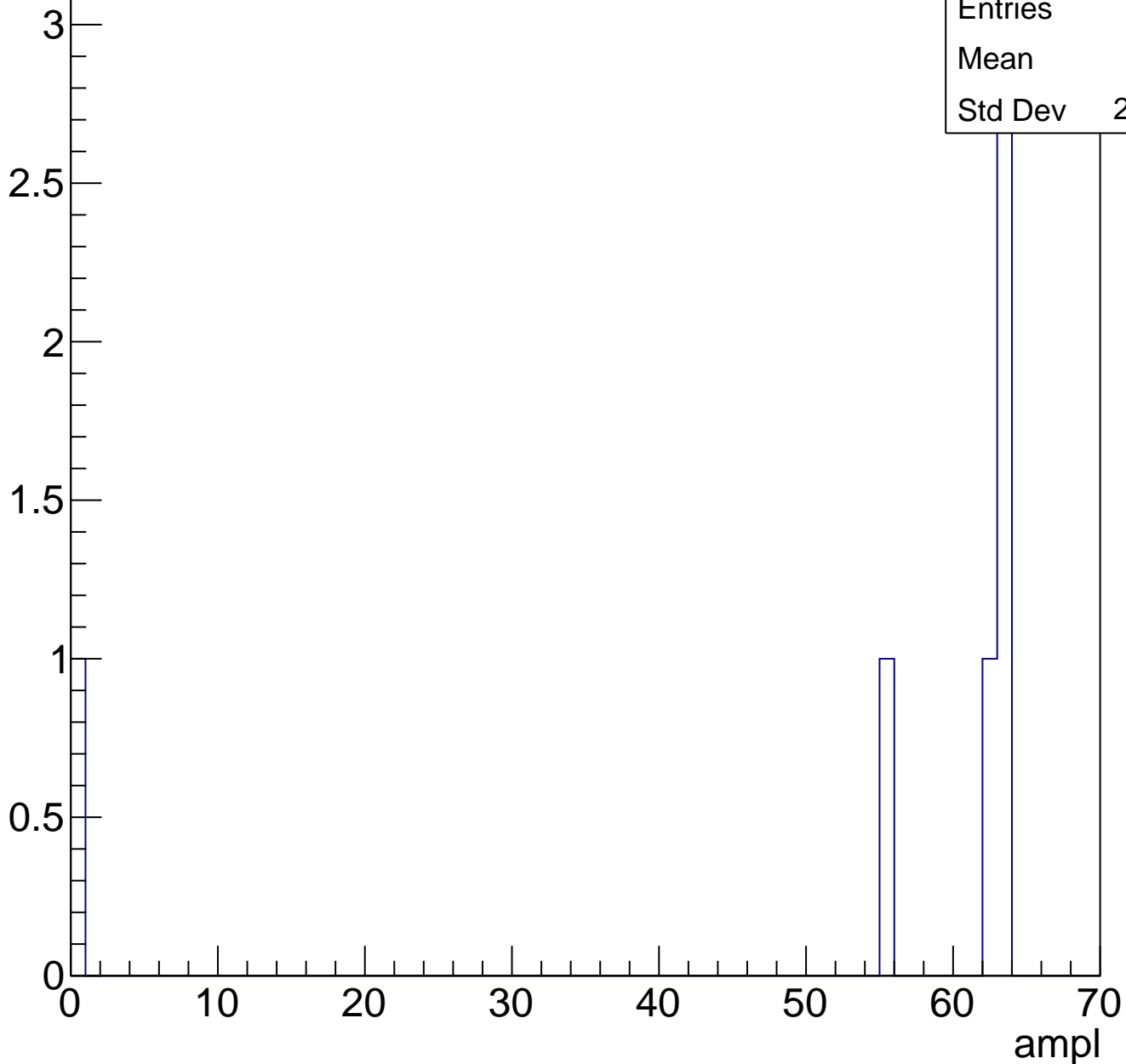
Entries	56
Mean	59.52
Std Dev	2.739



B1L100S, U1-ch2, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch2, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch3, adc0

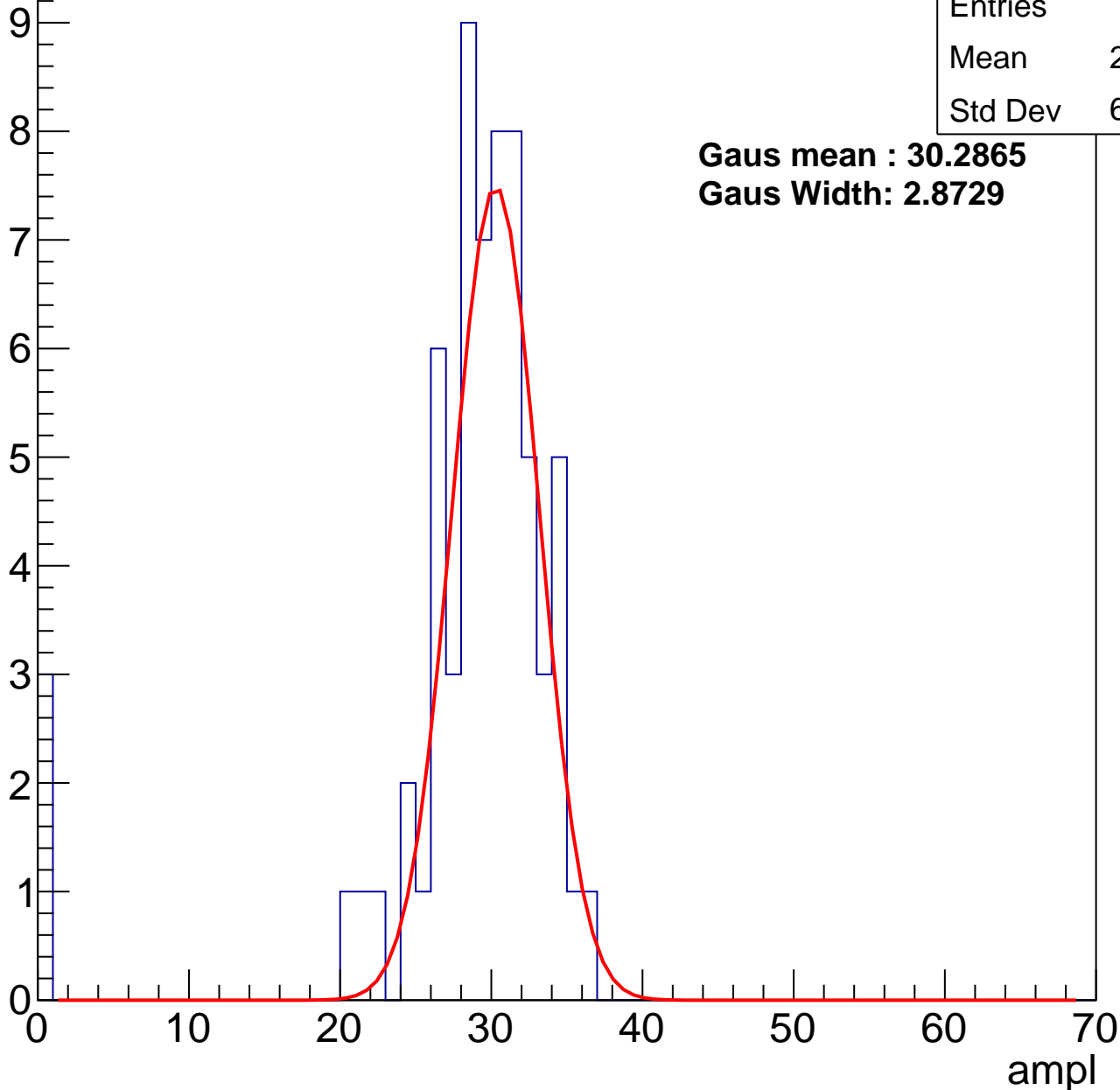
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	27.94
Std Dev	6.937

Gaus mean : 30.2865

Gaus Width: 2.8729



B1L100S, U1-ch3, adc1

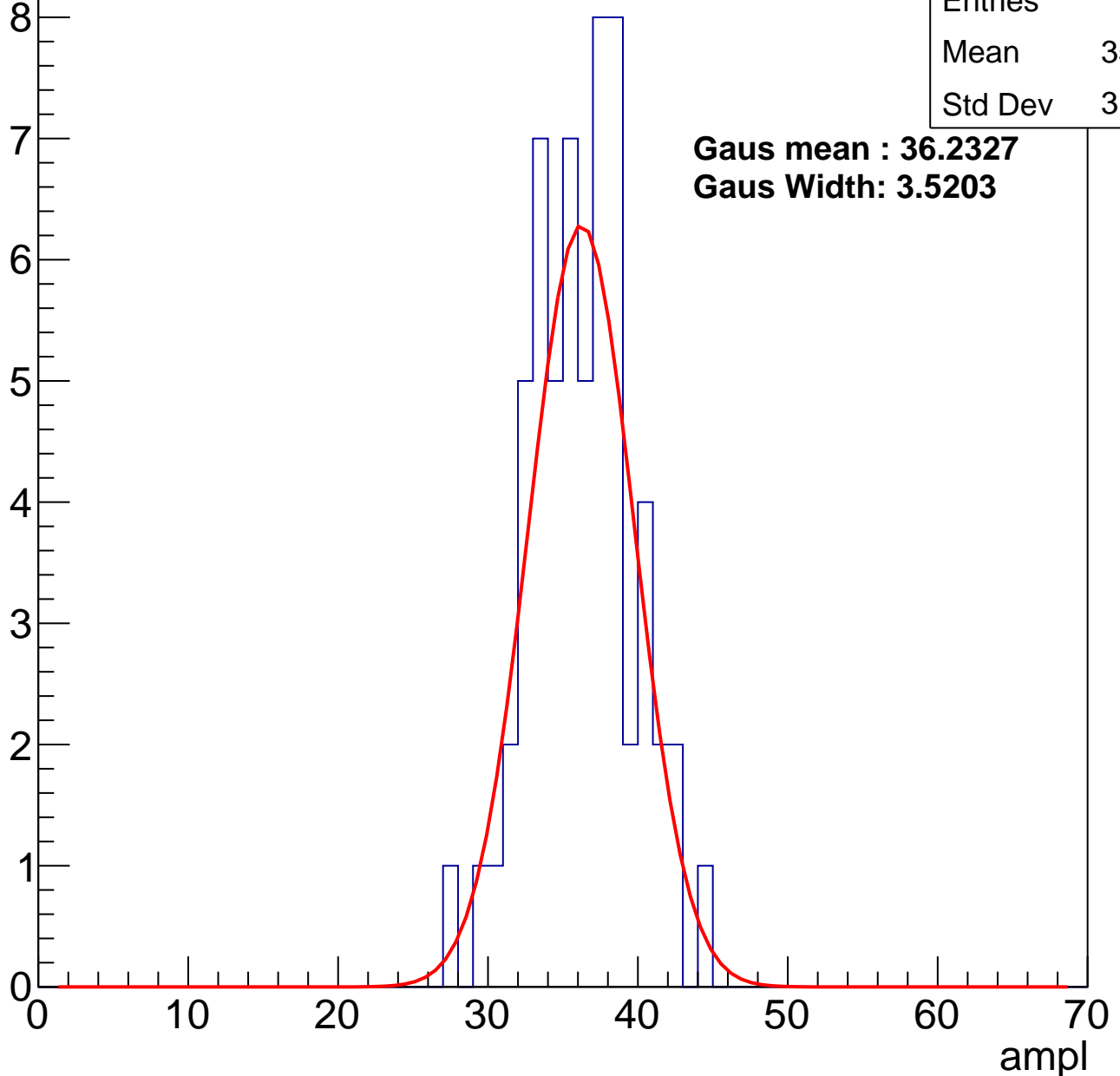
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.77
Std Dev	3.365

Gaus mean : 36.2327

Gaus Width: 3.5203



B1L100S, U1-ch3, adc2

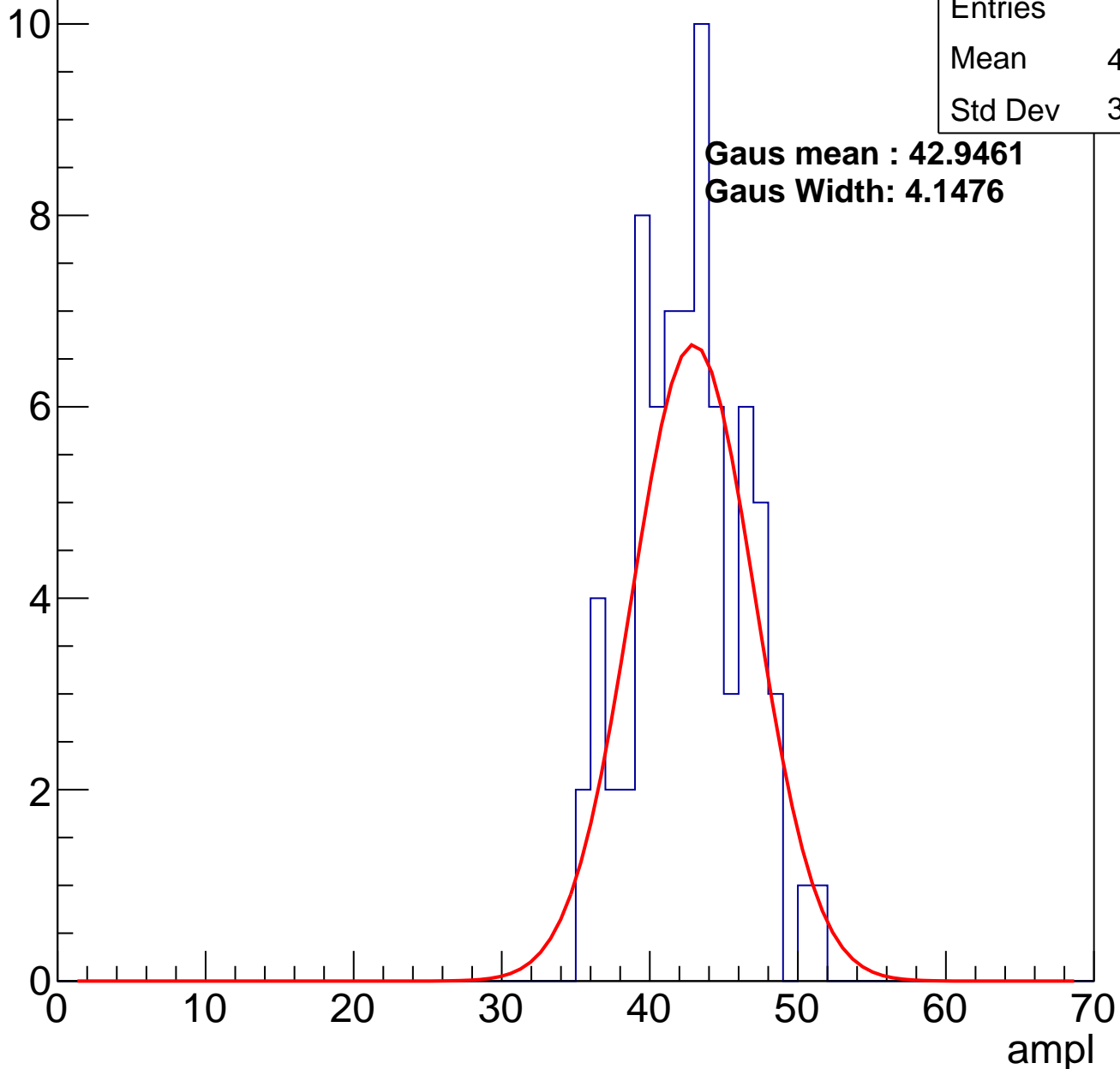
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	42.22
Std Dev	3.624

Gaus mean : 42.9461

Gaus Width: 4.1476

Entry

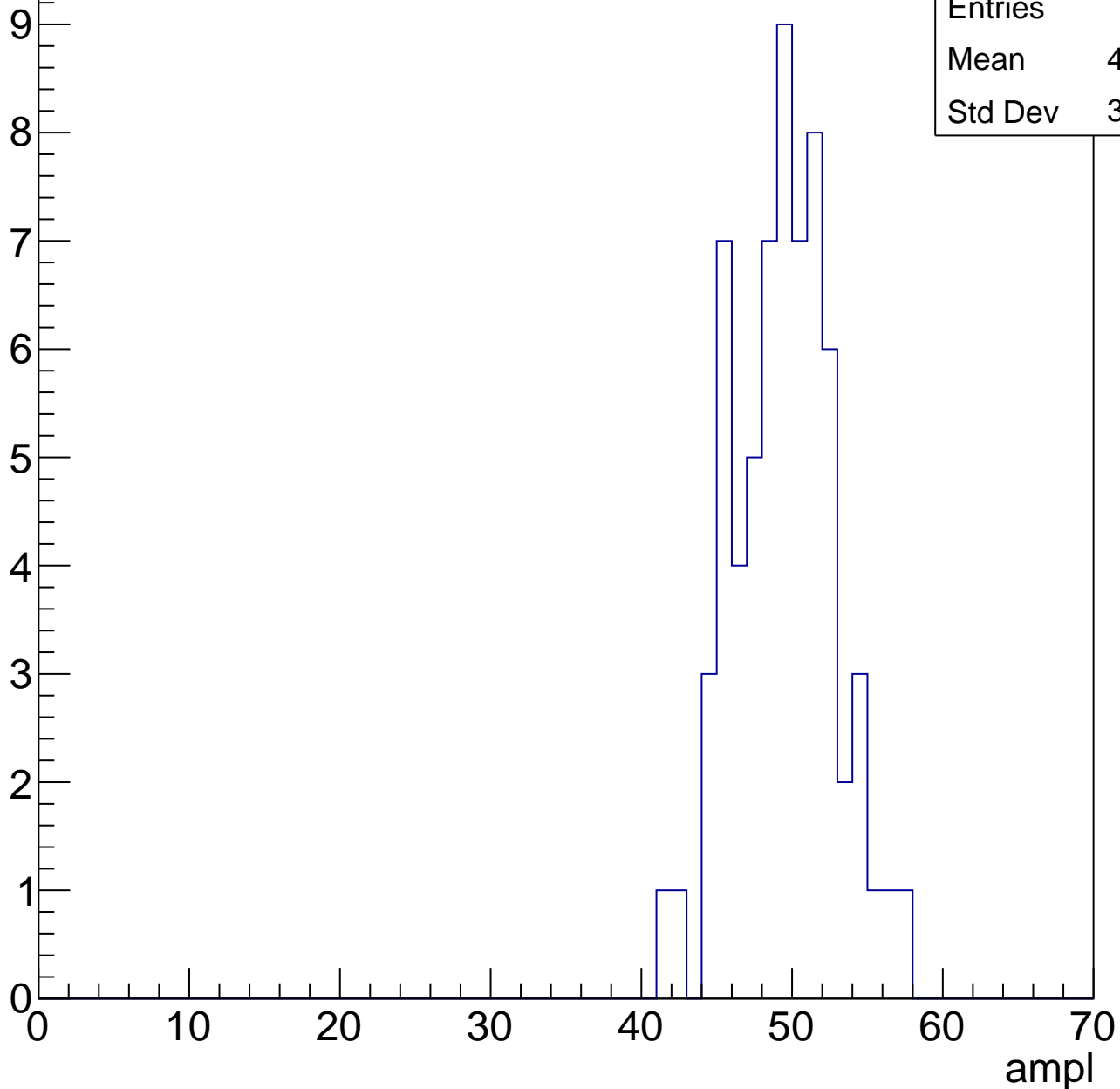


B1L100S, U1-ch3, adc3

calib_packv5_042523_0143.root, FC#4, port A2

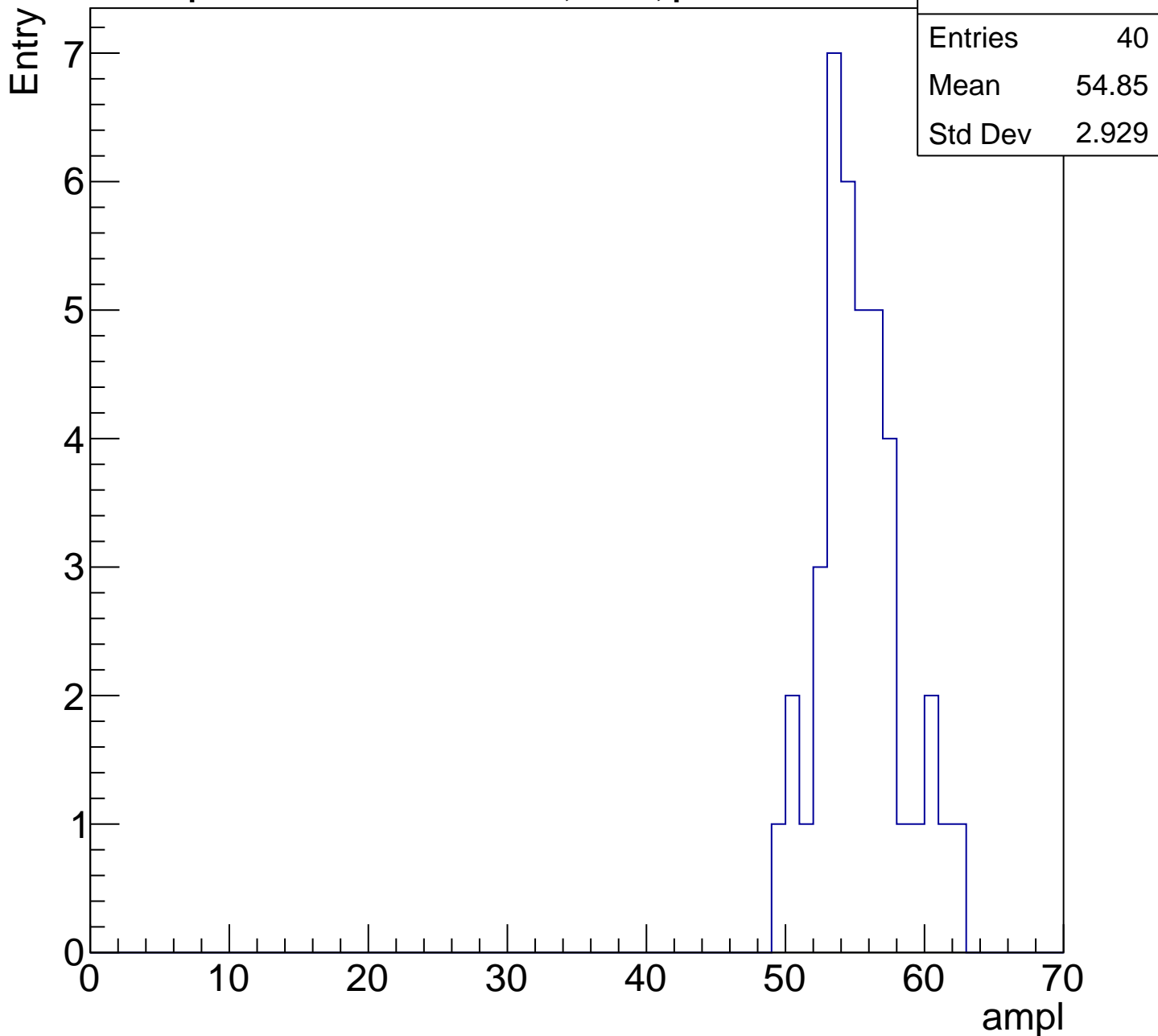
Entry

Entries	66
Mean	48.97
Std Dev	3.284



B1L100S, U1-ch3, adc4

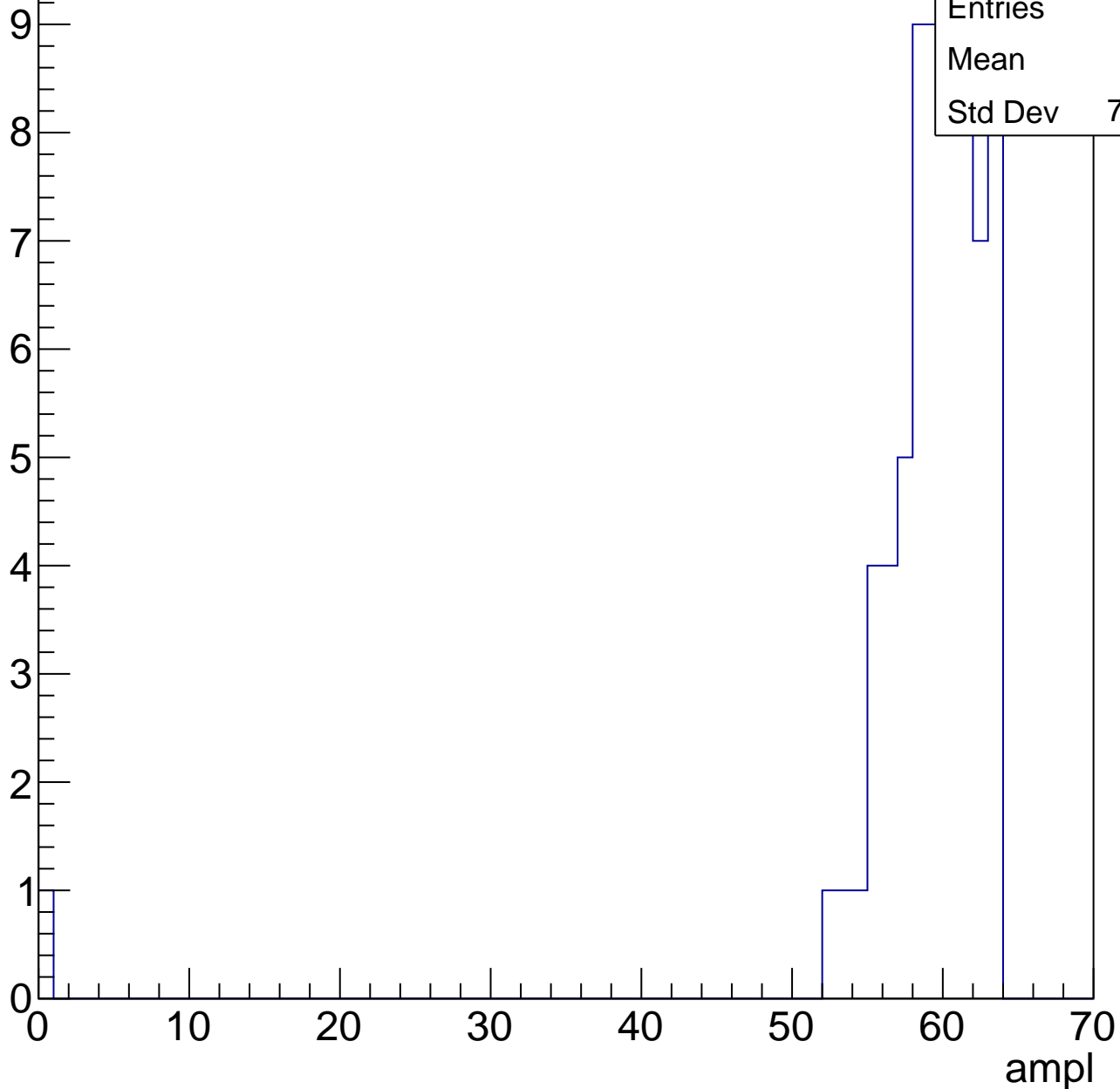
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch3, adc5

calib_packv5_042523_0143.root, FC#4, port A2

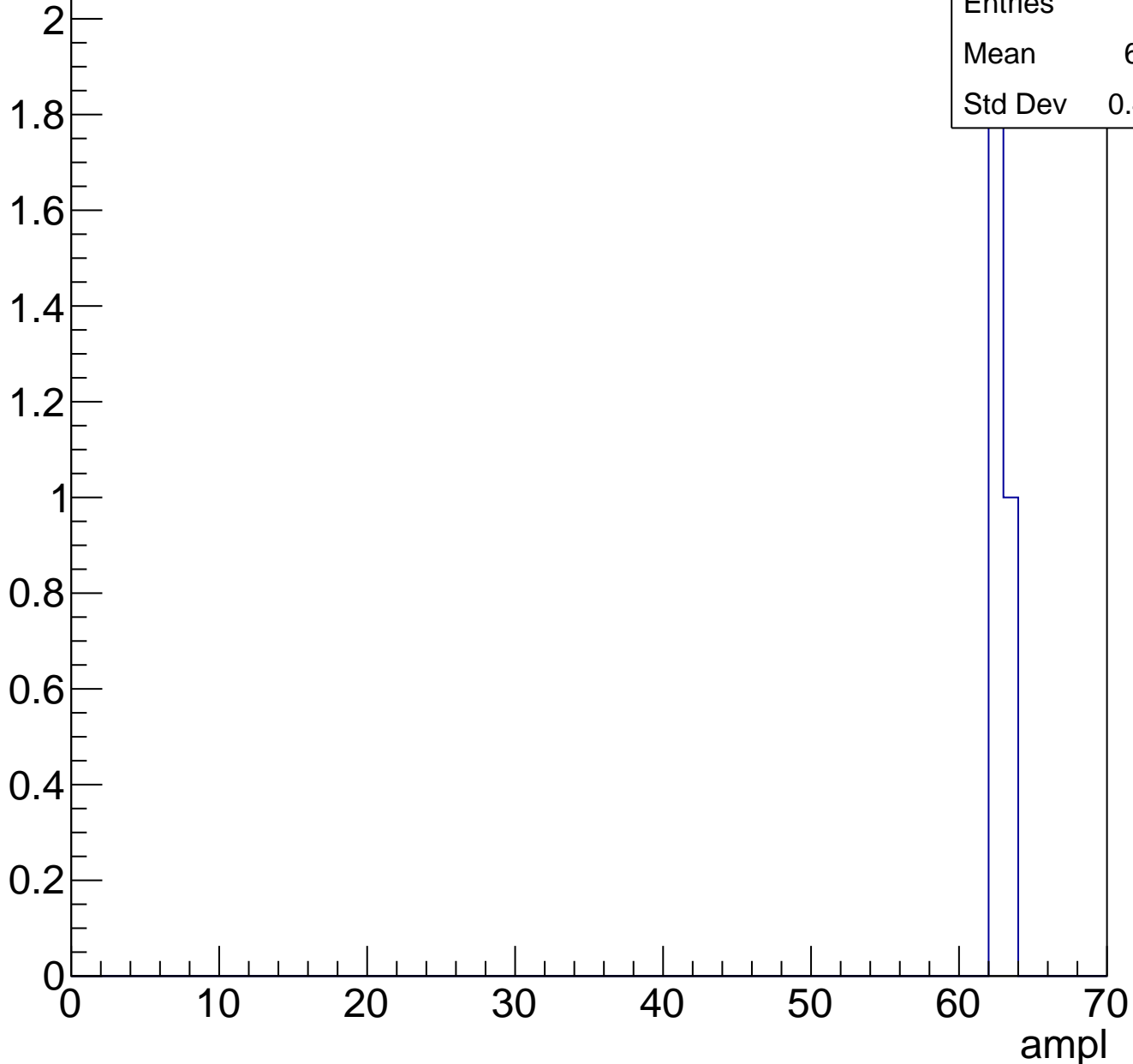
Entry



B1L100S, U1-ch3, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch3, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch4, adc0

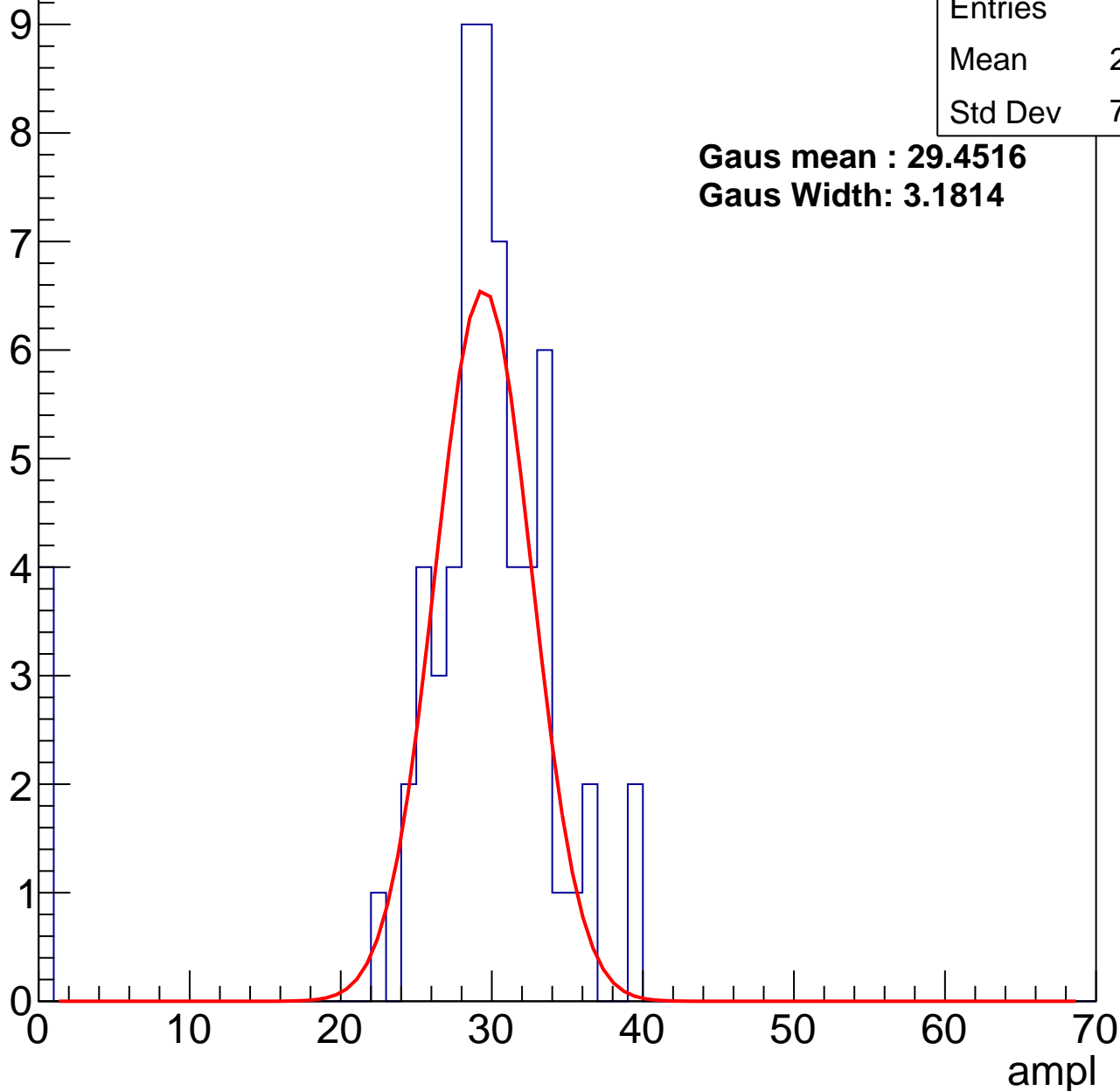
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.75
Std Dev	7.962

Gaus mean : 29.4516

Gaus Width: 3.1814



B1L100S, U1-ch4, adc1

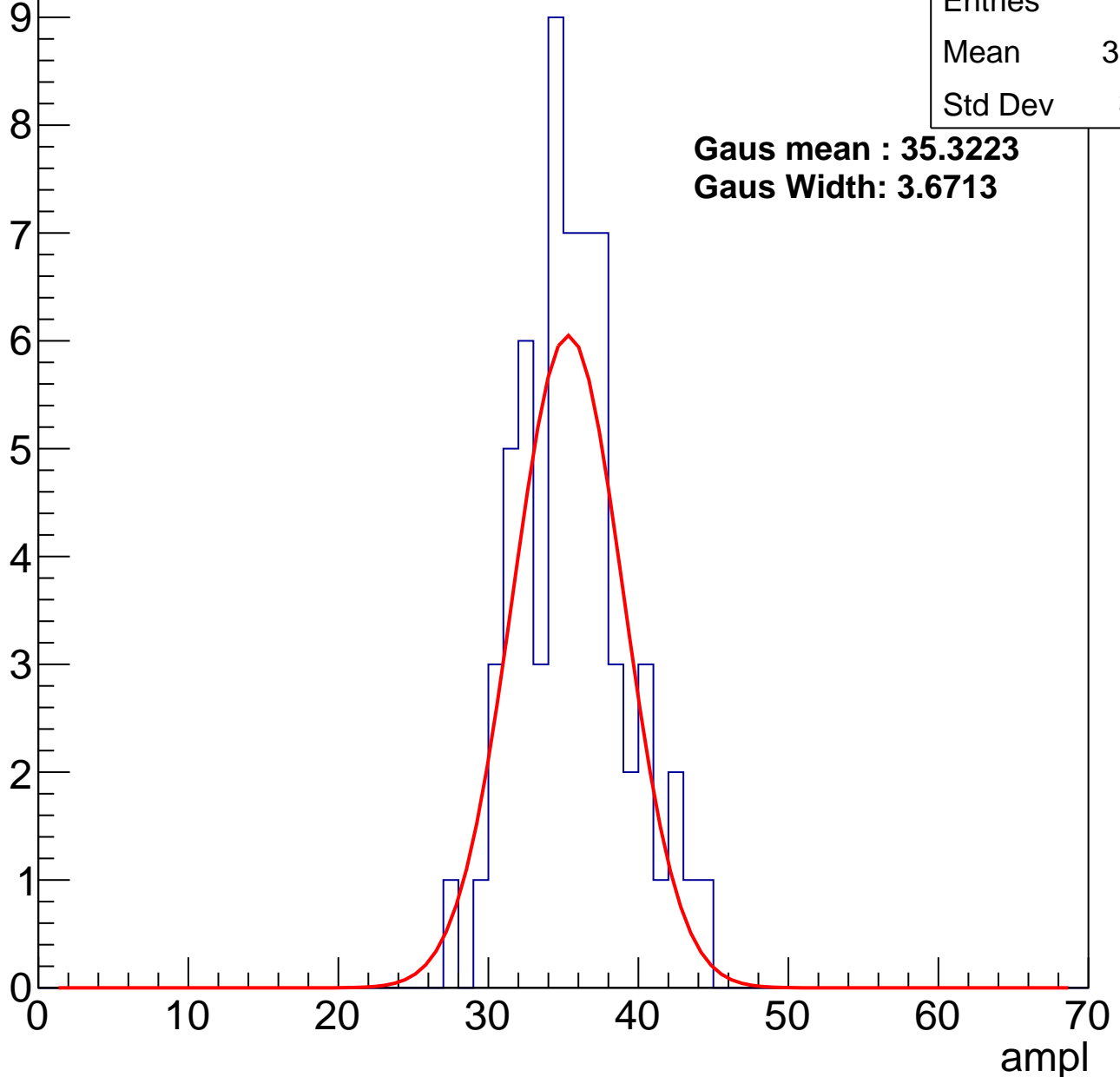
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	35.13
Std Dev	3.54

Gaus mean : 35.3223

Gaus Width: 3.6713



B1L100S, U1-ch4, adc2

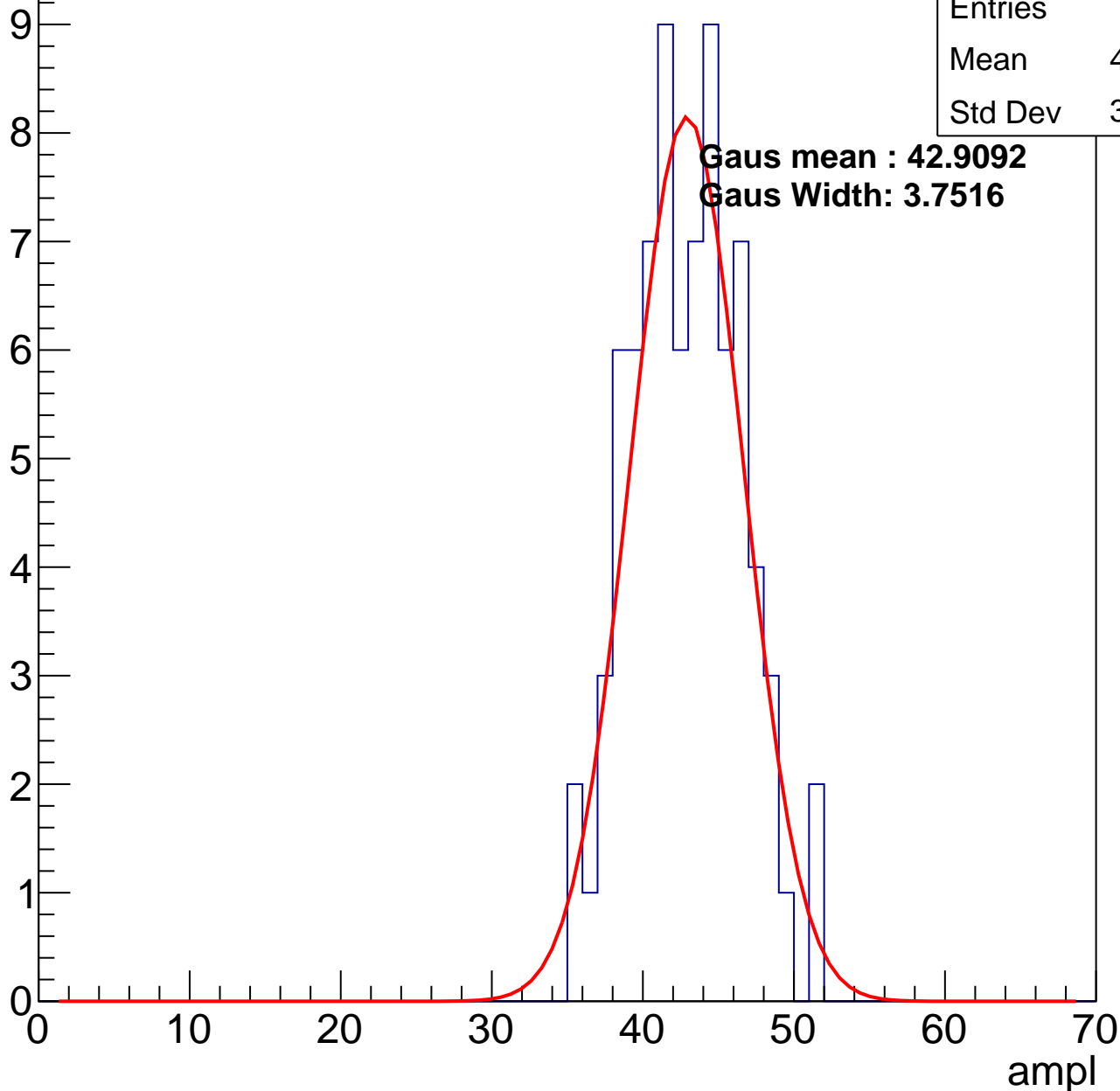
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	42.43
Std Dev	3.574

Gaus mean : 42.9092

Gaus Width: 3.7516

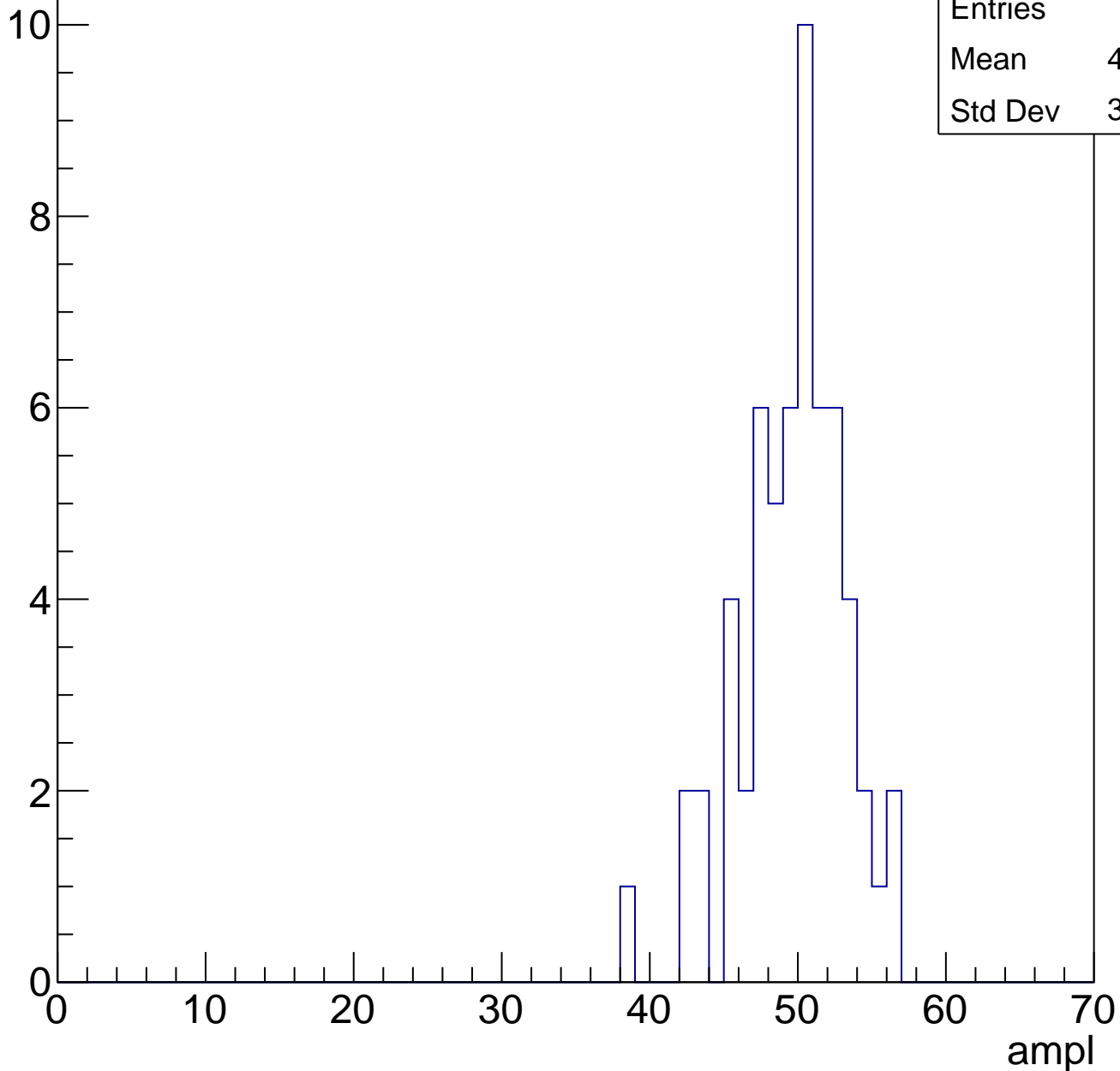


B1L100S, U1-ch4, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	59
Mean	49.17
Std Dev	3.533

Entry

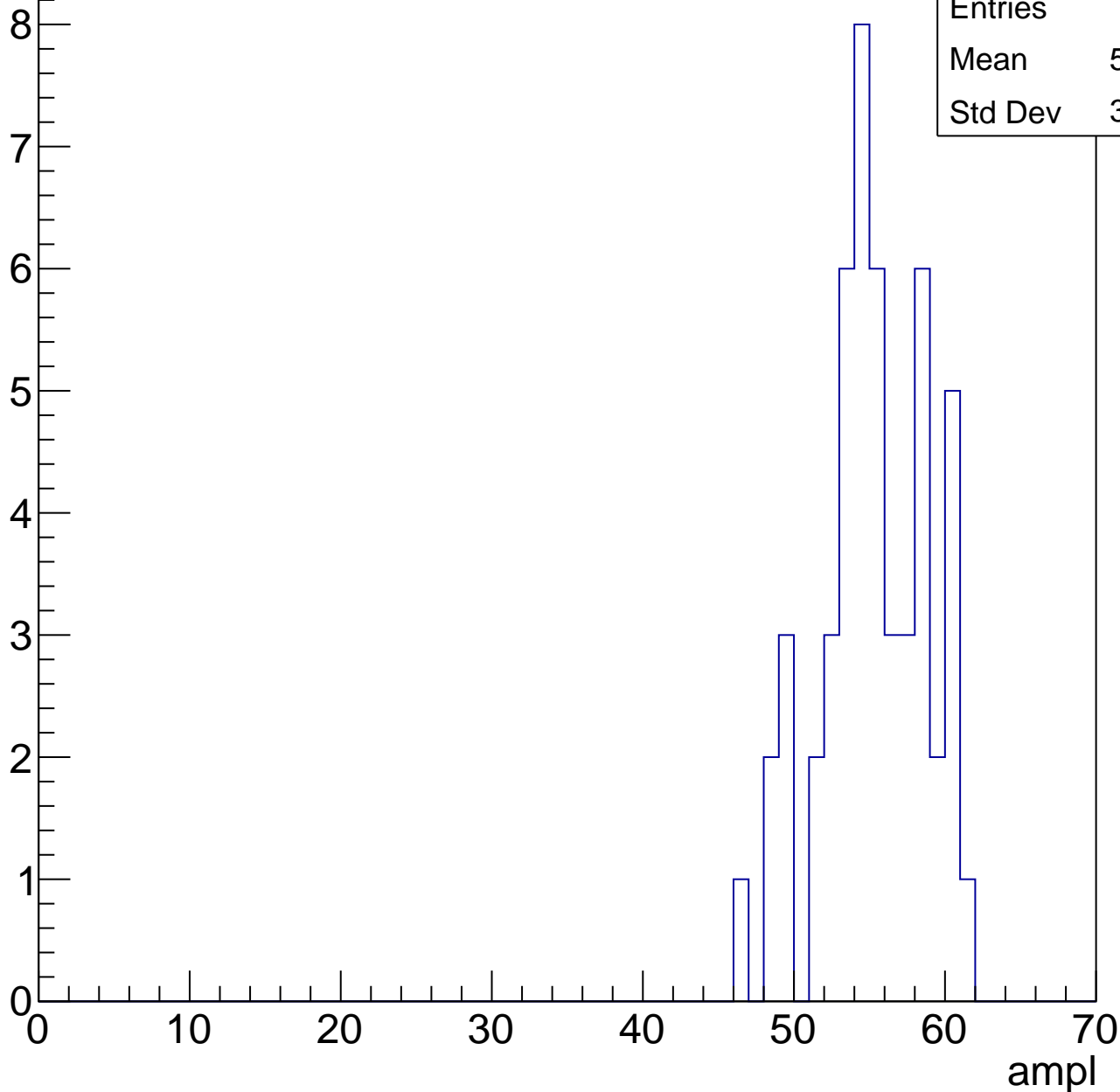


B1L100S, U1-ch4, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

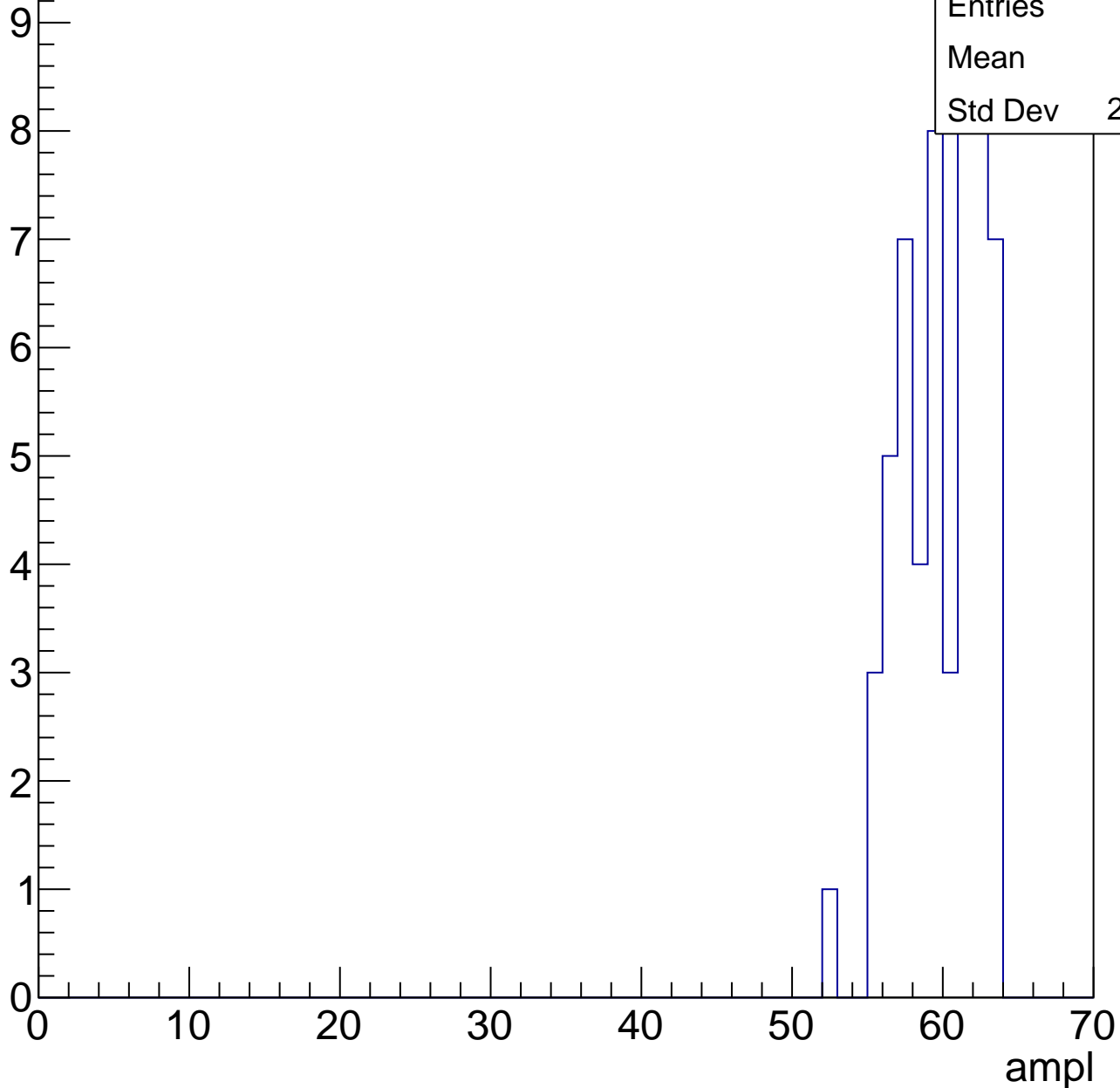
Entries	51
Mean	54.76
Std Dev	3.535



B1L100S, U1-ch4, adc5

calib_packv5_042523_0143.root, FC#4, port A2

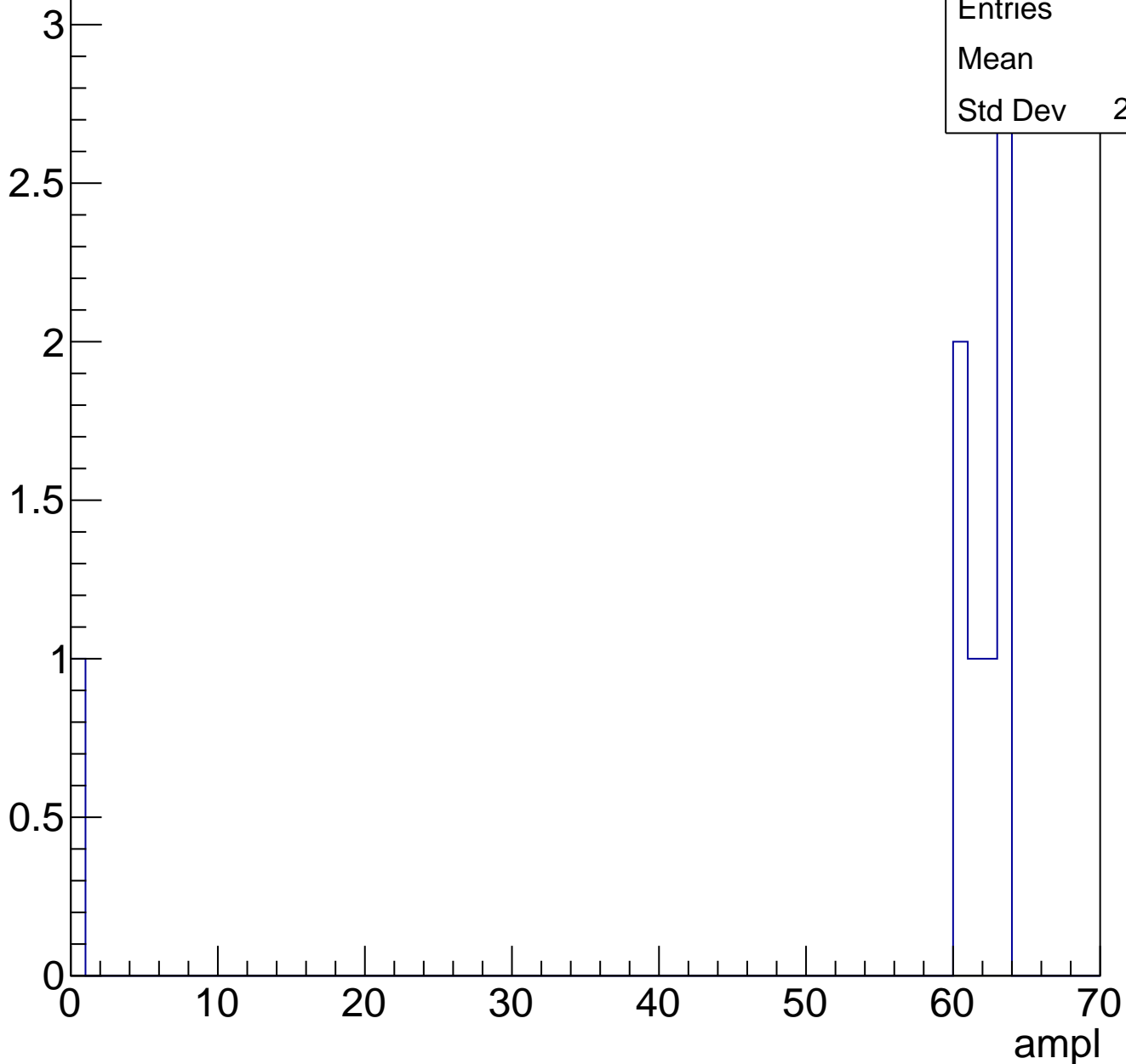
Entry



B1L100S, U1-ch4, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch4, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch5, adc0

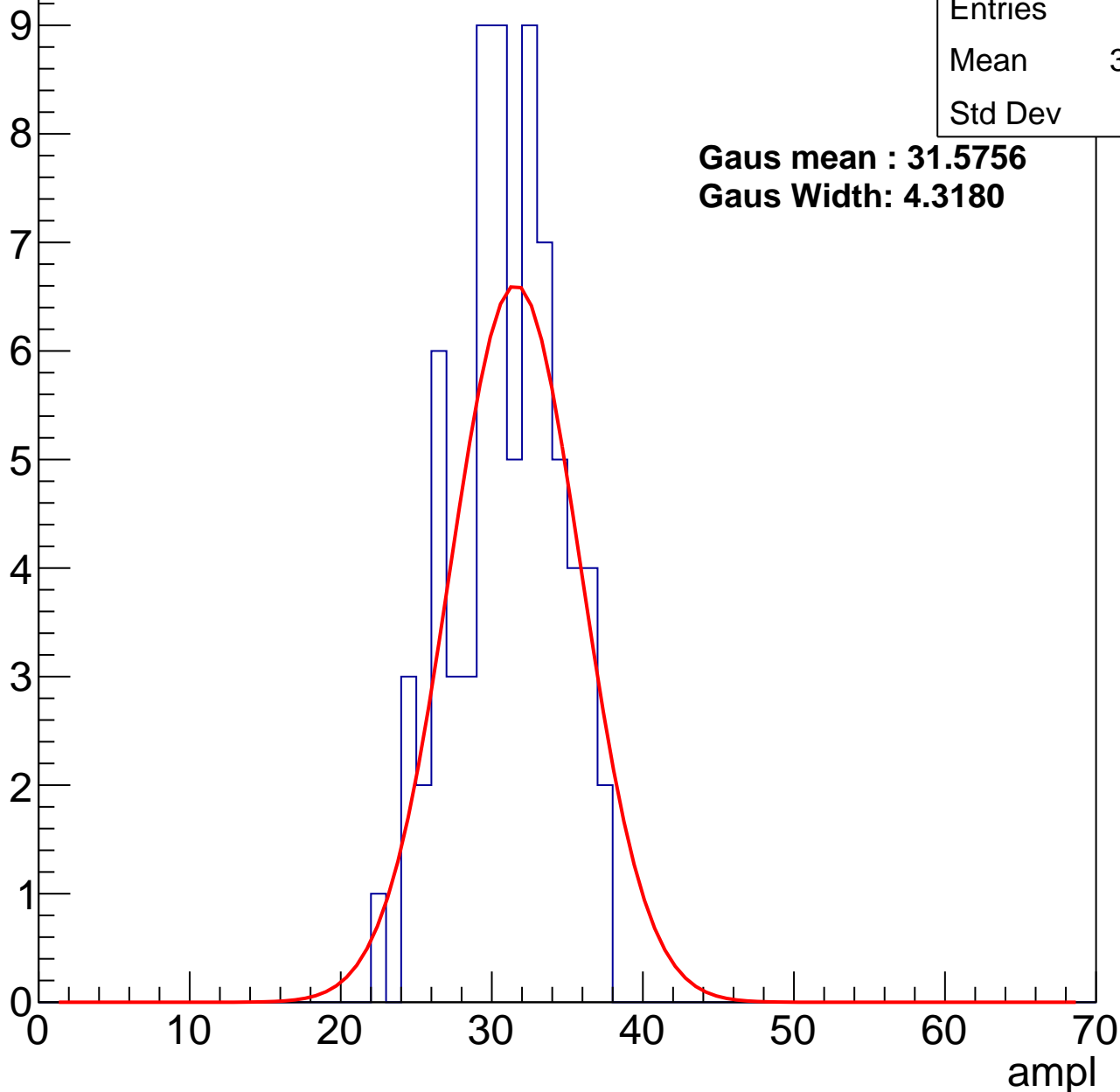
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.53
Std Dev	3.48

Gaus mean : 31.5756

Gaus Width: 4.3180



B1L100S, U1-ch5, adc1

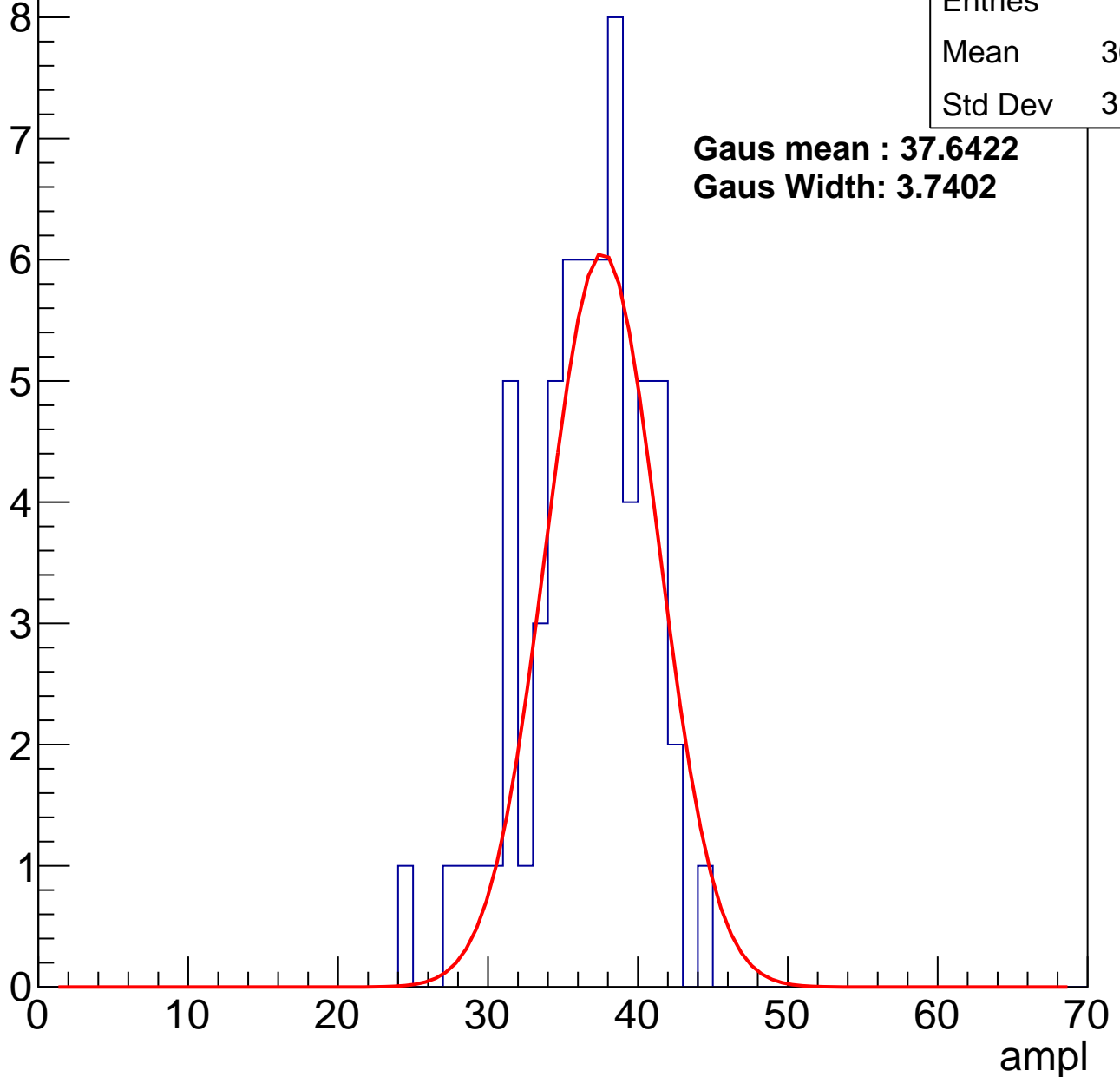
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	36.05
Std Dev	3.982

Gaus mean : 37.6422

Gaus Width: 3.7402



B1L100S, U1-ch5, adc2

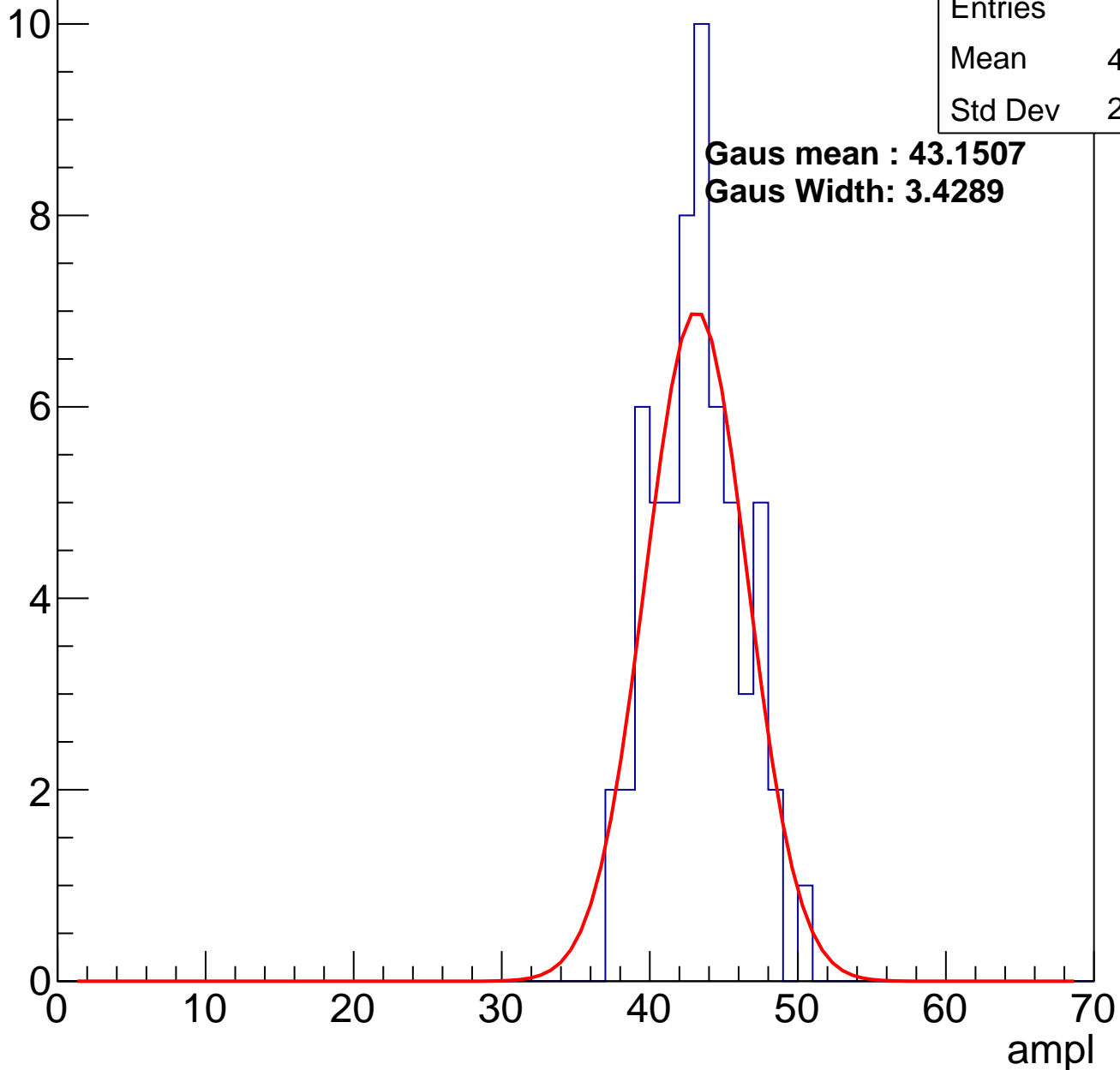
calib_packv5_042523_0143.root, FC#4, port A2

Entries	60
Mean	42.72
Std Dev	2.939

Gaus mean : 43.1507

Gaus Width: 3.4289

Entry



B1L100S, U1-ch5, adc3

calib_packv5_042523_0143.root, FC#4, port A2

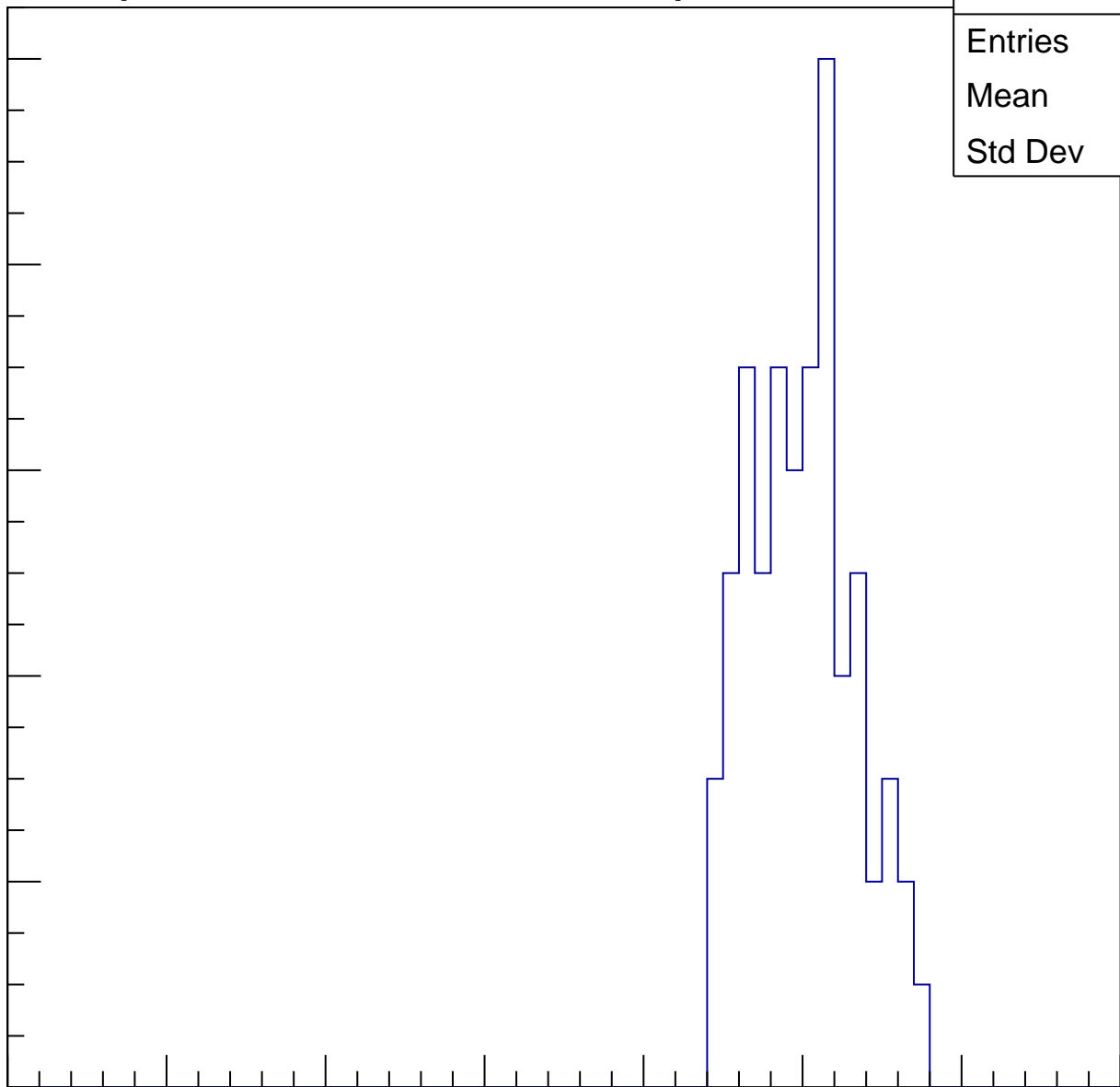
Entries	67
Mean	49.54
Std Dev	3.252

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

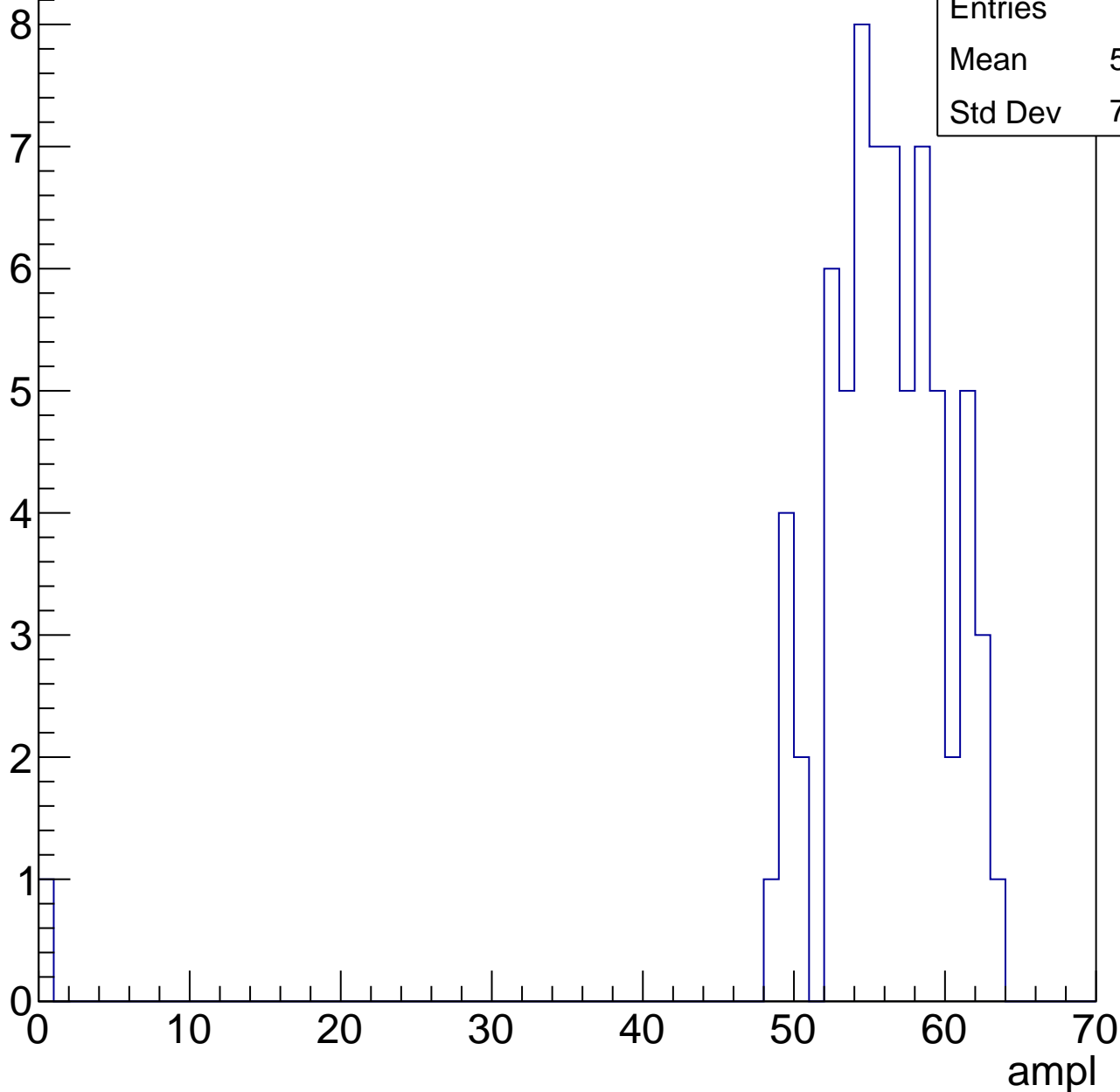


B1L100S, U1-ch5, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	54.93
Std Dev	7.576



B1L100S, U1-ch5, adc5

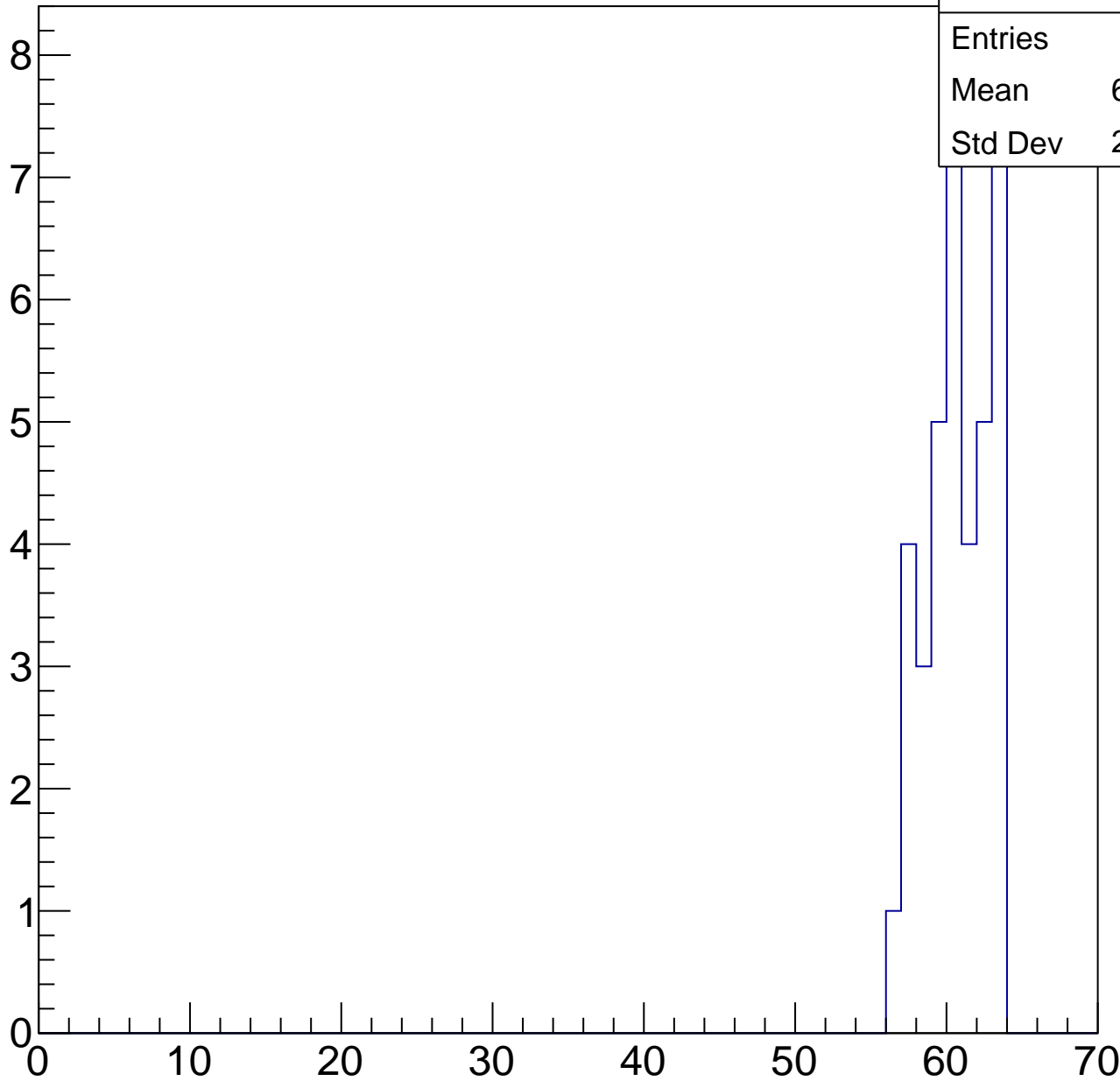
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	38
Mean	60.29
Std Dev	2.064

ampl



B1L100S, U1-ch5, adc6

calib_packv5_042523_0143.root, FC#4, port A2

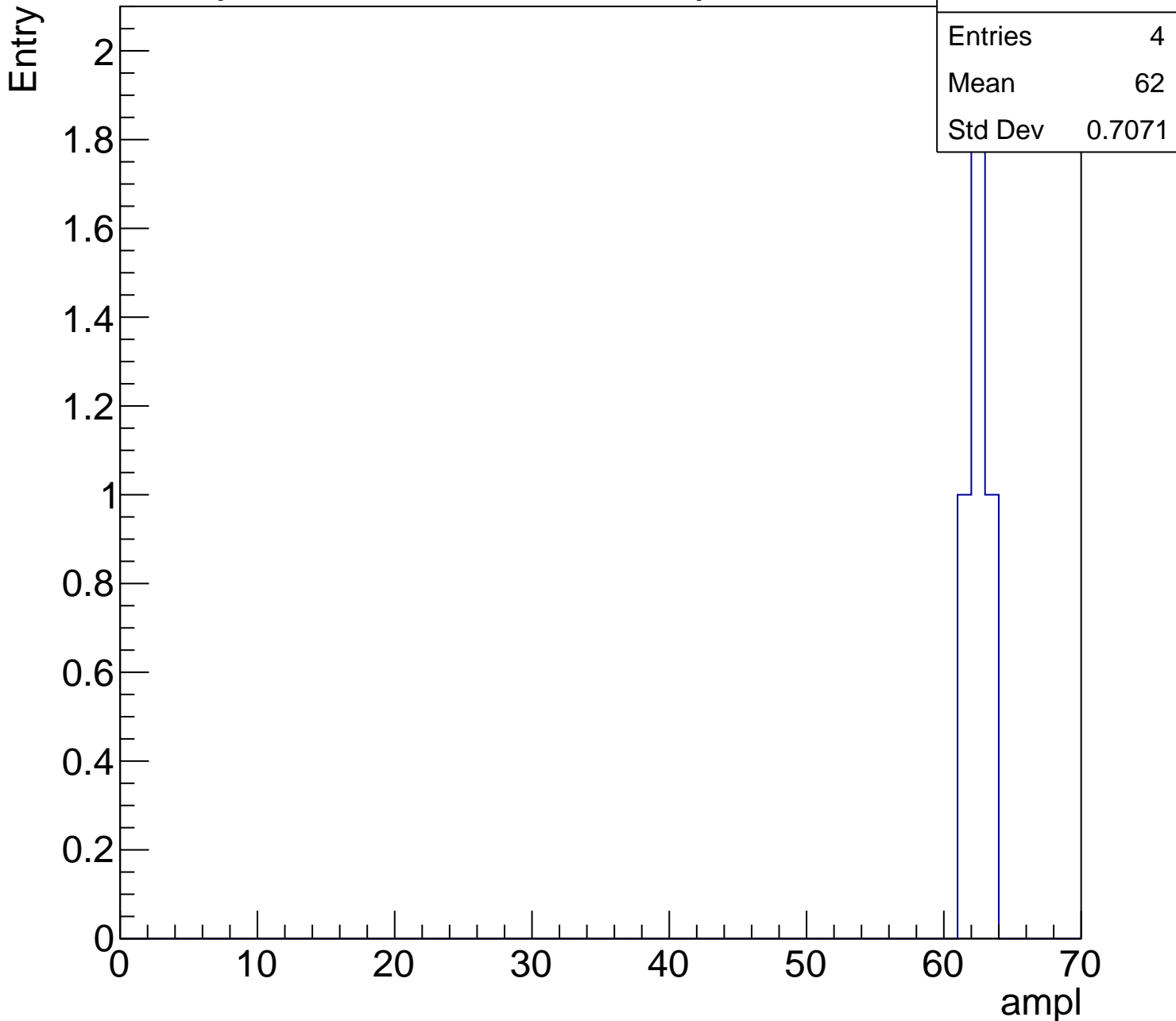
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	4
Mean	62
Std Dev	0.7071

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch5, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch6, adc0

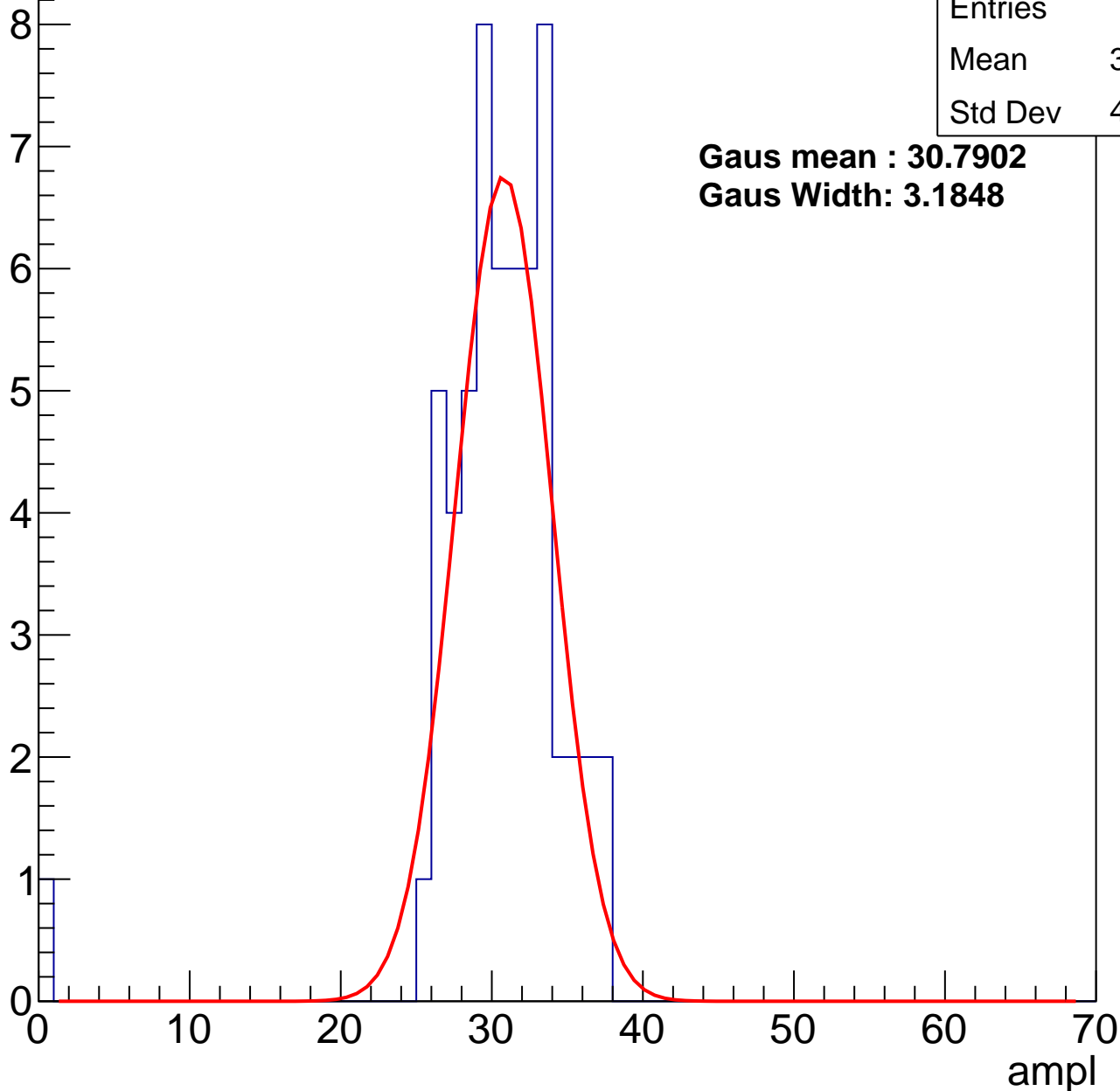
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	30.02
Std Dev	4.953

Gaus mean : 30.7902

Gaus Width: 3.1848



B1L100S, U1-ch6, adc1

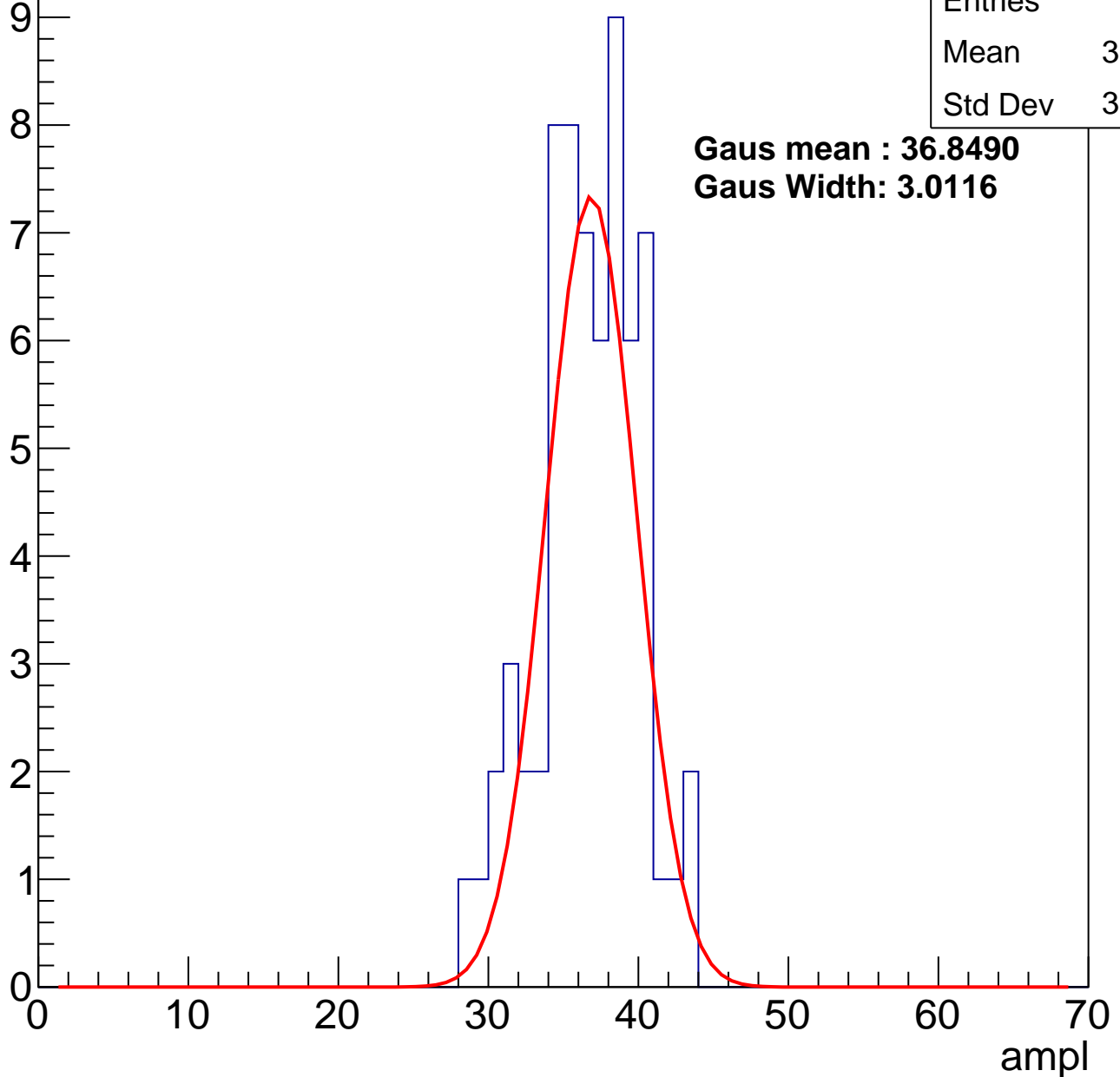
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.23
Std Dev	3.288

Gaus mean : 36.8490

Gaus Width: 3.0116



B1L100S, U1-ch6, adc2

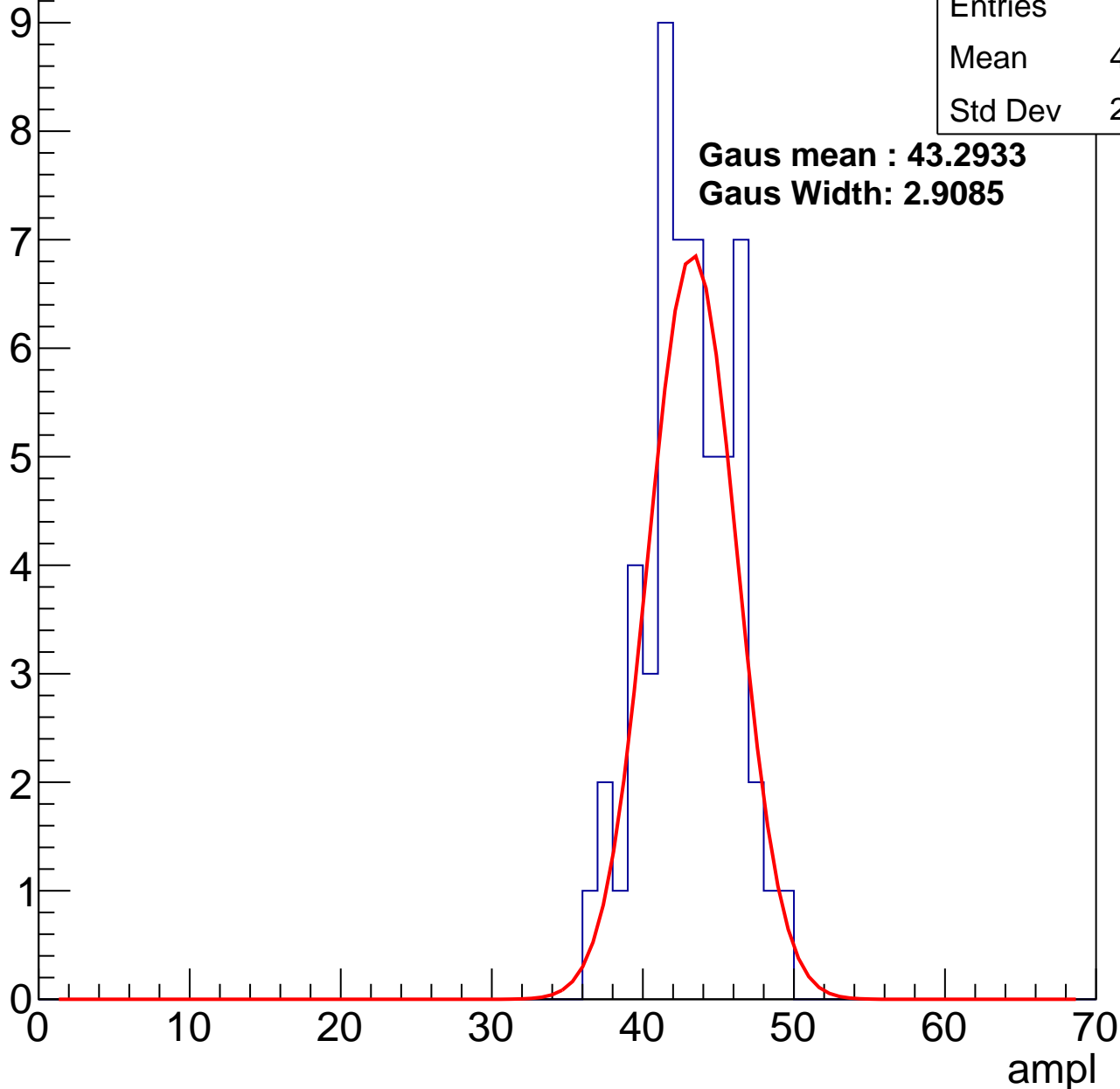
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	42.65
Std Dev	2.874

Gaus mean : 43.2933

Gaus Width: 2.9085



B1L100S, U1-ch6, adc3

calib_packv5_042523_0143.root, FC#4, port A2

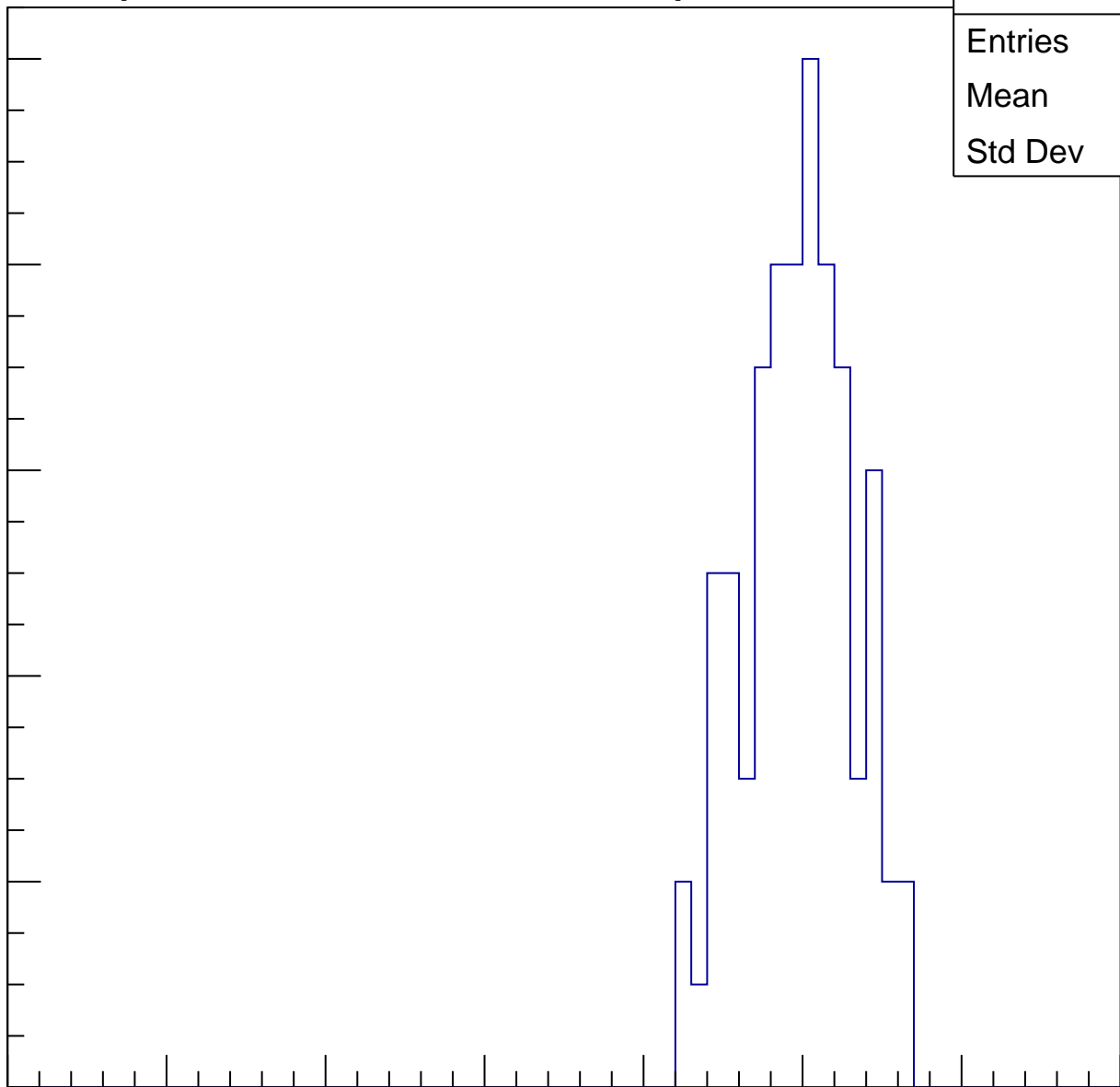
Entries	77
Mean	49.25
Std Dev	3.362

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

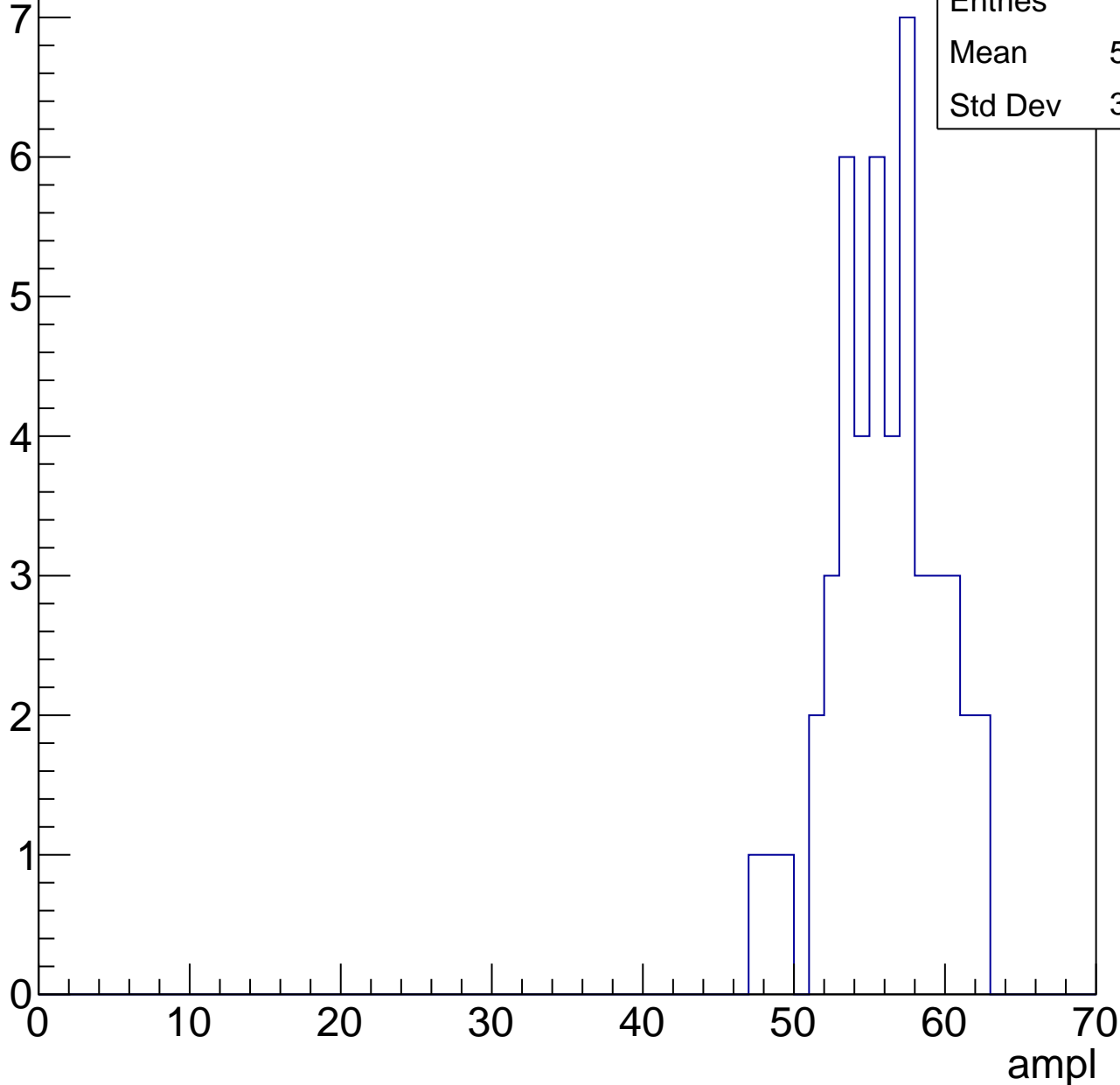
ampl



B1L100S, U1-ch6, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch6, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.33
Std Dev	8.581

ampl

0 10 20 30 40 50 60 70

1

2

3

4

5

6

7

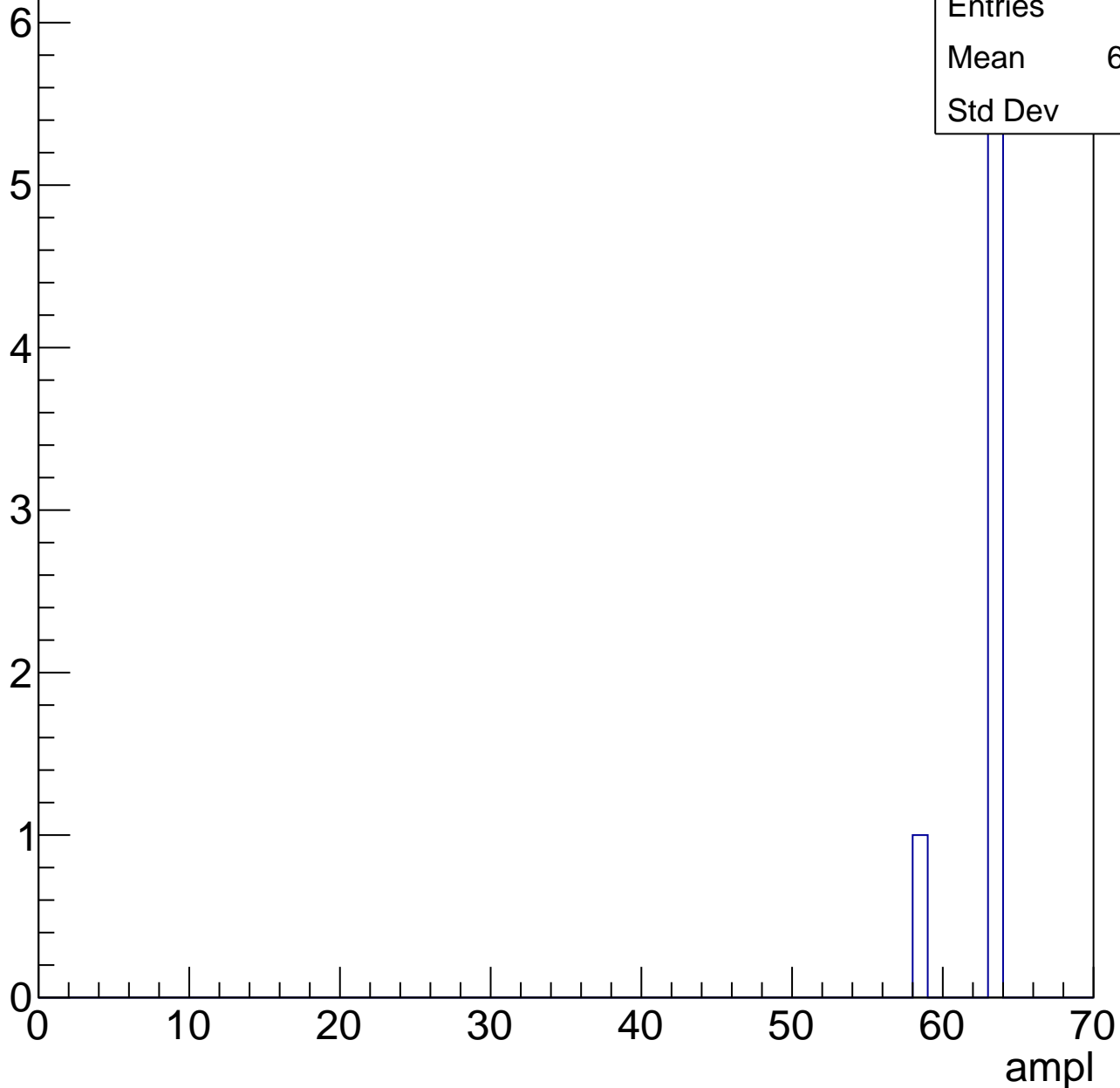
8

B1L100S, U1-ch6, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	62.29
Std Dev	1.75



B1L100S, U1-ch6, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	2
Mean	12
Std Dev	12

ampl

B1L100S, U1-ch7, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	29.64
Std Dev	4.993

Gaus mean : 30.6715

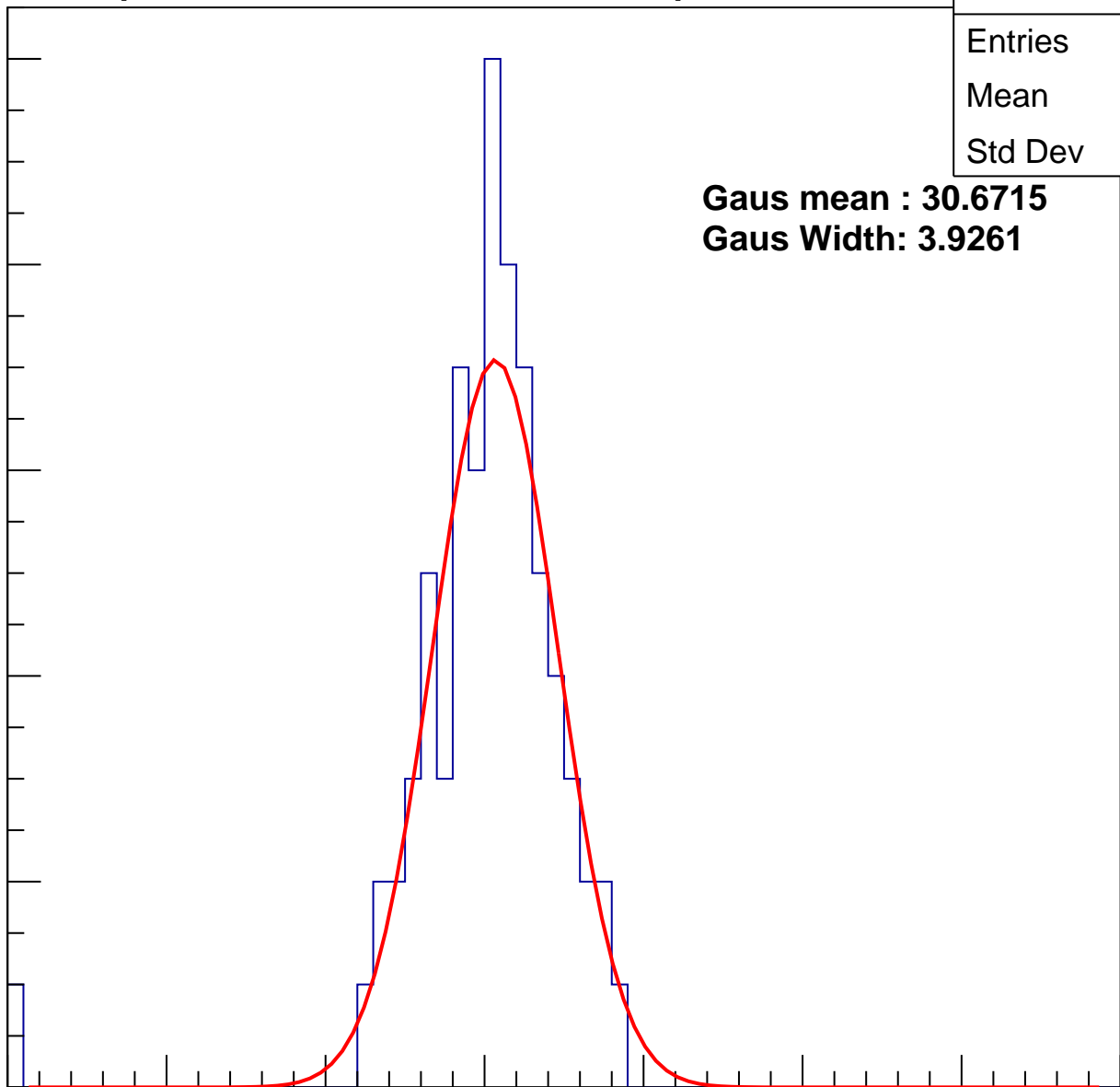
Gaus Width: 3.9261

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch7, adc1

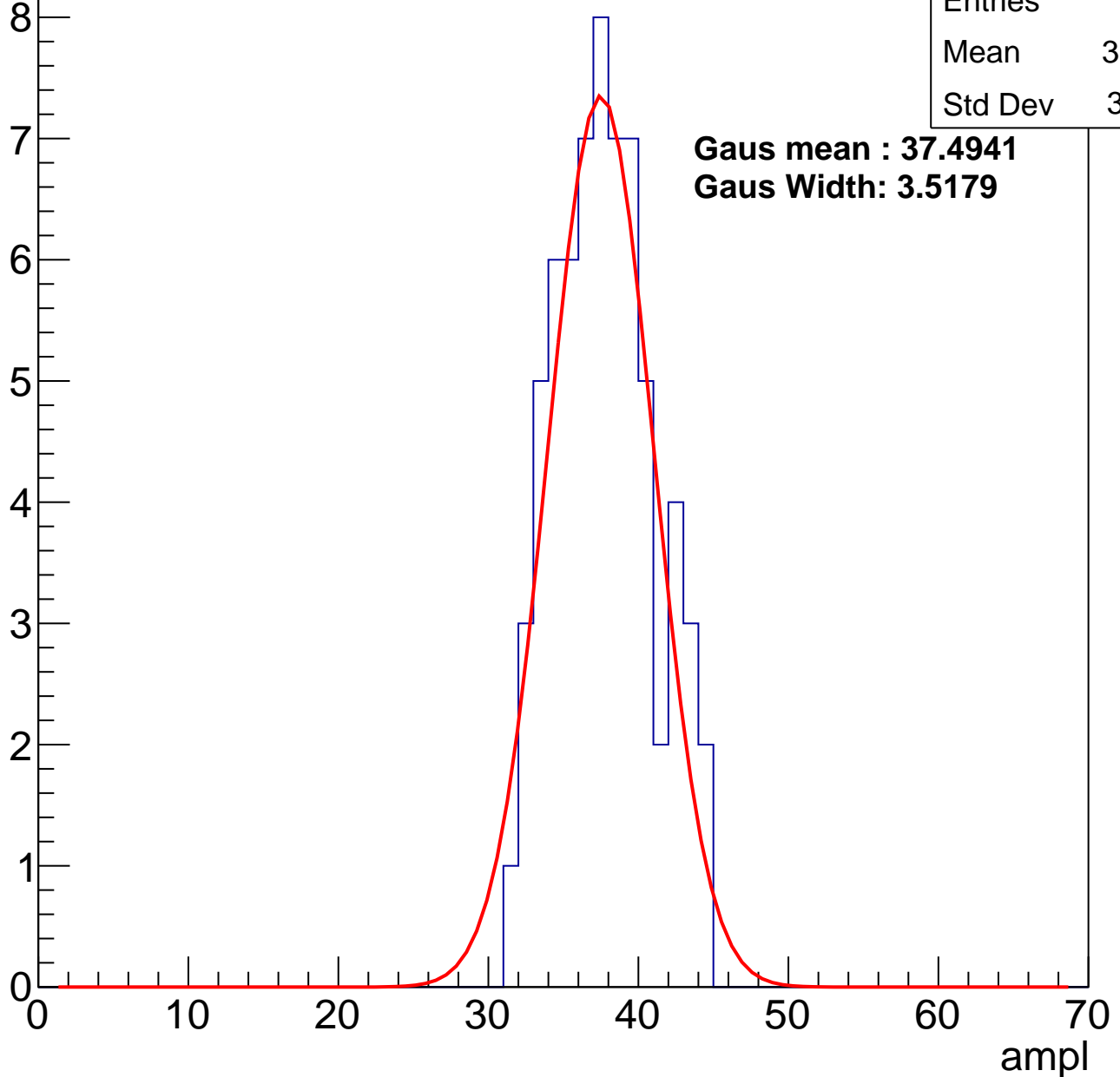
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	37.27
Std Dev	3.231

Gaus mean : 37.4941

Gaus Width: 3.5179



B1L100S, U1-ch7, adc2

calib_packv5_042523_0143.root, FC#4, port A2

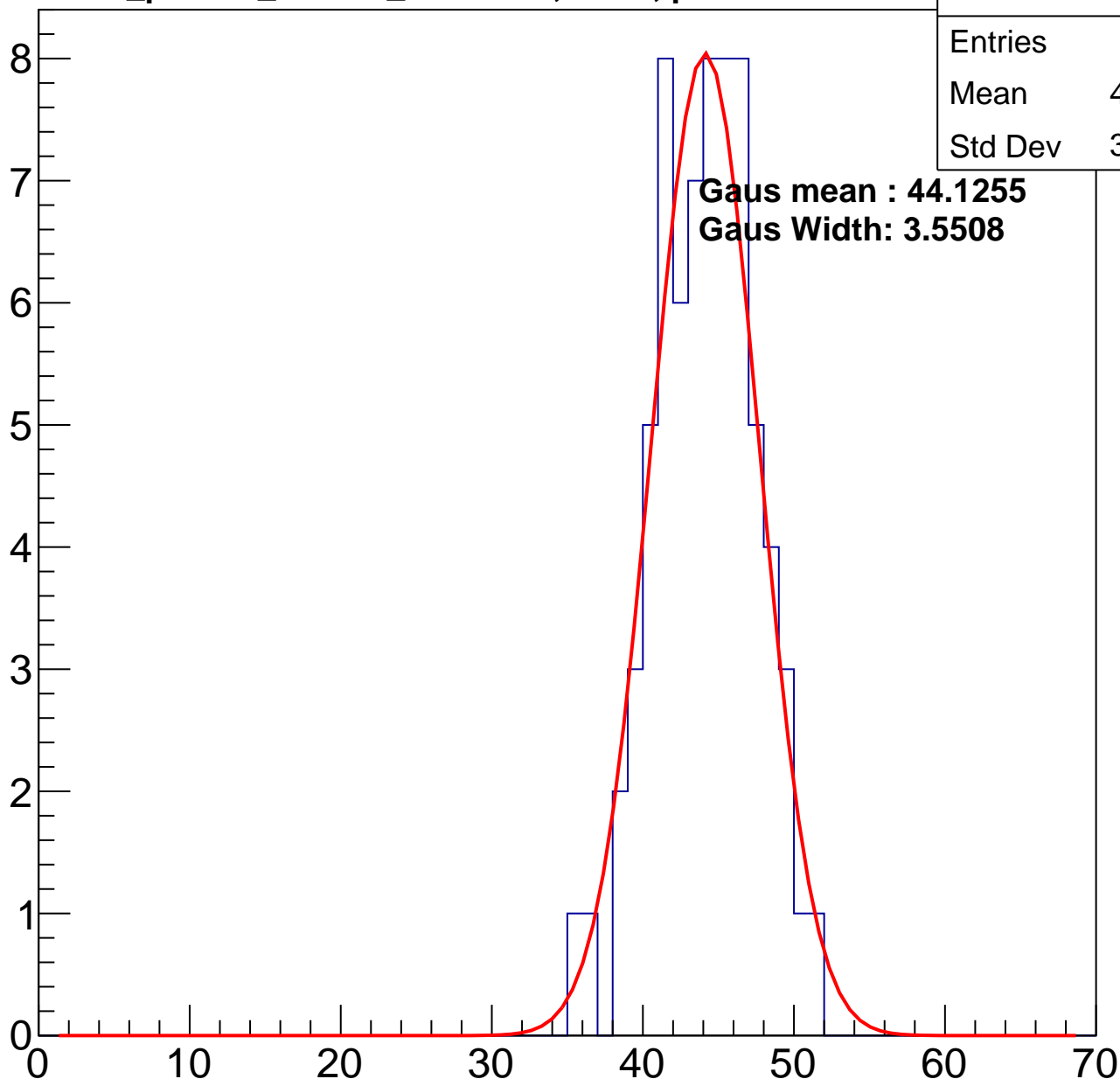
Entry

8
7
6
5
4
3
2
1
0

Entries	71
Mean	43.66
Std Dev	3.306

Gaus mean : 44.1255
Gaus Width: 3.5508

ampl

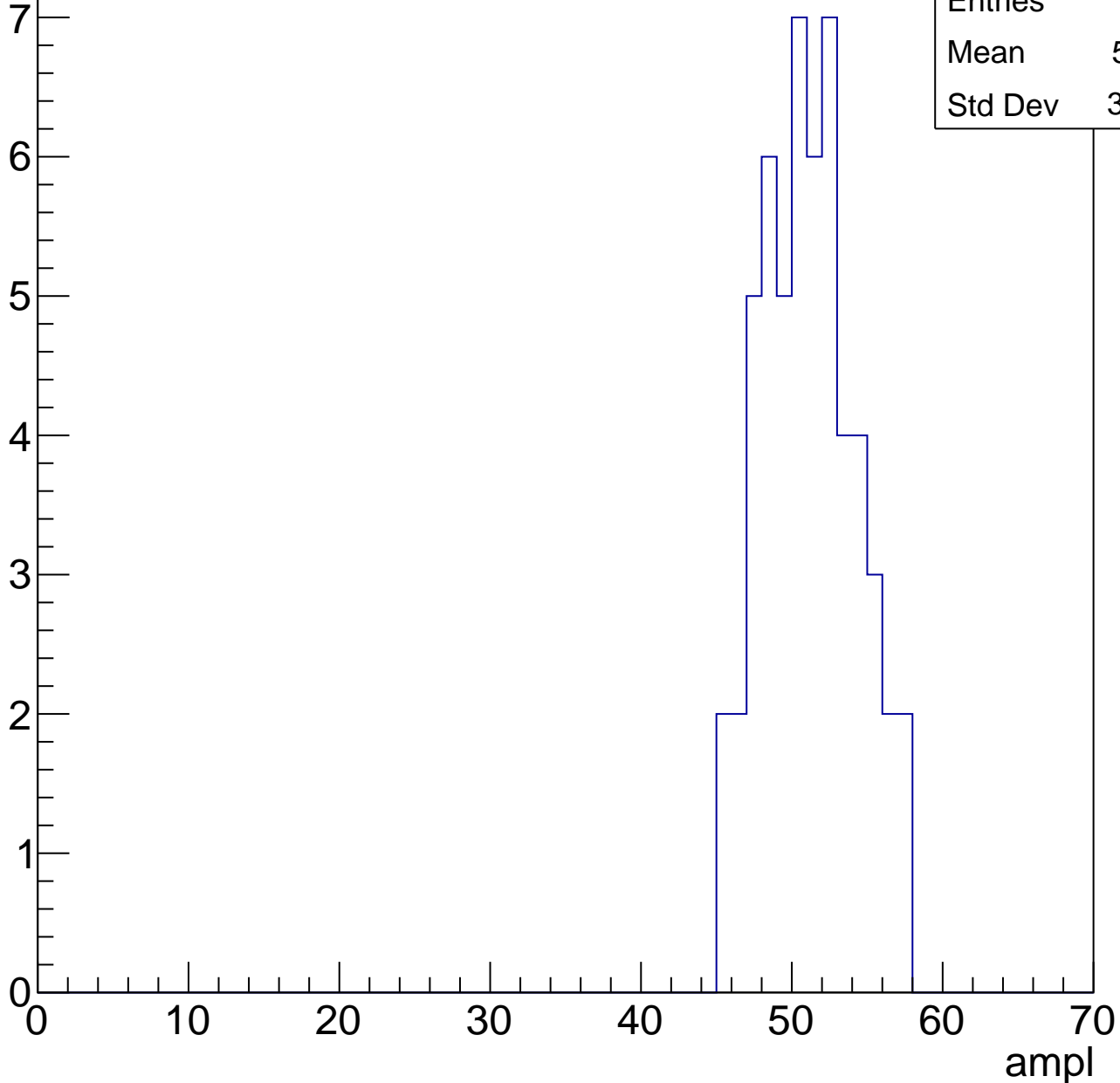


B1L100S, U1-ch7, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	50.71
Std Dev	3.037



B1L100S, U1-ch7, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

0

10

20

30

40

50

60

70

ampl

Entries

52

Mean

55.38

Std Dev

3.312

0

1

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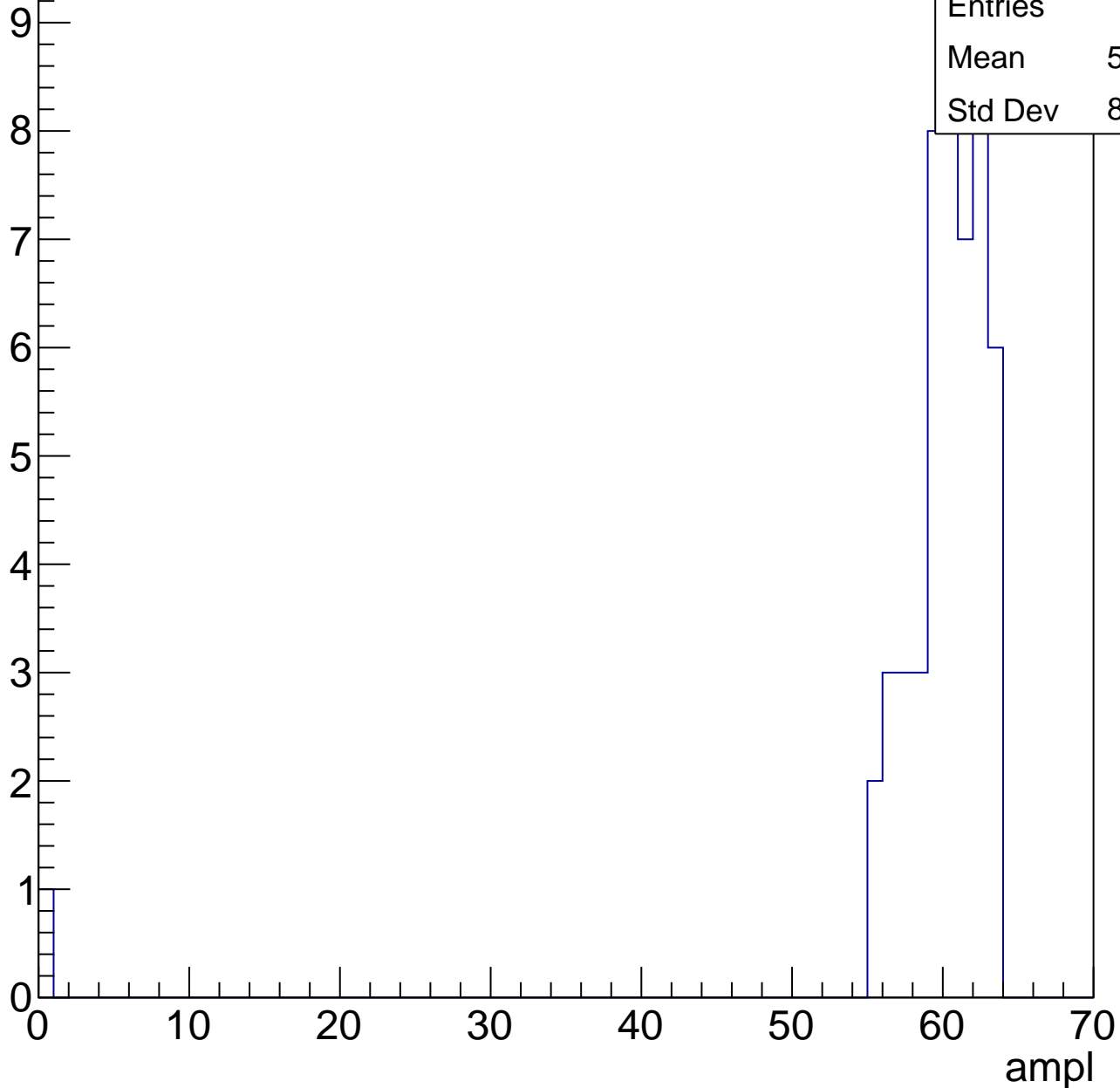
321

322

B1L100S, U1-ch7, adc5

calib_packv5_042523_0143.root, FC#4, port A2

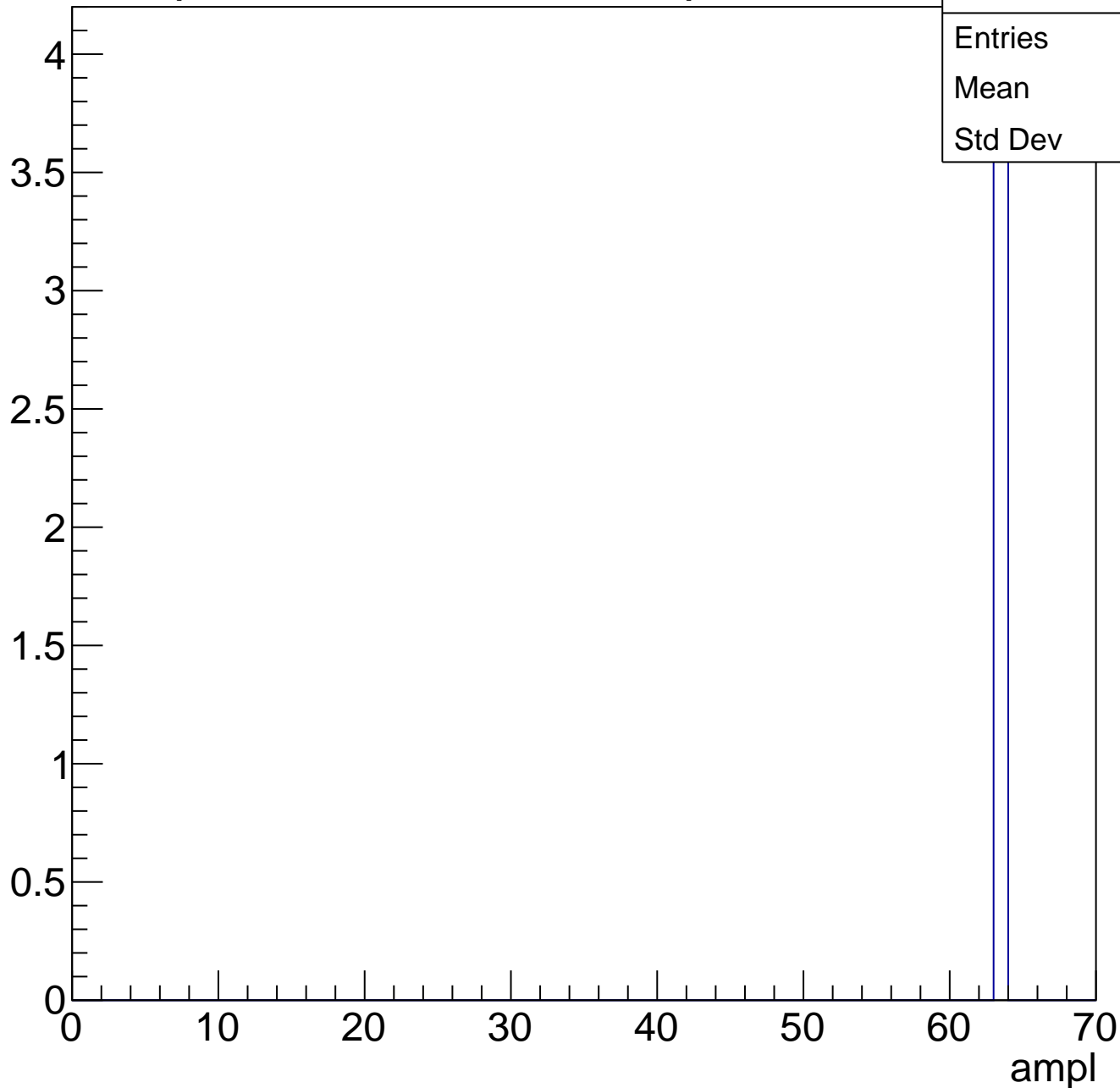
Entry



B1L100S, U1-ch7, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch7, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch8, adc0

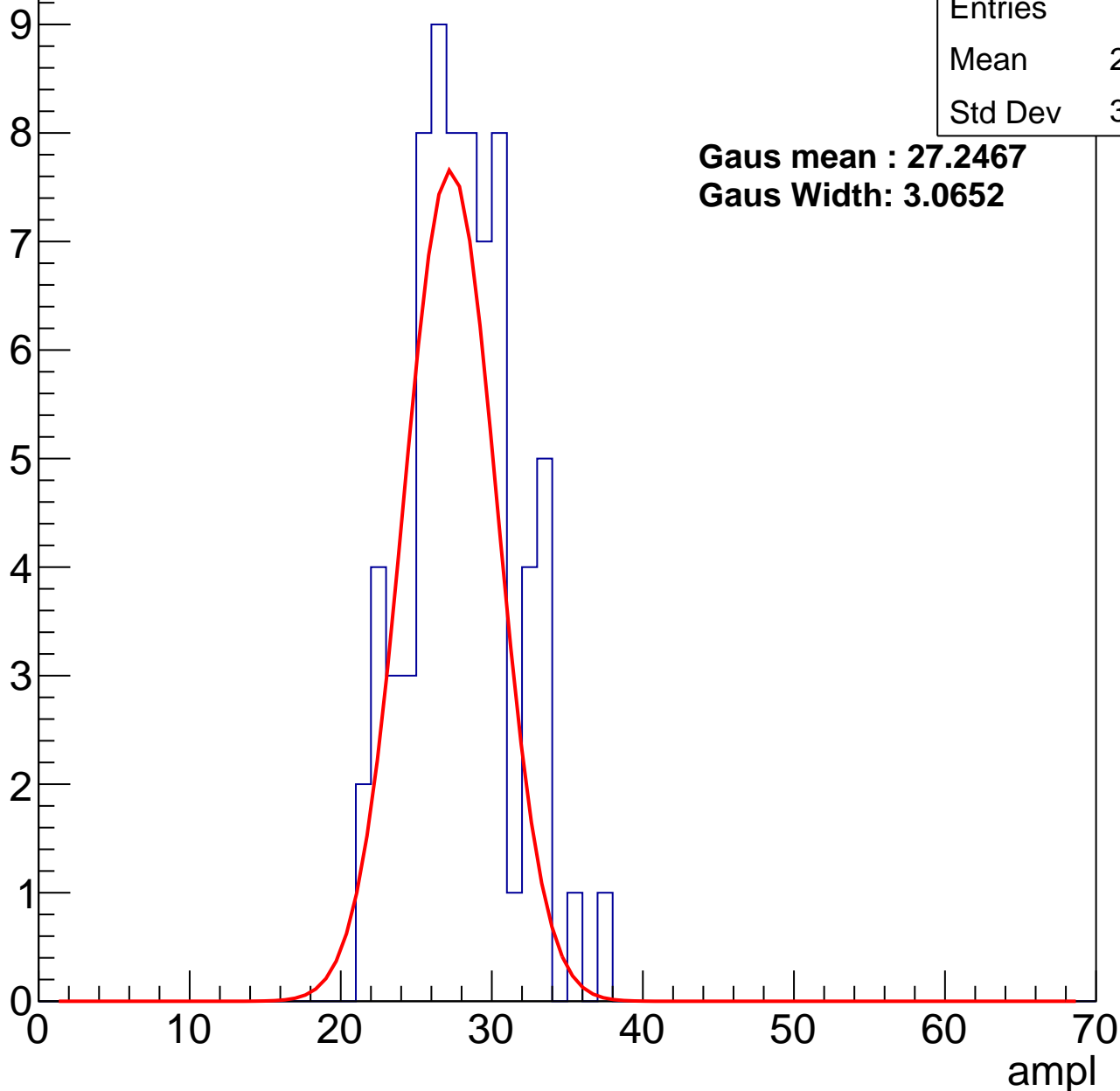
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	27.56
Std Dev	3.403

Gaus mean : 27.2467

Gaus Width: 3.0652



B1L100S, U1-ch8, adc1

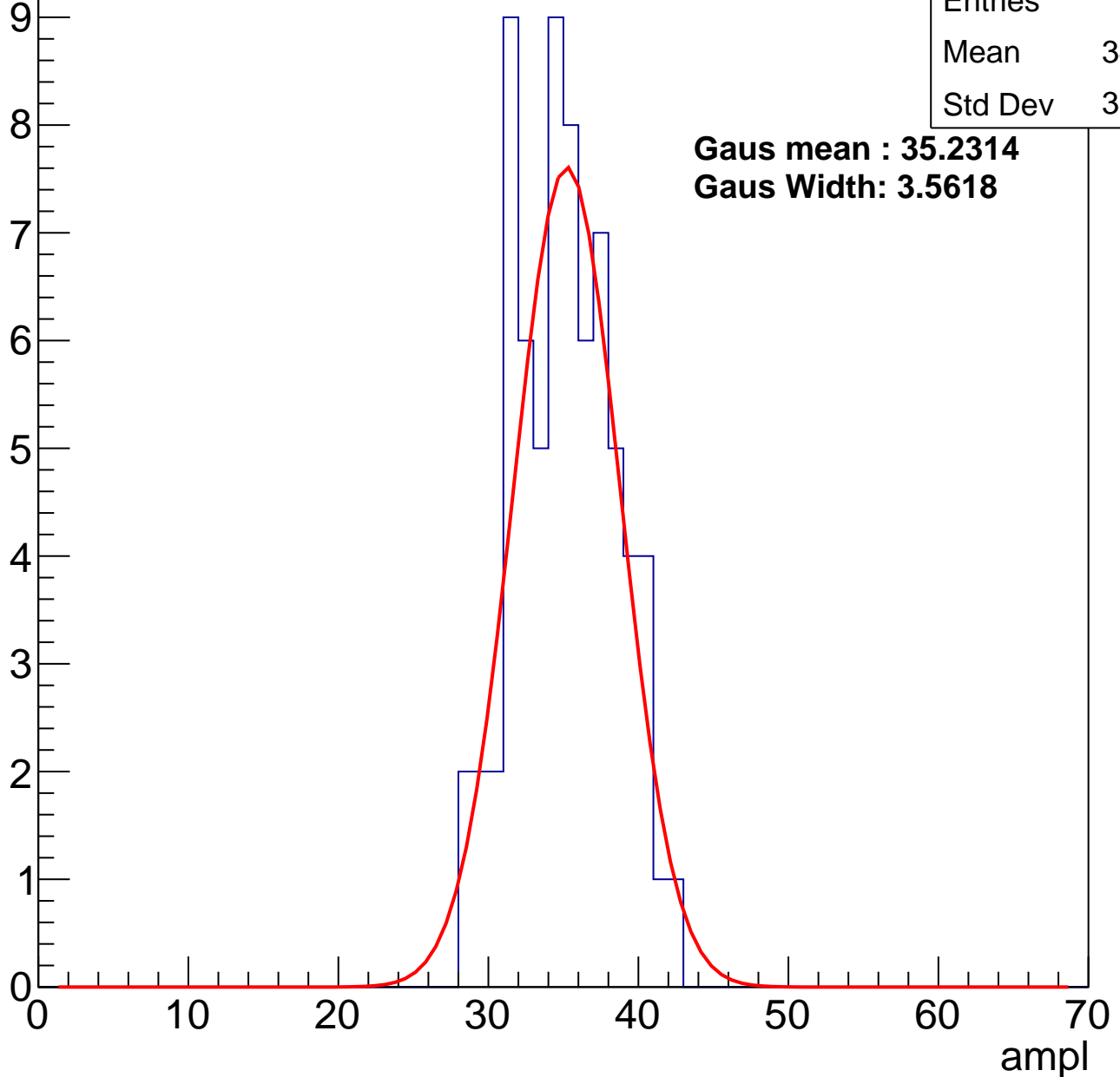
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	34.65
Std Dev	3.285

Gaus mean : 35.2314

Gaus Width: 3.5618



B1L100S, U1-ch8, adc2

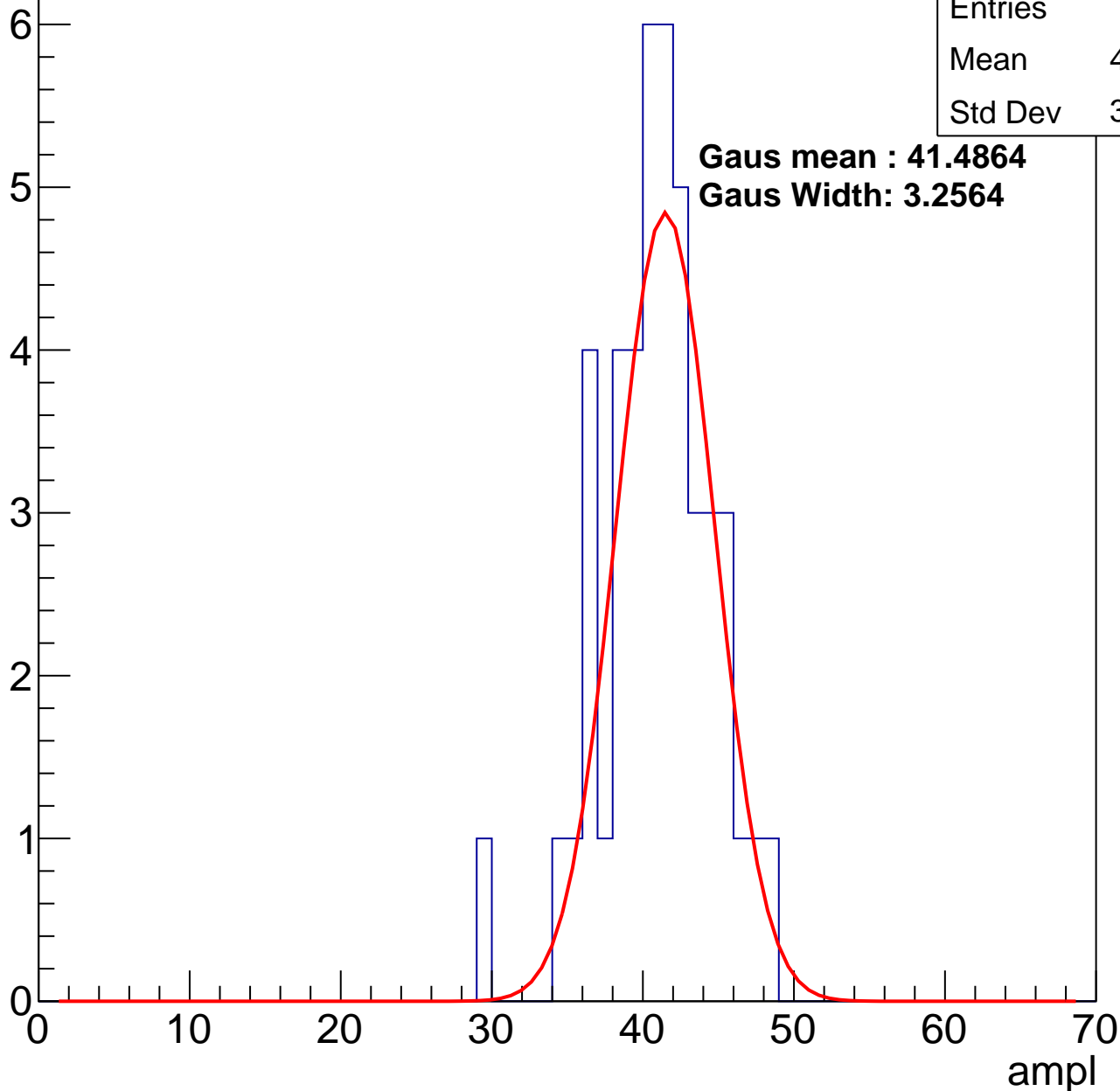
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	40.44
Std Dev	3.624

Gaus mean : 41.4864

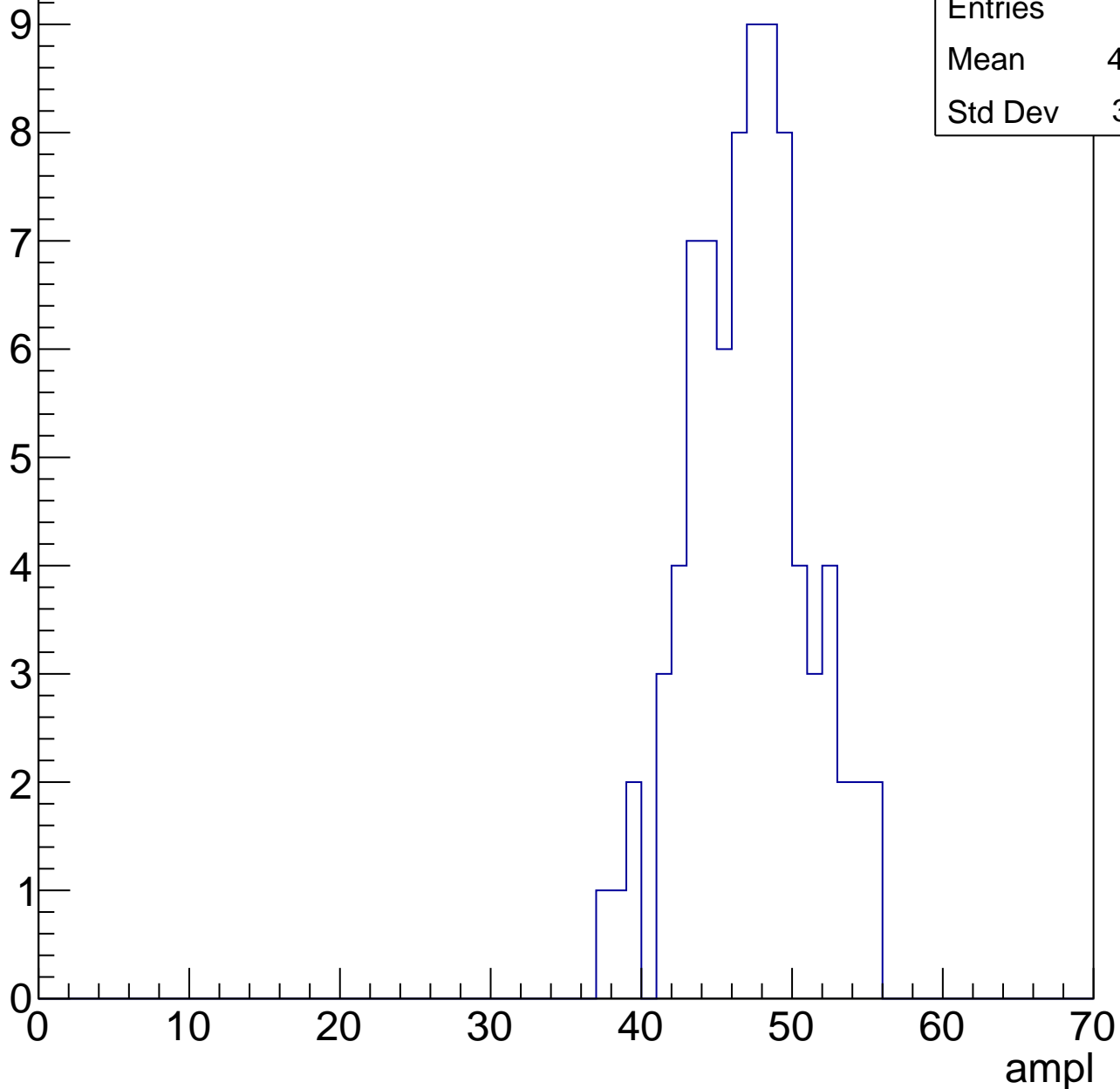
Gaus Width: 3.2564



B1L100S, U1-ch8, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

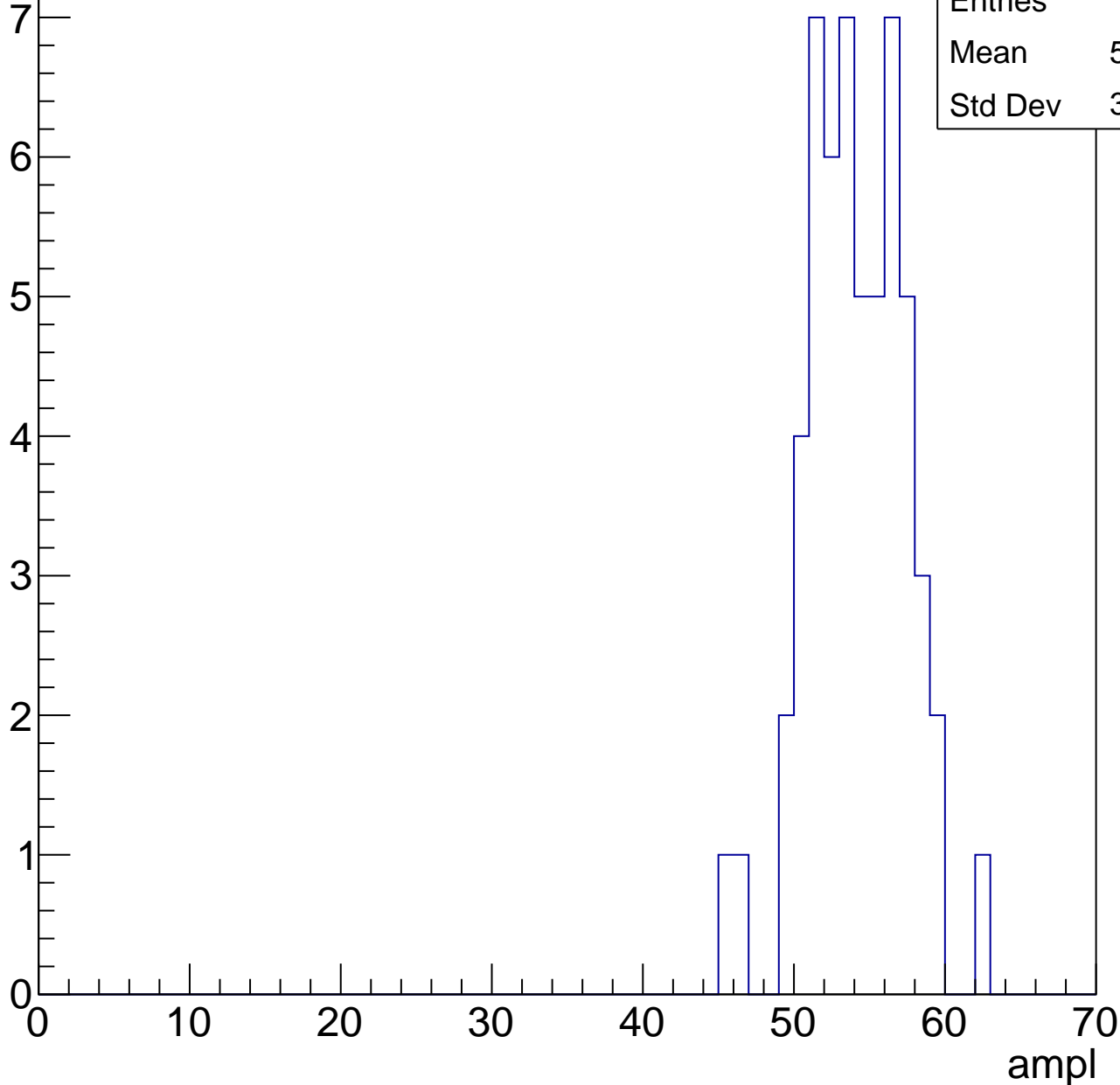


B1L100S, U1-ch8, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	53.66
Std Dev	3.236

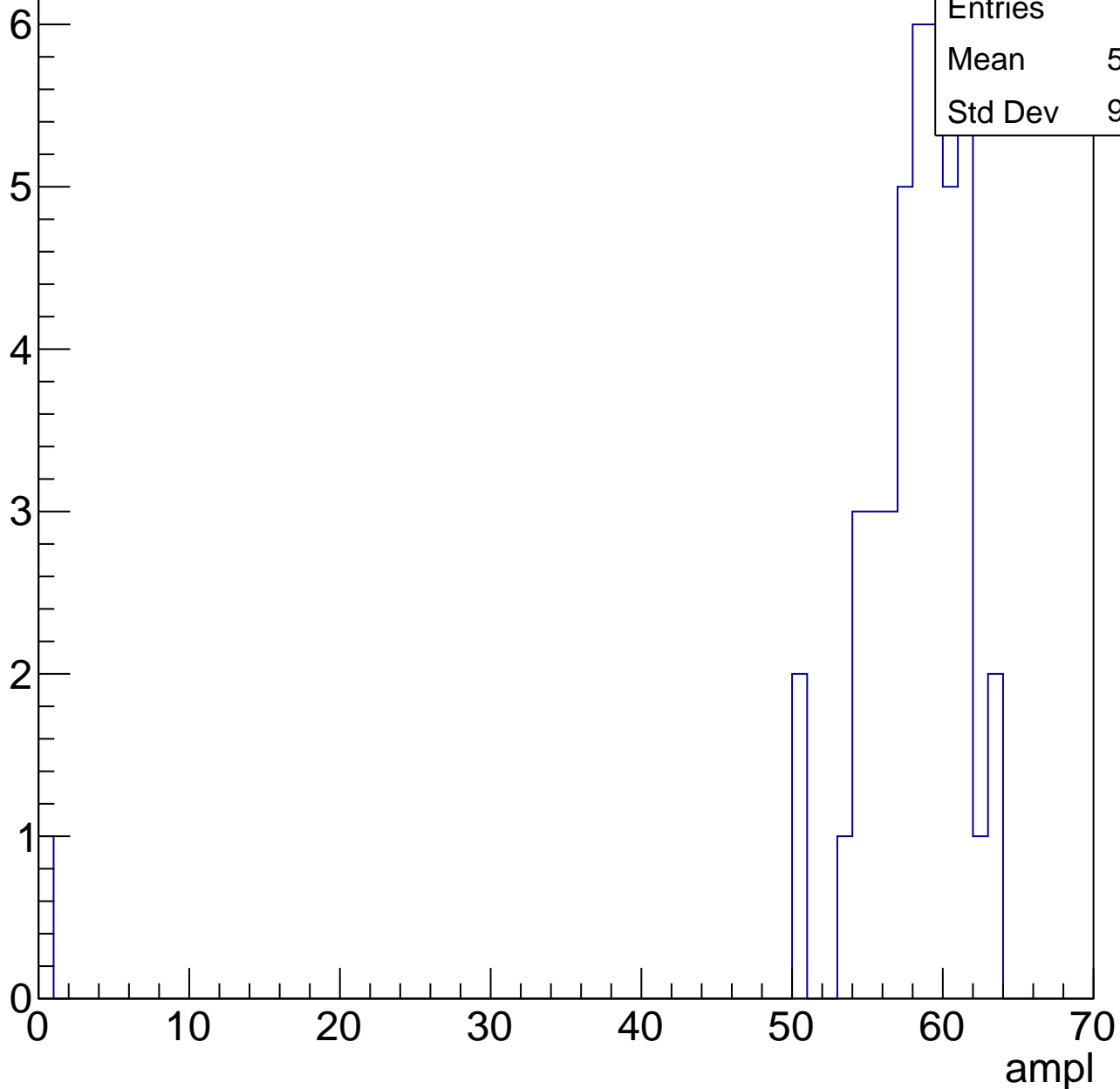


B1L100S, U1-ch8, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	56.57
Std Dev	9.126



B1L100S, U1-ch8, adc6

calib_packv5_042523_0143.root, FC#4, port A2

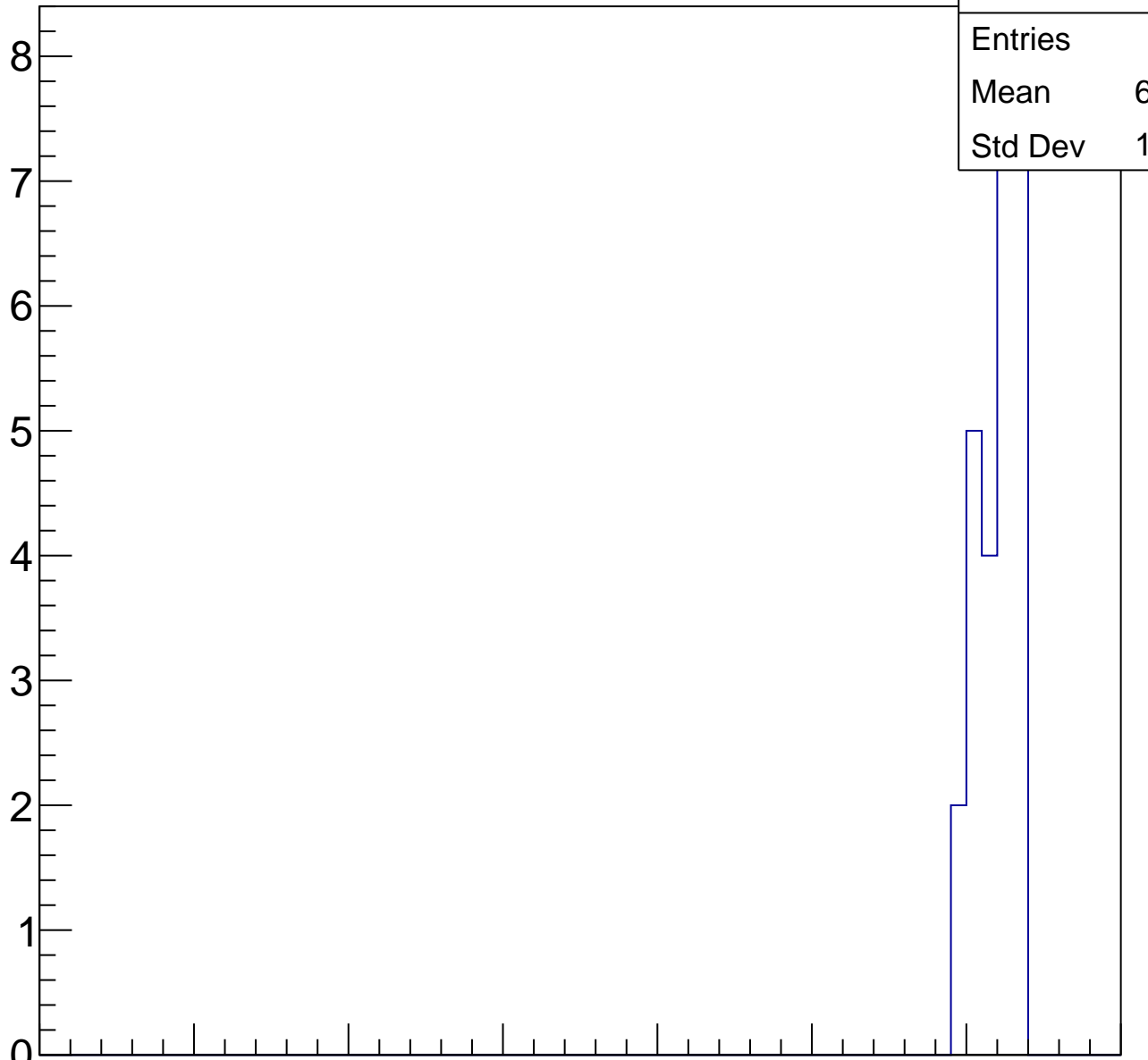
Entry

8
7
6
5
4
3
2
1
0

Entries	27
Mean	61.56
Std Dev	1.286

ampl

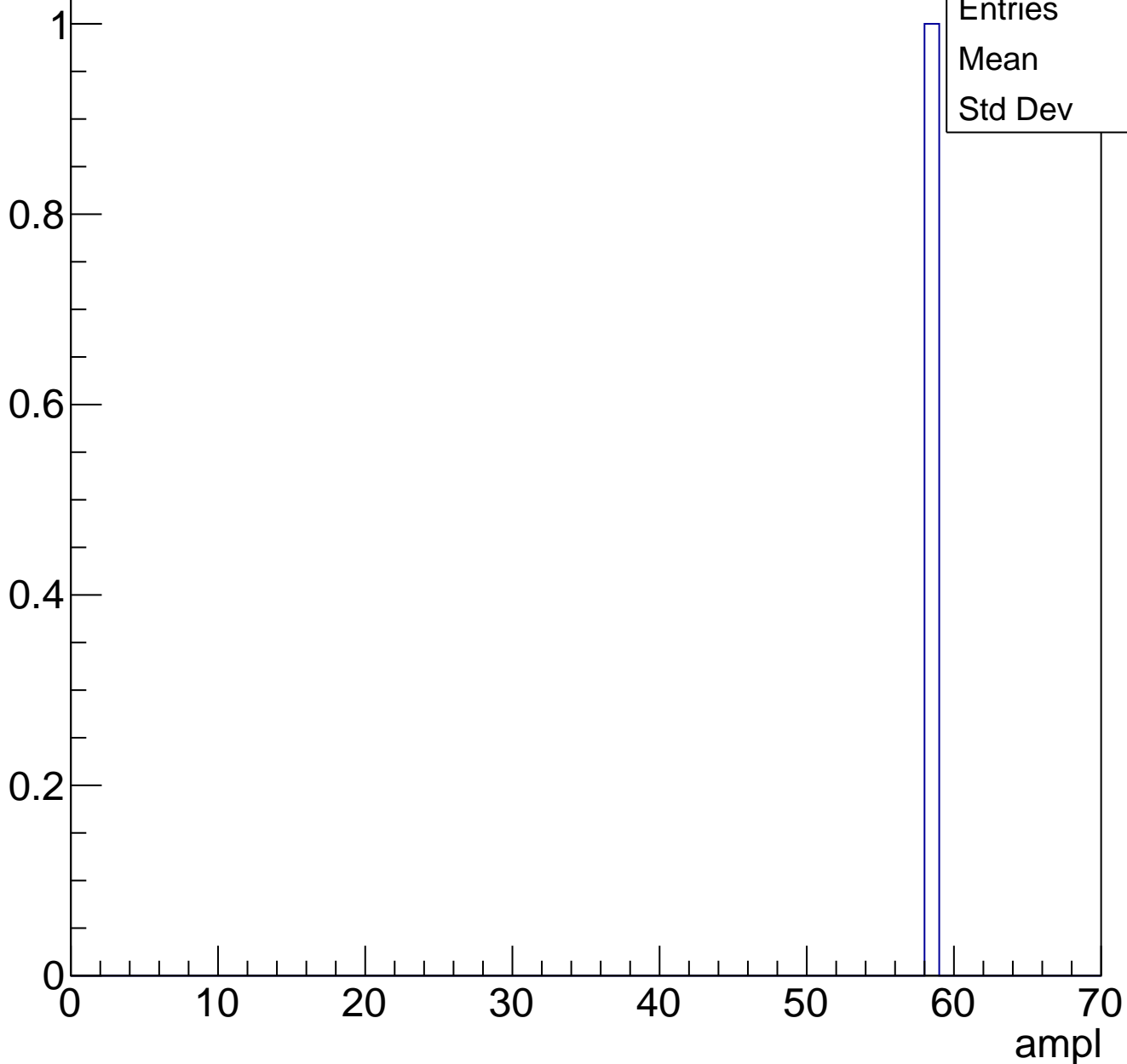
0 10 20 30 40 50 60 70



B1L100S, U1-ch8, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch9, adc0

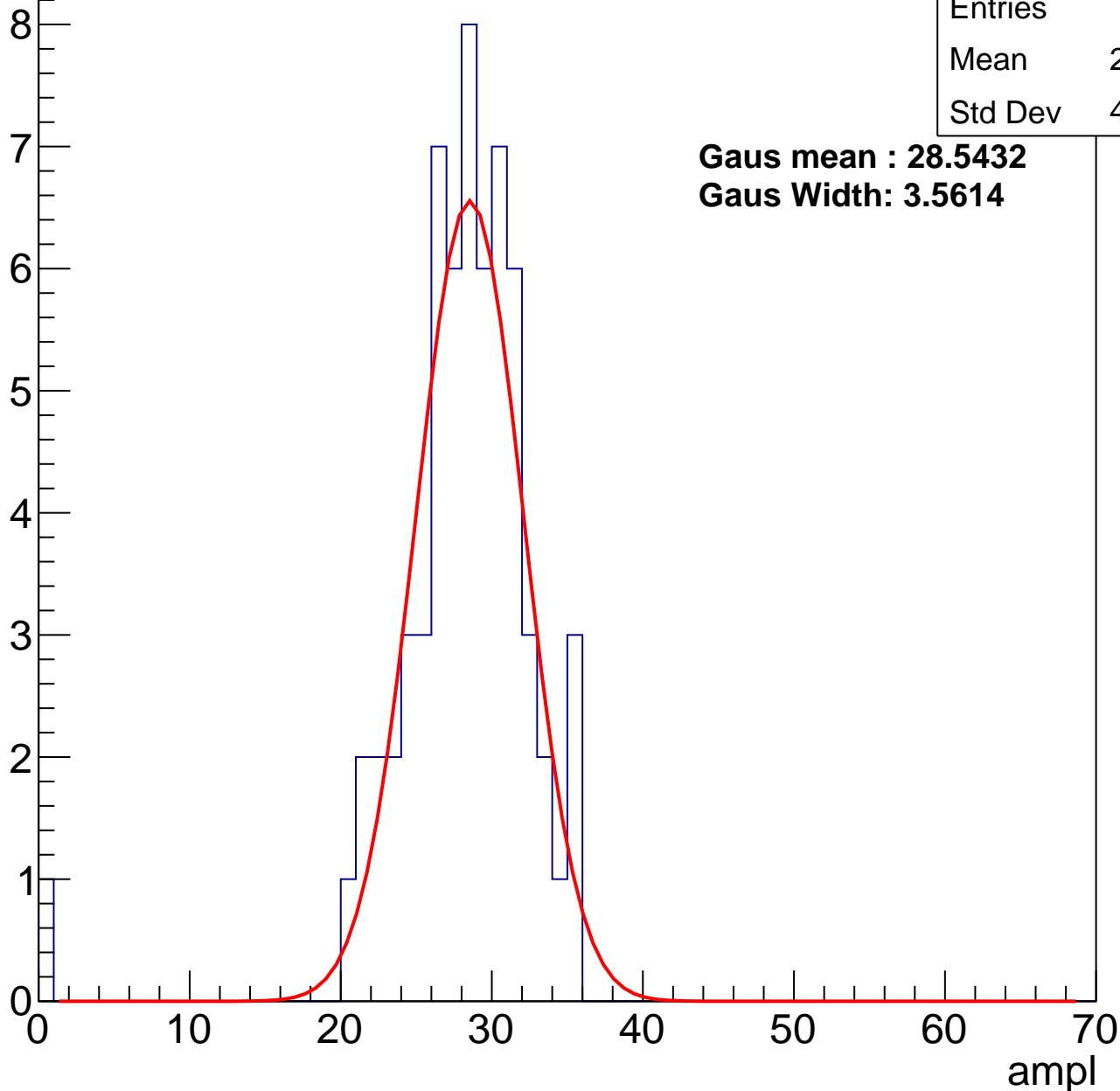
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	27.59
Std Dev	4.937

Gaus mean : 28.5432

Gaus Width: 3.5614



B1L100S, U1-ch9, adc1

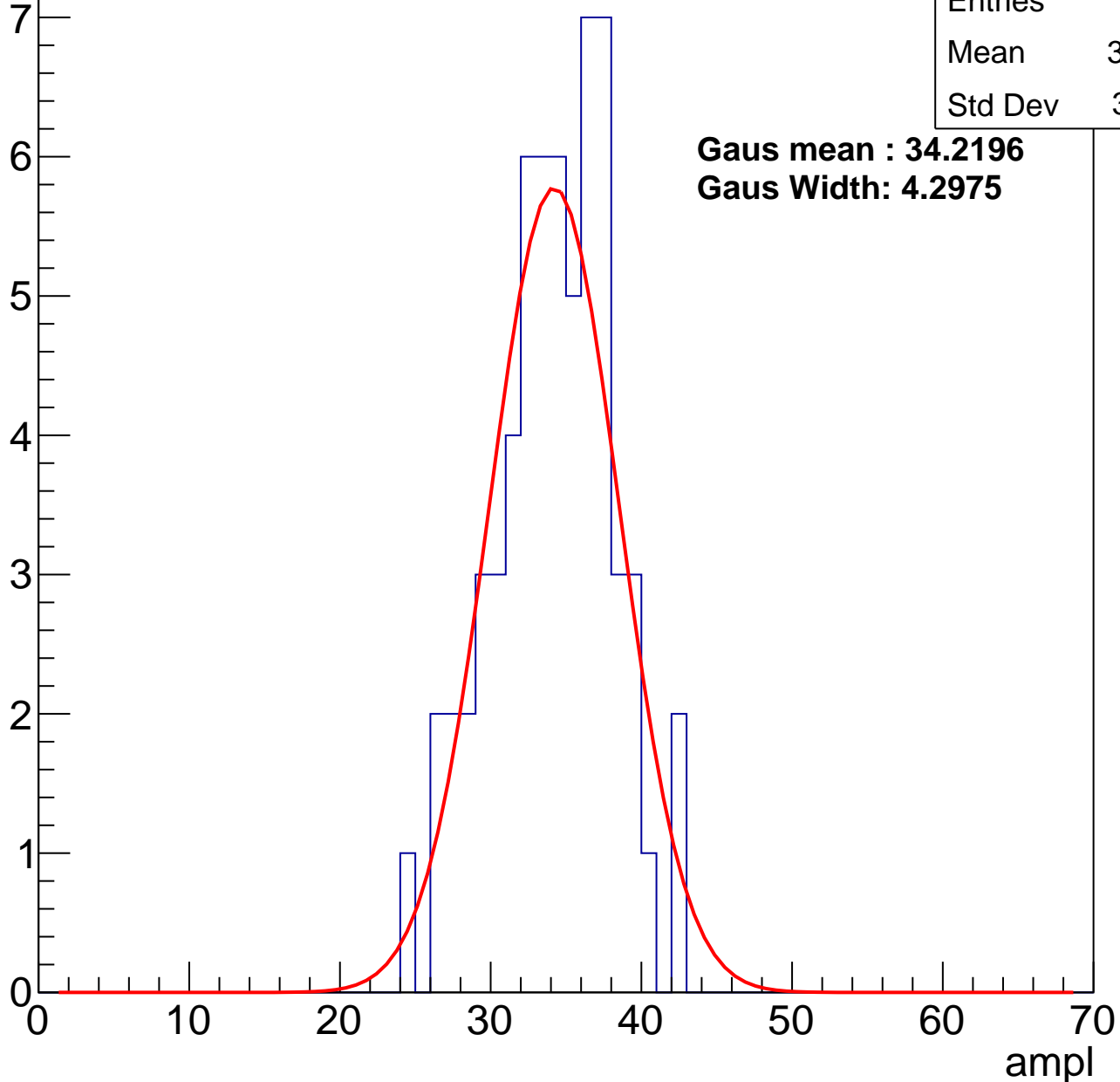
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	33.68
Std Dev	3.911

Gaus mean : 34.2196

Gaus Width: 4.2975



B1L100S, U1-ch9, adc2

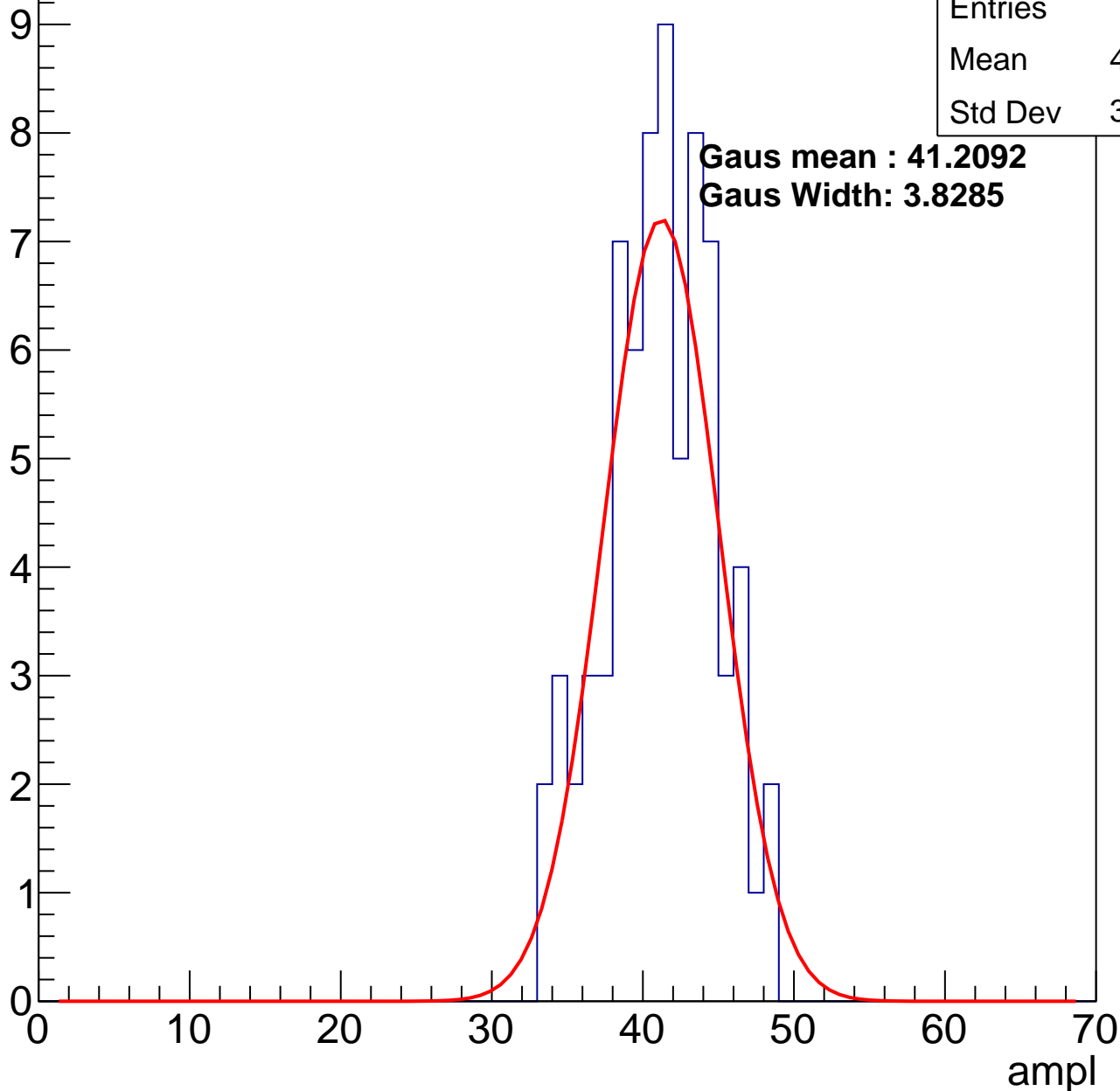
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	40.68
Std Dev	3.588

Gaus mean : 41.2092

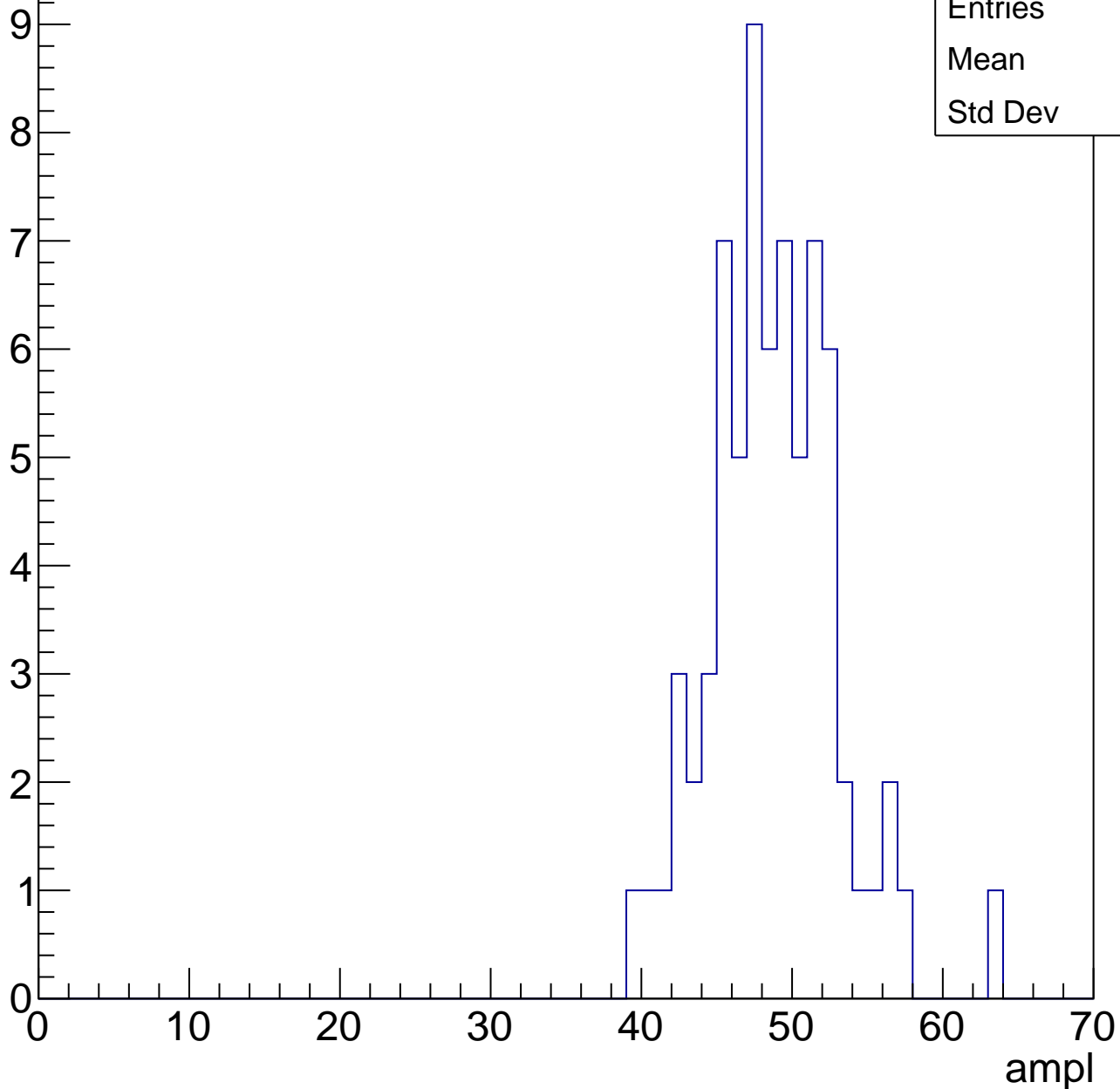
Gaus Width: 3.8285



B1L100S, U1-ch9, adc3

calib_packv5_042523_0143.root, FC#4, port A2

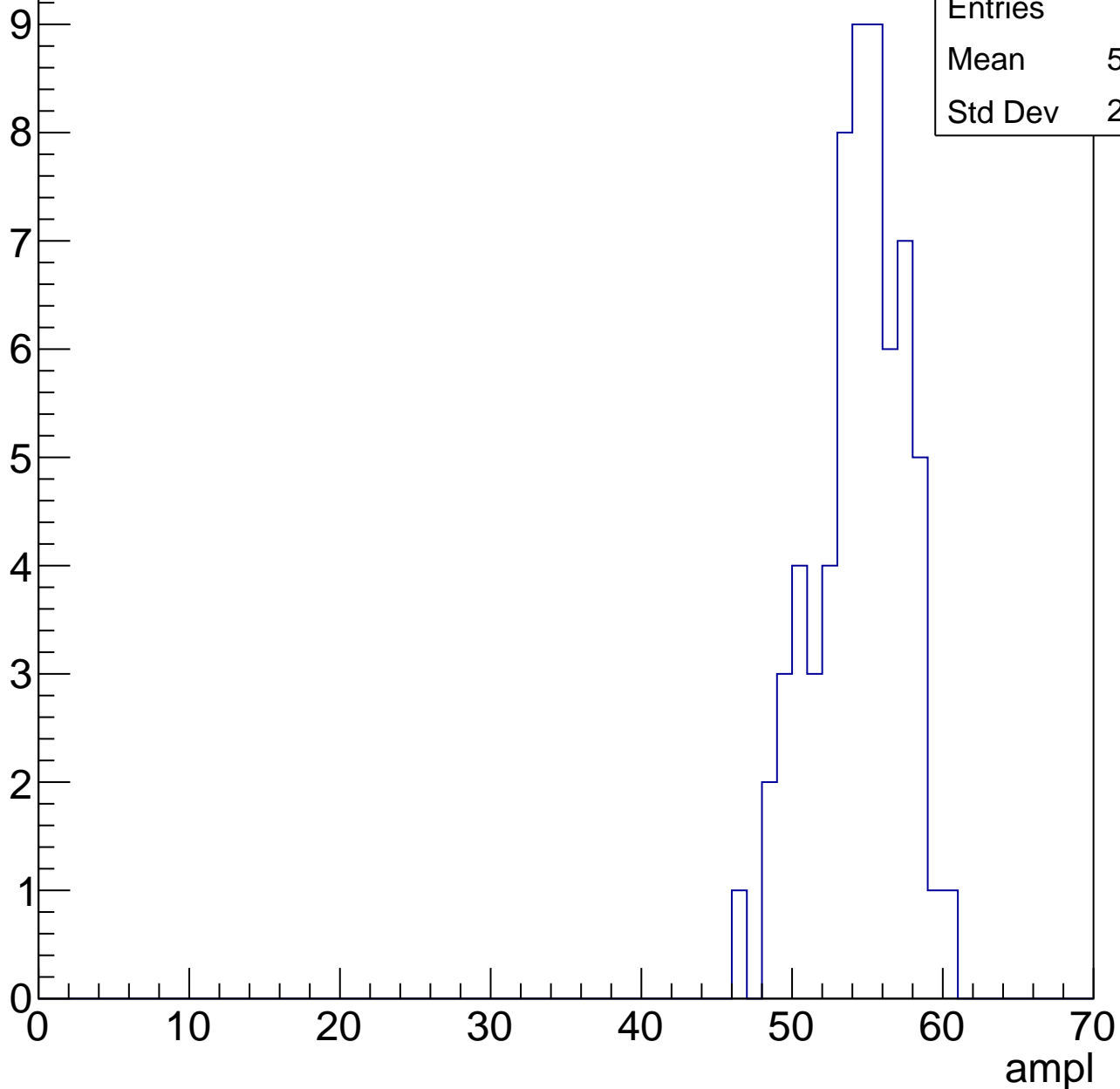
Entry



B1L100S, U1-ch9, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch9, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entries	51
Mean	58.9
Std Dev	8.651

Entry

10

8

6

4

2

0

0

10

20

30

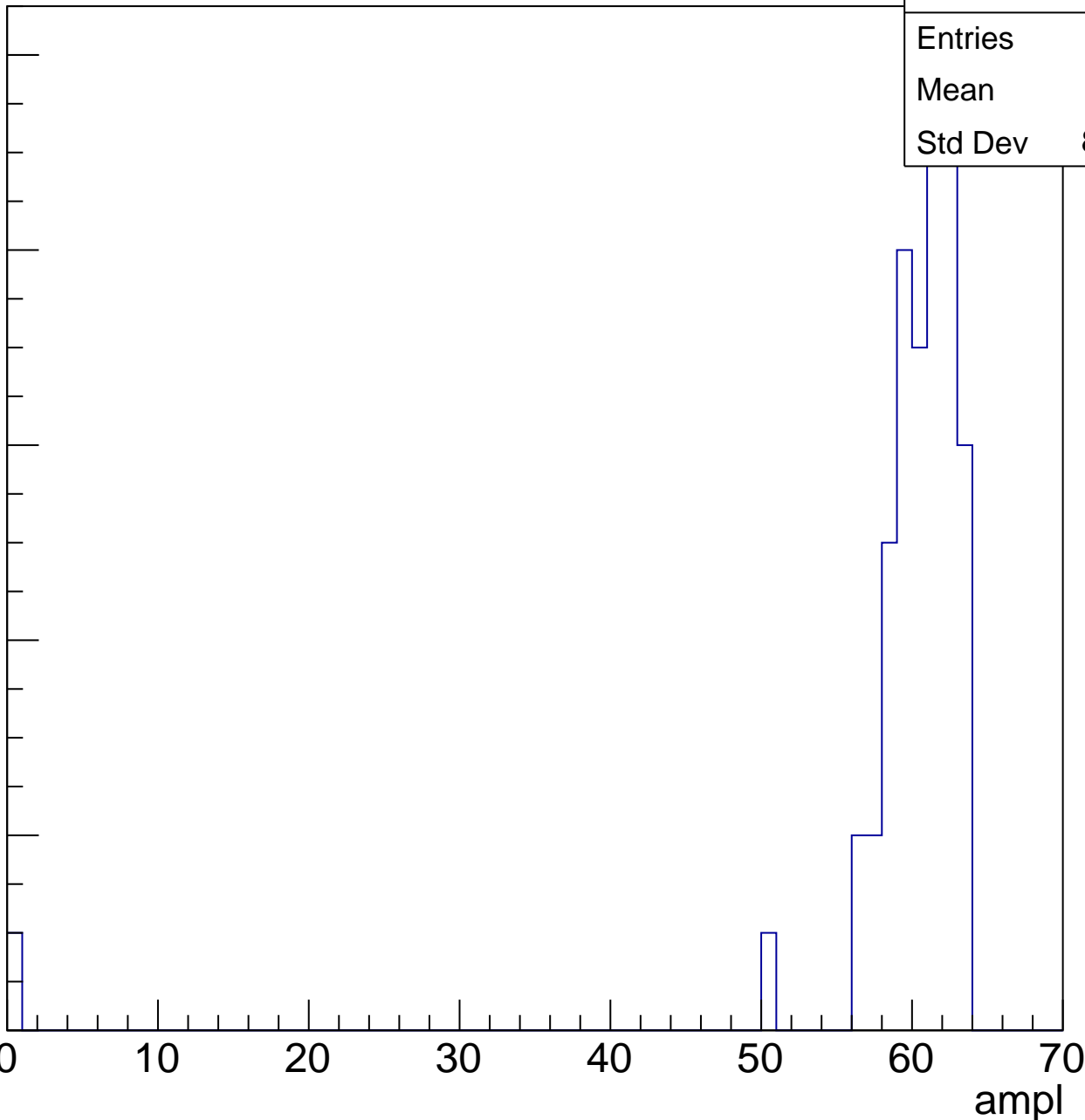
40

50

60

70

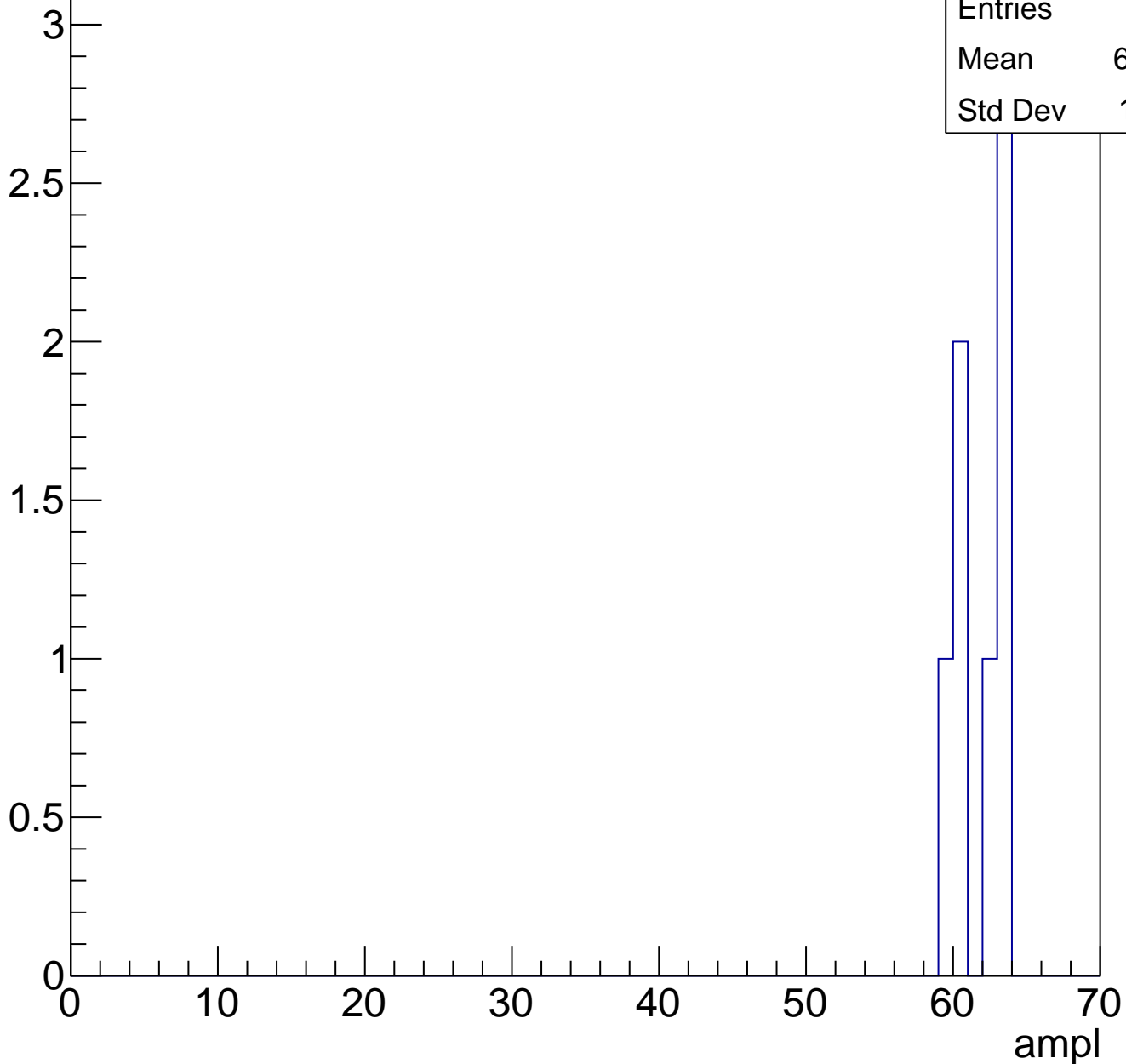
ampl



B1L100S, U1-ch9, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch9, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch10, adc0

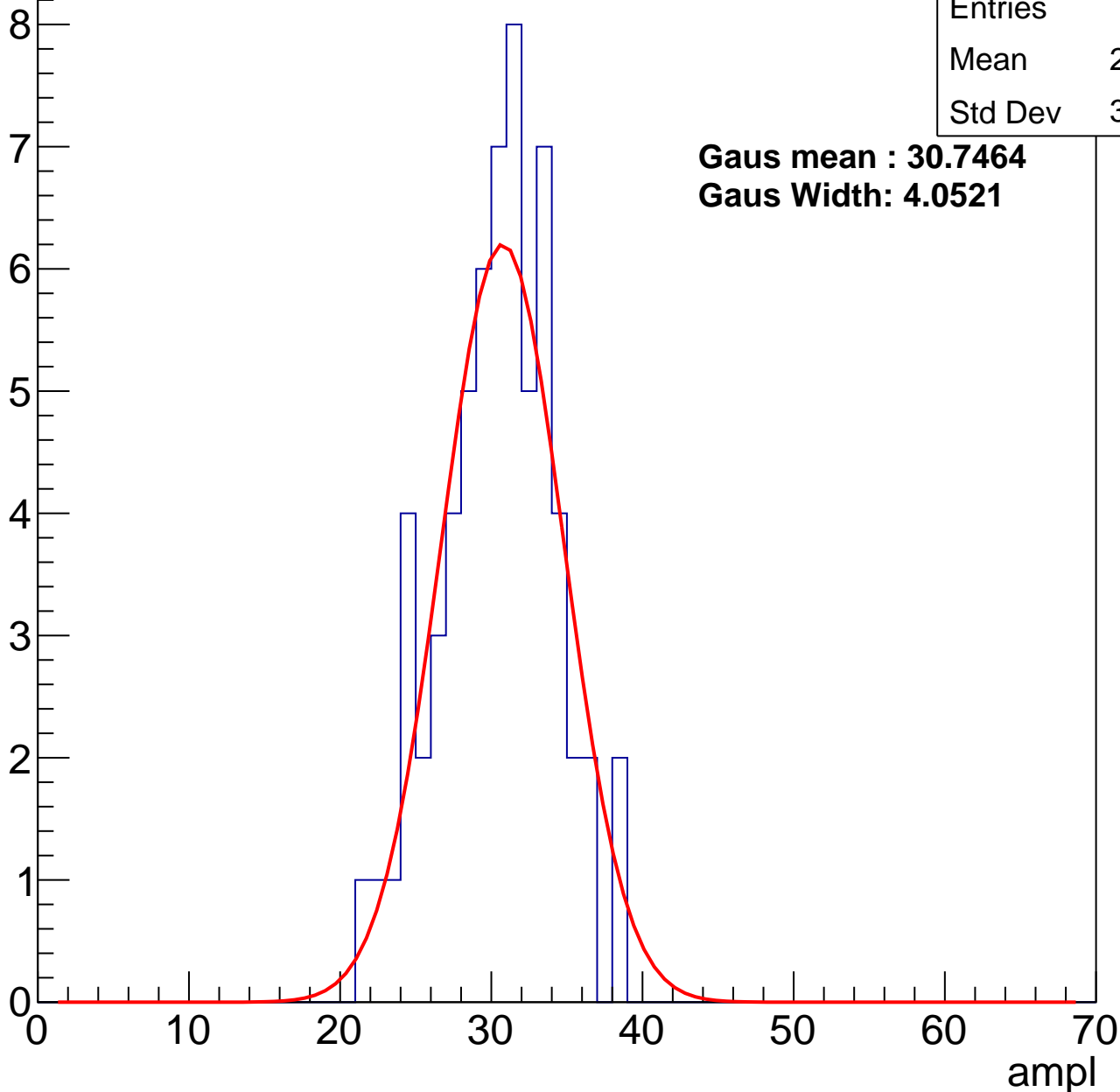
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.92
Std Dev	3.755

Gaus mean : 30.7464

Gaus Width: 4.0521



B1L100S, U1-ch10, adc1

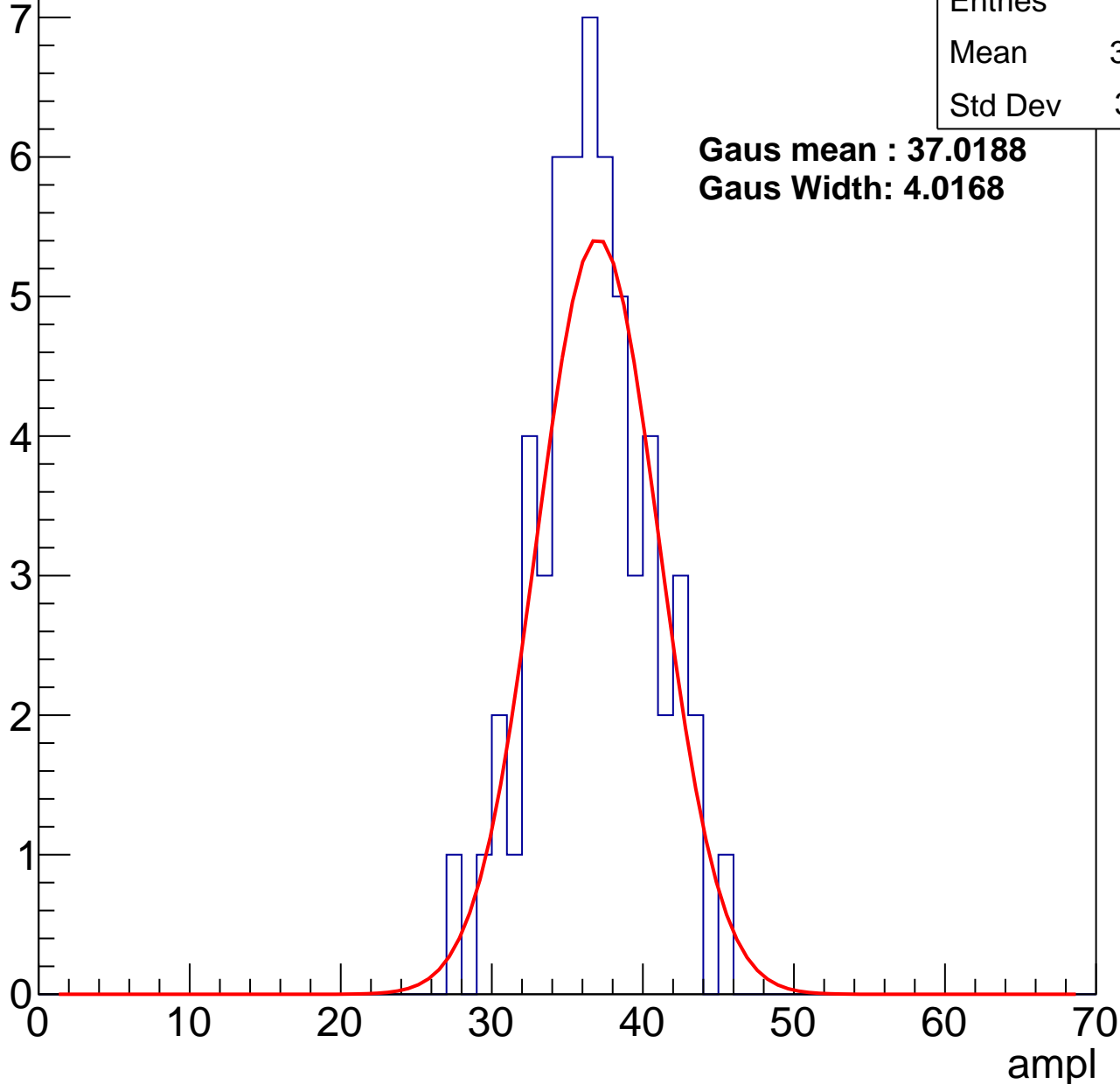
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	36.28
Std Dev	3.741

Gaus mean : 37.0188

Gaus Width: 4.0168



B1L100S, U1-ch10, adc2

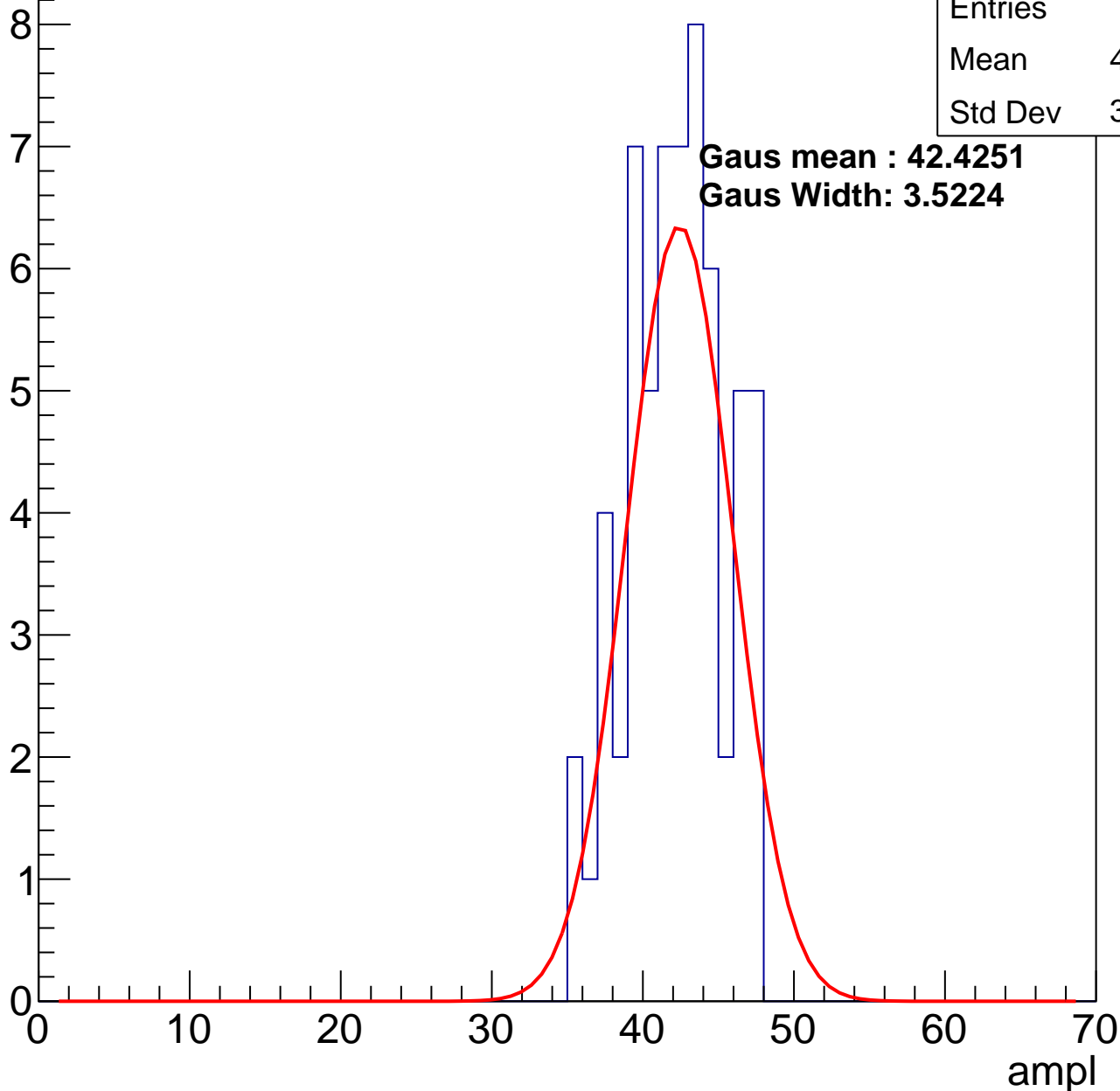
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.75
Std Dev	3.155

Gaus mean : 42.4251

Gaus Width: 3.5224

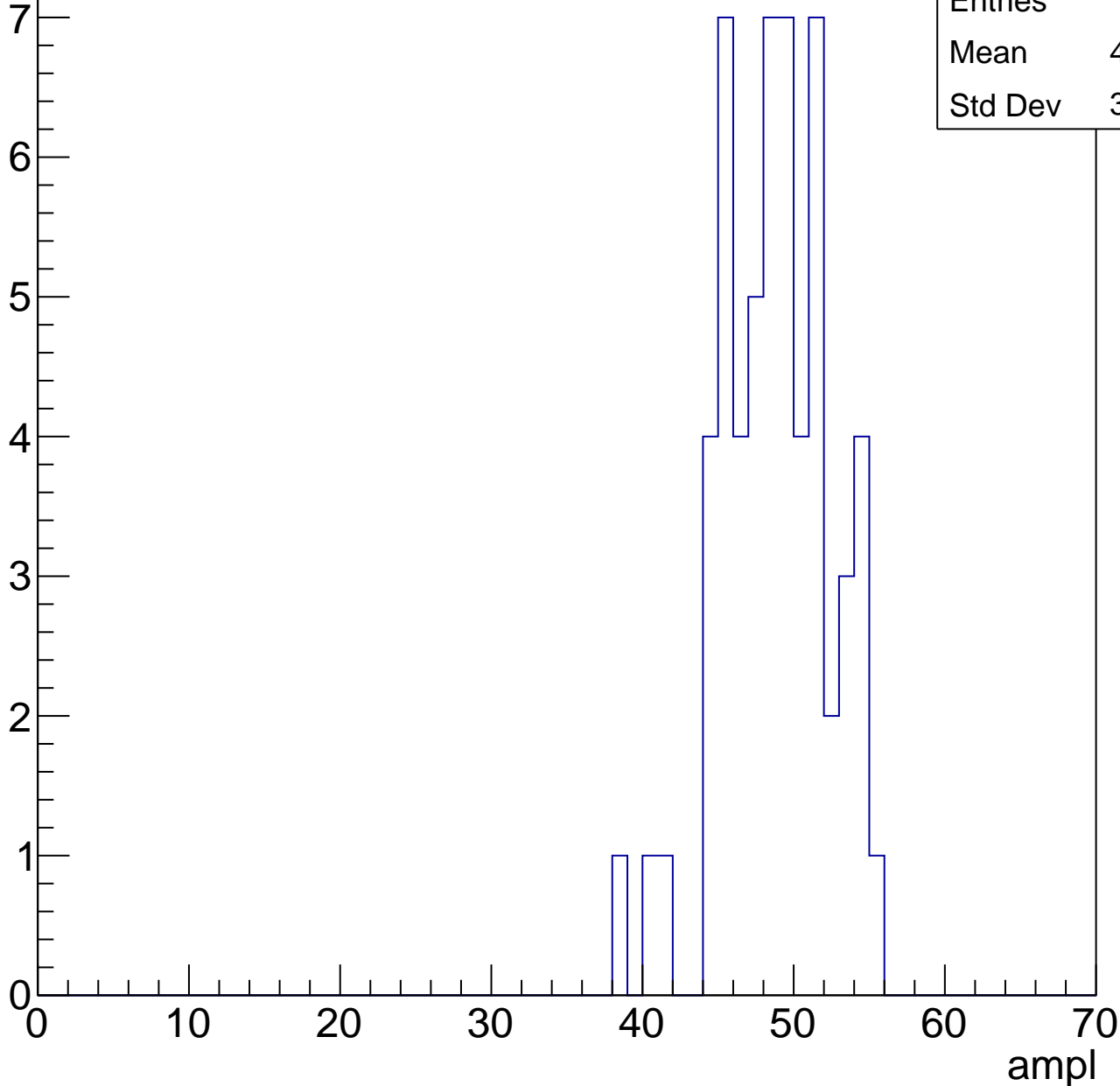


B1L100S, U1-ch10, adc3

calib_packv5_042523_0143.root, FC#4, port A2

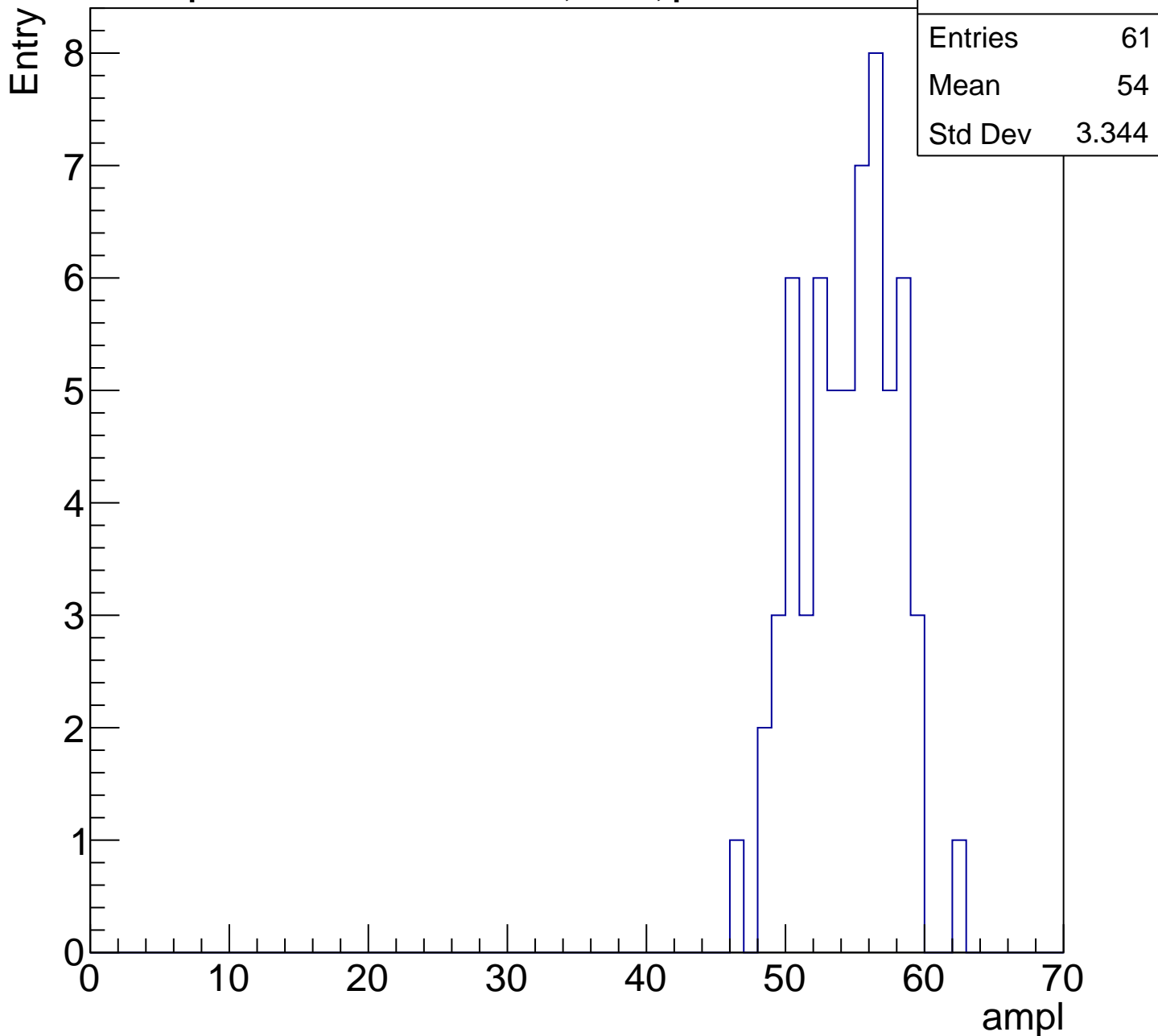
Entry

Entries	58
Mean	48.26
Std Dev	3.579



B1L100S, U1-ch10, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch10, adc5

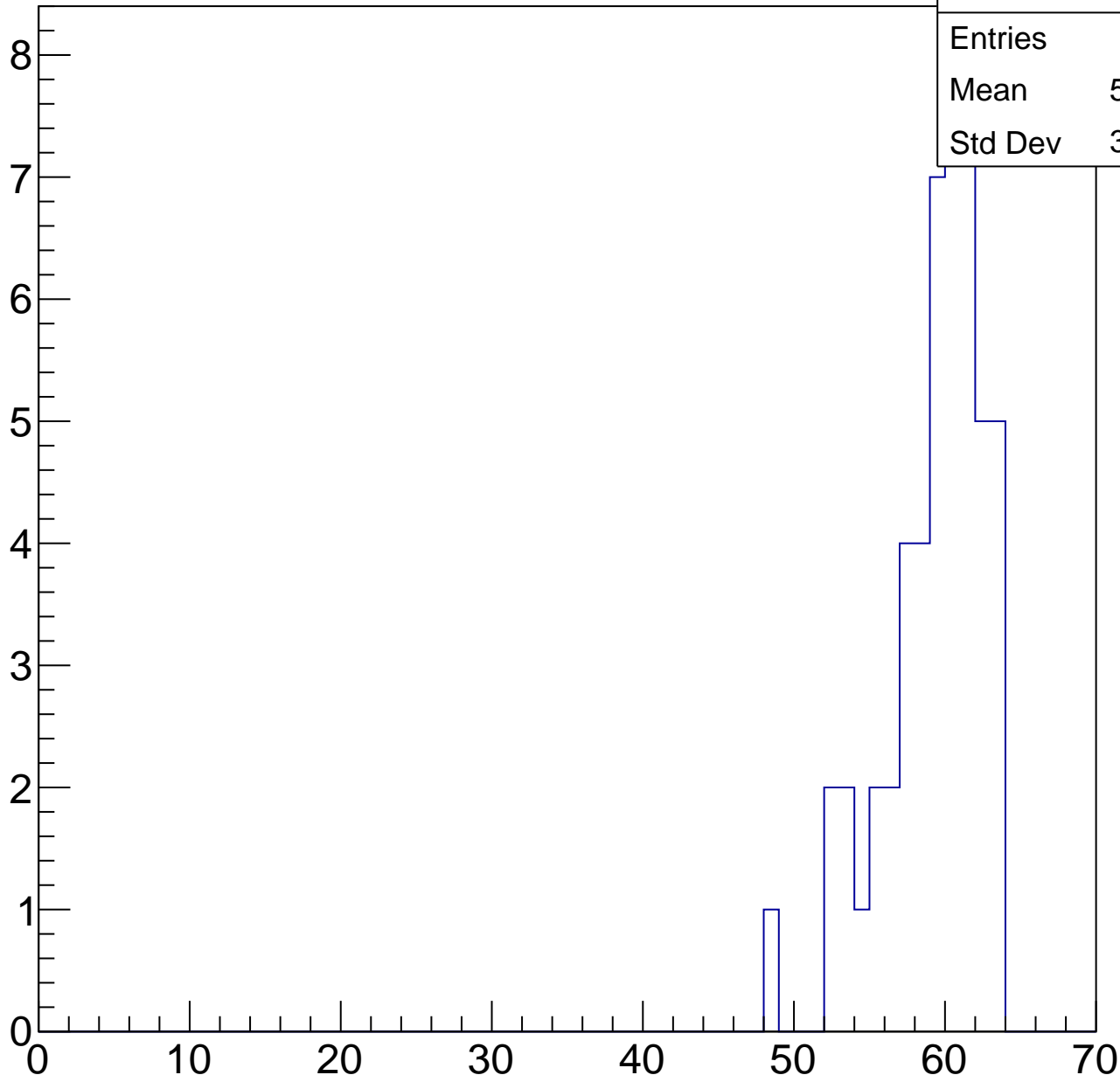
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	51
Mean	58.82
Std Dev	3.282

ampl

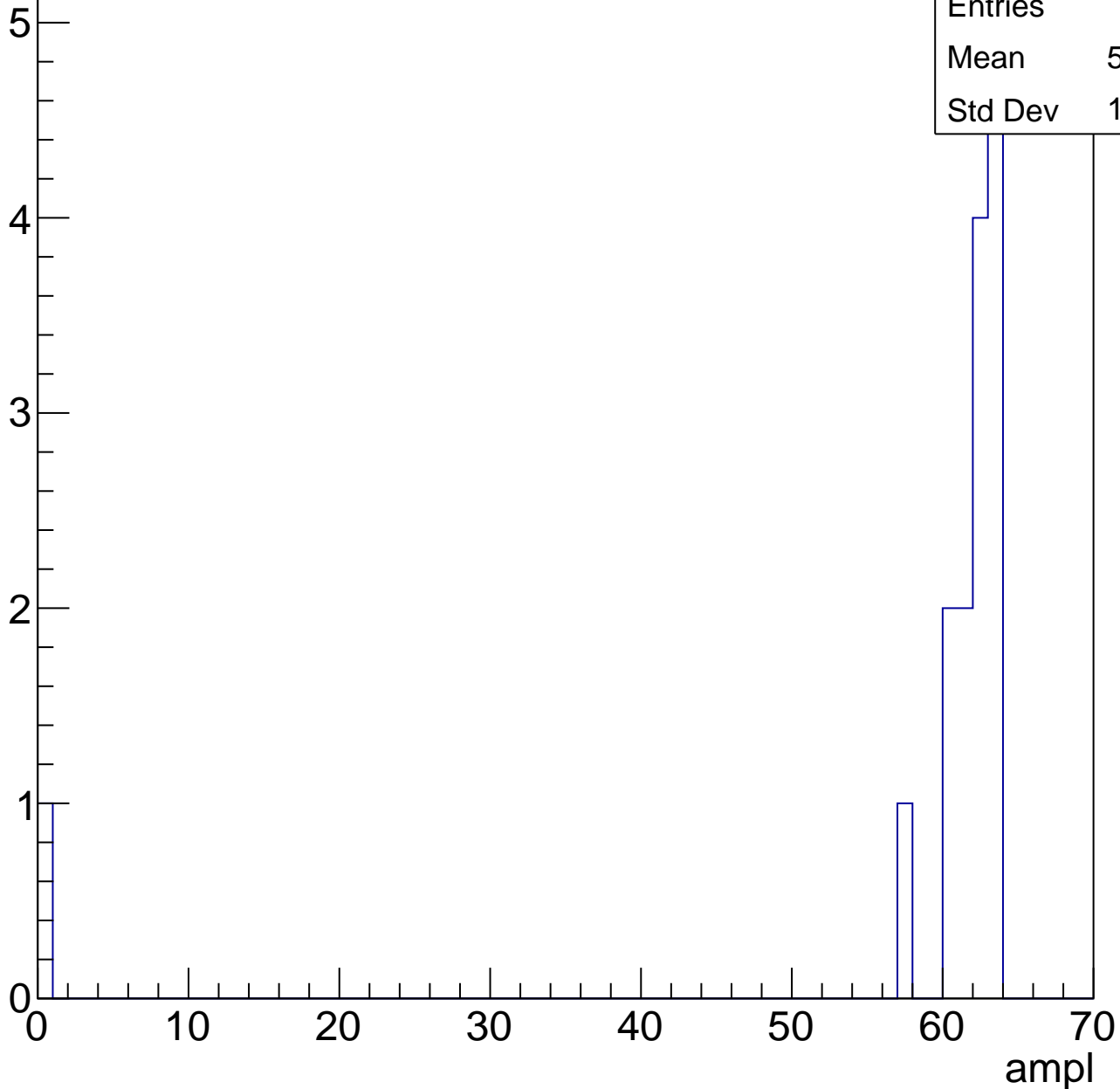


B1L100S, U1-ch10, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	57.47
Std Dev	15.44



B1L100S, U1-ch10, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch11, adc0

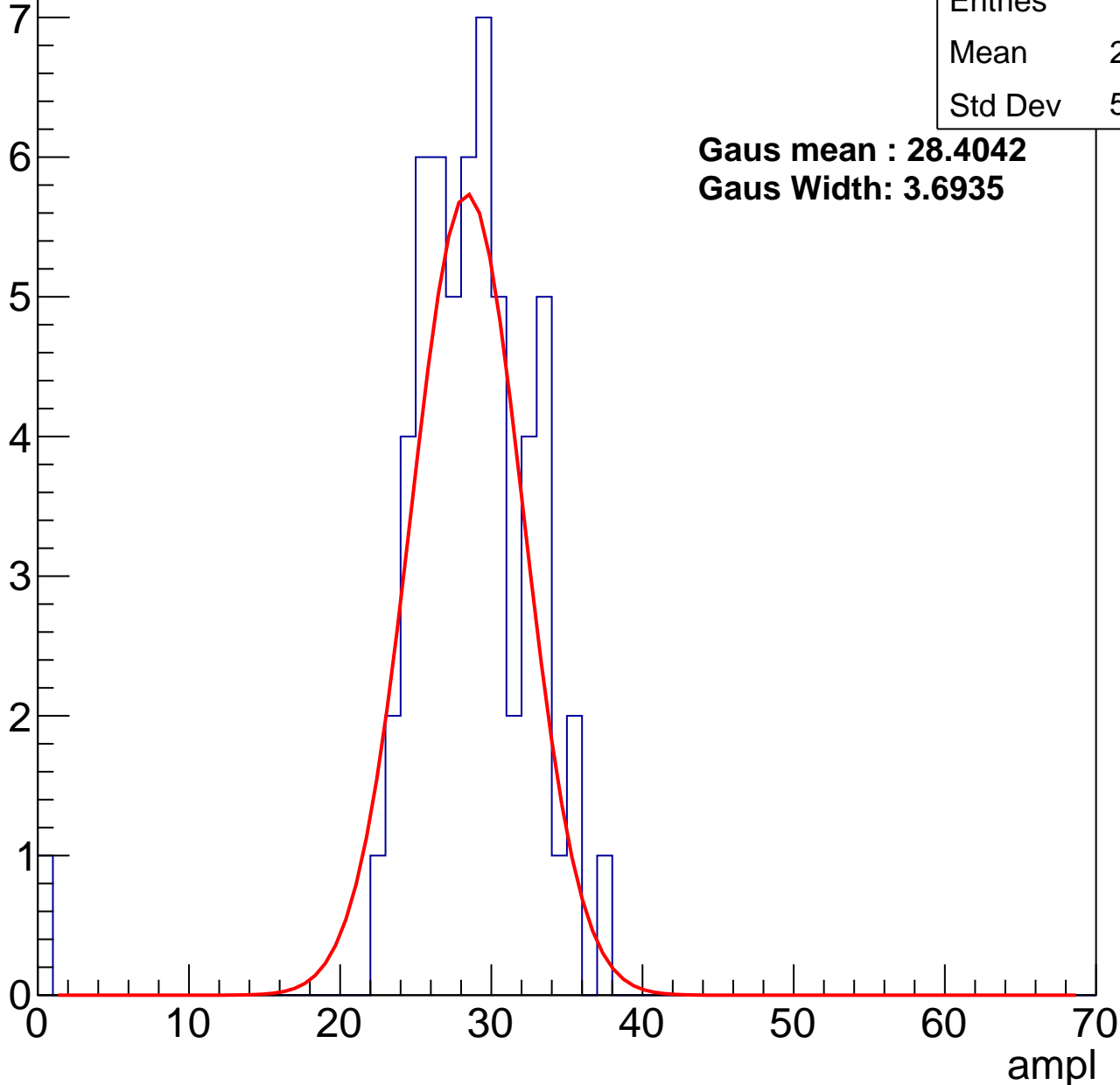
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	27.97
Std Dev	5.027

Gaus mean : 28.4042

Gaus Width: 3.6935



B1L100S, U1-ch11, adc1

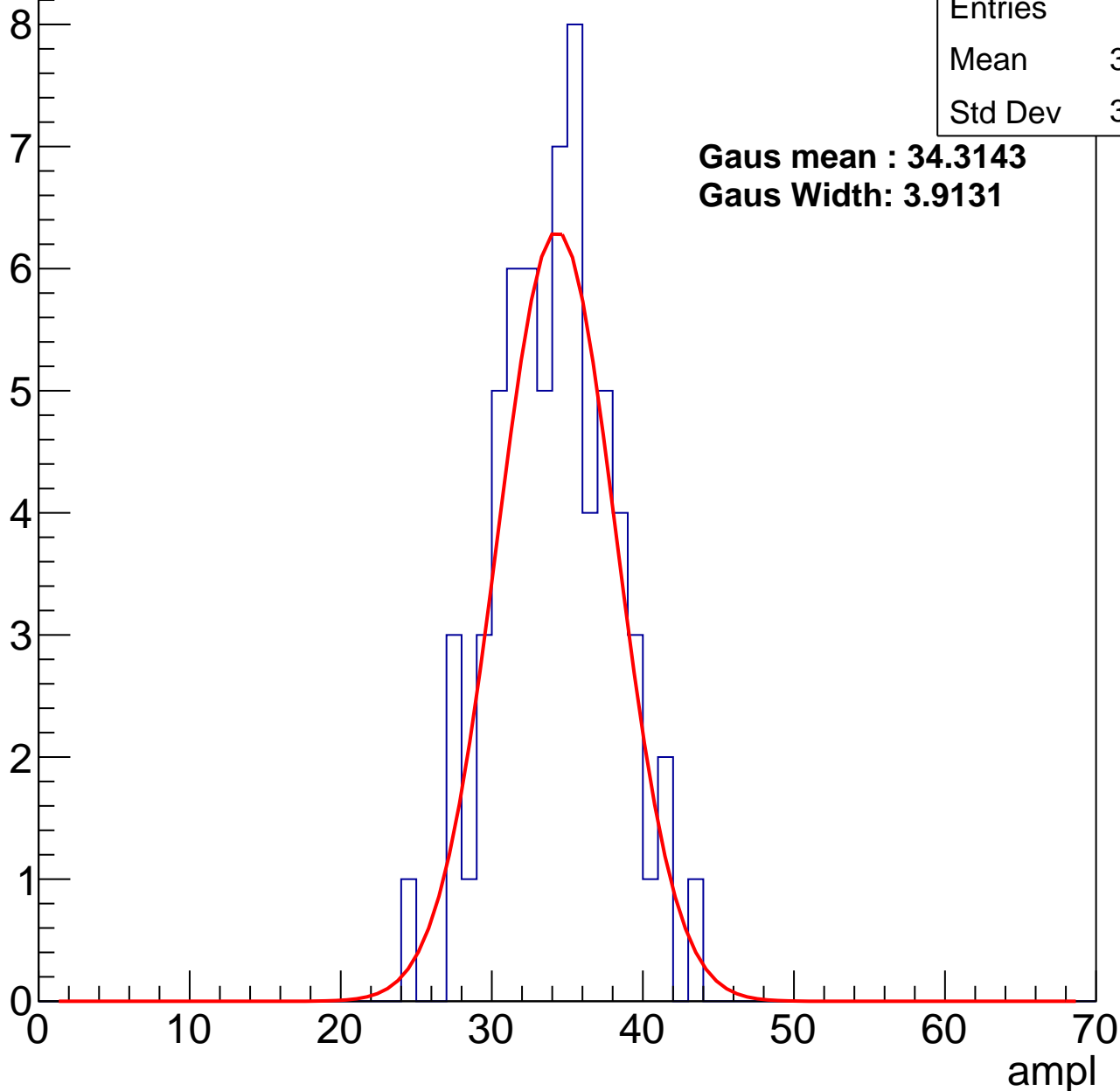
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	33.75
Std Dev	3.803

Gaus mean : 34.3143

Gaus Width: 3.9131



B1L100S, U1-ch11, adc2

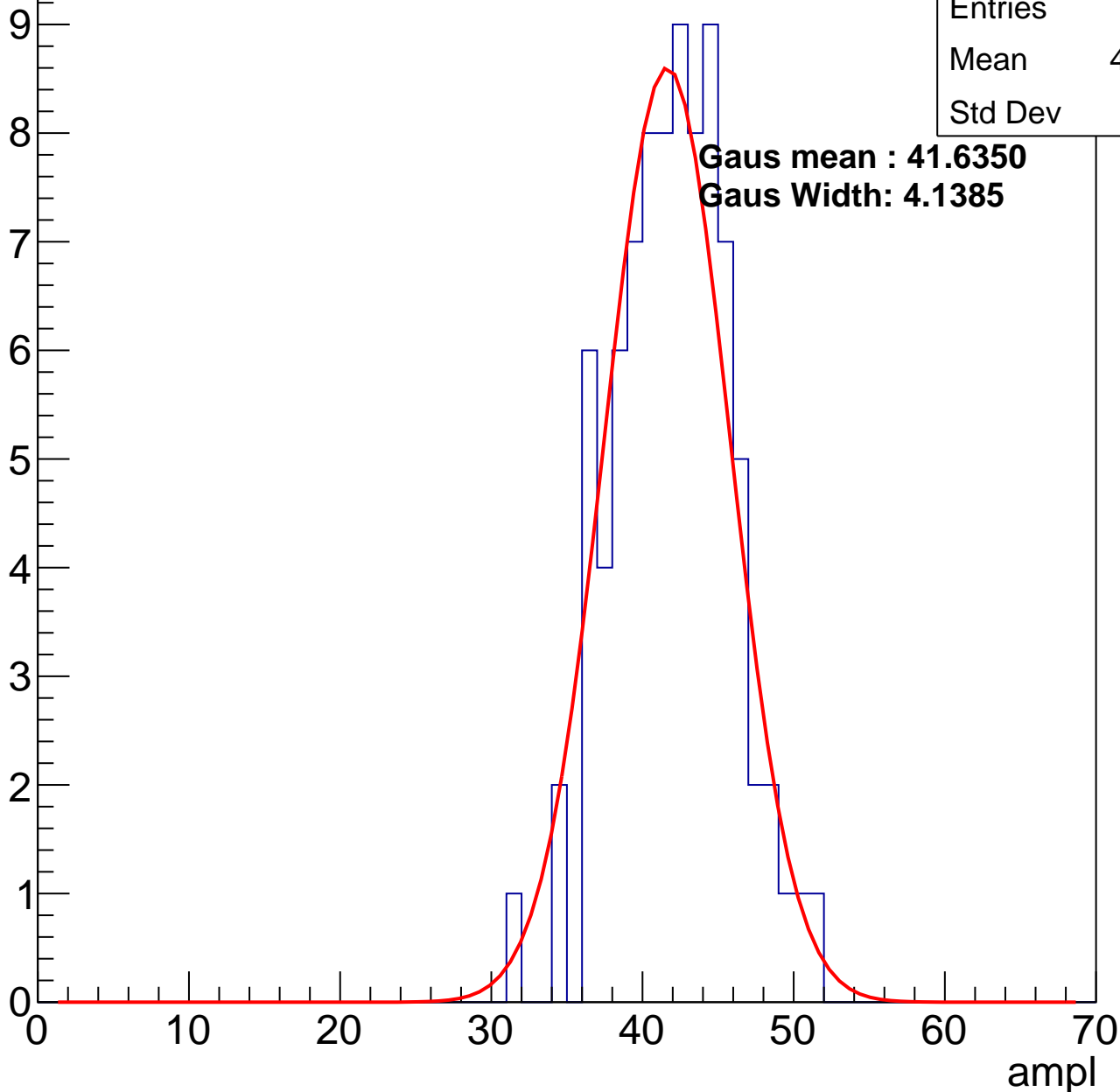
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	41.55
Std Dev	3.79

Gaus mean : 41.6350

Gaus Width: 4.1385

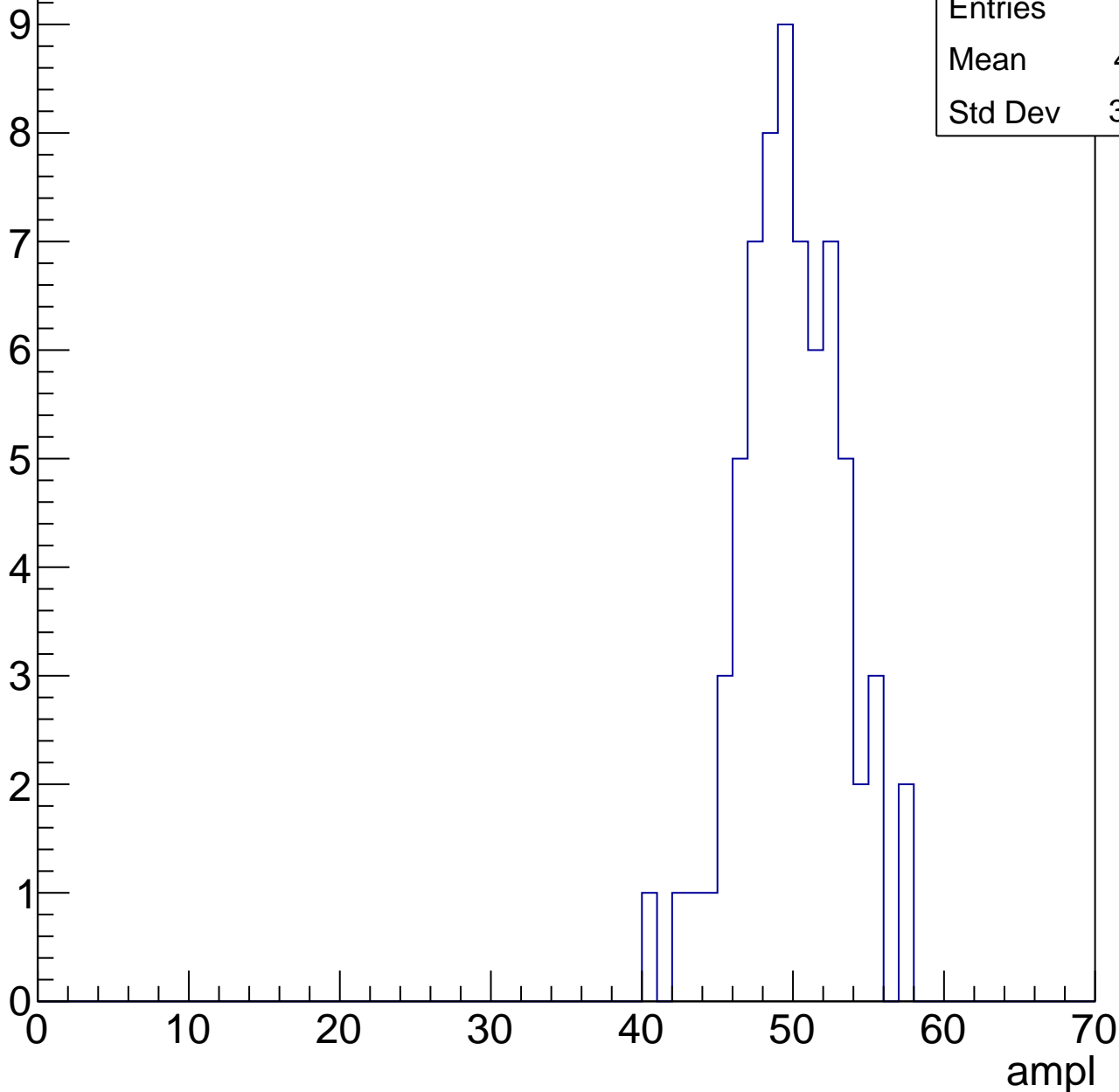


B1L100S, U1-ch11, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	49.41
Std Dev	3.362

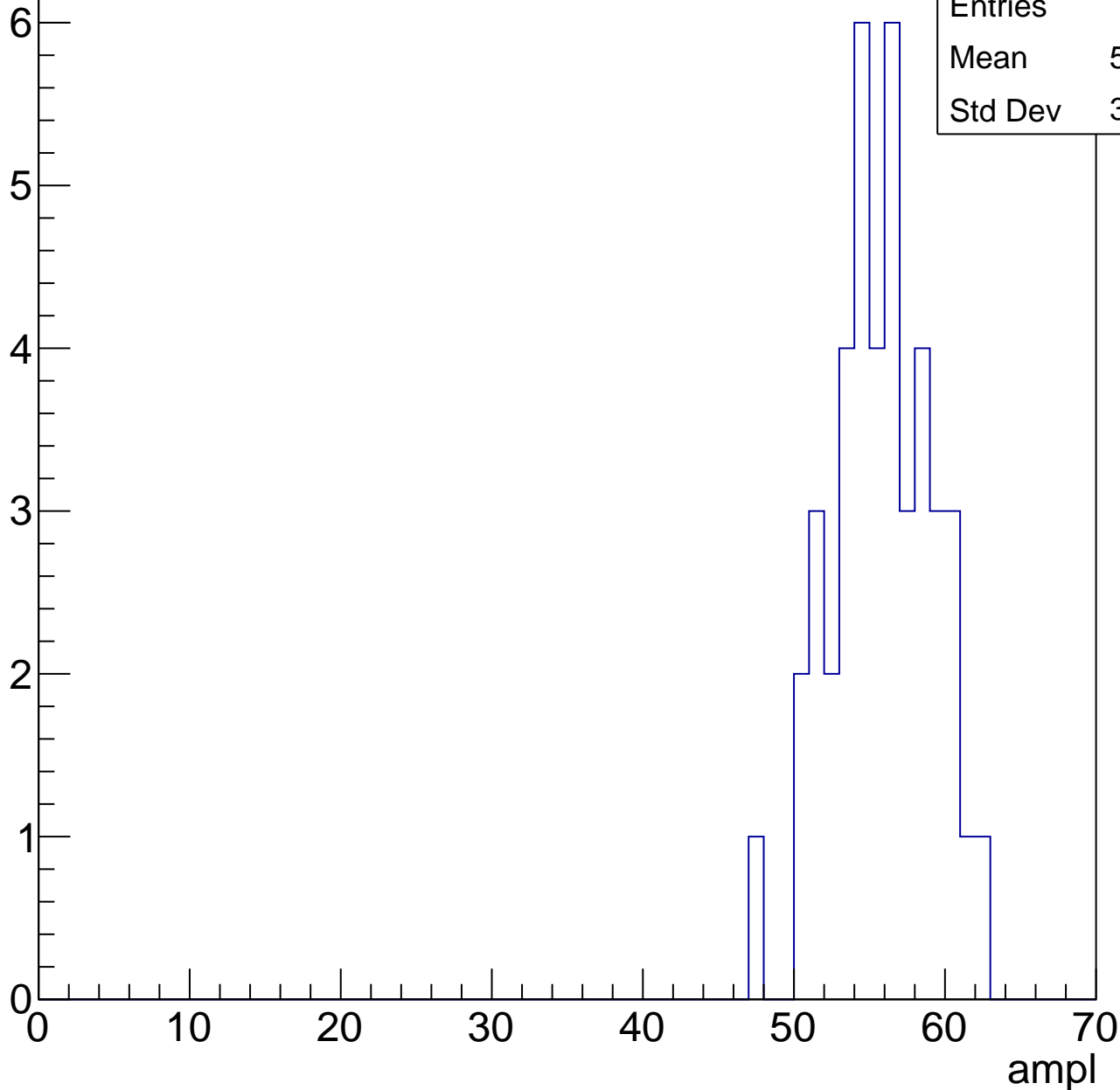


B1L100S, U1-ch11, adc4

calib_packv5_042523_0143.root, FC#4, port A2

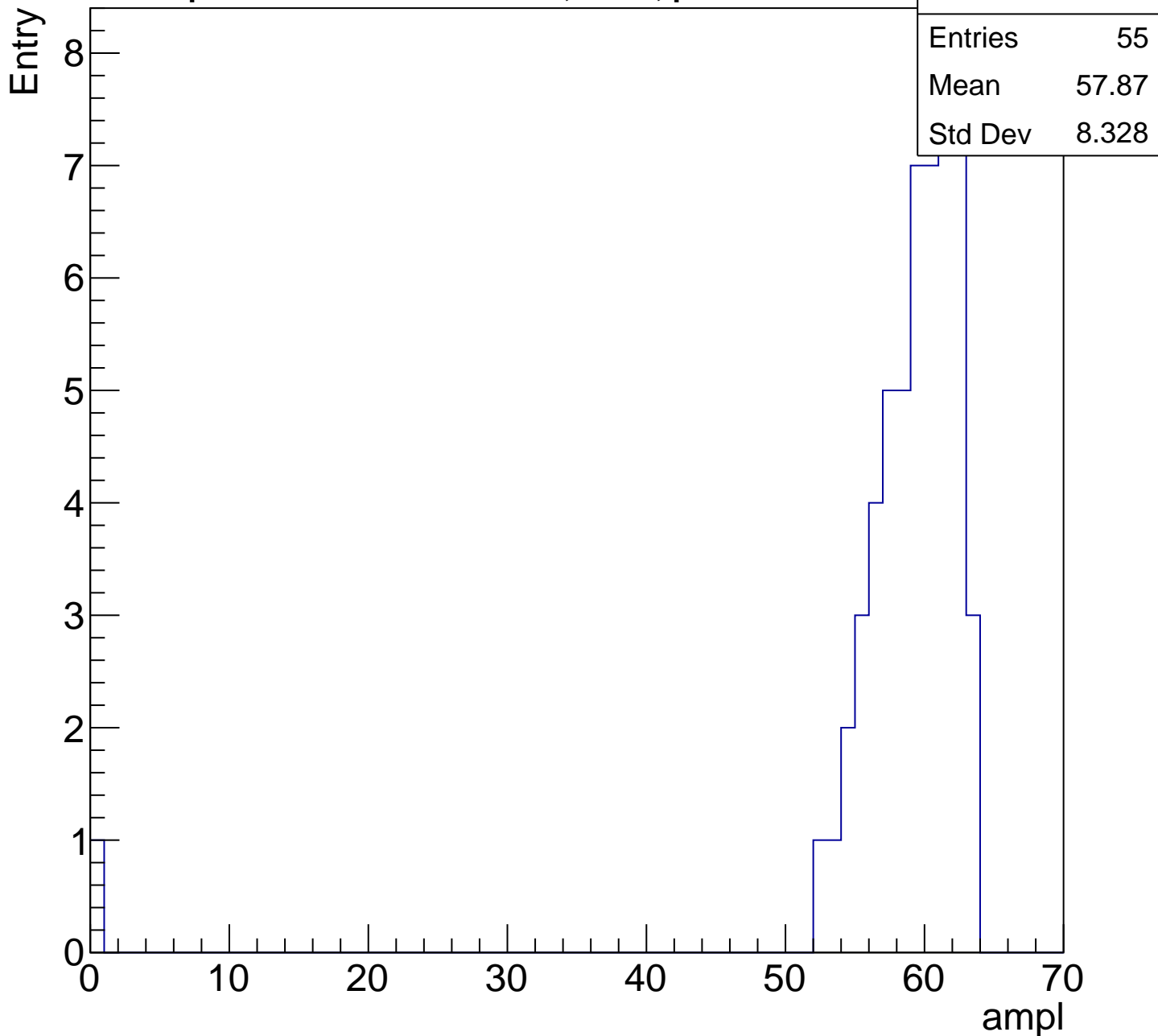
Entry

Entries	43
Mean	55.33
Std Dev	3.269



B1L100S, U1-ch11, adc5

calib_packv5_042523_0143.root, FC#4, port A2

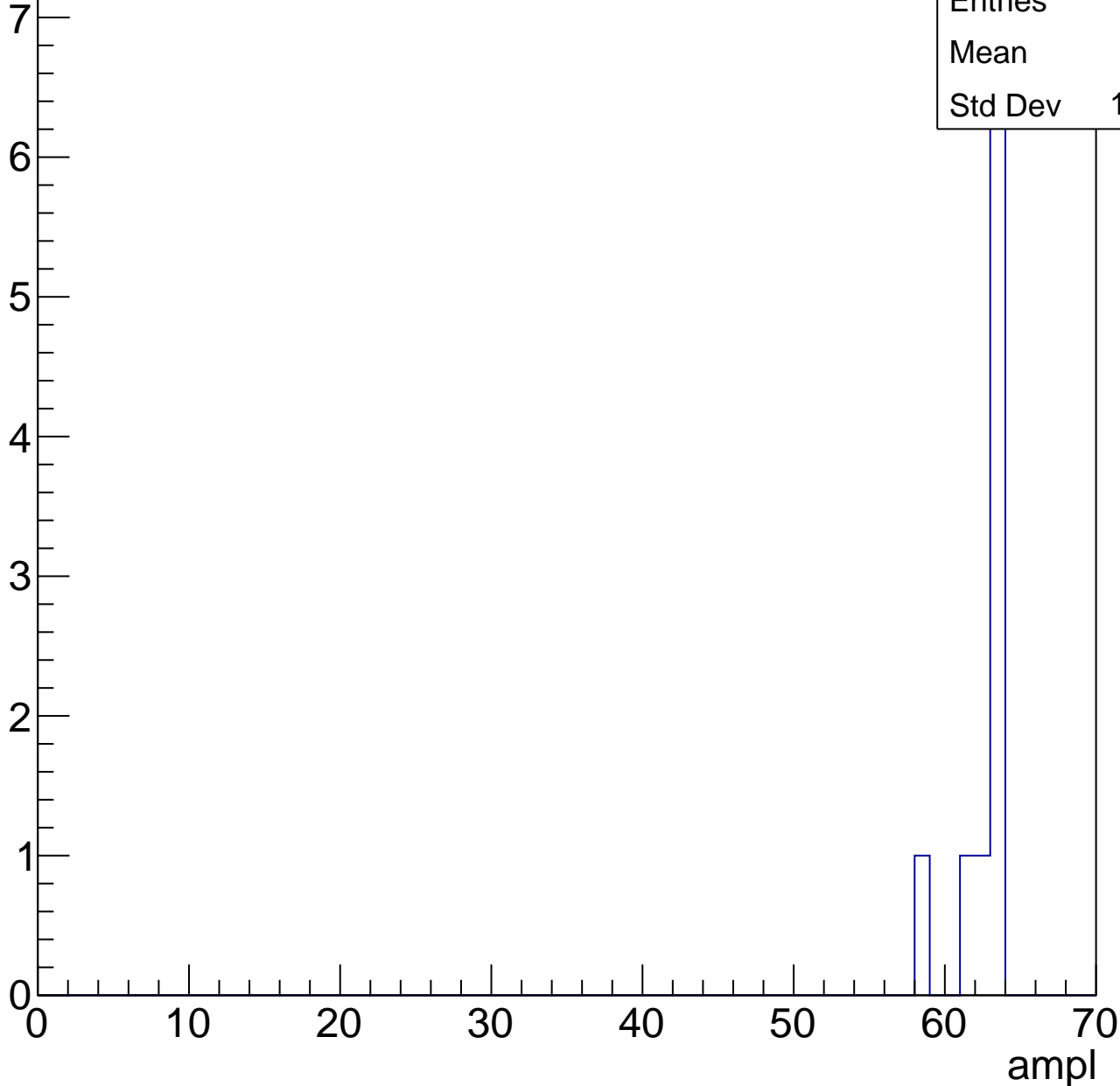


B1L100S, U1-ch11, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	62.2
Std Dev	1.536



B1L100S, U1-ch11, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch12, adc0

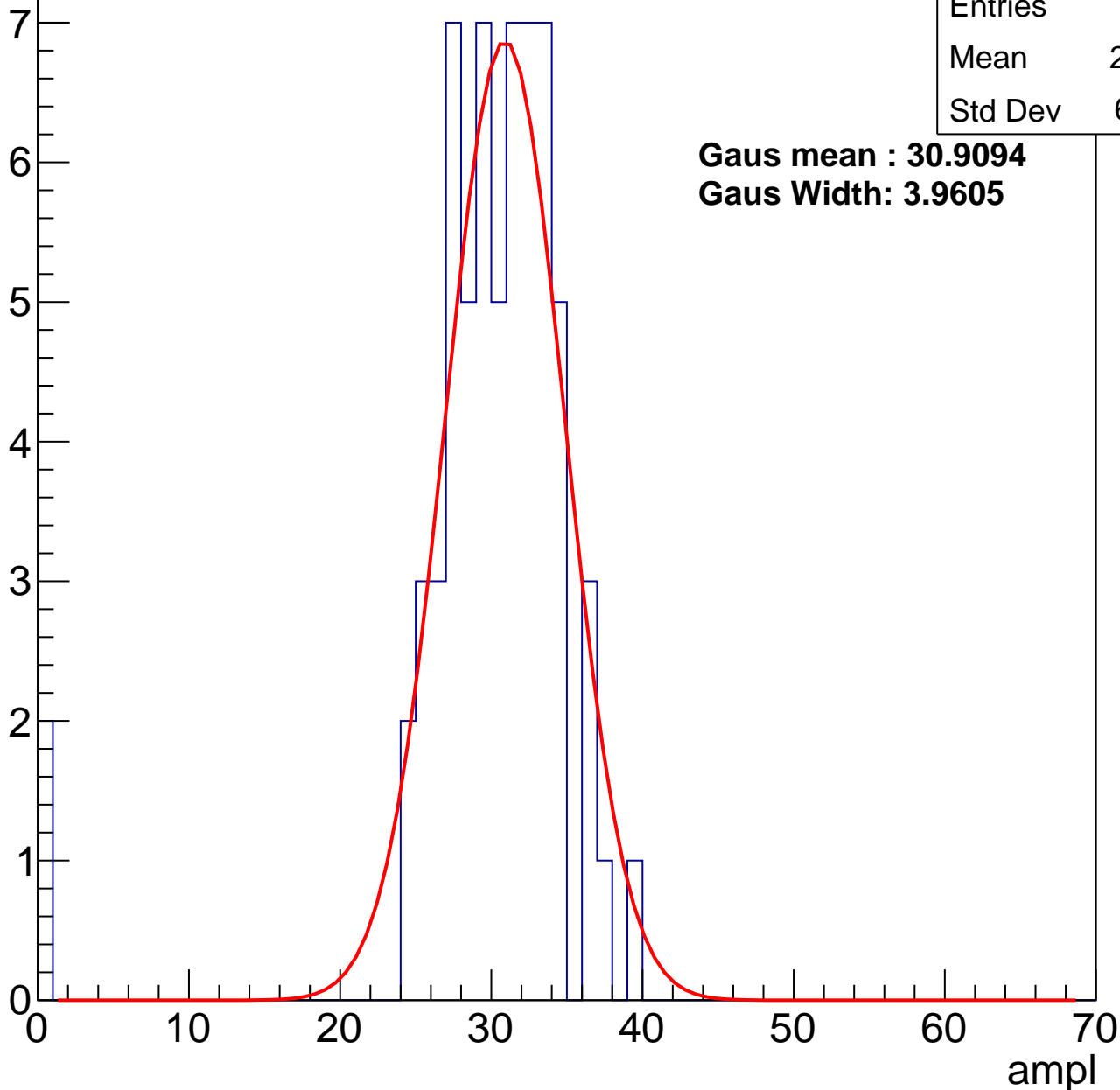
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	29.37
Std Dev	6.171

Gaus mean : 30.9094

Gaus Width: 3.9605



B1L100S, U1-ch12, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	37.25
Std Dev	3.249

Gaus mean : 37.8172

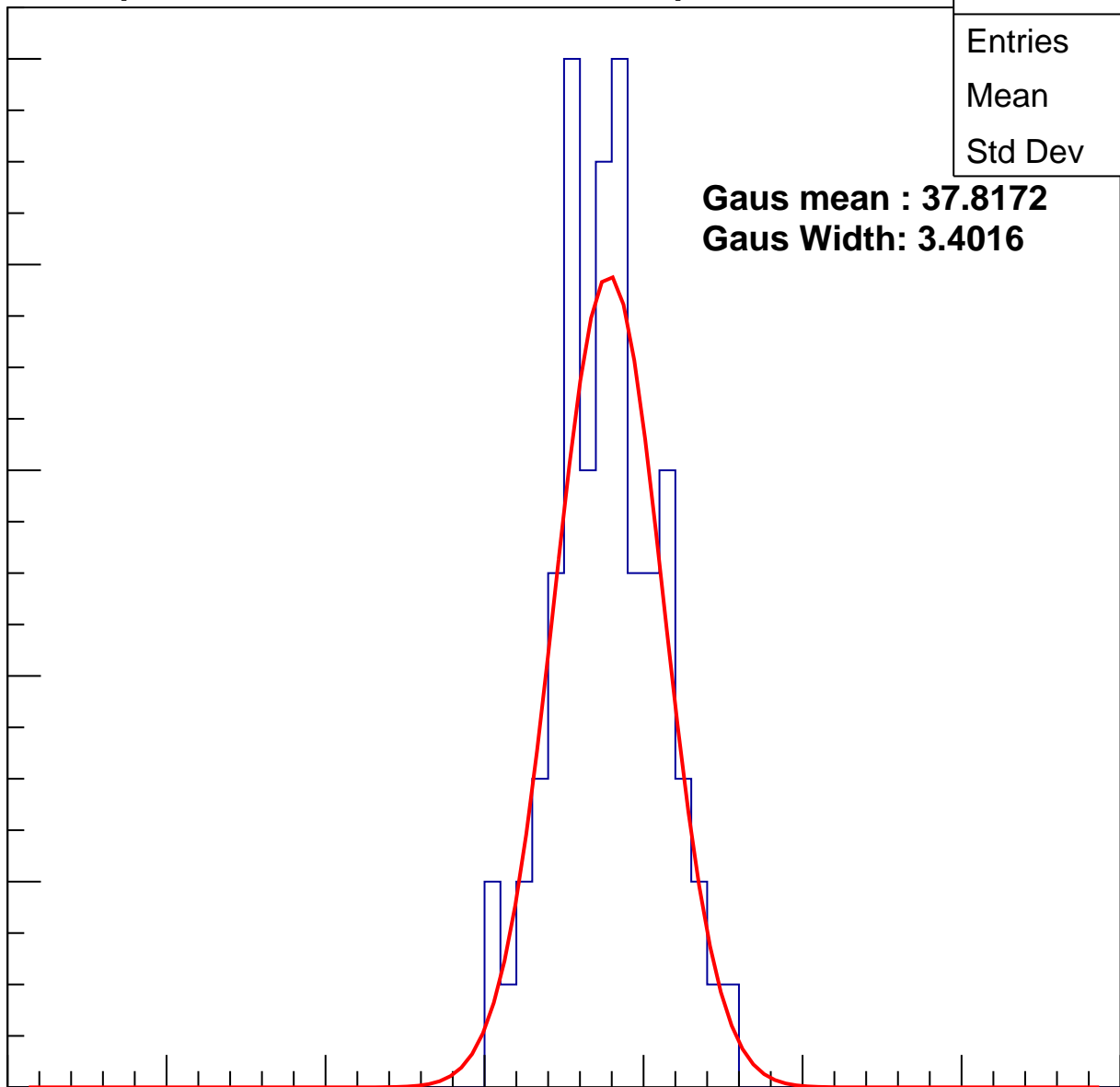
Gaus Width: 3.4016

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch12, adc2

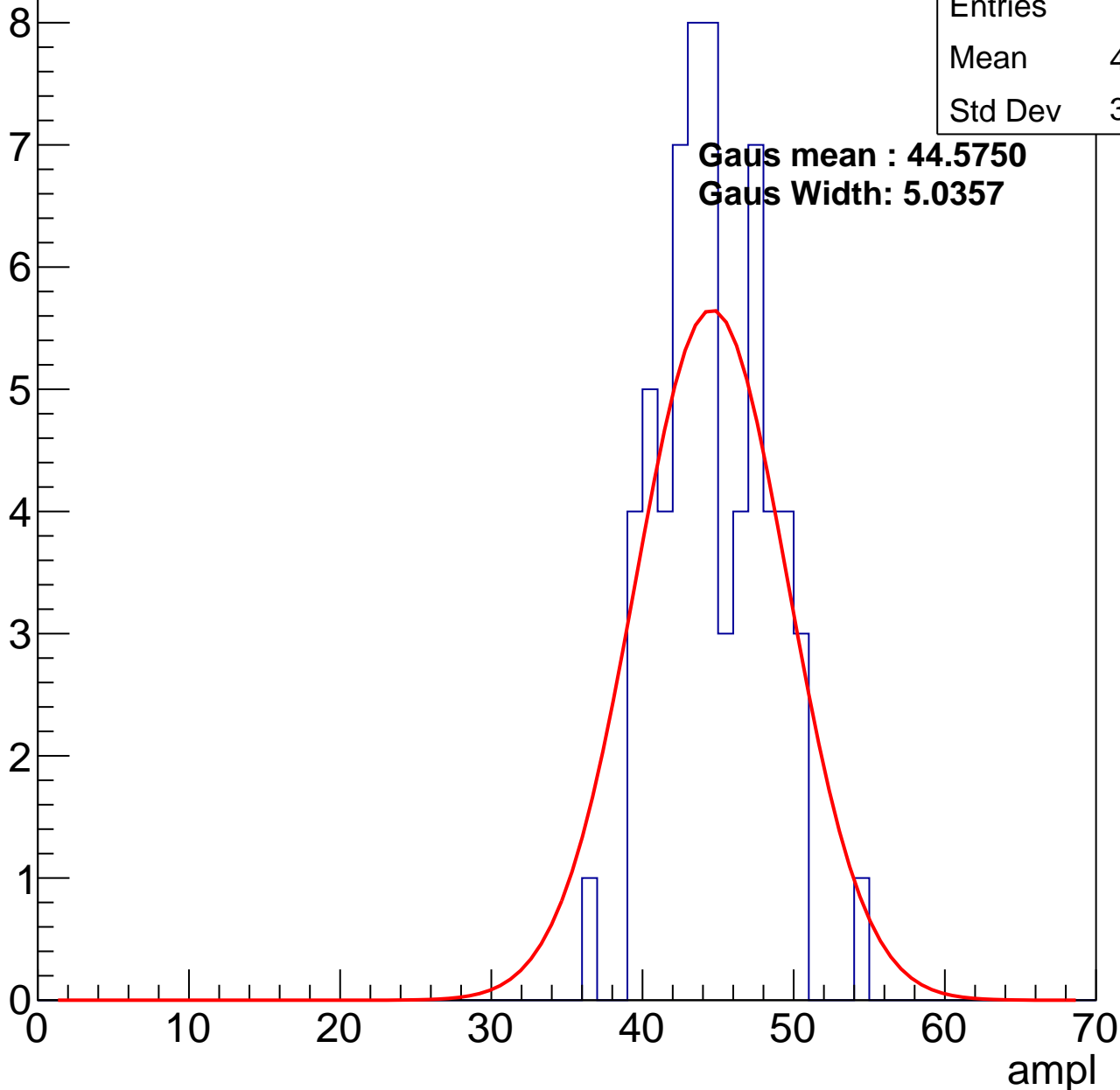
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	44.22
Std Dev	3.489

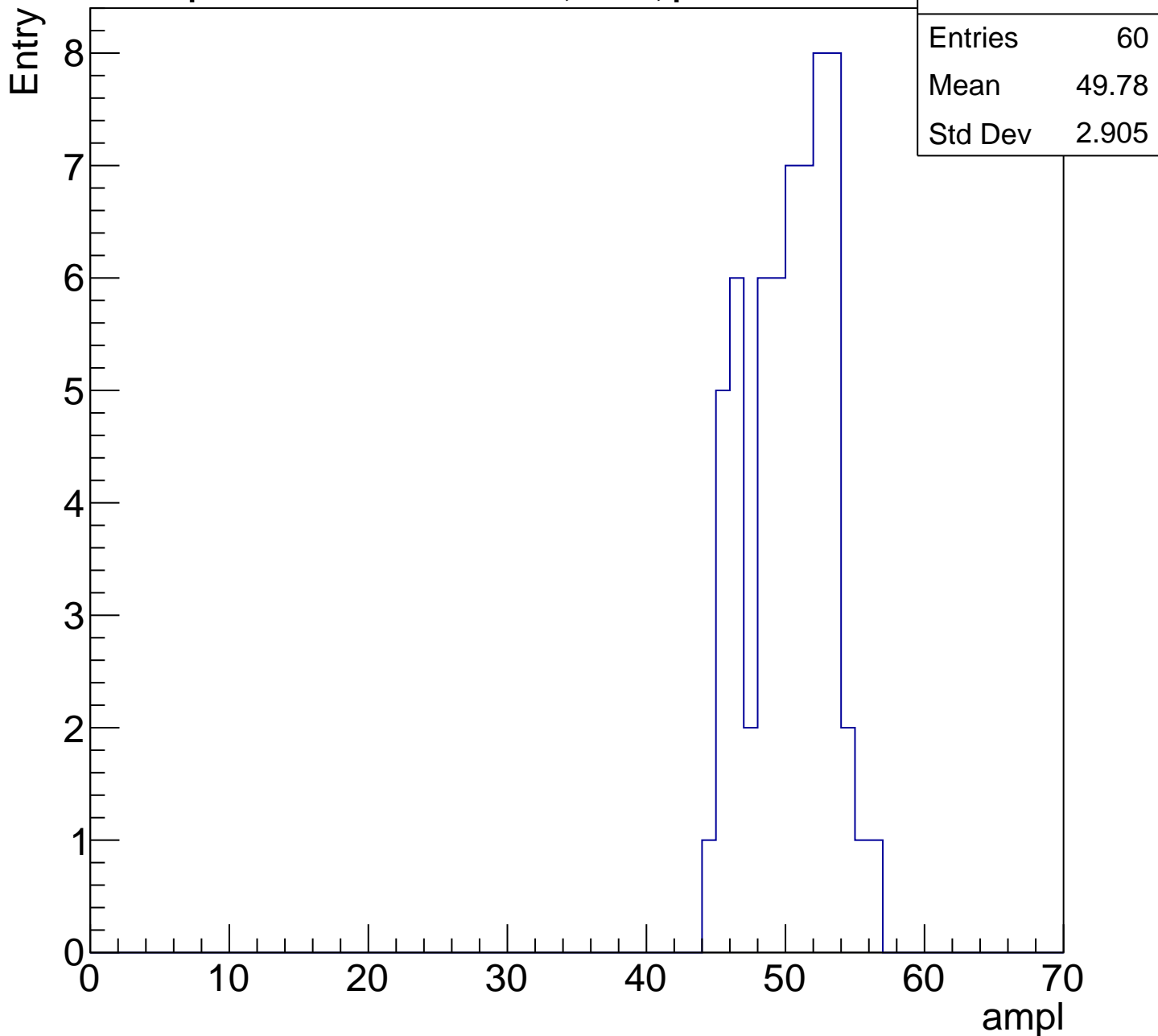
Gaus mean : 44.5750

Gaus Width: 5.0357



B1L100S, U1-ch12, adc3

calib_packv5_042523_0143.root, FC#4, port A2

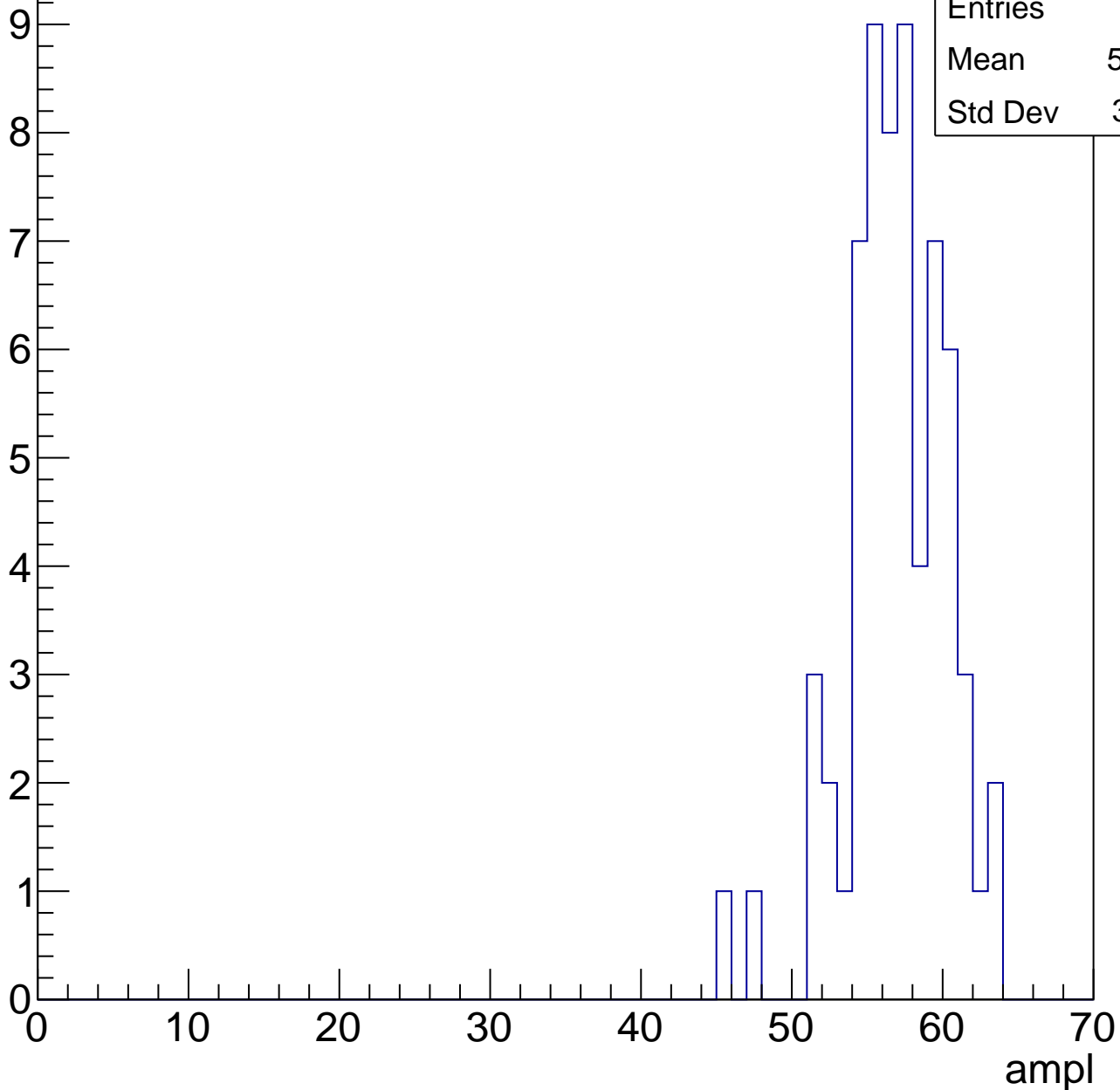


B1L100S, U1-ch12, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	56.44
Std Dev	3.391

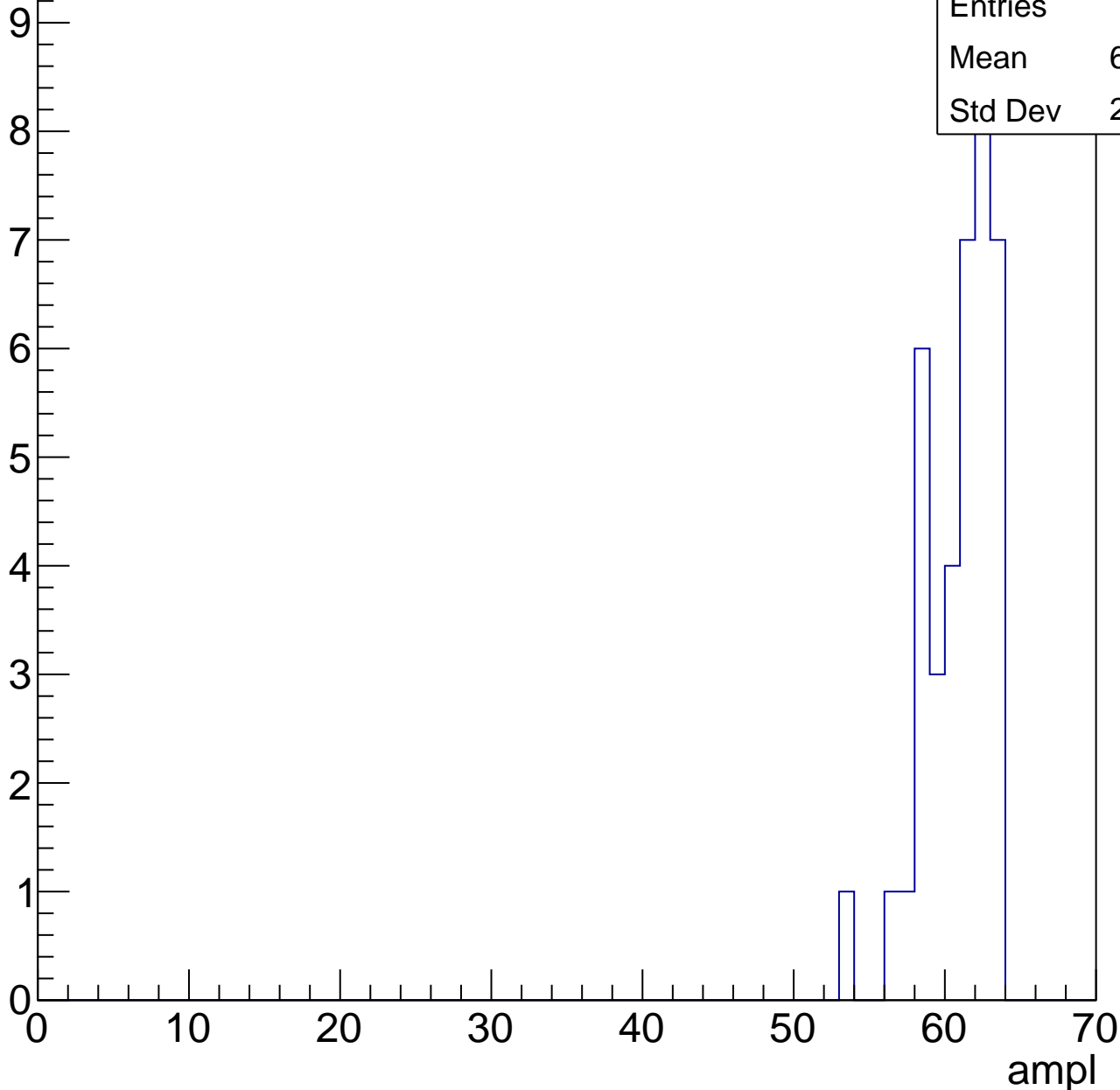


B1L100S, U1-ch12, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	60.44
Std Dev	2.262



B1L100S, U1-ch12, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch12, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch13, adc0

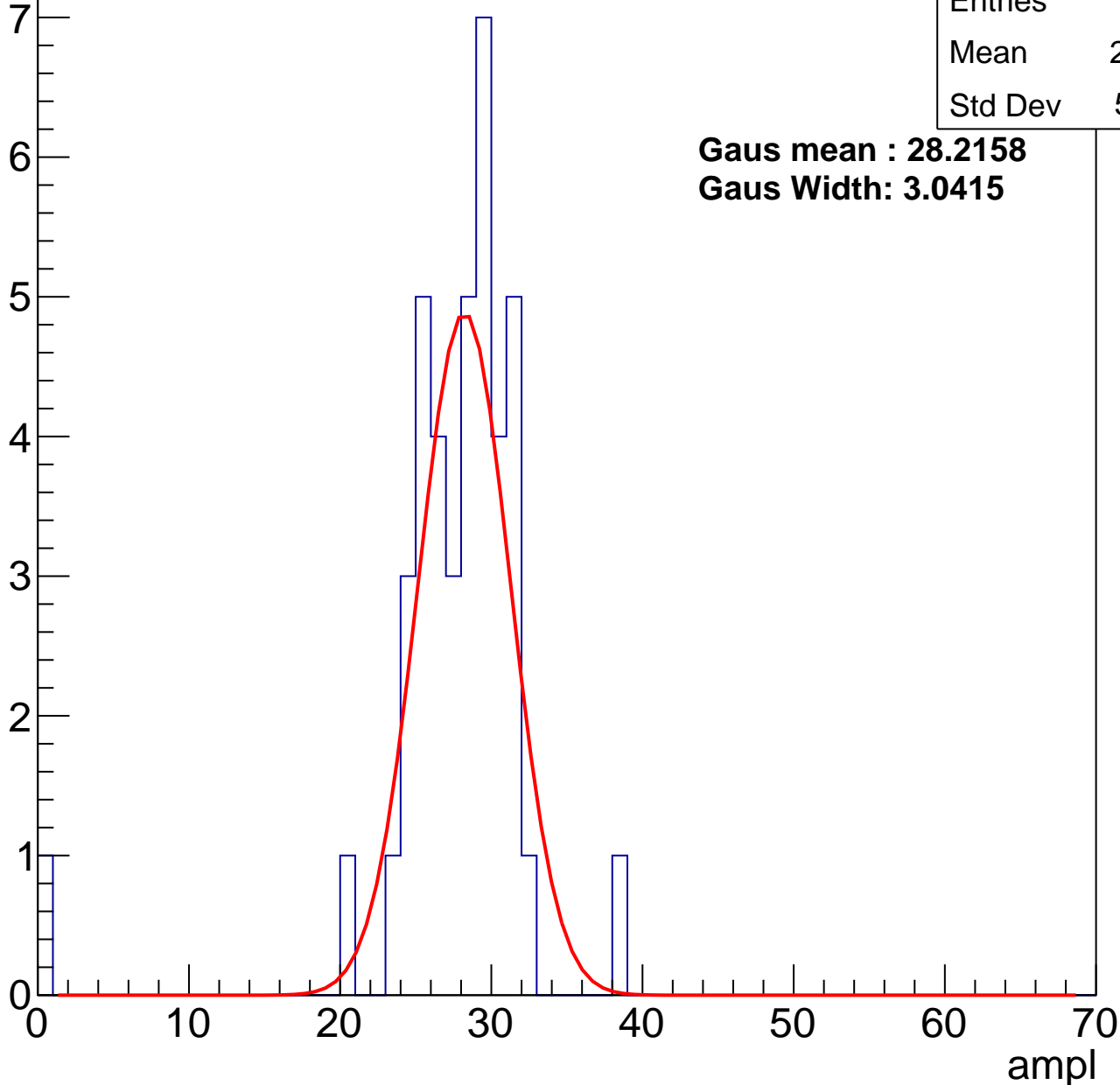
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	27.15
Std Dev	5.271

Gaus mean : 28.2158

Gaus Width: 3.0415



B1L100S, U1-ch13, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	91
Mean	34.38
Std Dev	3.679

Gaus mean : 34.9336

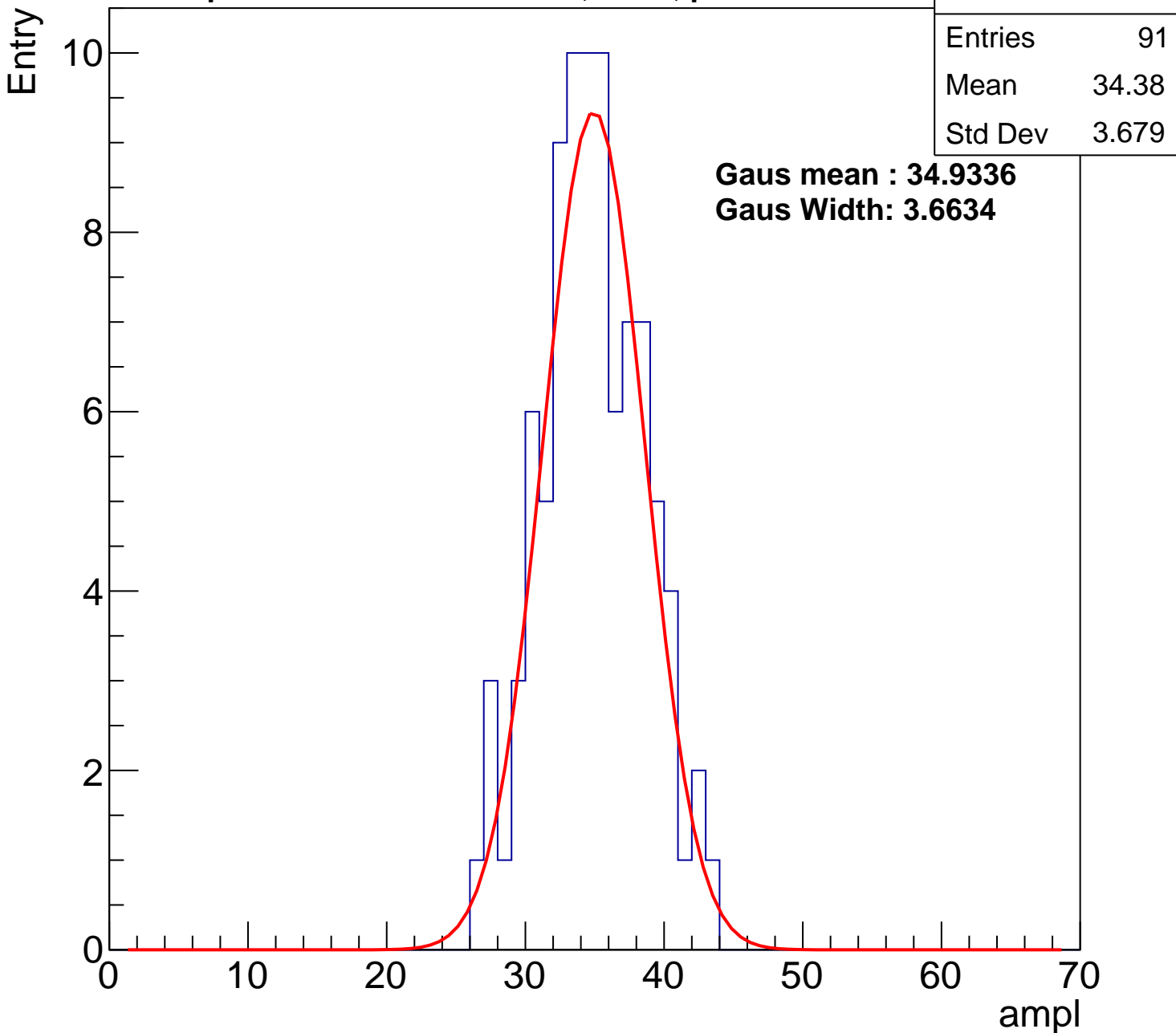
Gaus Width: 3.6634

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch13, adc2

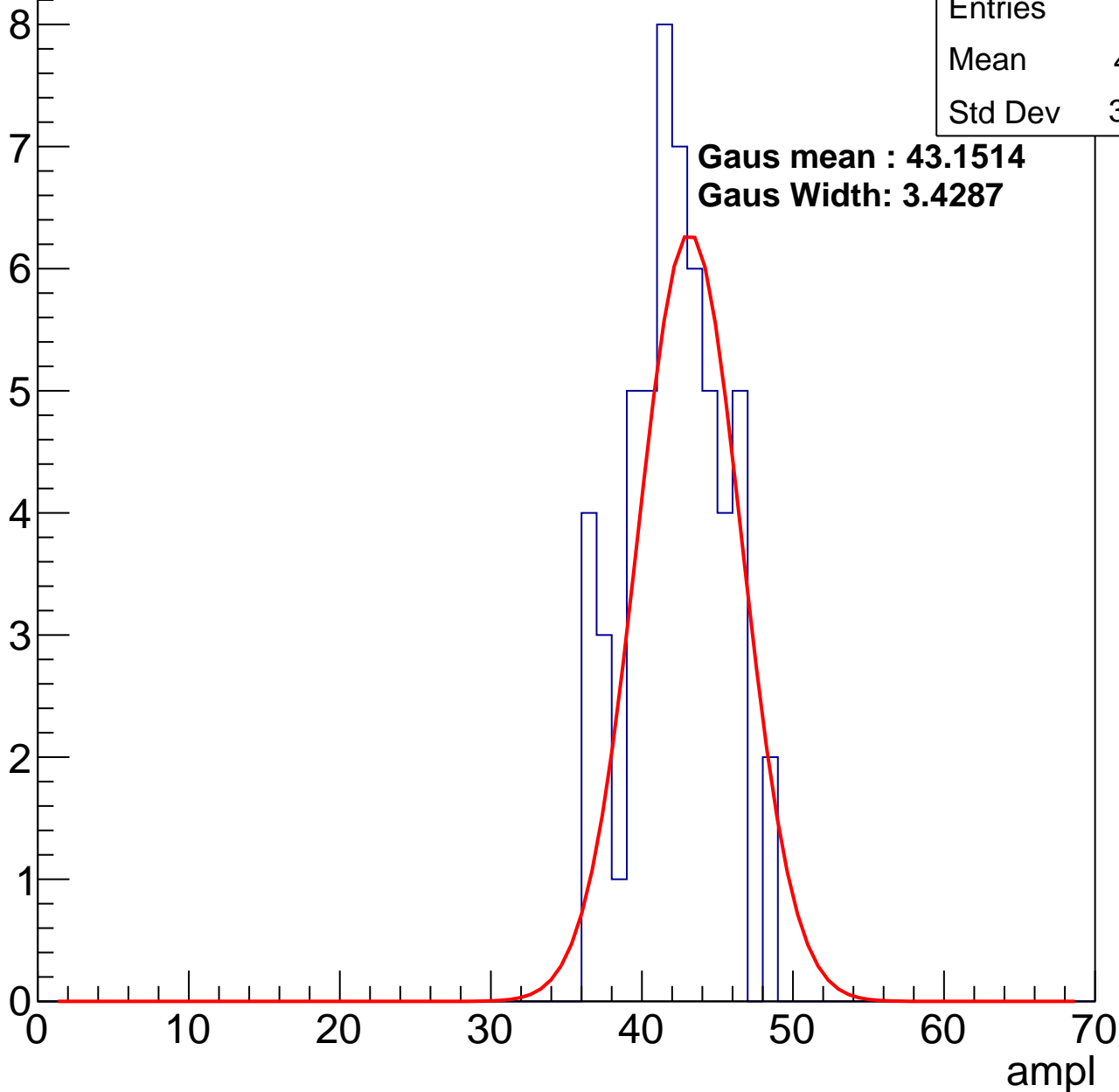
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	41.71
Std Dev	3.067

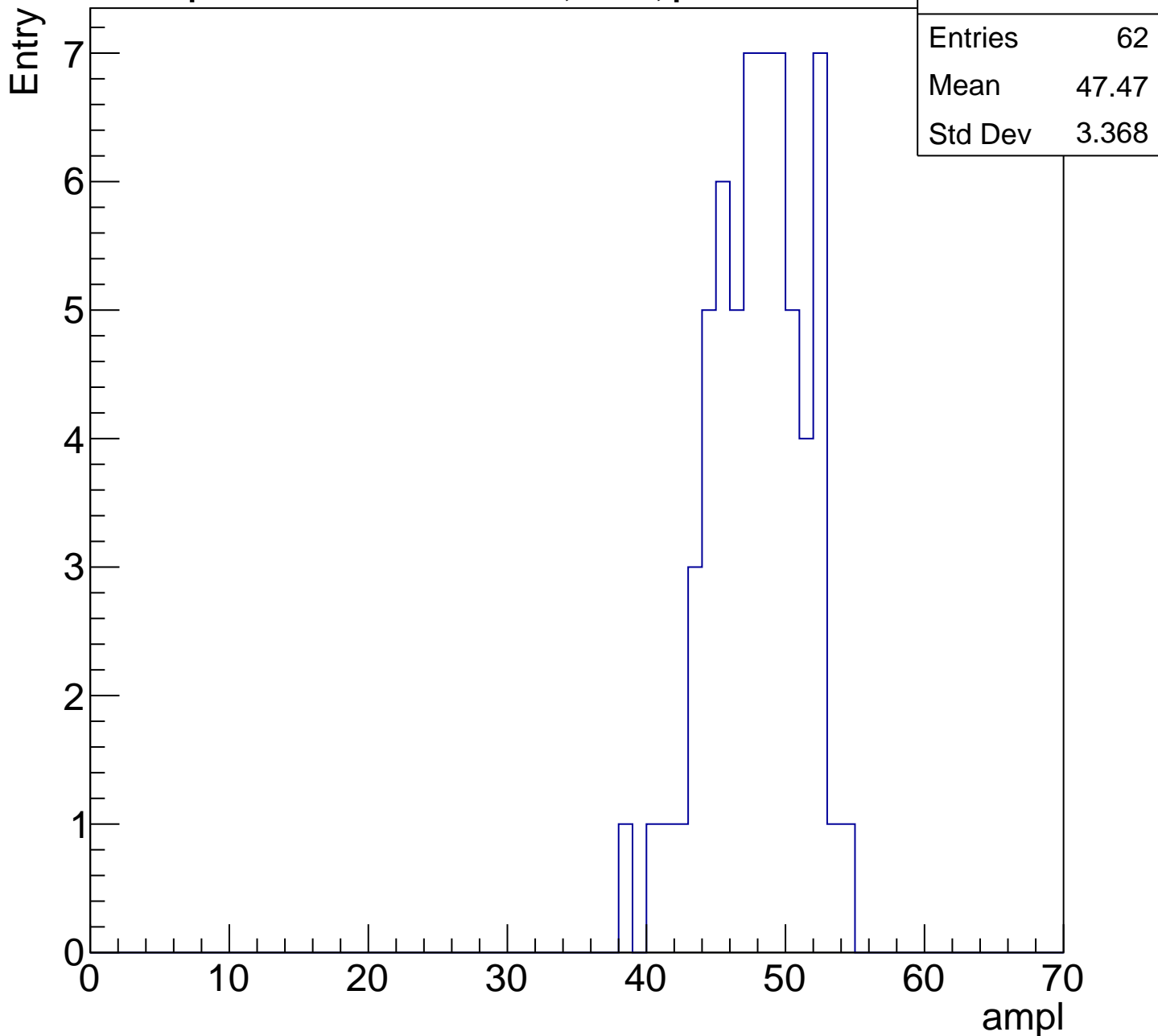
Gaus mean : 43.1514

Gaus Width: 3.4287



B1L100S, U1-ch13, adc3

calib_packv5_042523_0143.root, FC#4, port A2

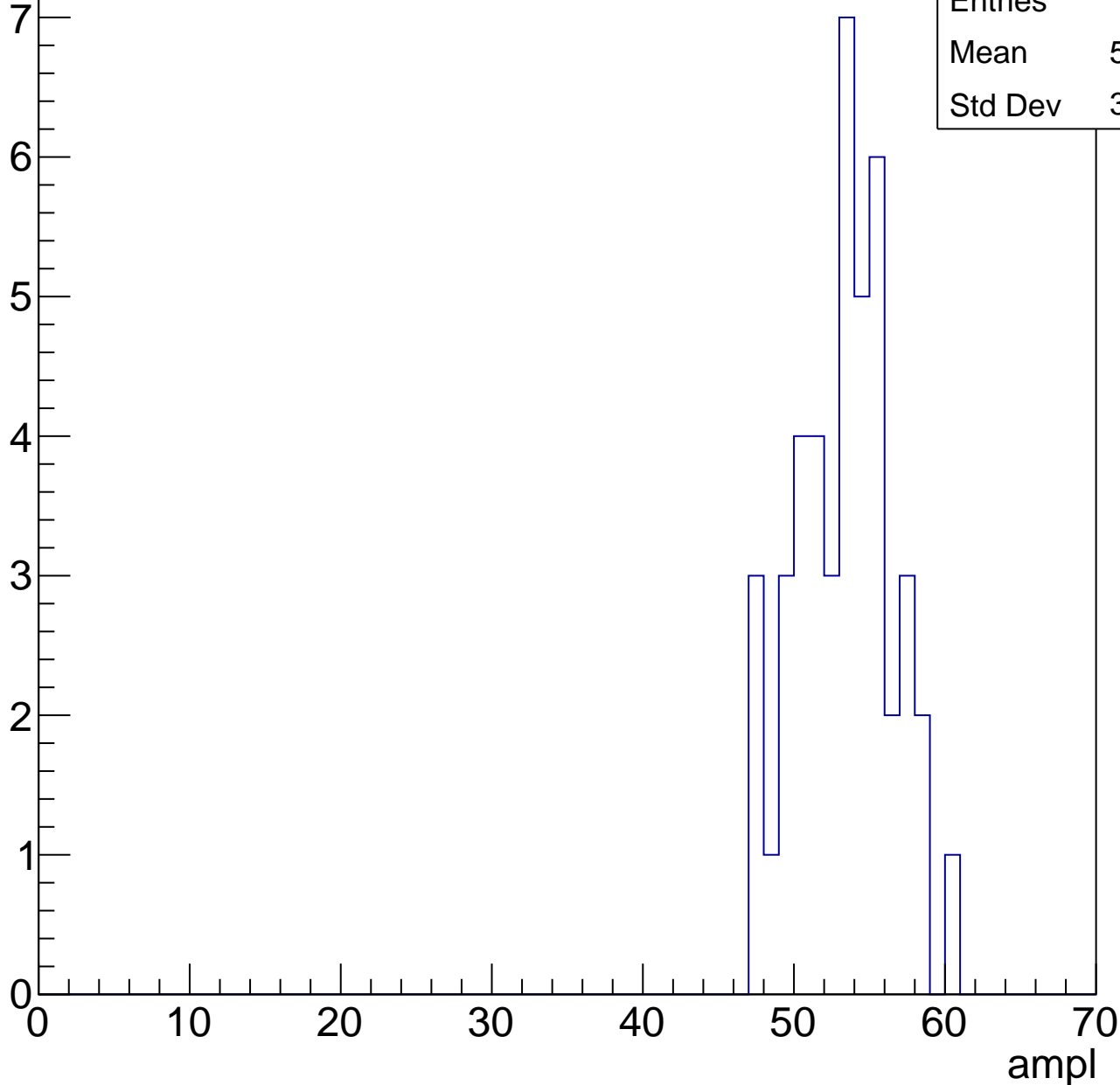


B1L100S, U1-ch13, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	52.86
Std Dev	3.123

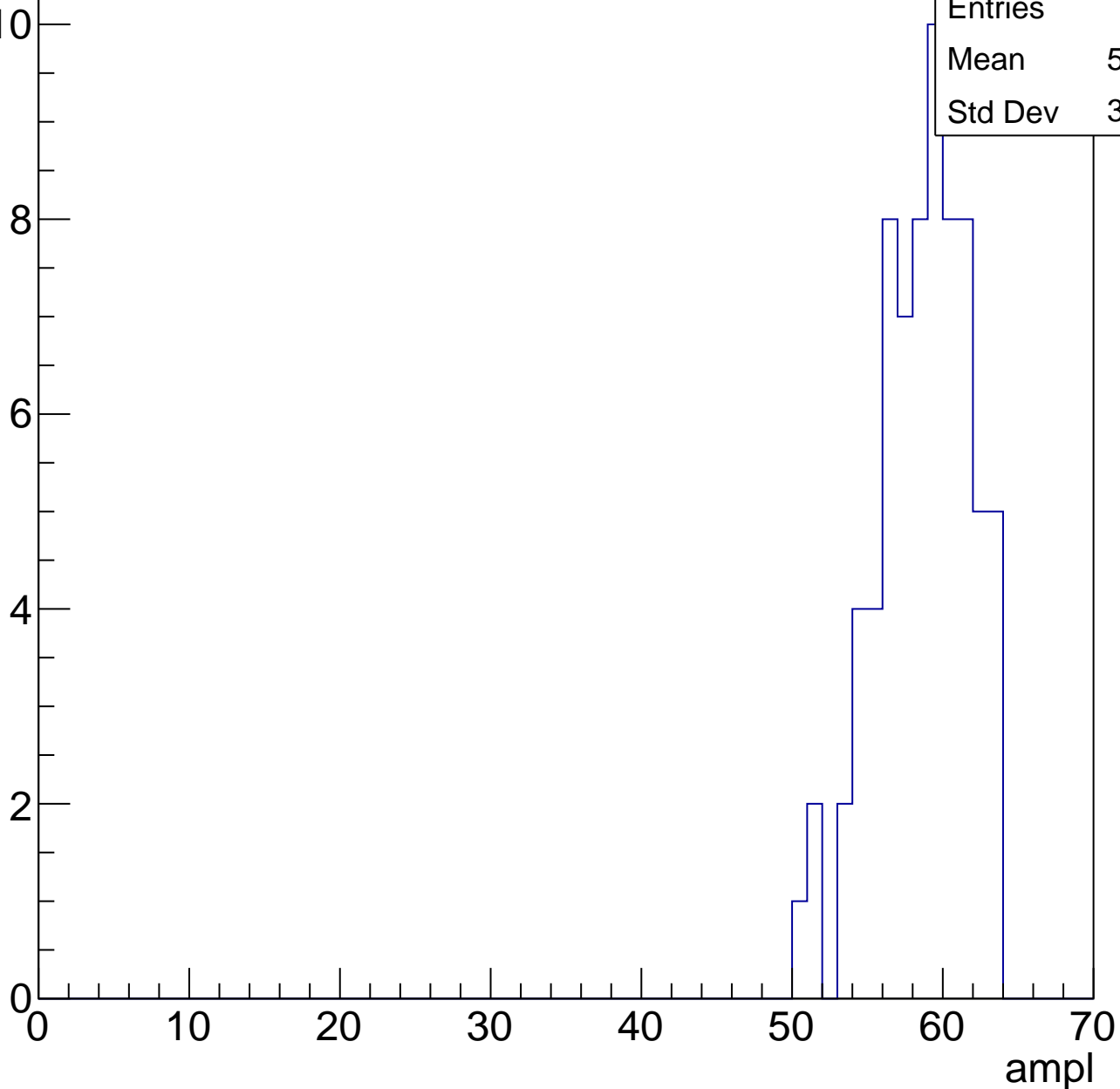


B1L100S, U1-ch13, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	58.17
Std Dev	3.046

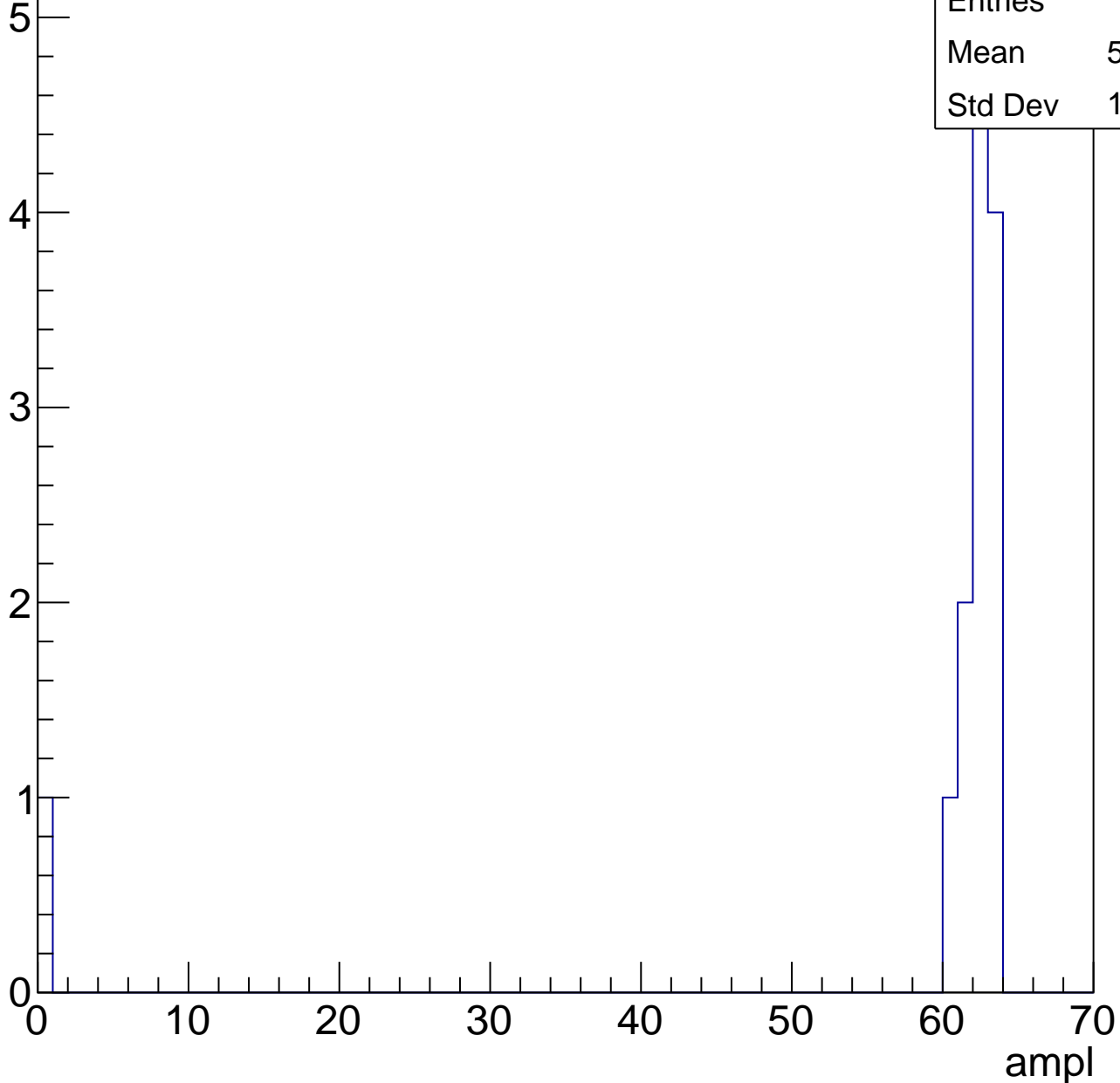


B1L100S, U1-ch13, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

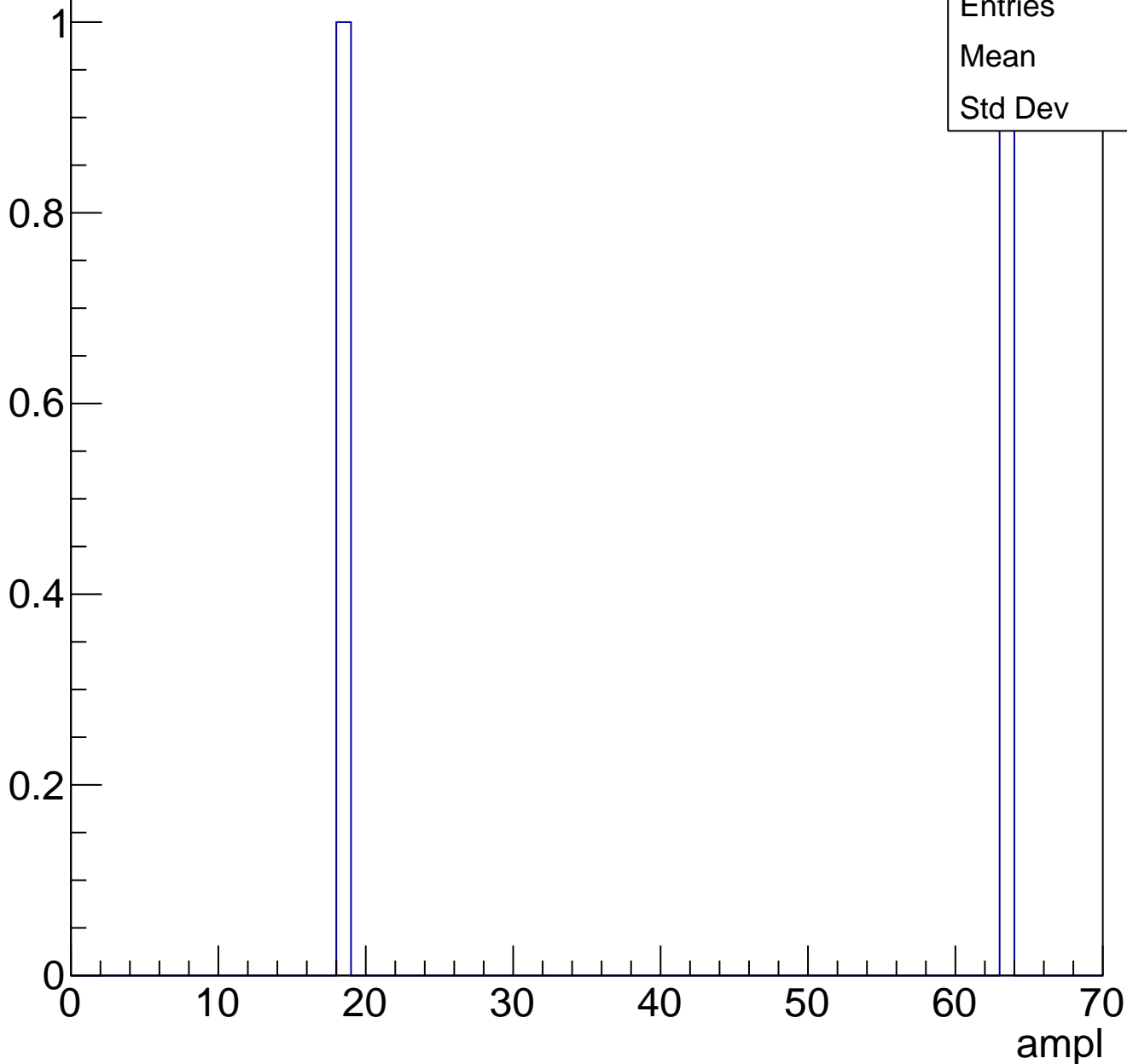
Entries	13
Mean	57.23
Std Dev	16.54



B1L100S, U1-ch13, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch14, adc0

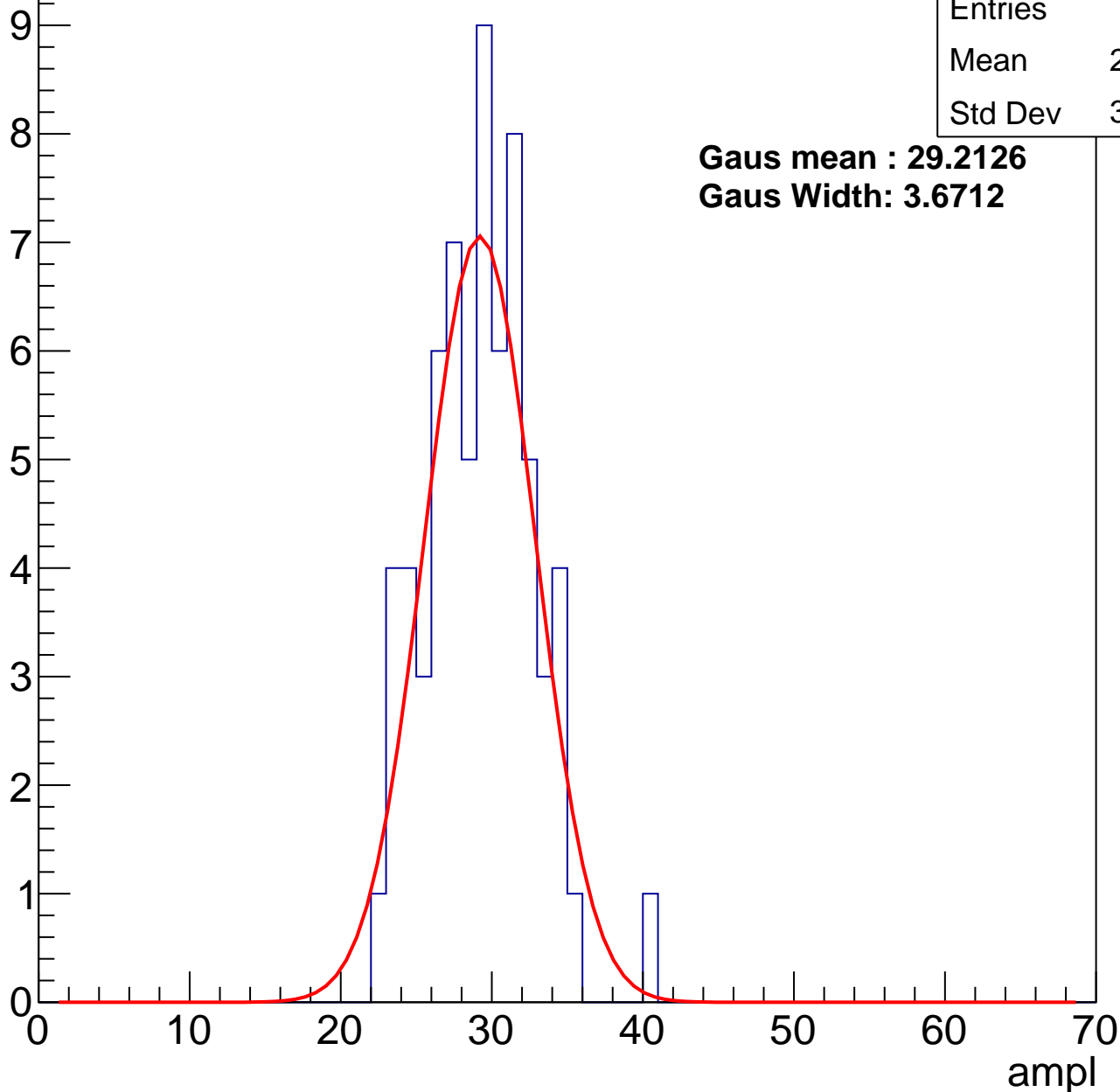
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.79
Std Dev	3.484

Gaus mean : 29.2126

Gaus Width: 3.6712



B1L100S, U1-ch14, adc1

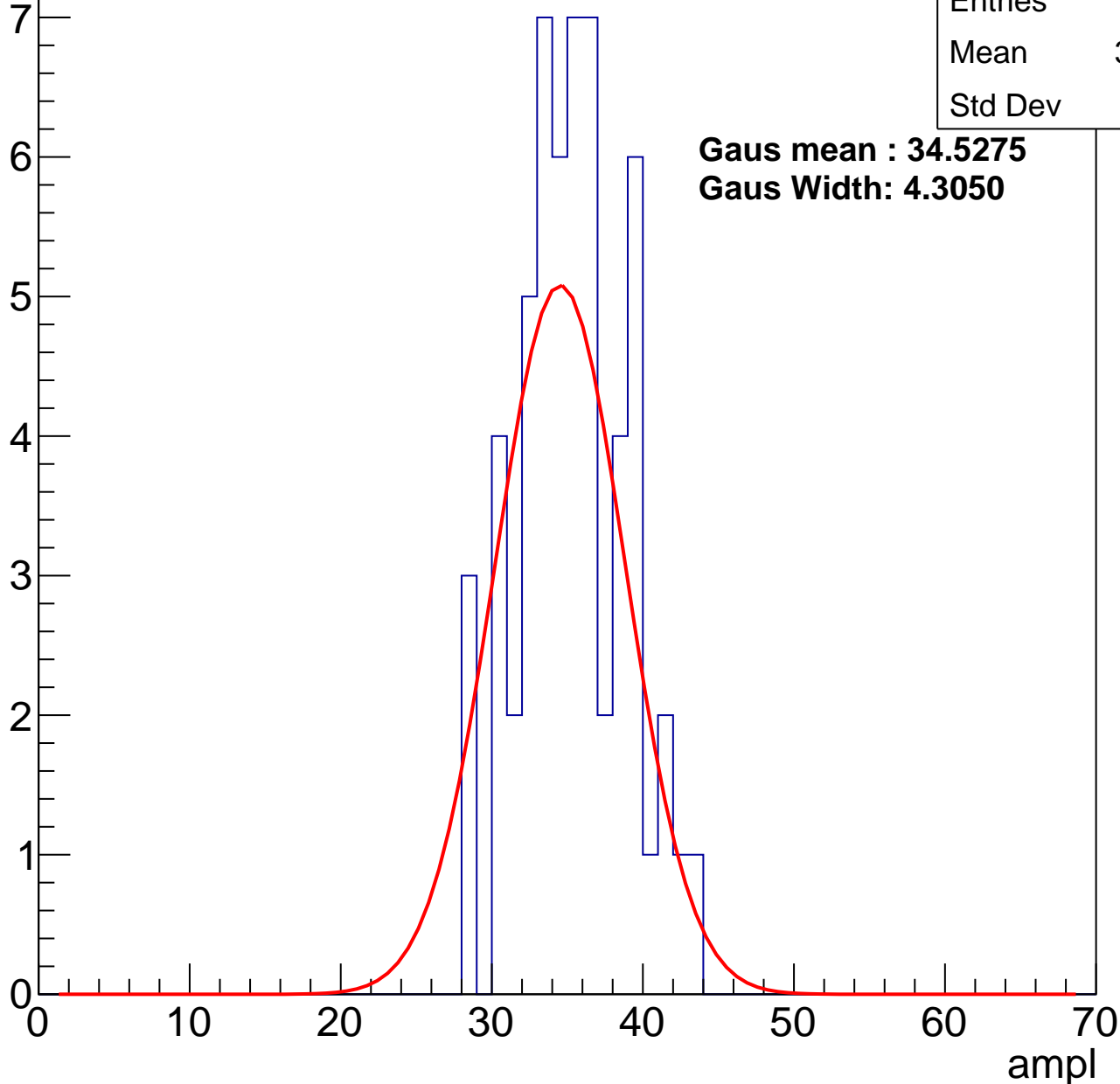
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	34.91
Std Dev	3.51

Gaus mean : 34.5275

Gaus Width: 4.3050



B1L100S, U1-ch14, adc2

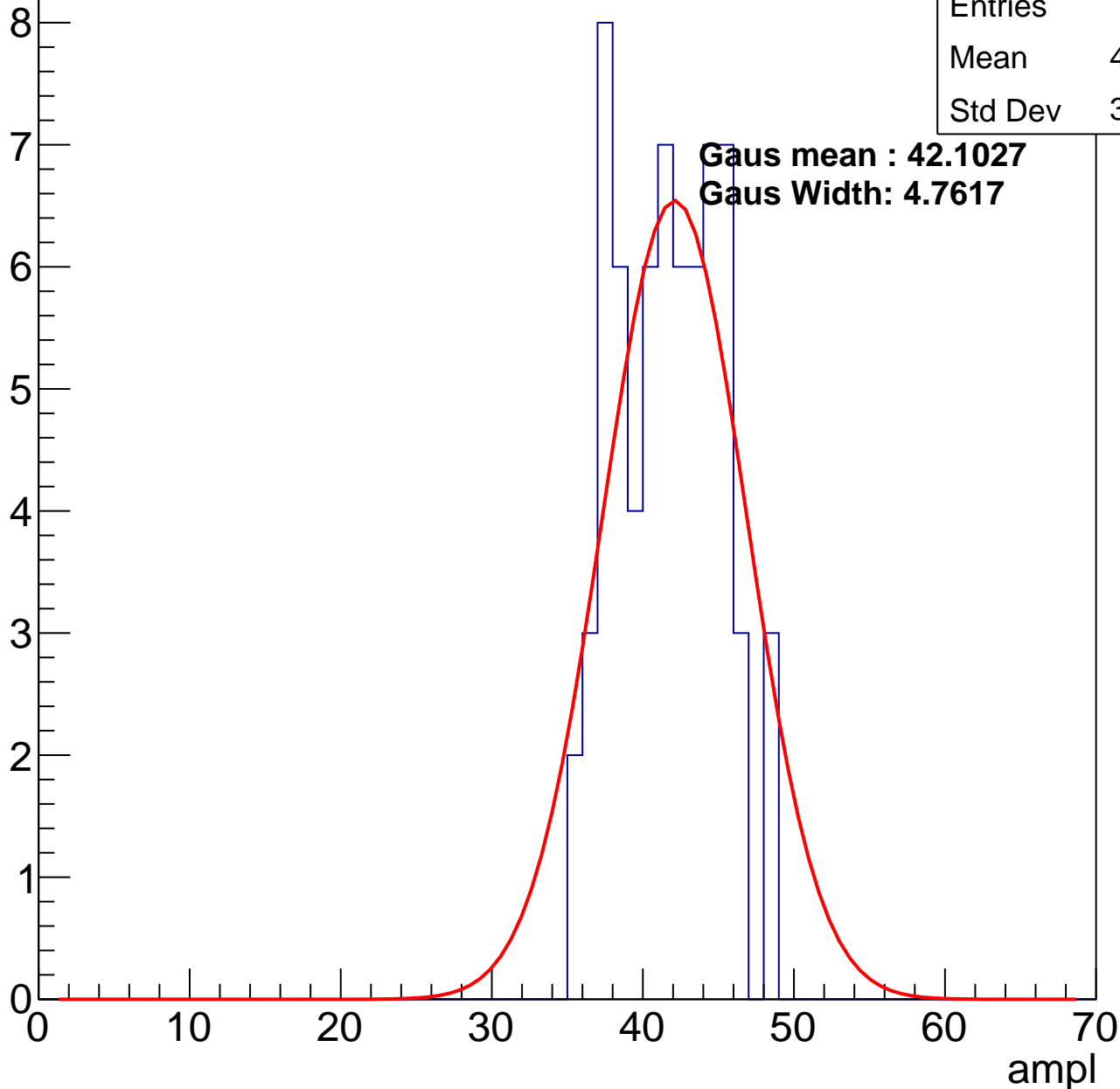
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	41.18
Std Dev	3.378

Gaus mean : 42.1027

Gaus Width: 4.7617

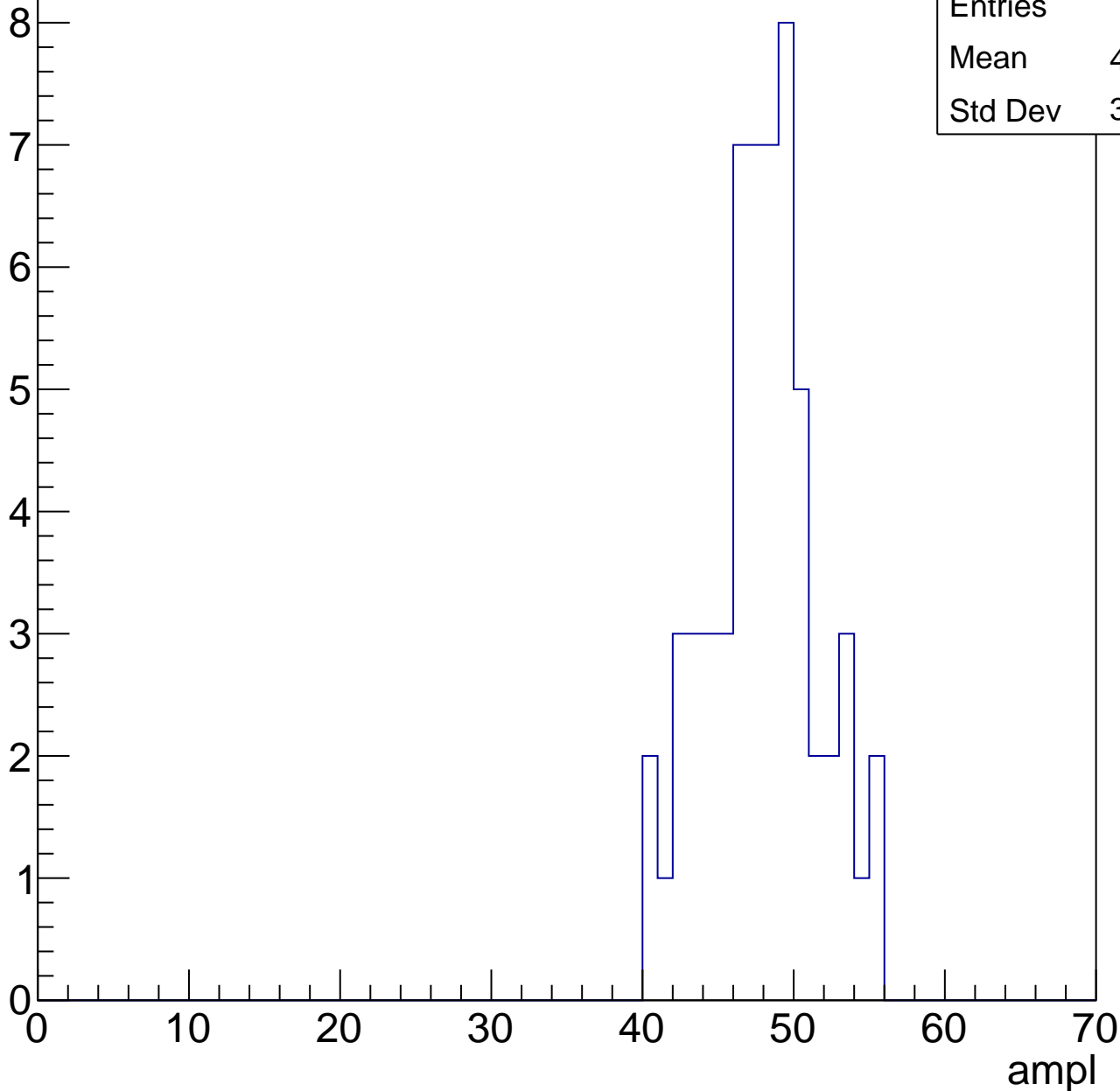


B1L100S, U1-ch14, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	47.47
Std Dev	3.543

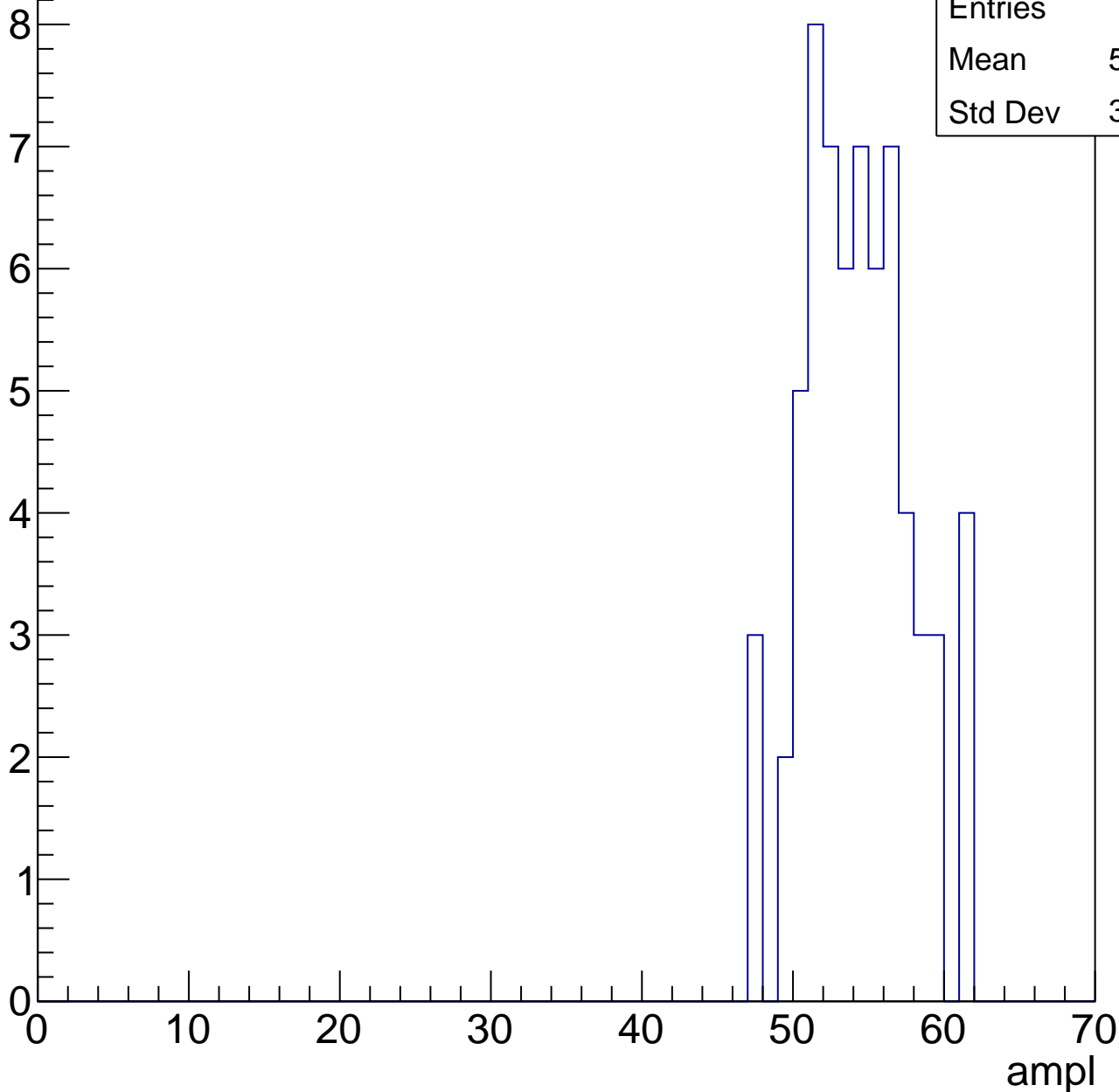


B1L100S, U1-ch14, adc4

calib_packv5_042523_0143.root, FC#4, port A2

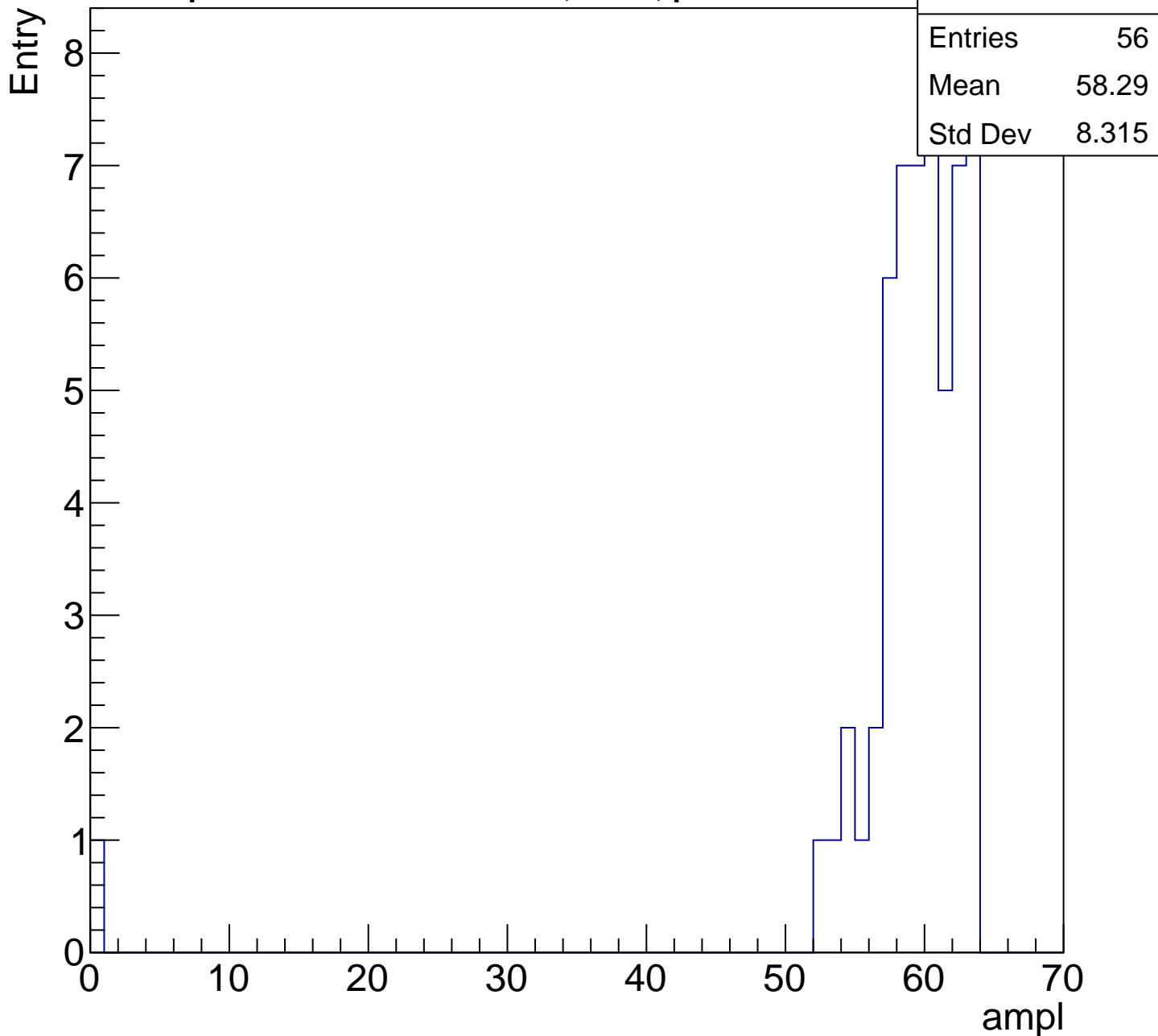
Entry

Entries	65
Mean	53.88
Std Dev	3.444



B1L100S, U1-ch14, adc5

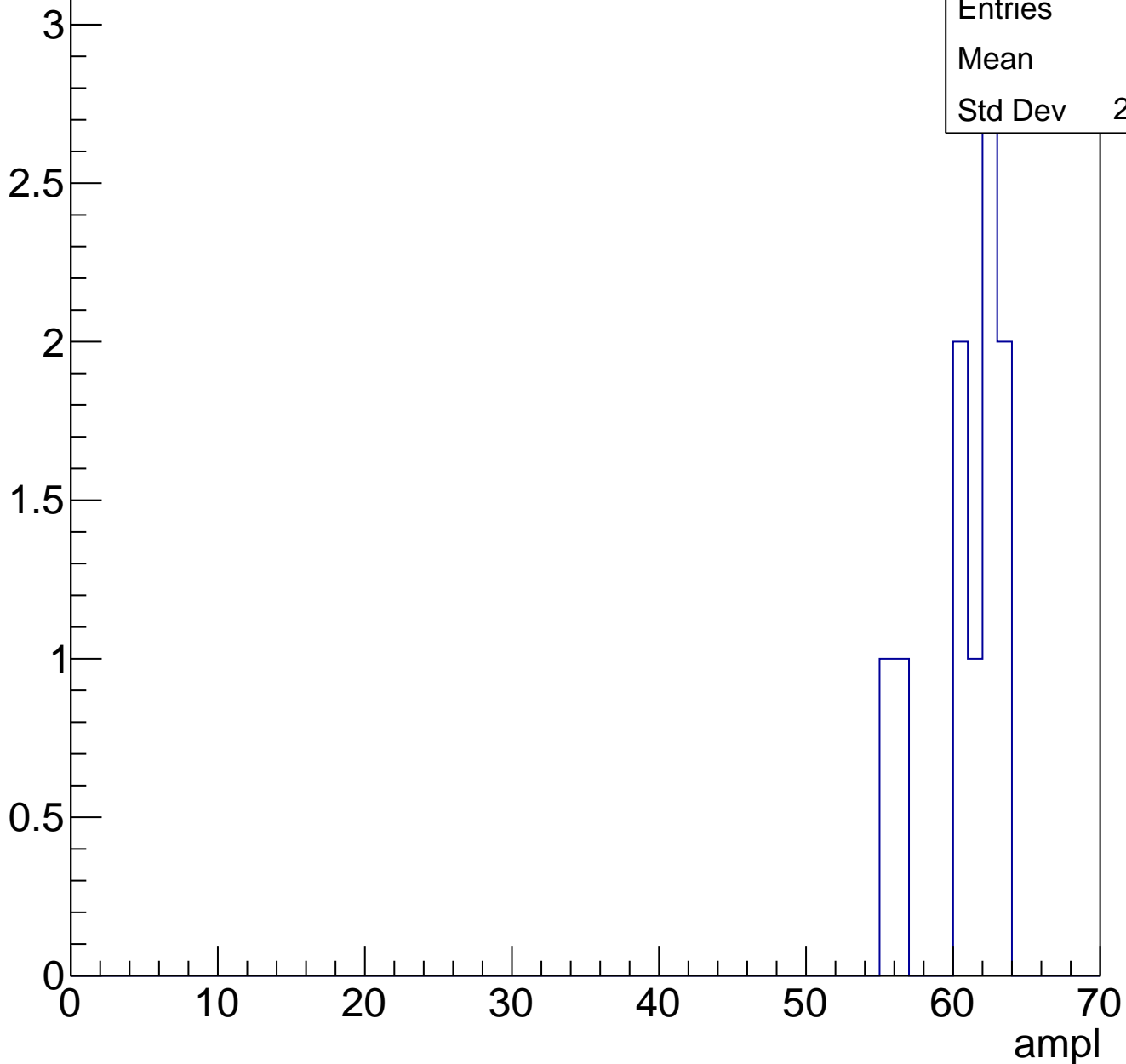
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch14, adc6

calib_packv5_042523_0143.root, FC#4, port A2

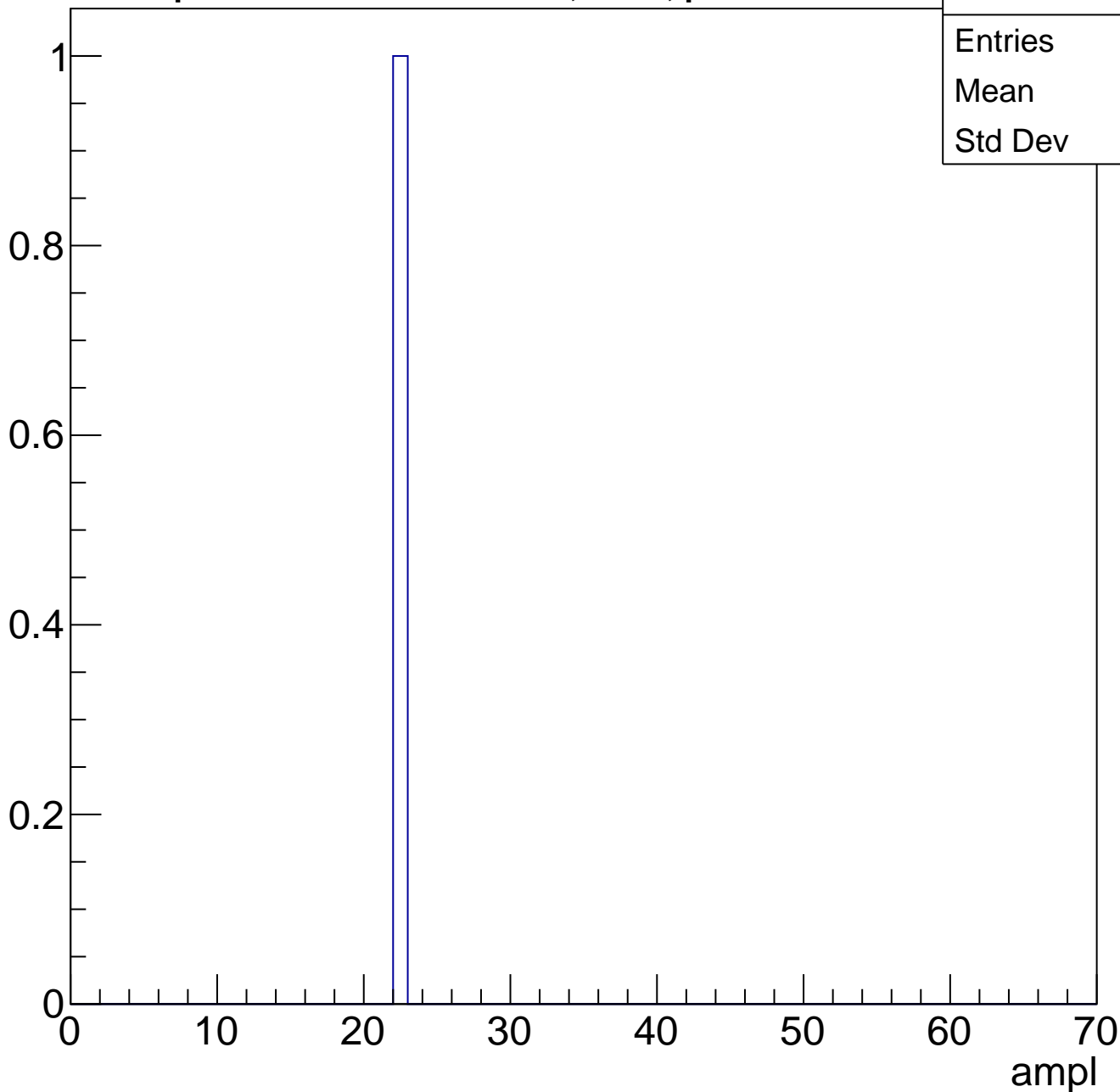
Entry



B1L100S, U1-ch14, adc7

calib_packv5_042523_0143.root, FC#4, port A2

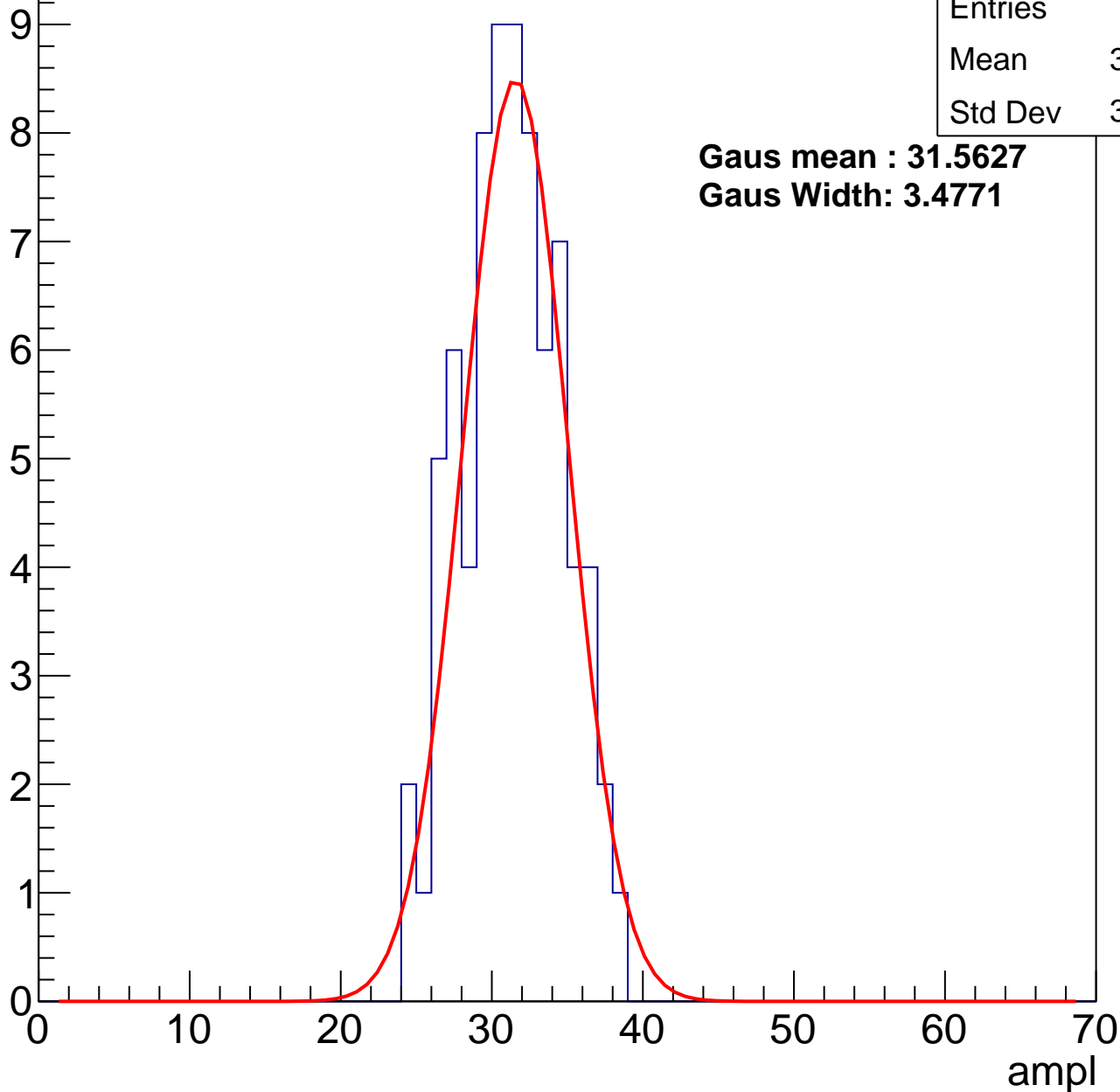
Entry



B1L100S, U1-ch15, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch15, adc1

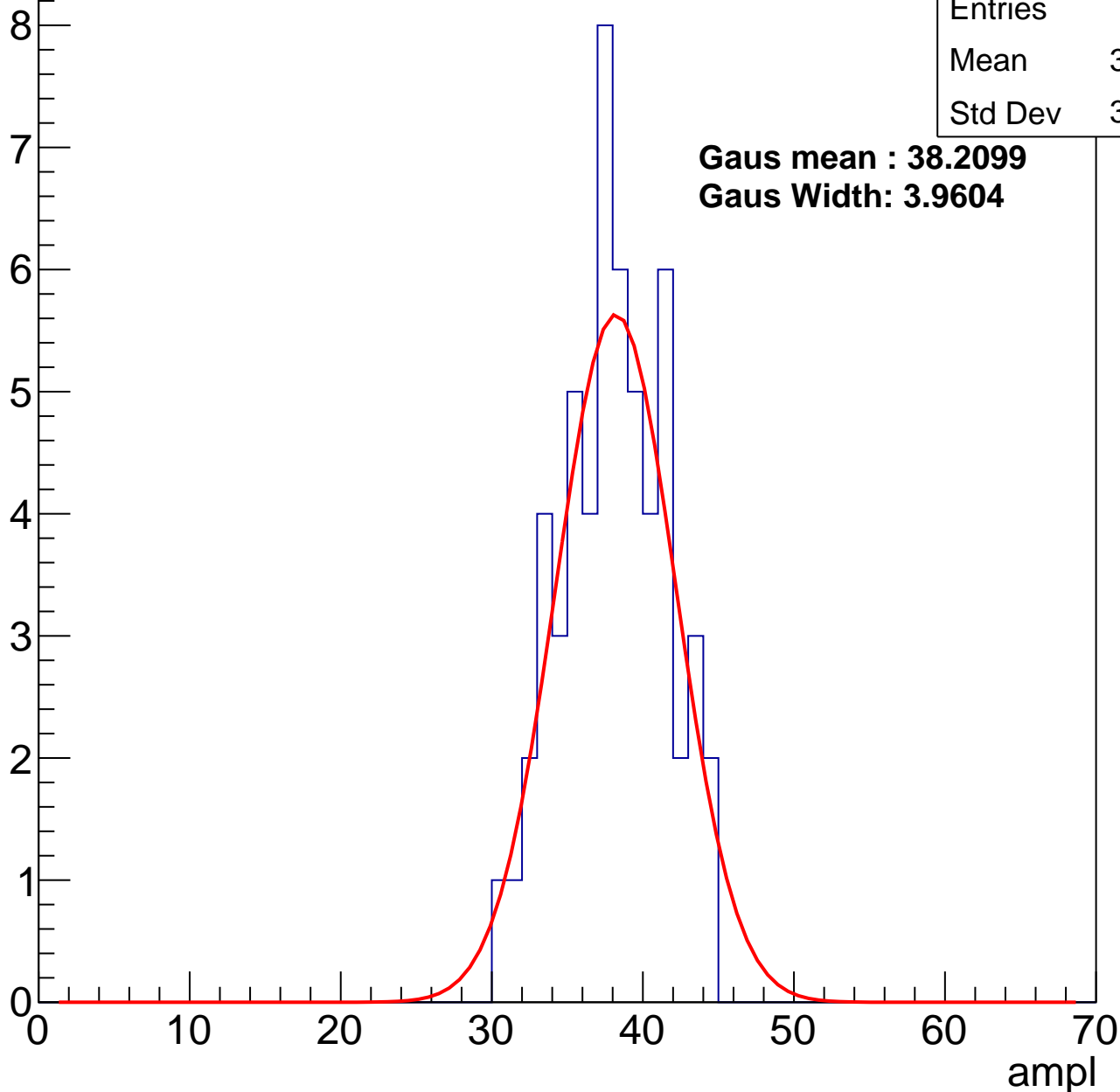
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	37.57
Std Dev	3.396

Gaus mean : 38.2099

Gaus Width: 3.9604



B1L100S, U1-ch15, adc2

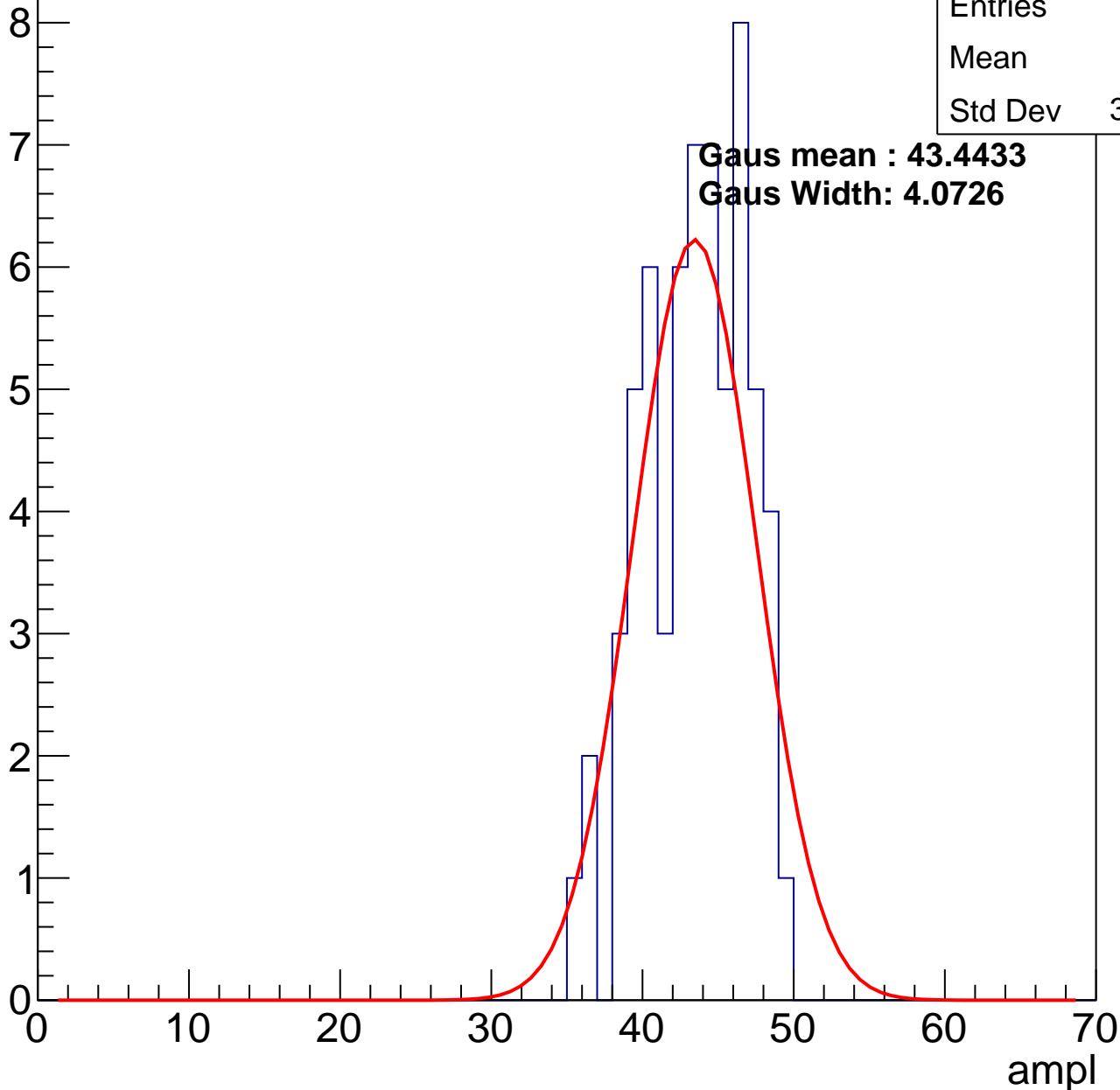
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	43
Std Dev	3.343

Gaus mean : 43.4433

Gaus Width: 4.0726

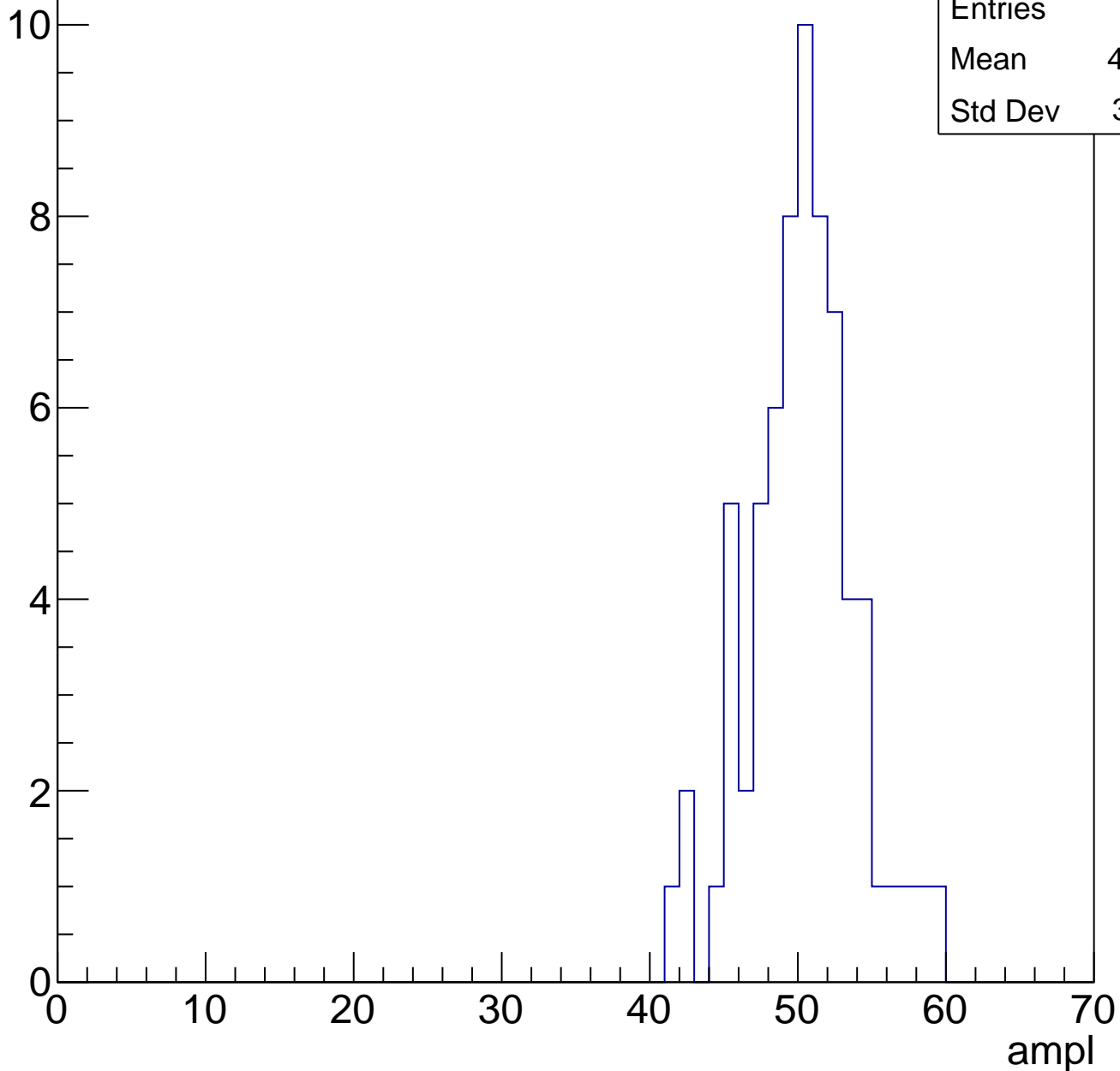


B1L100S, U1-ch15, adc3

calib_packv5_042523_0143.root, FC#4, port A2

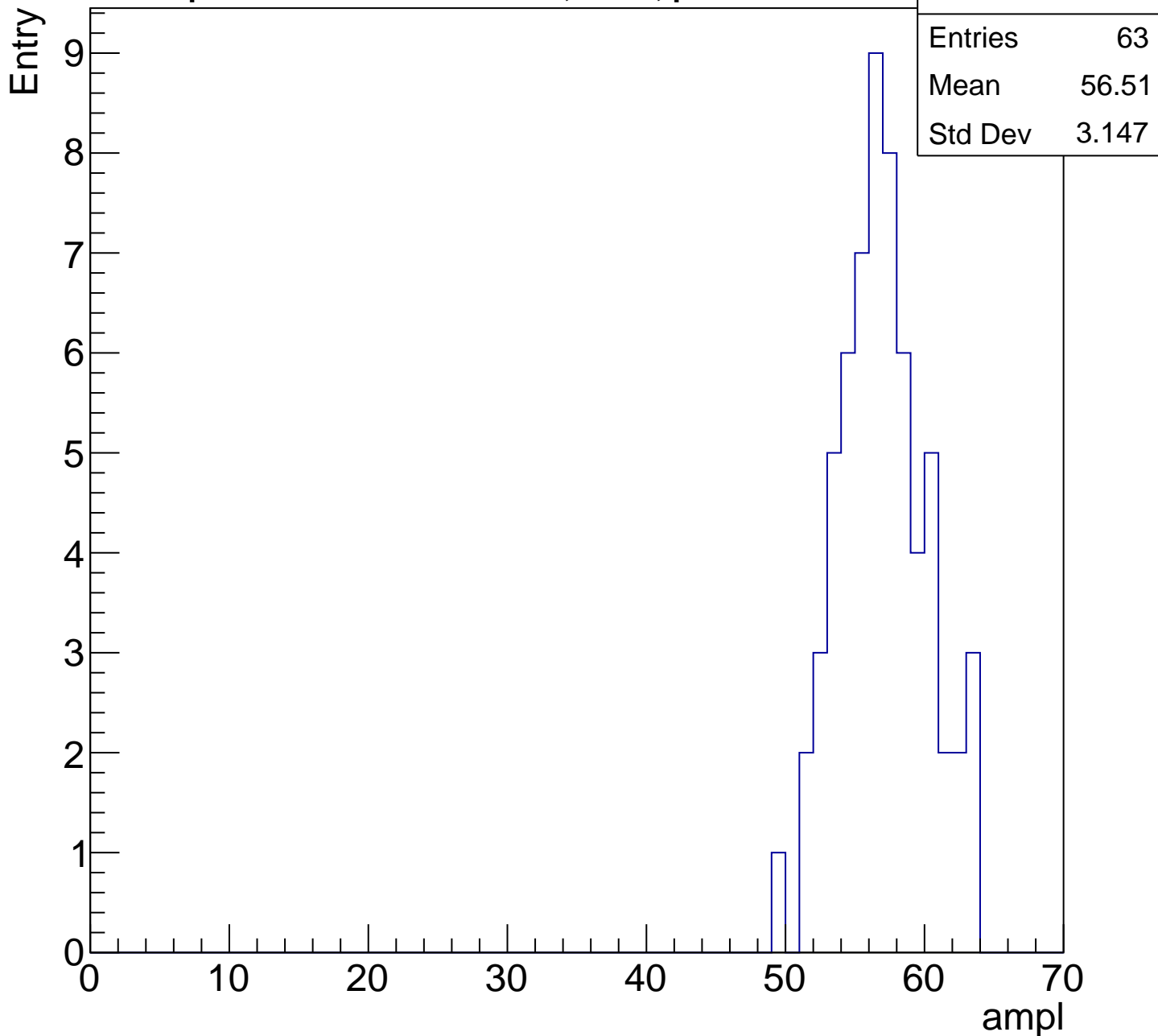
Entries	68
Mean	49.79
Std Dev	3.571

Entry



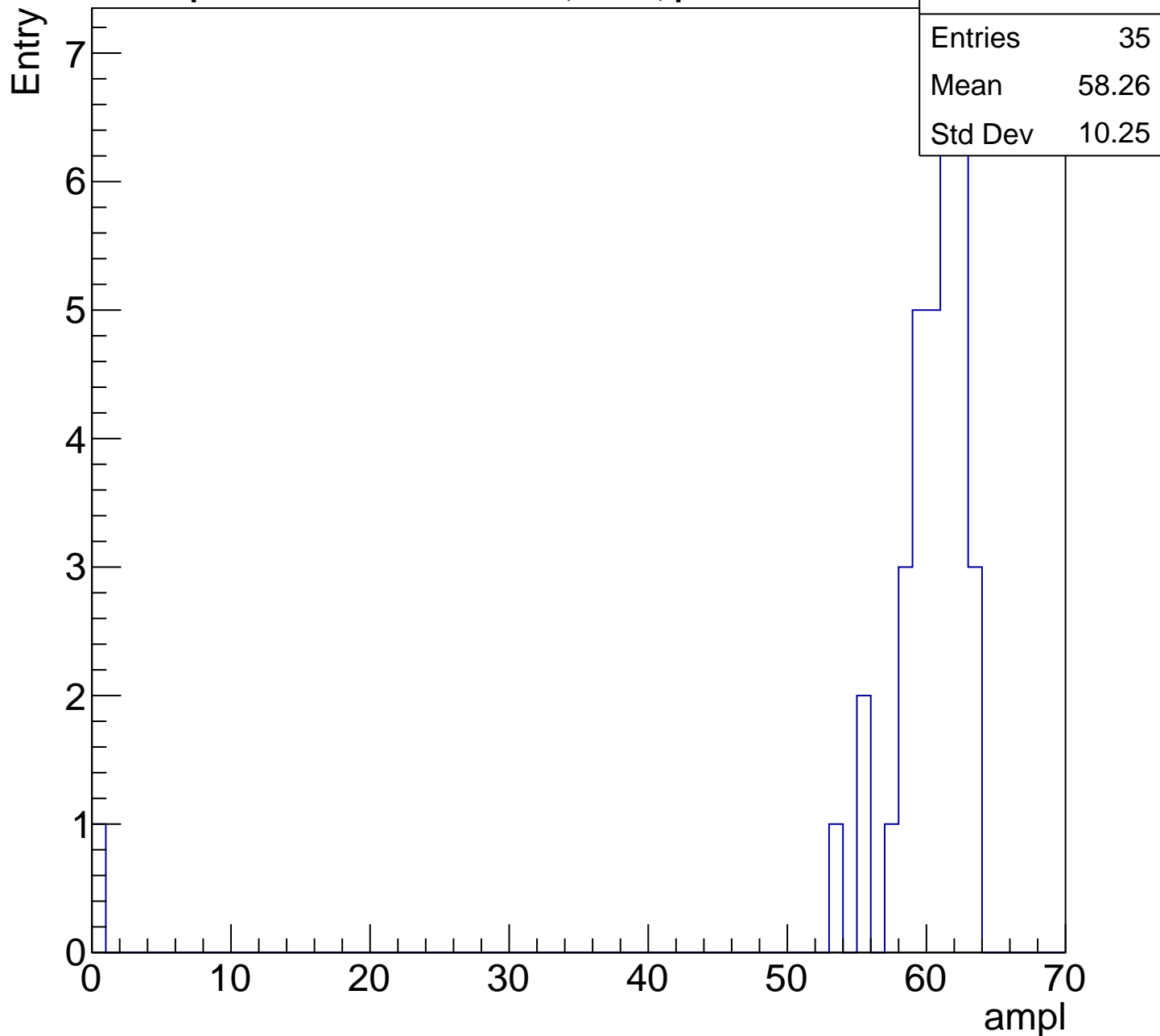
B1L100S, U1-ch15, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch15, adc5

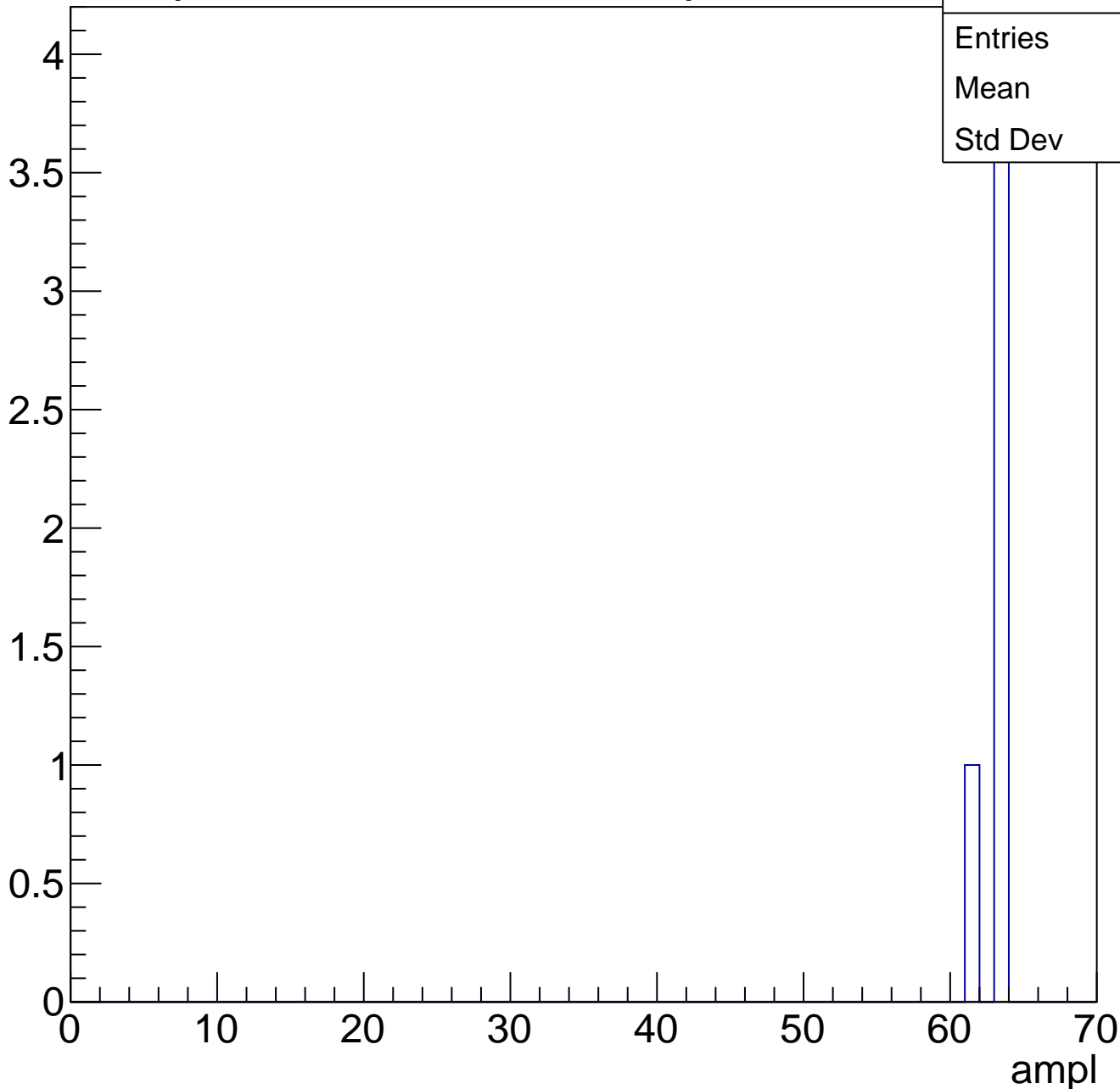
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch15, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch15, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	1
Mean	62
Std Dev	0

ampl

B1L100S, U1-ch16, adc0

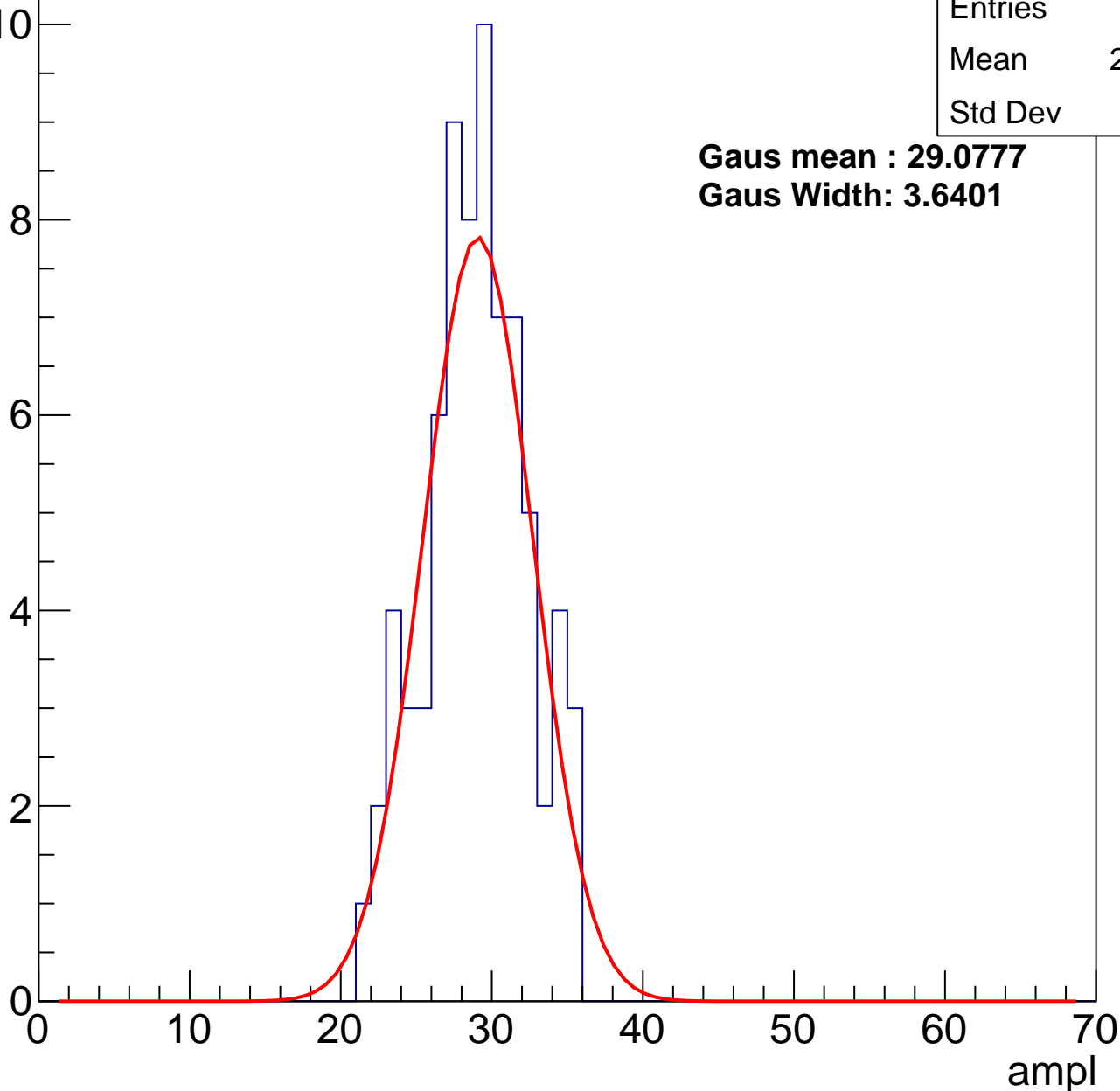
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.53
Std Dev	3.35

Gaus mean : 29.0777

Gaus Width: 3.6401



B1L100S, U1-ch16, adc1

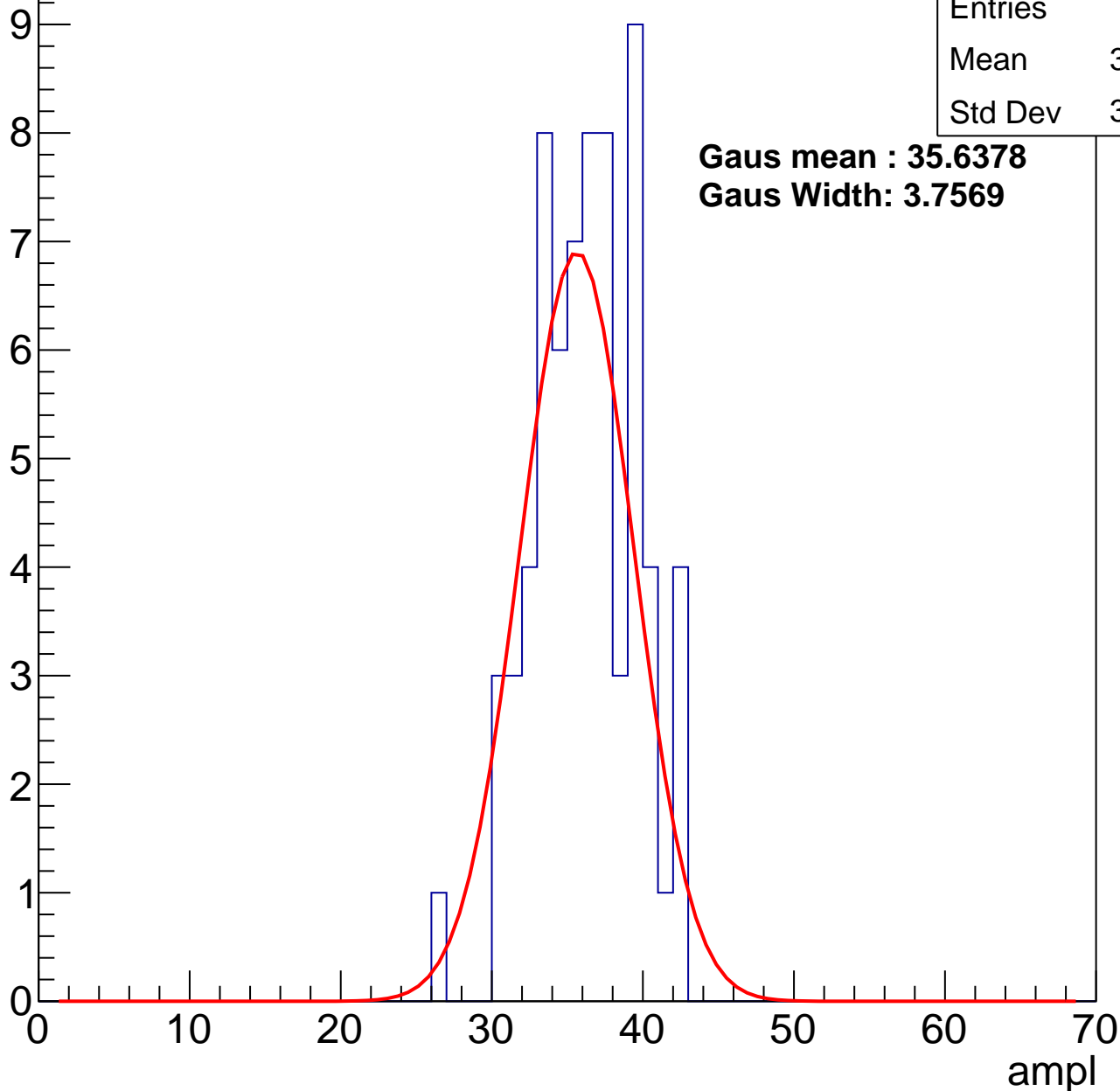
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.77
Std Dev	3.363

Gaus mean : 35.6378

Gaus Width: 3.7569



B1L100S, U1-ch16, adc2

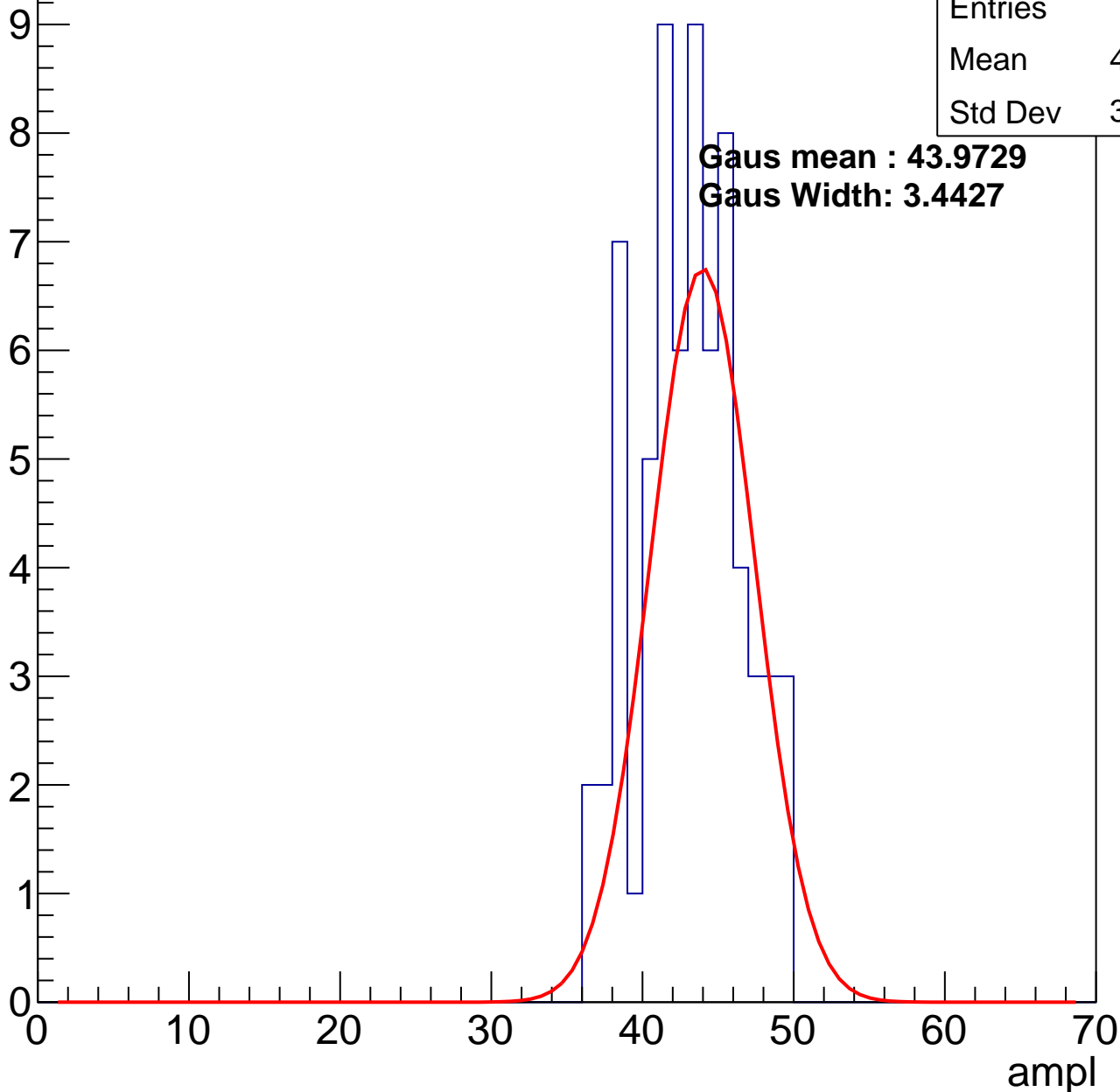
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	42.63
Std Dev	3.307

Gaus mean : 43.9729

Gaus Width: 3.4427

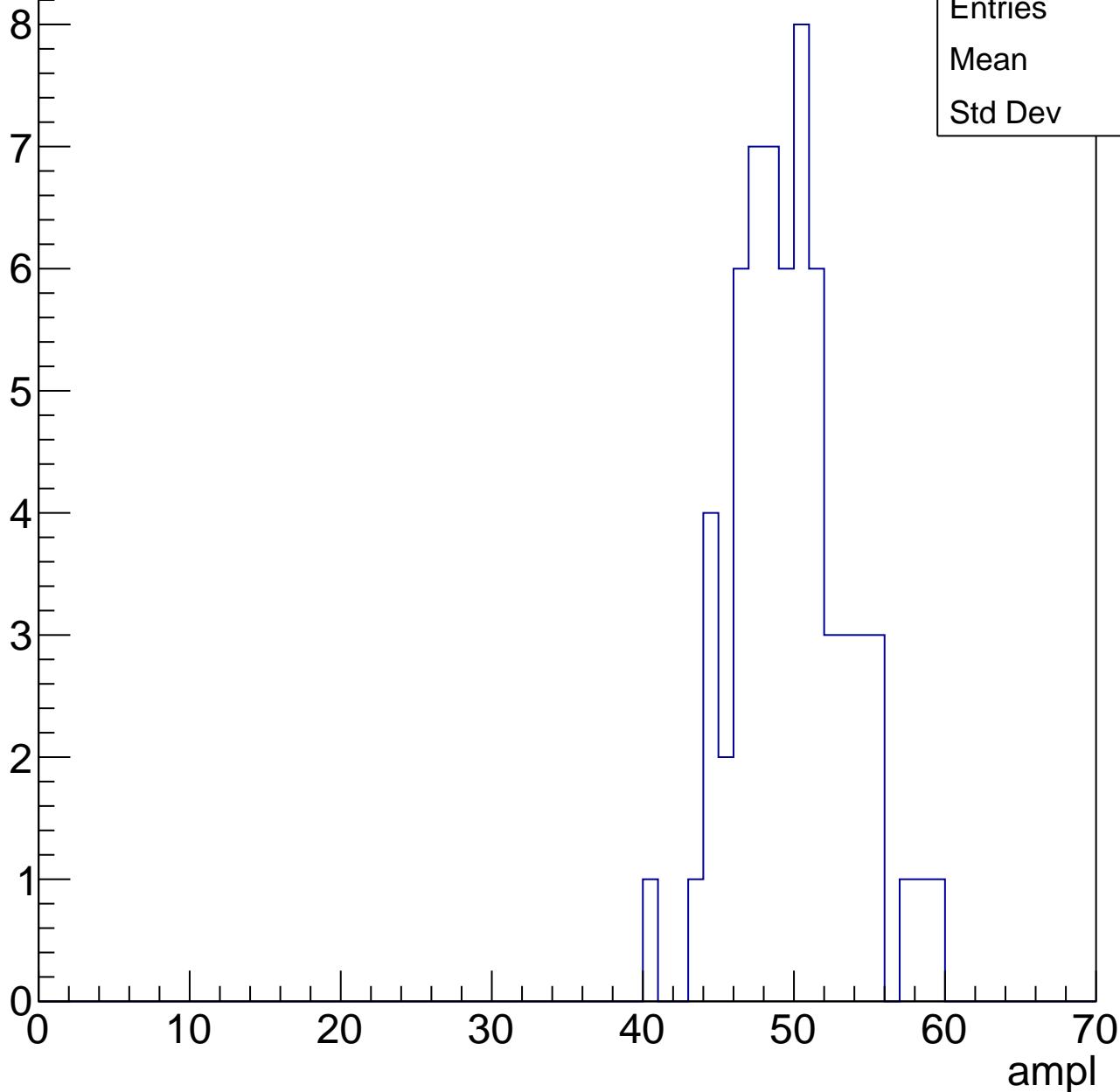


B1L100S, U1-ch16, adc3

calib_packv5_042523_0143.root, FC#4, port A2

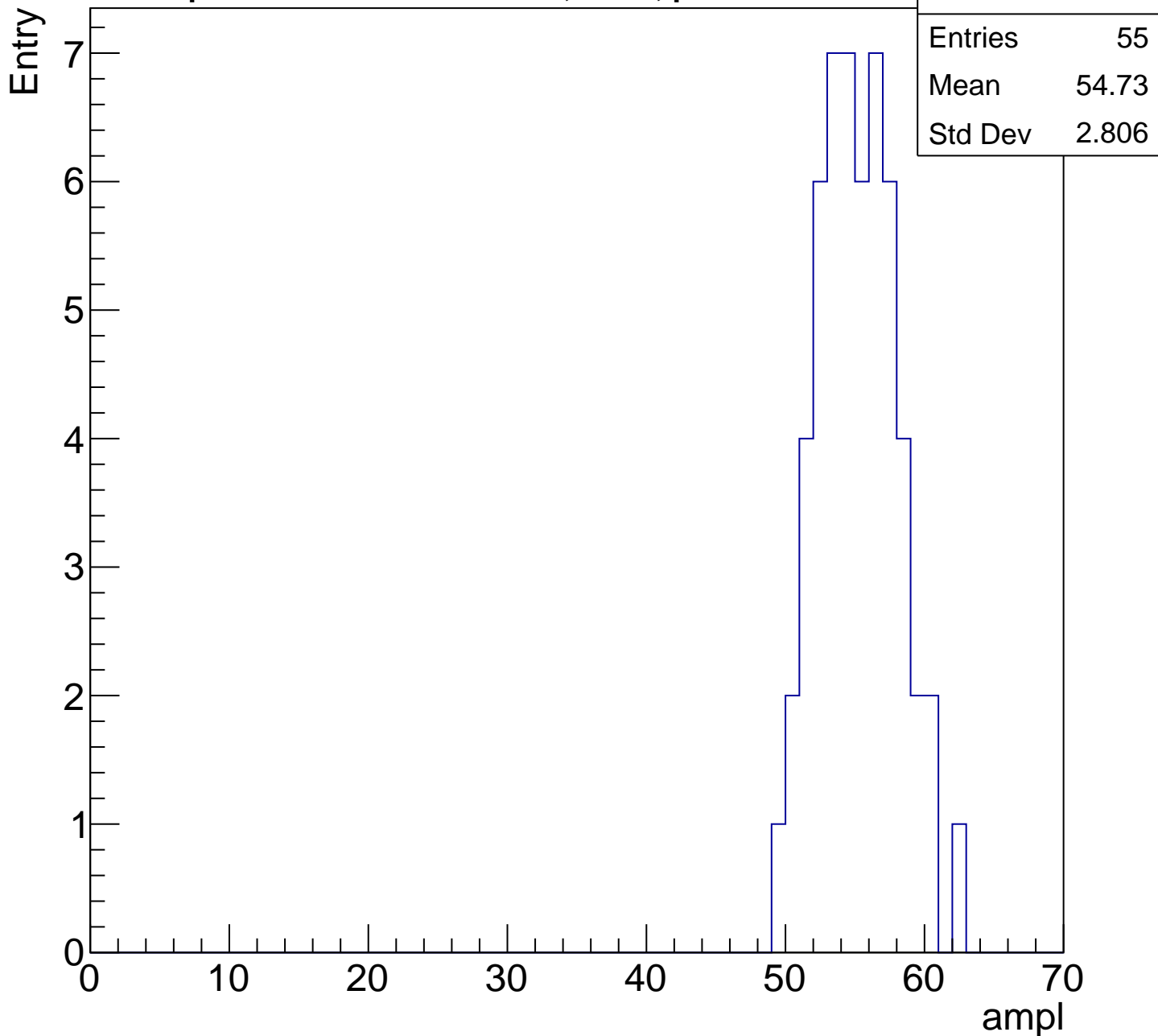
Entry

Entries	63
Mean	49.3
Std Dev	3.71



B1L100S, U1-ch16, adc4

calib_packv5_042523_0143.root, FC#4, port A2

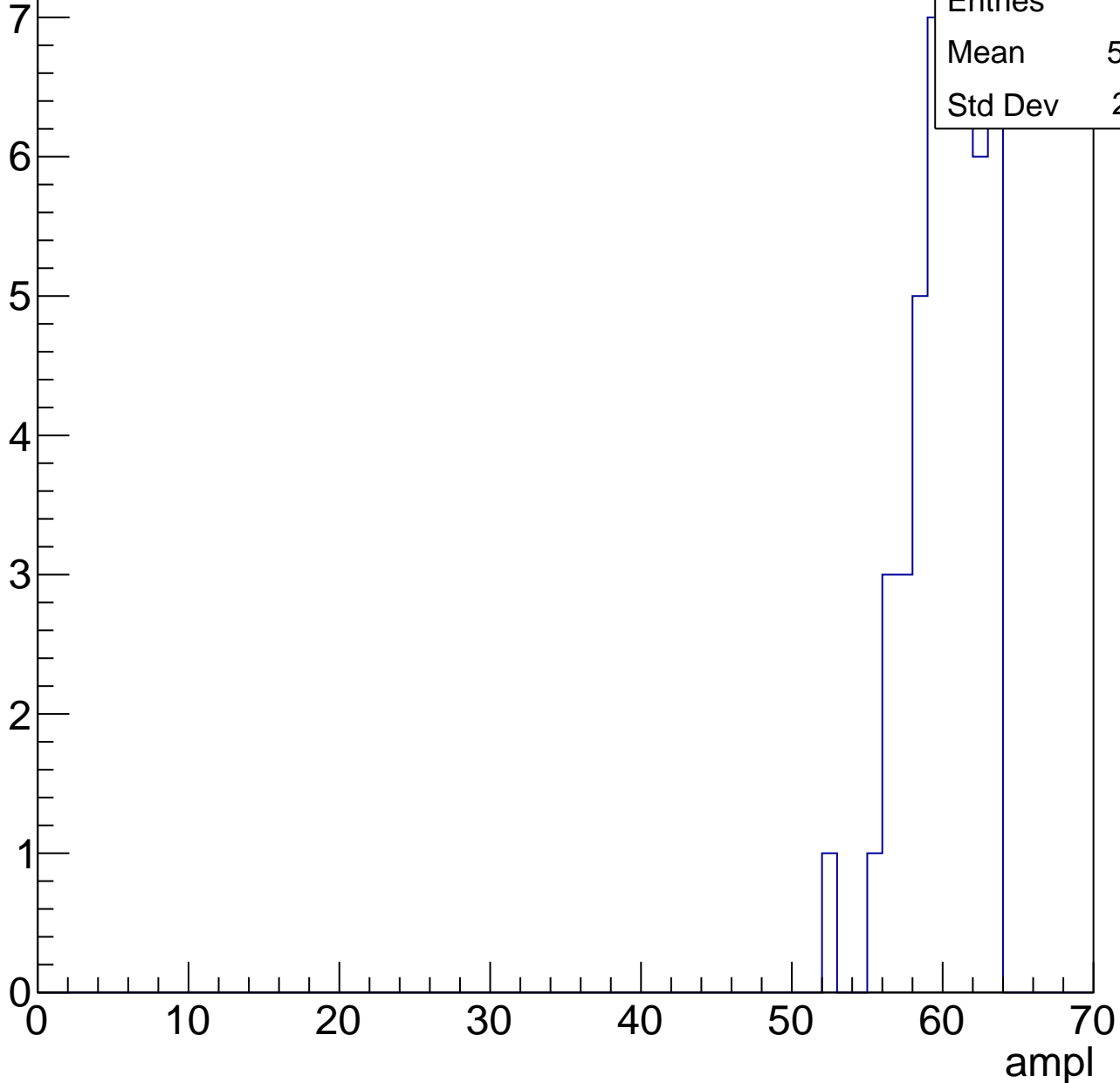


B1L100S, U1-ch16, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

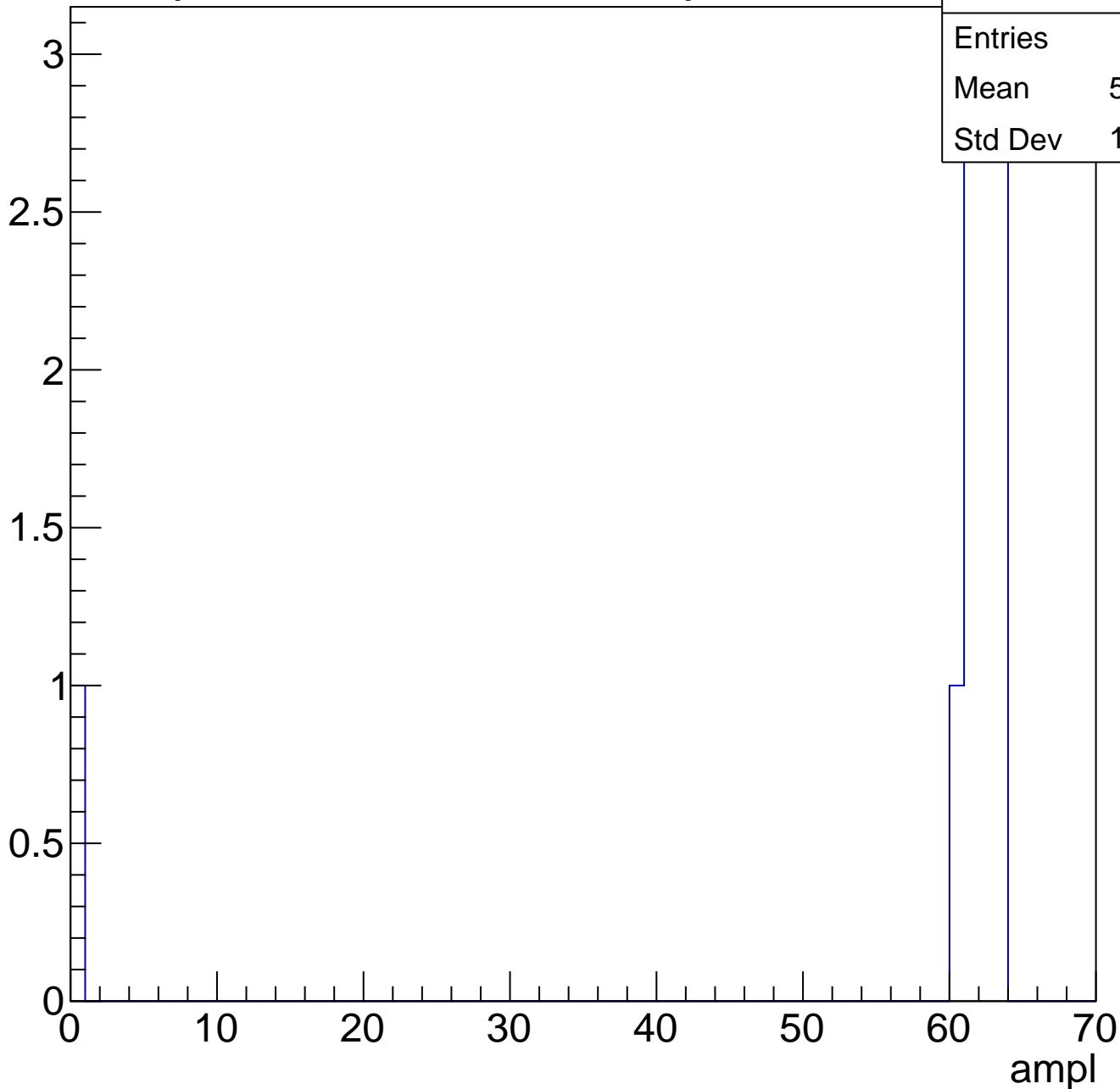
Entries	47
Mean	59.77
Std Dev	2.451



B1L100S, U1-ch16, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch16, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch17, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	63
Mean	28.03
Std Dev	3.122

Gaus mean : 28.6633

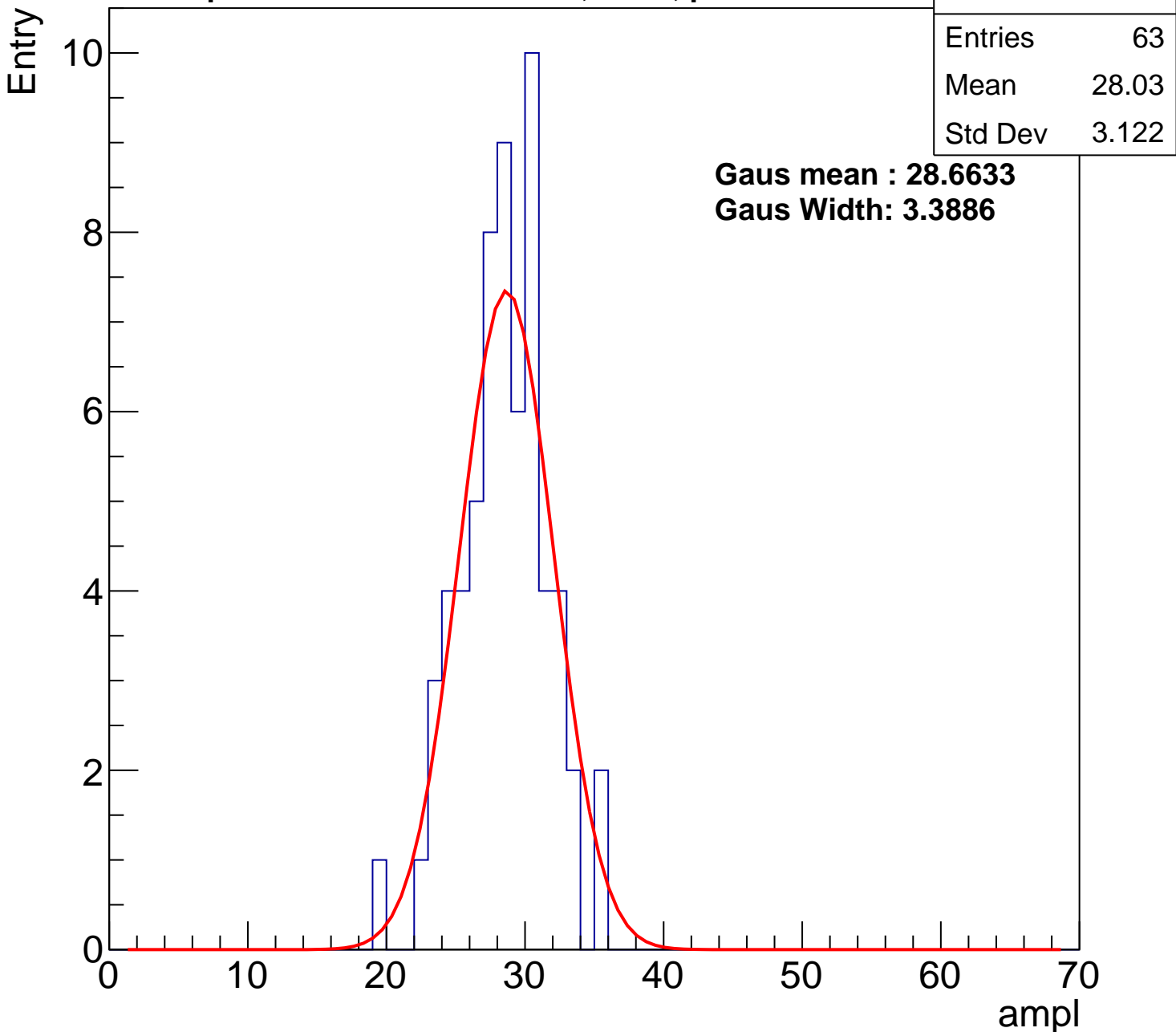
Gaus Width: 3.3886

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch17, adc1

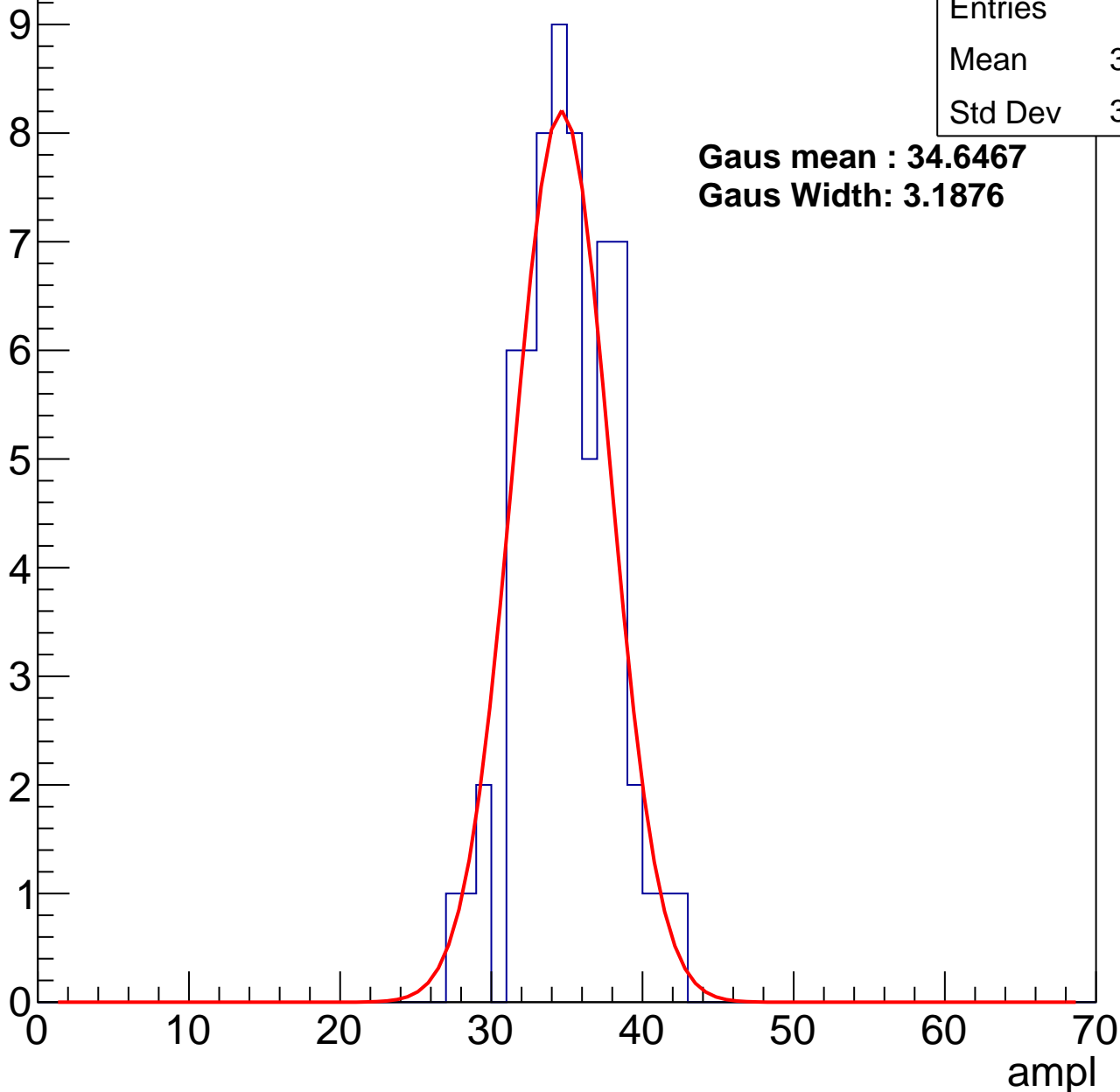
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	34.57
Std Dev	3.043

Gaus mean : 34.6467

Gaus Width: 3.1876



B1L100S, U1-ch17, adc2

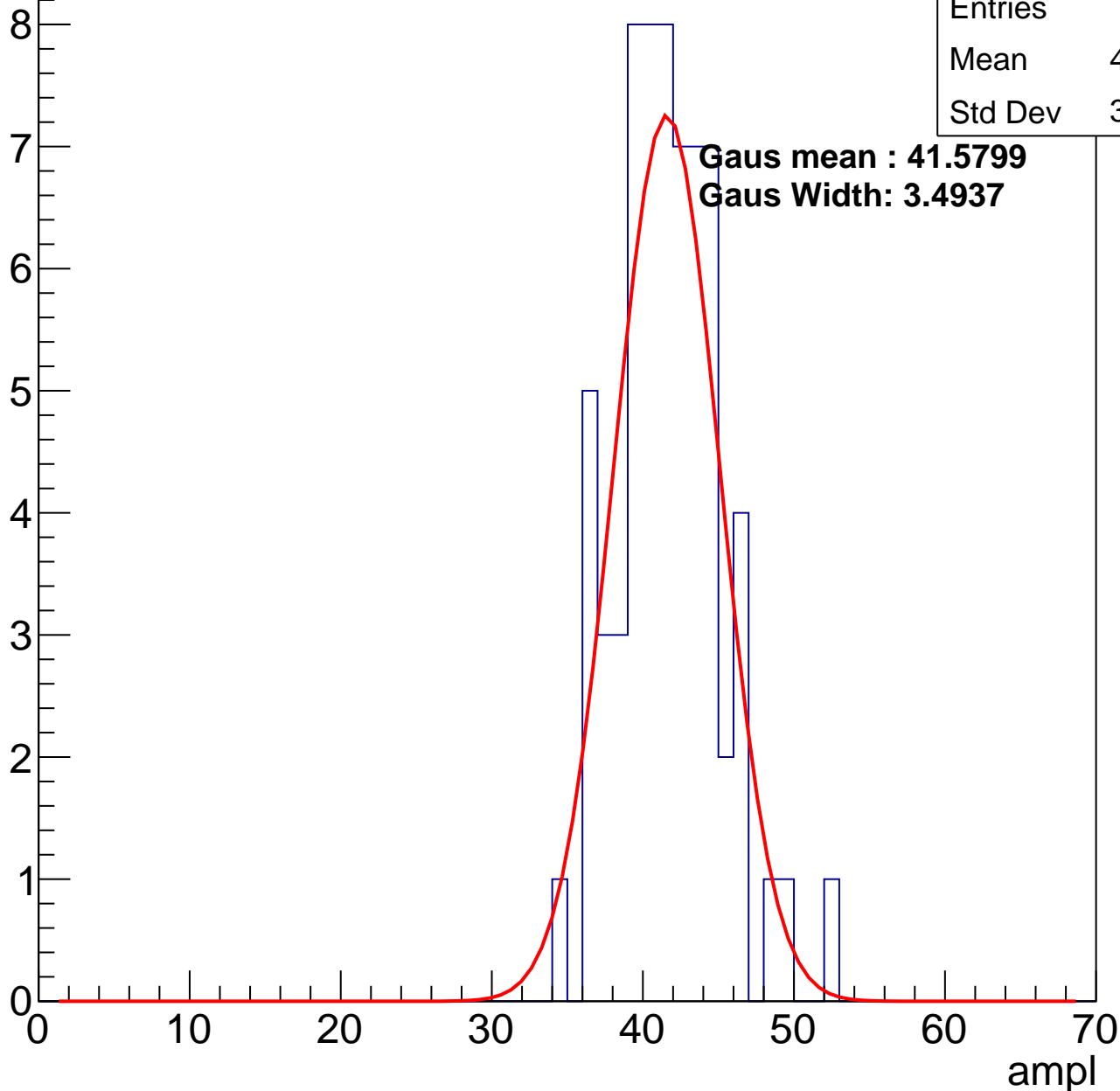
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.29
Std Dev	3.365

Gaus mean : 41.5799

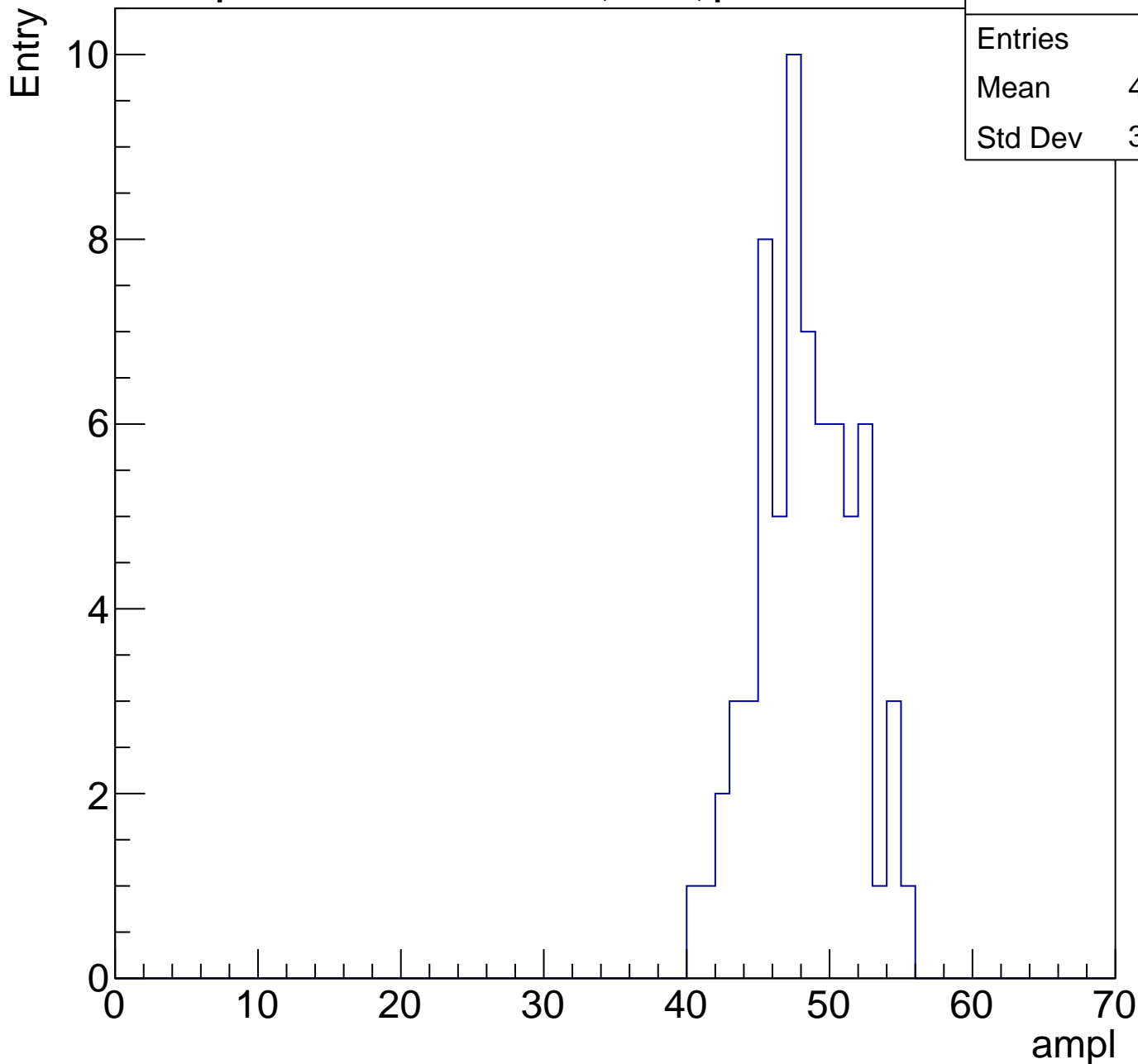
Gaus Width: 3.4937



B1L100S, U1-ch17, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	47.84
Std Dev	3.346

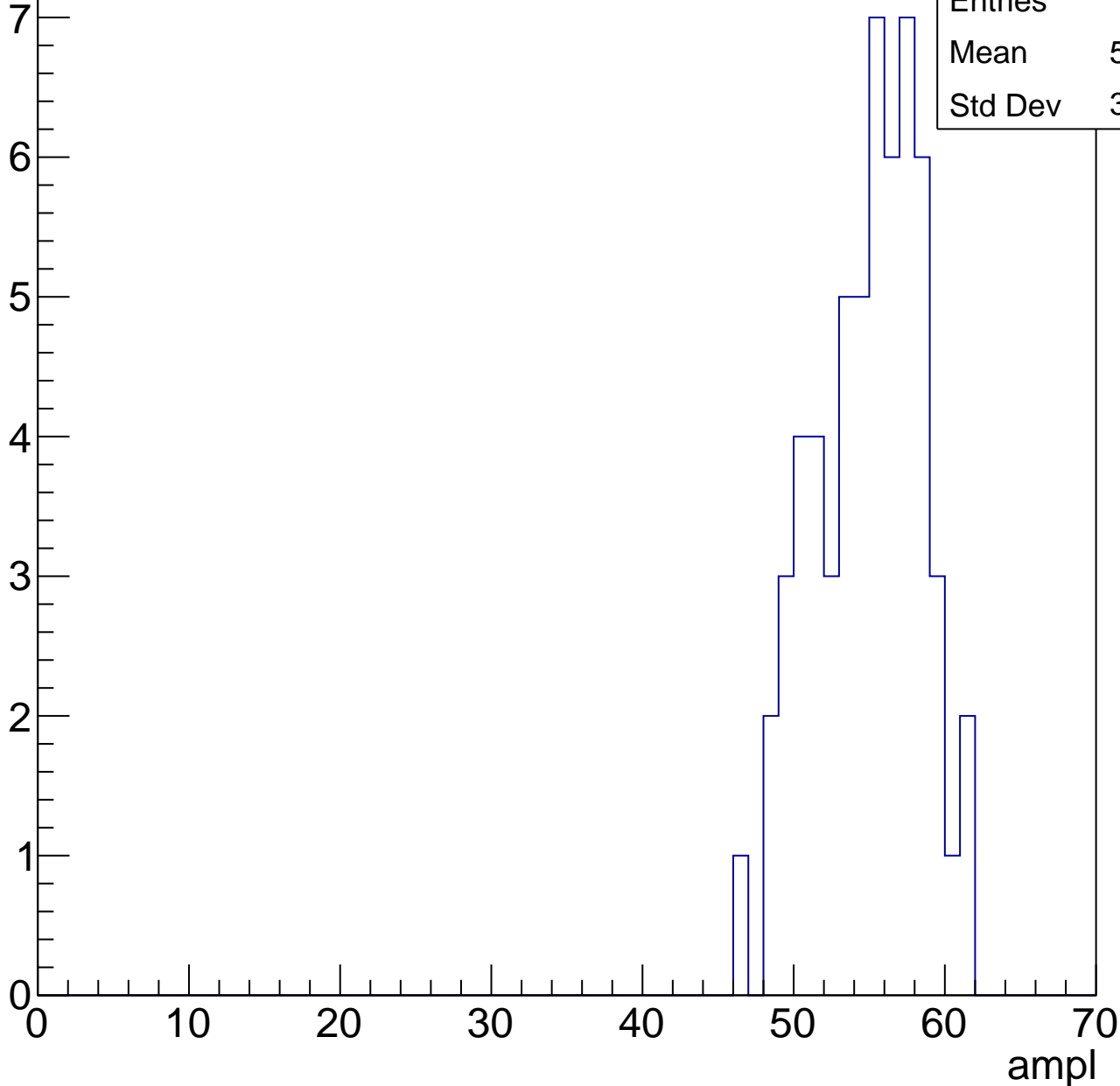


B1L100S, U1-ch17, adc4

calib_packv5_042523_0143.root, FC#4, port A2

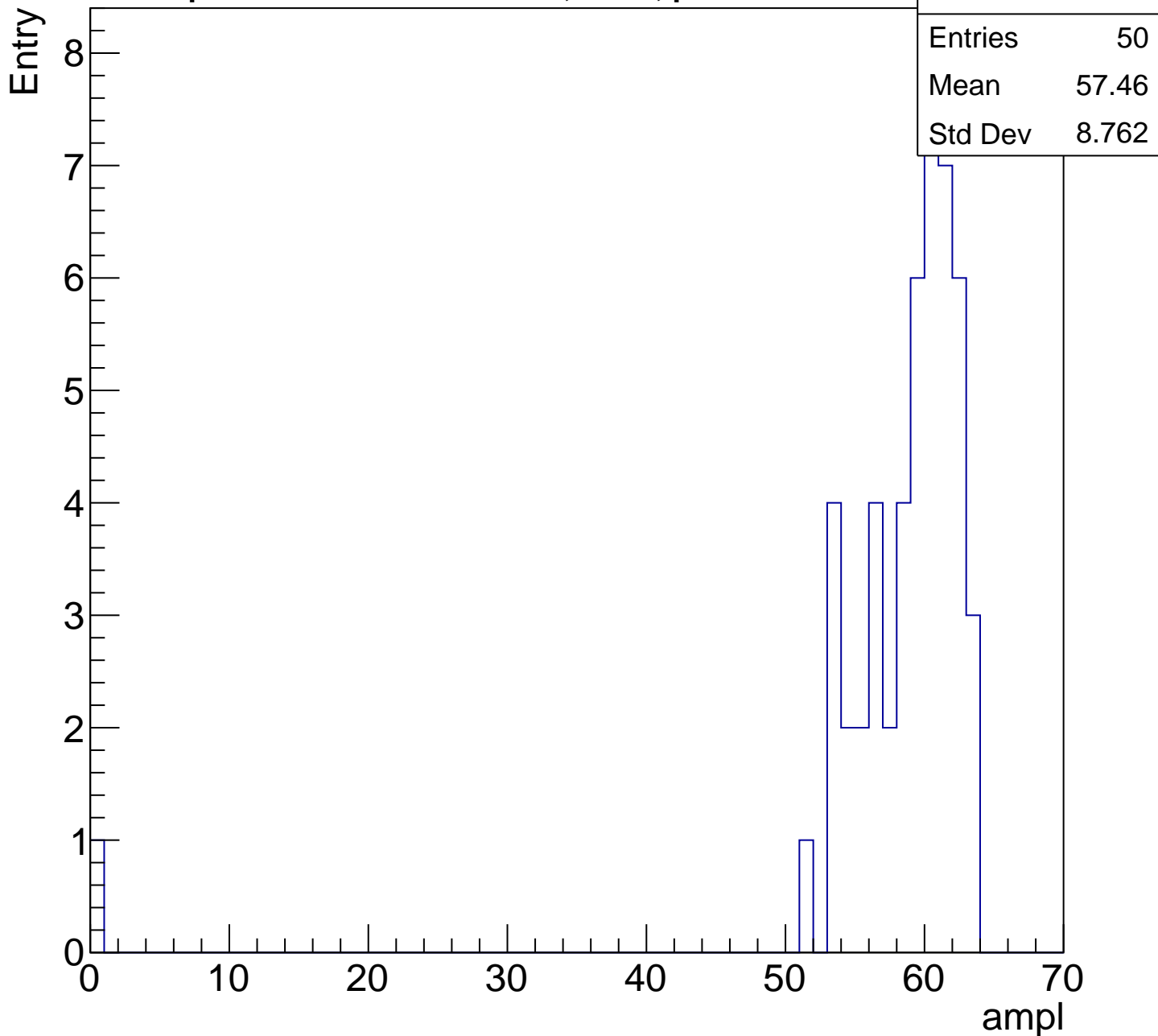
Entry

Entries	59
Mean	54.42
Std Dev	3.485



B1L100S, U1-ch17, adc5

calib_packv5_042523_0143.root, FC#4, port A2

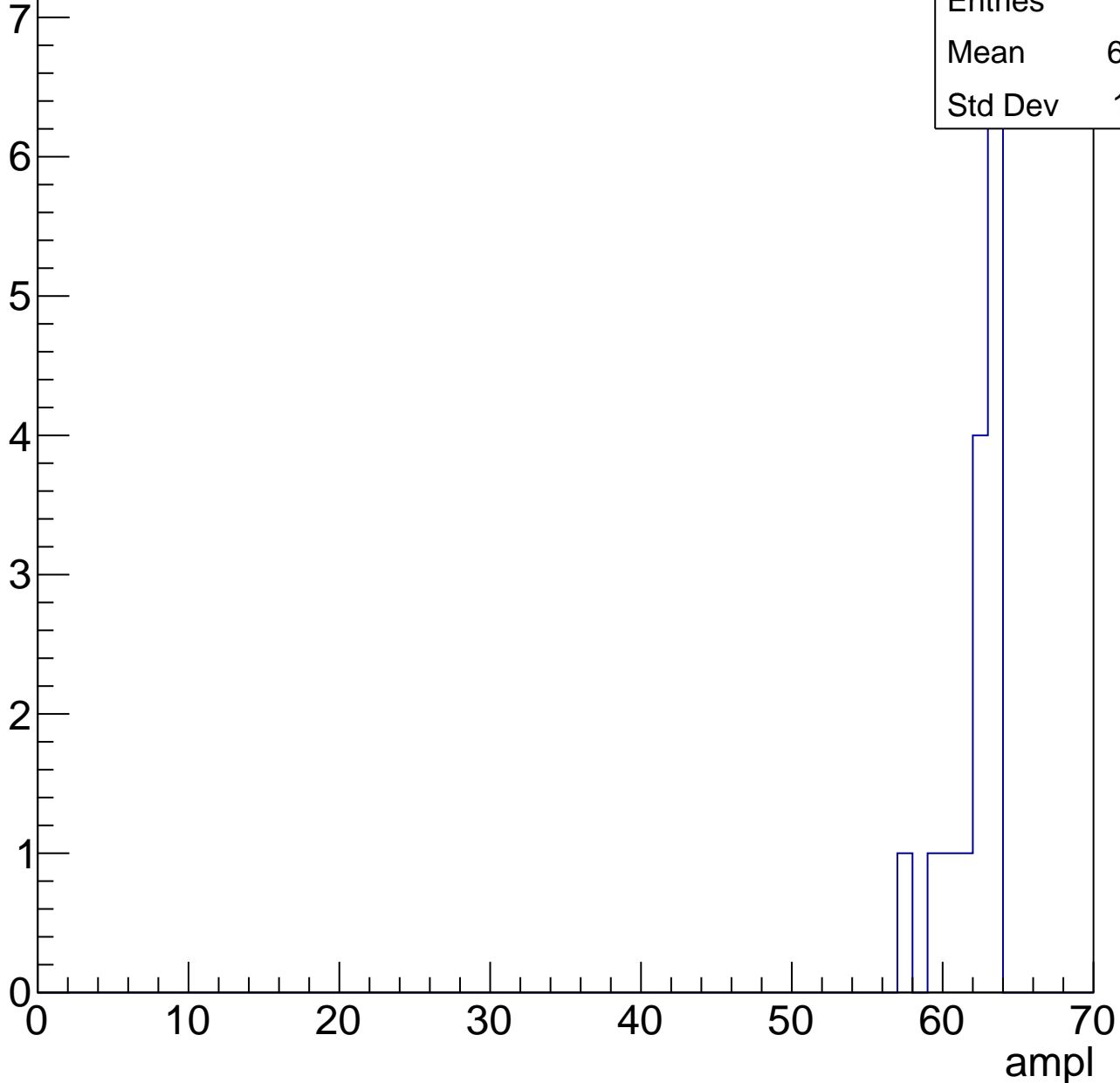


B1L100S, U1-ch17, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	15
Mean	61.73
Std Dev	1.731



B1L100S, U1-ch17, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch18, adc0

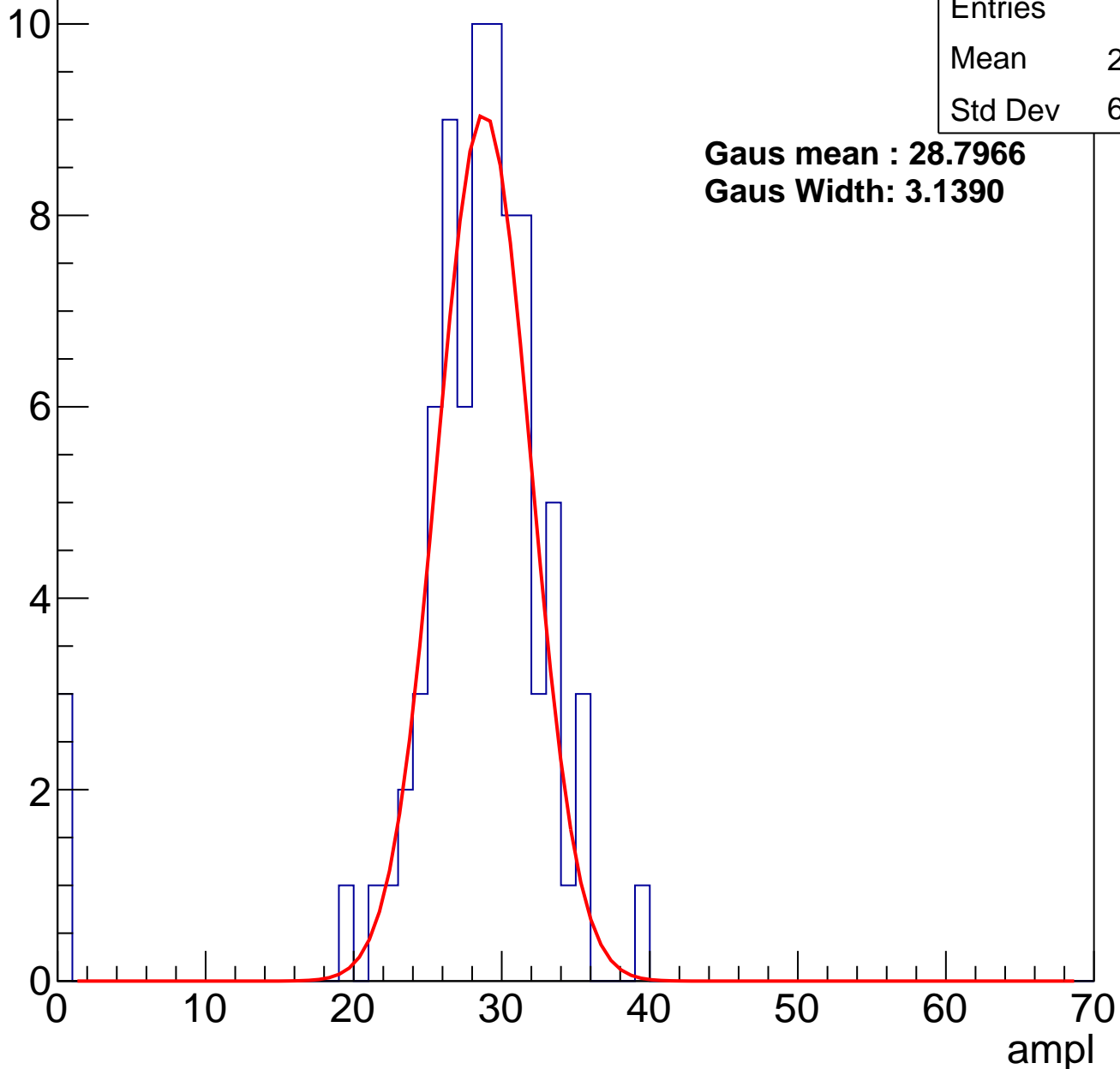
calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	27.44
Std Dev	6.362

Gaus mean : 28.7966

Gaus Width: 3.1390

Entry



B1L100S, U1-ch18, adc1

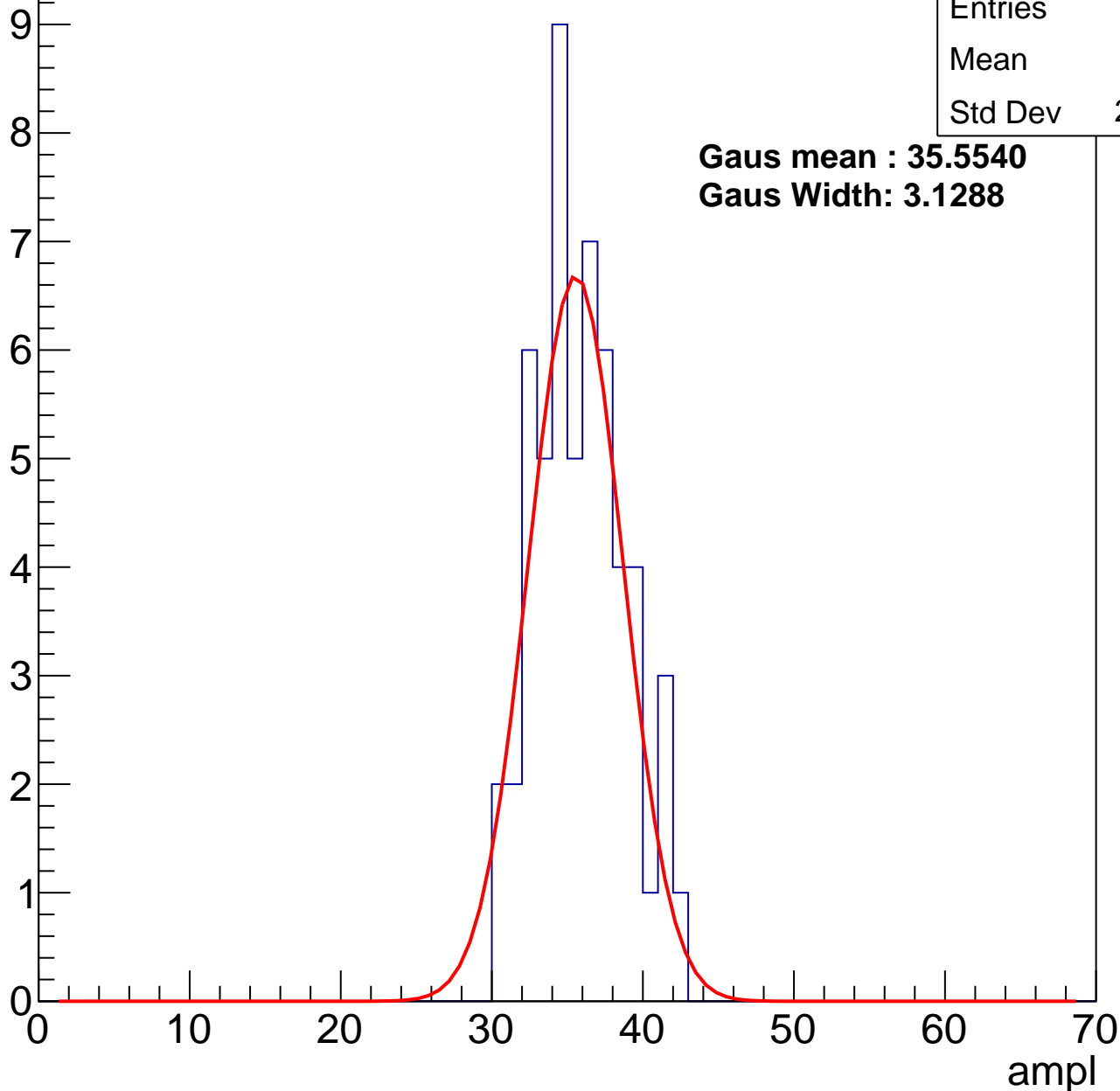
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.4
Std Dev	2.921

Gaus mean : 35.5540

Gaus Width: 3.1288



B1L100S, U1-ch18, adc2

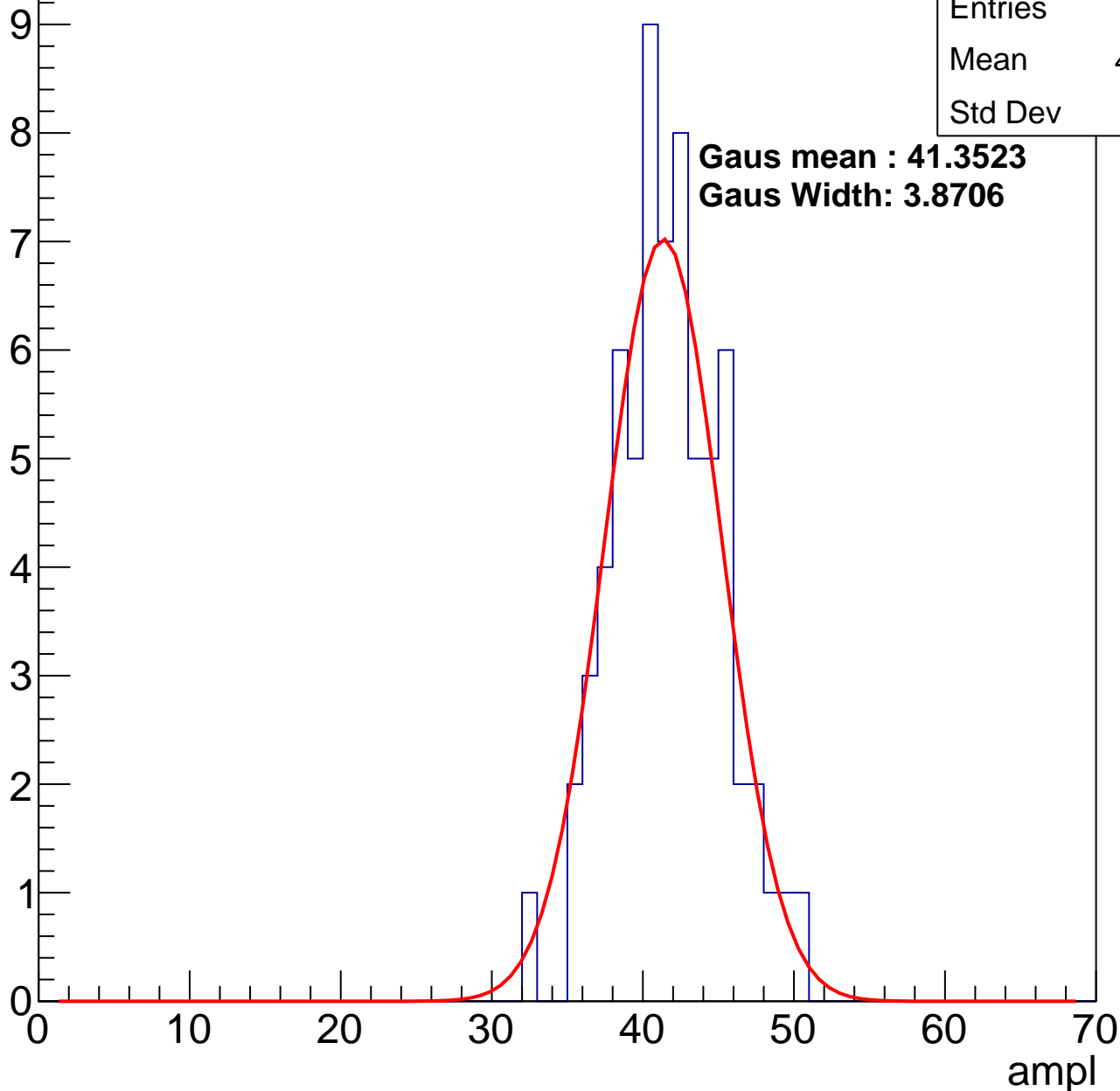
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	41.21
Std Dev	3.55

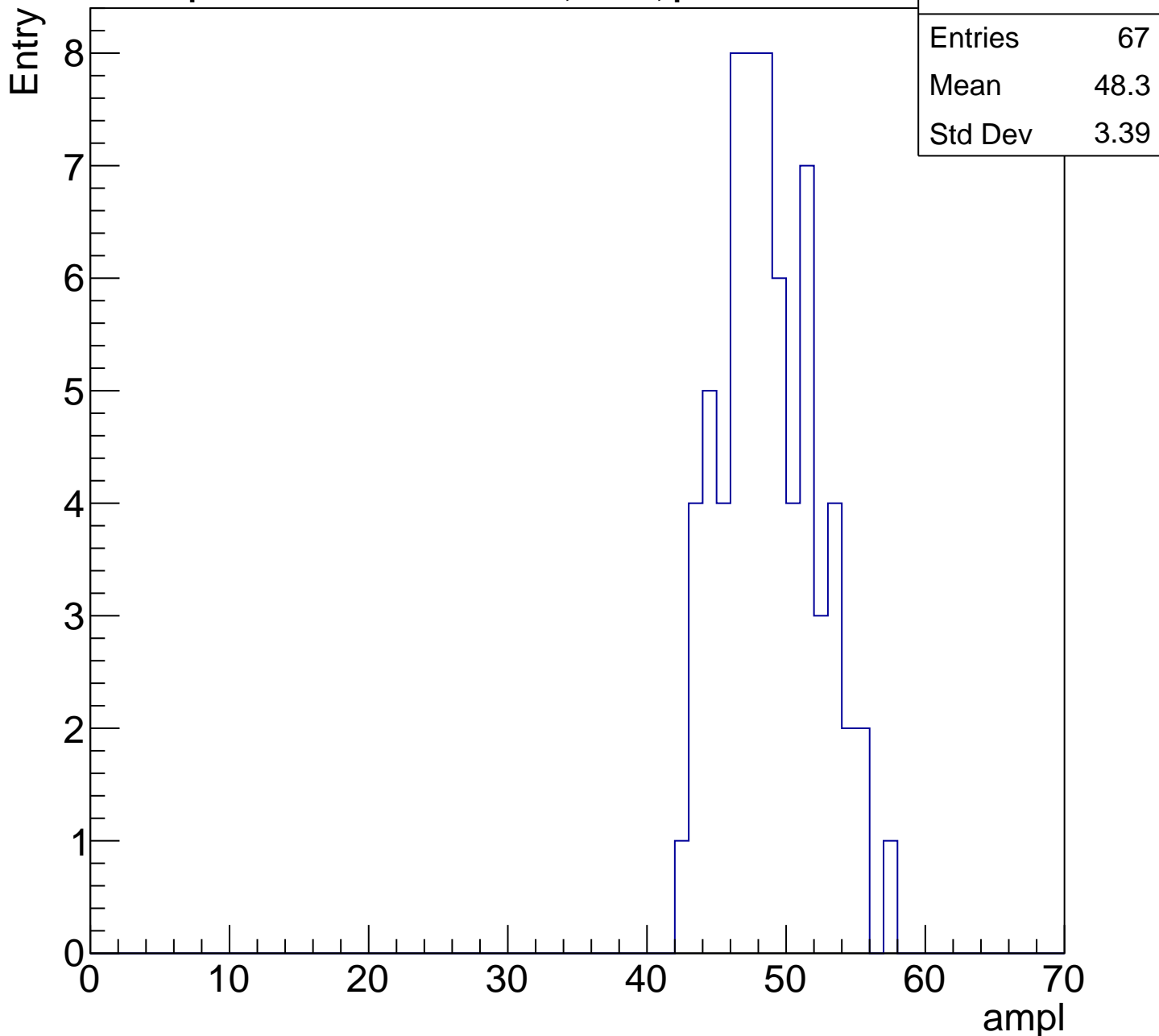
Gaus mean : 41.3523

Gaus Width: 3.8706



B1L100S, U1-ch18, adc3

calib_packv5_042523_0143.root, FC#4, port A2

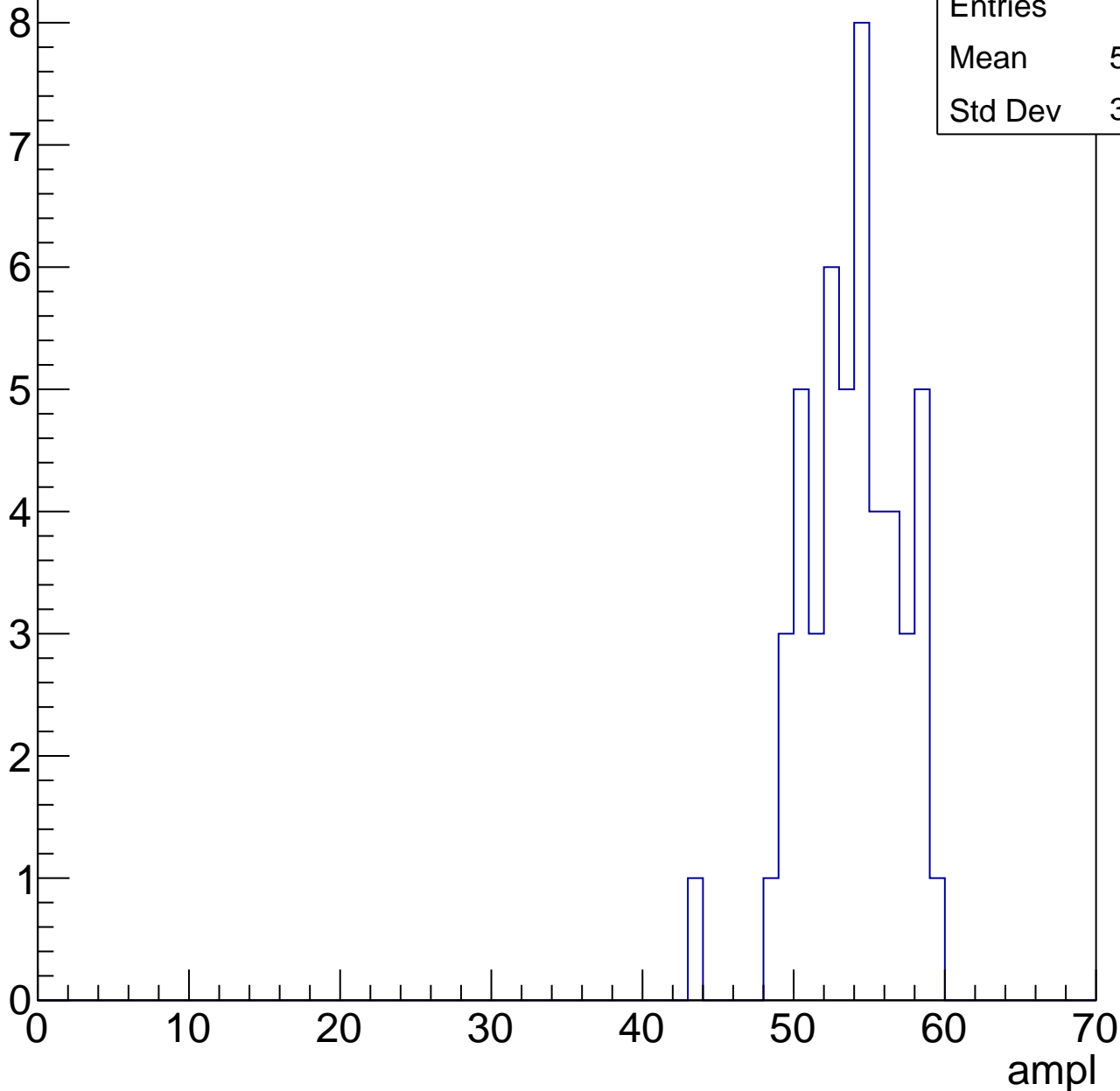


B1L100S, U1-ch18, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	53.35
Std Dev	3.192

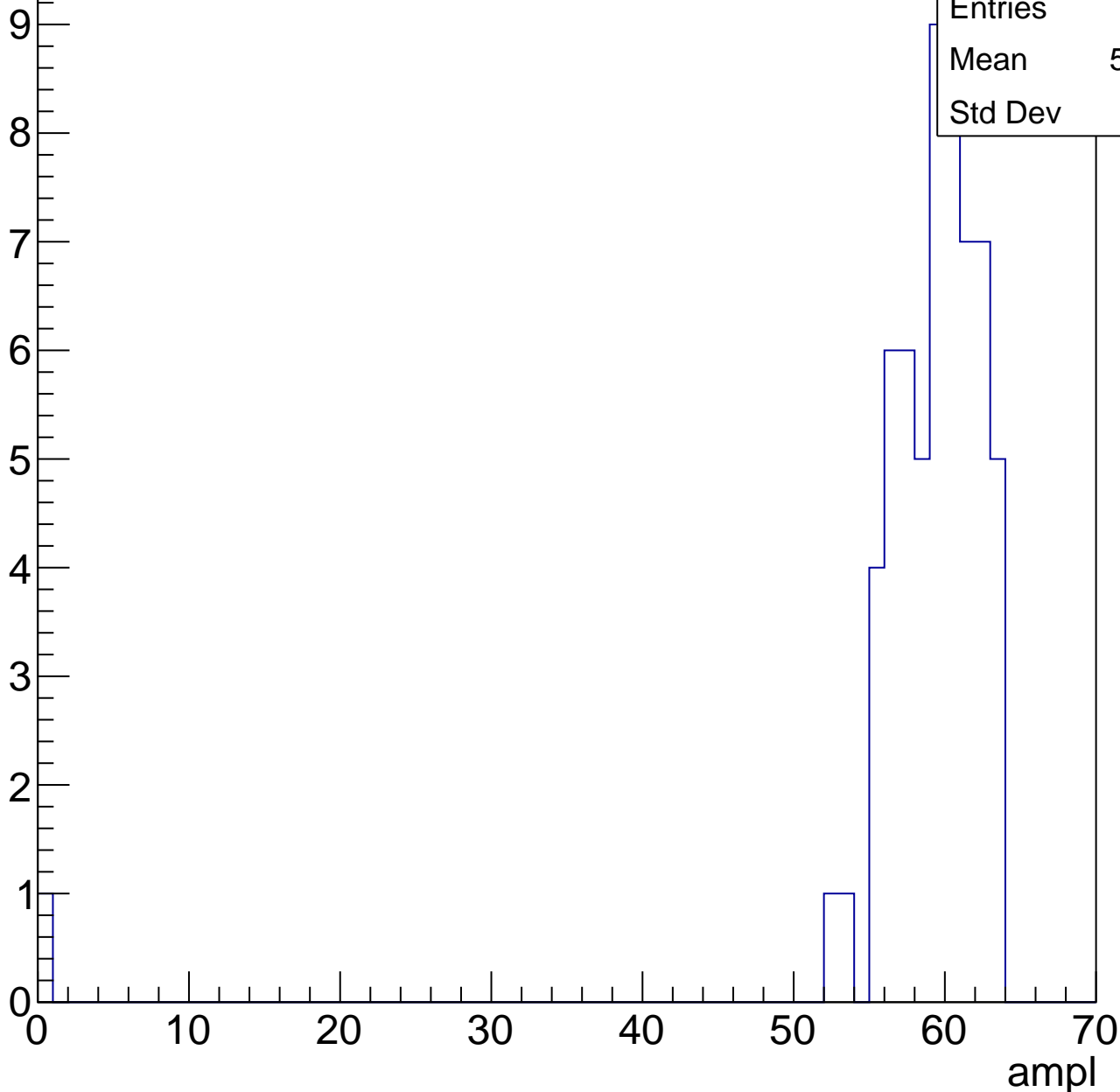


B1L100S, U1-ch18, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

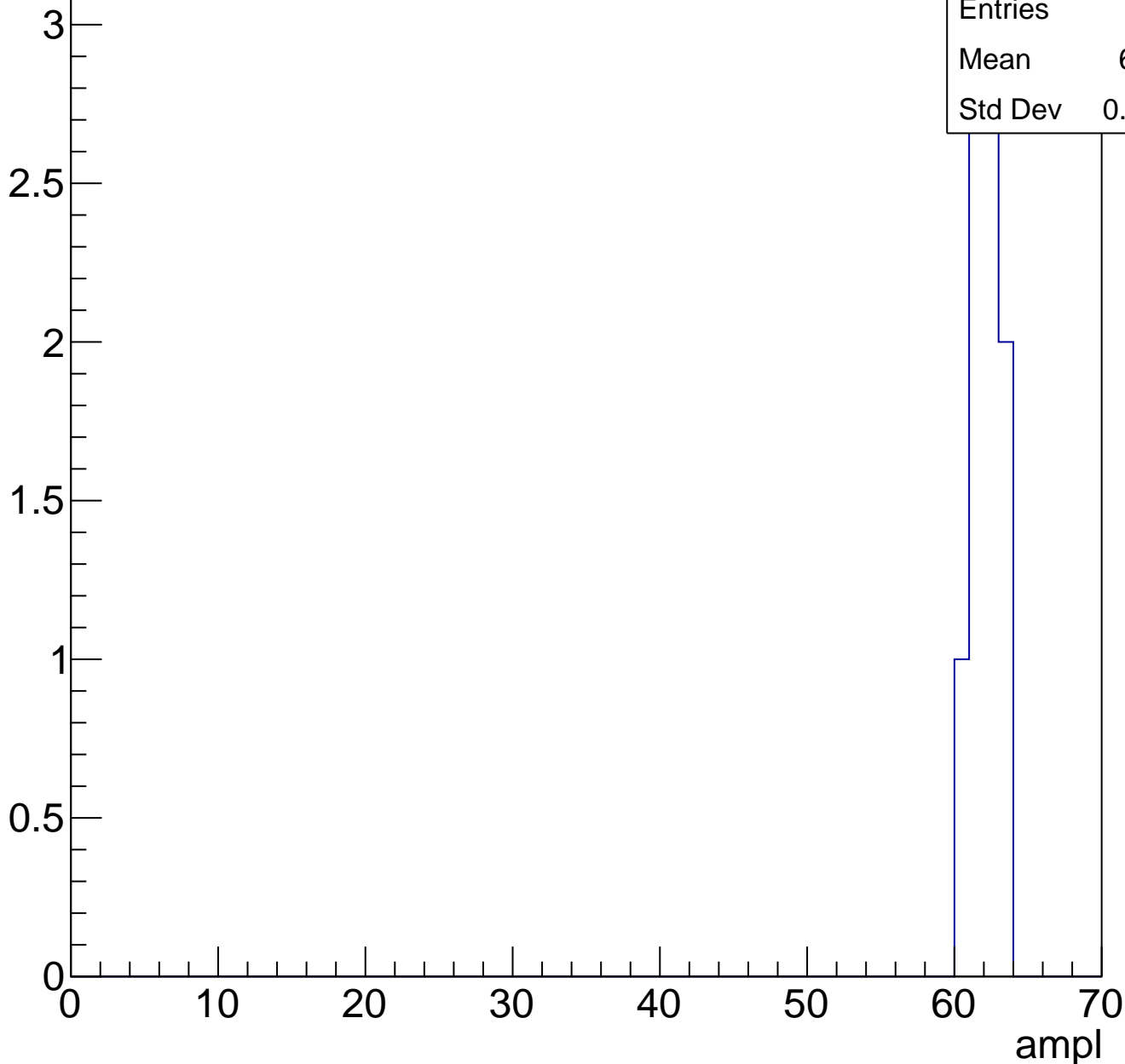
Entries	61
Mean	58.03
Std Dev	7.93



B1L100S, U1-ch18, adc6

calib_packv5_042523_0143.root, FC#4, port A2

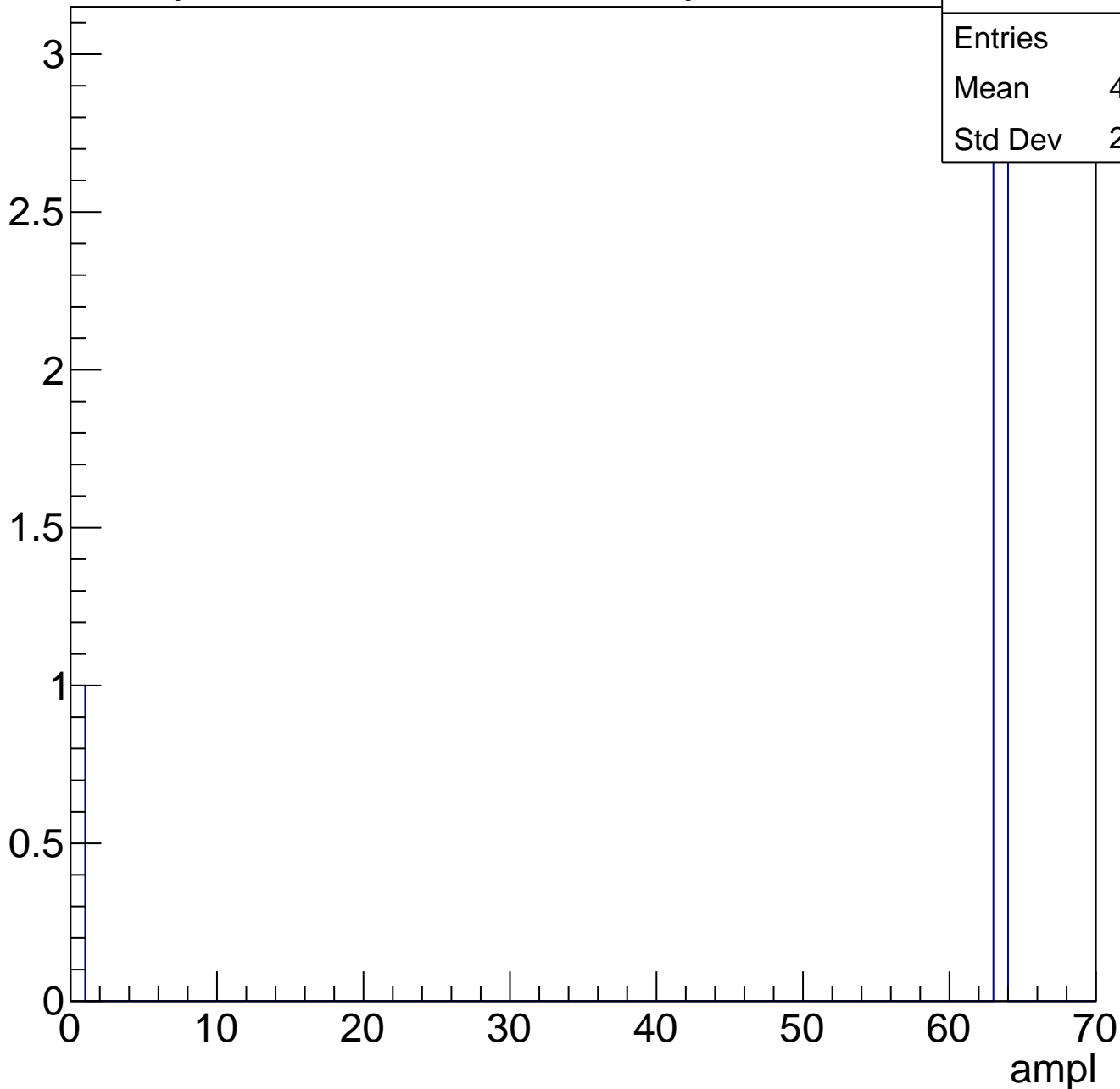
Entry



B1L100S, U1-ch18, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch19, adc0

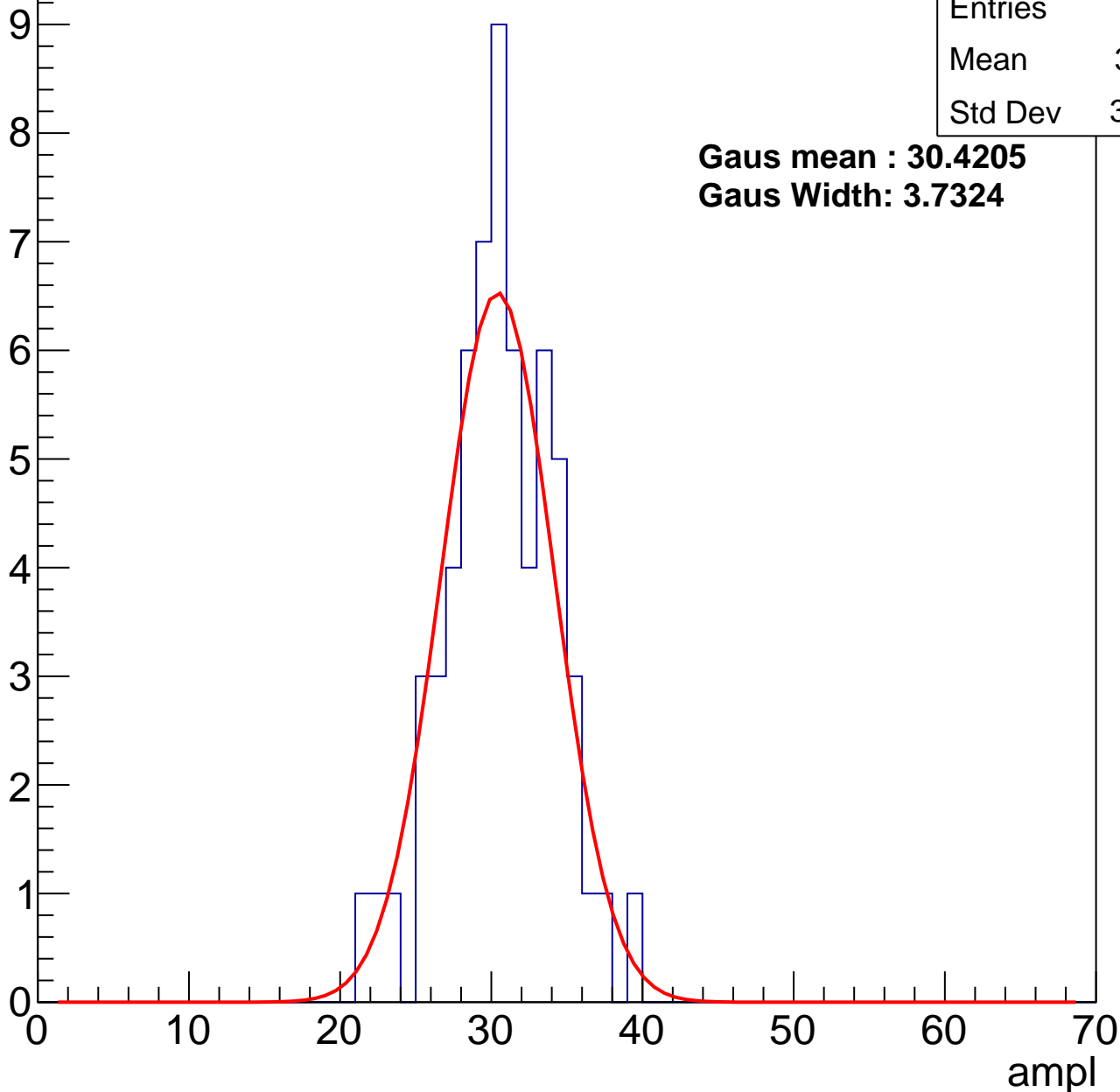
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	30.11
Std Dev	3.547

Gaus mean : 30.4205

Gaus Width: 3.7324



B1L100S, U1-ch19, adc1

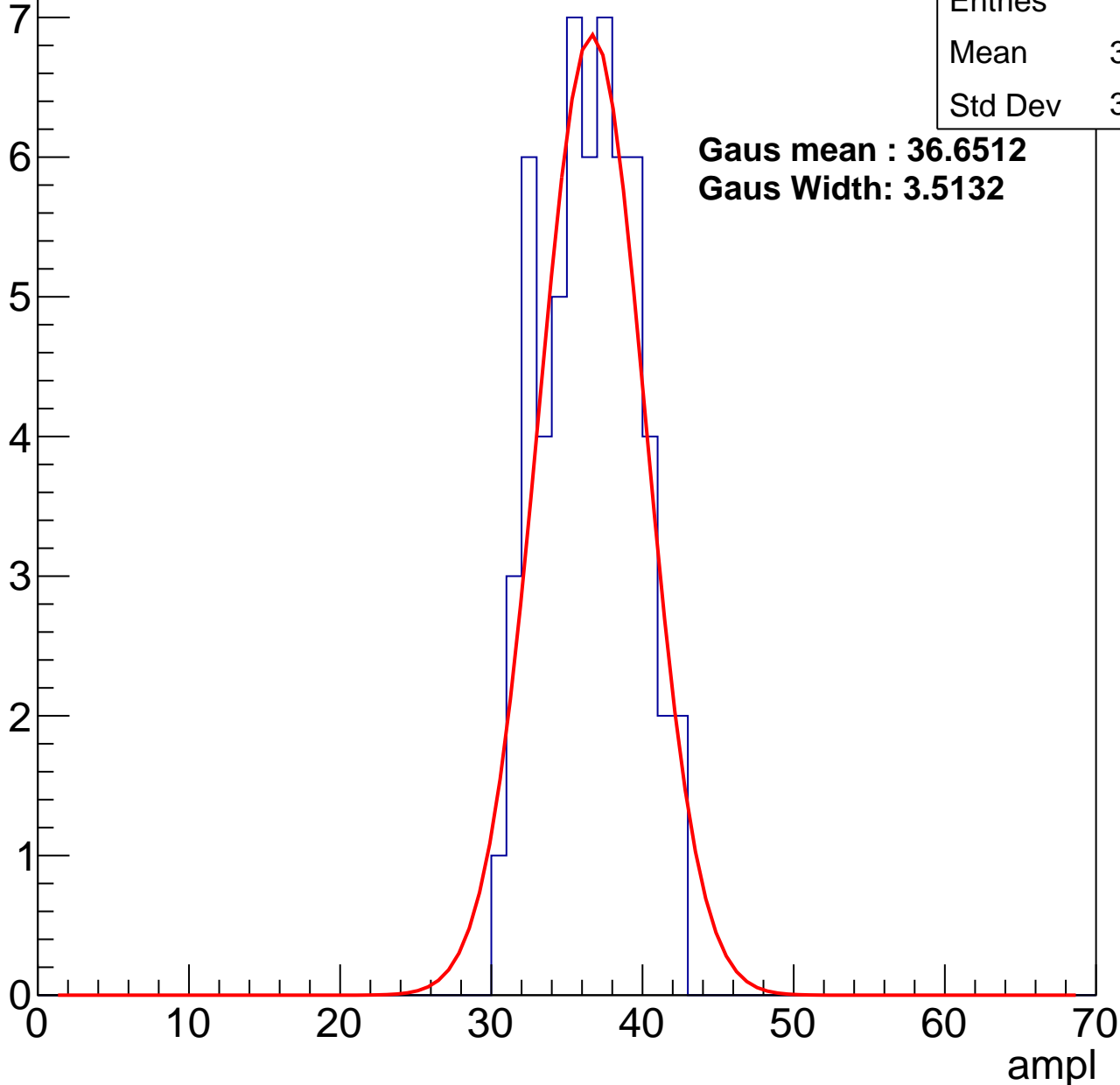
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	36.02
Std Dev	3.028

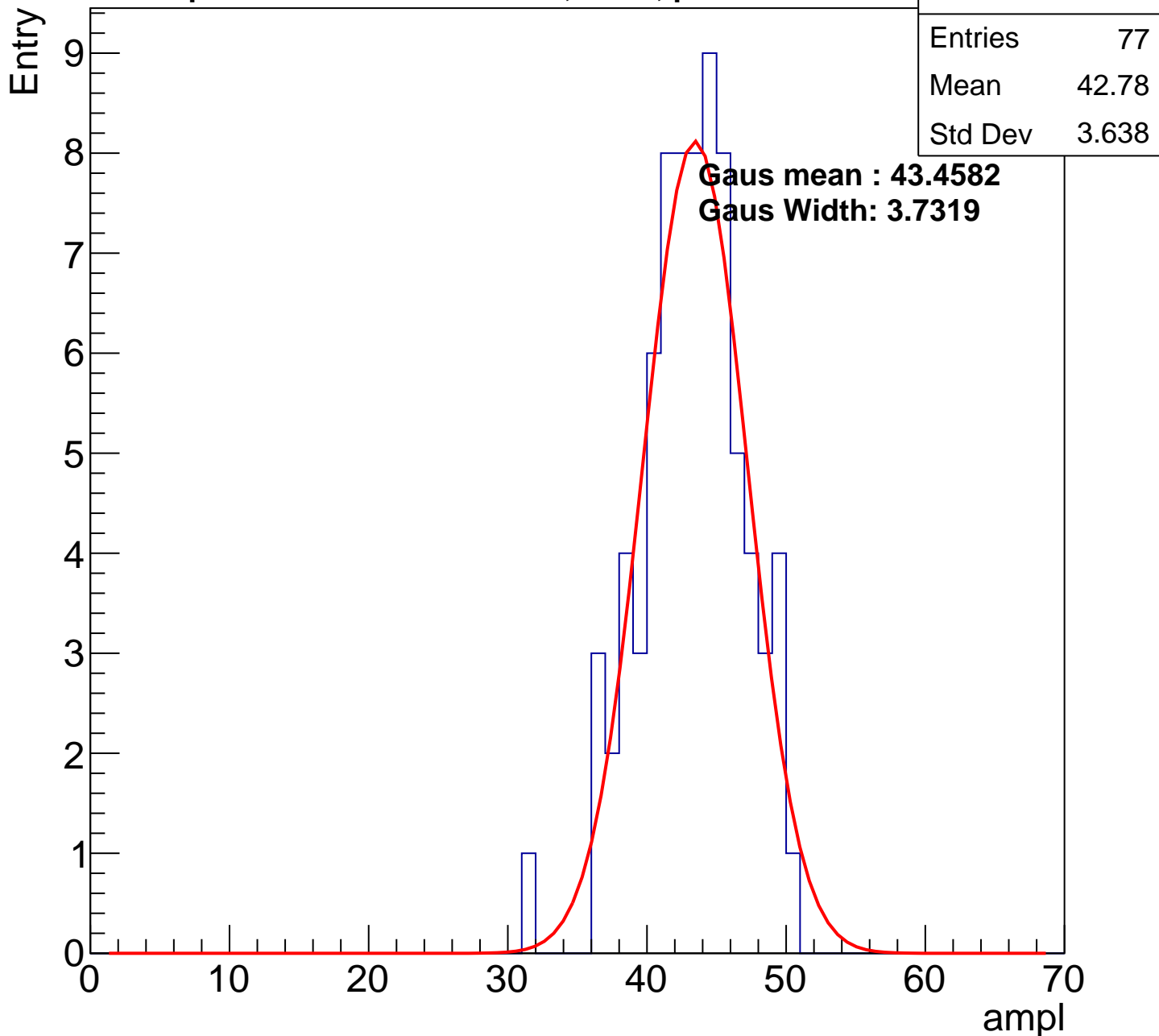
Gaus mean : 36.6512

Gaus Width: 3.5132



B1L100S, U1-ch19, adc2

calib_packv5_042523_0143.root, FC#4, port A2

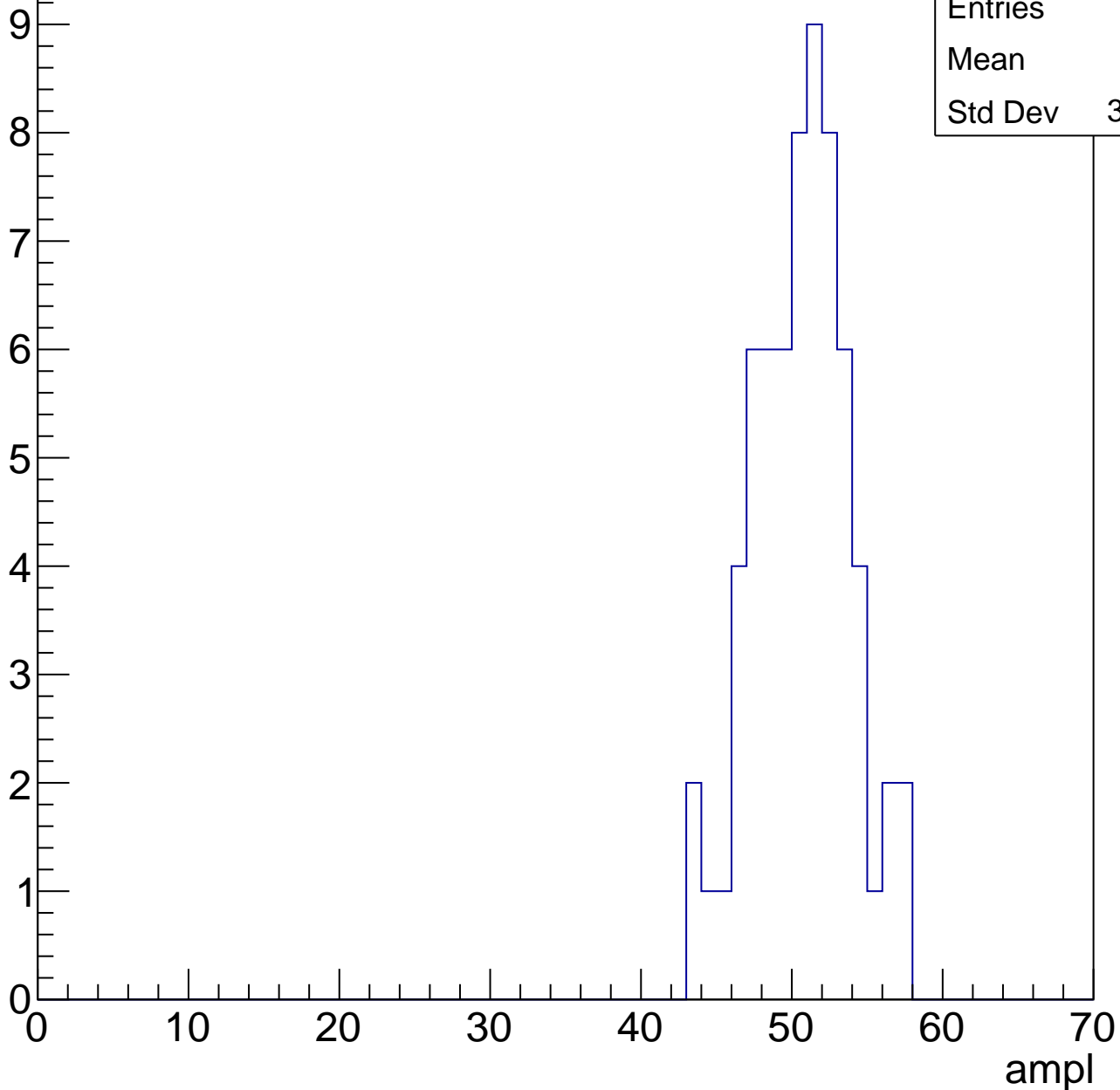


B1L100S, U1-ch19, adc3

calib_packv5_042523_0143.root, FC#4, port A2

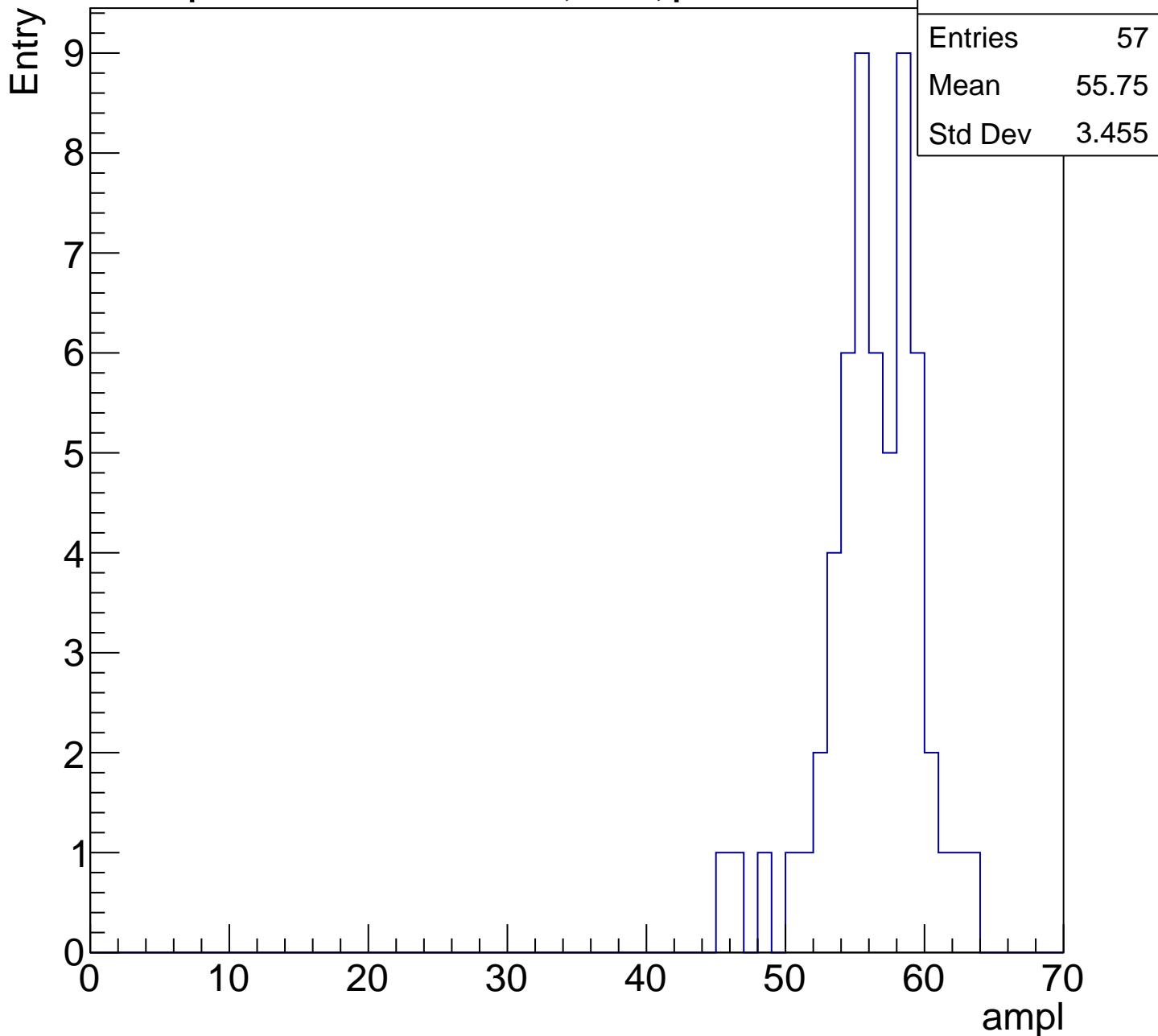
Entry

Entries	66
Mean	50.2
Std Dev	3.159



B1L100S, U1-ch19, adc4

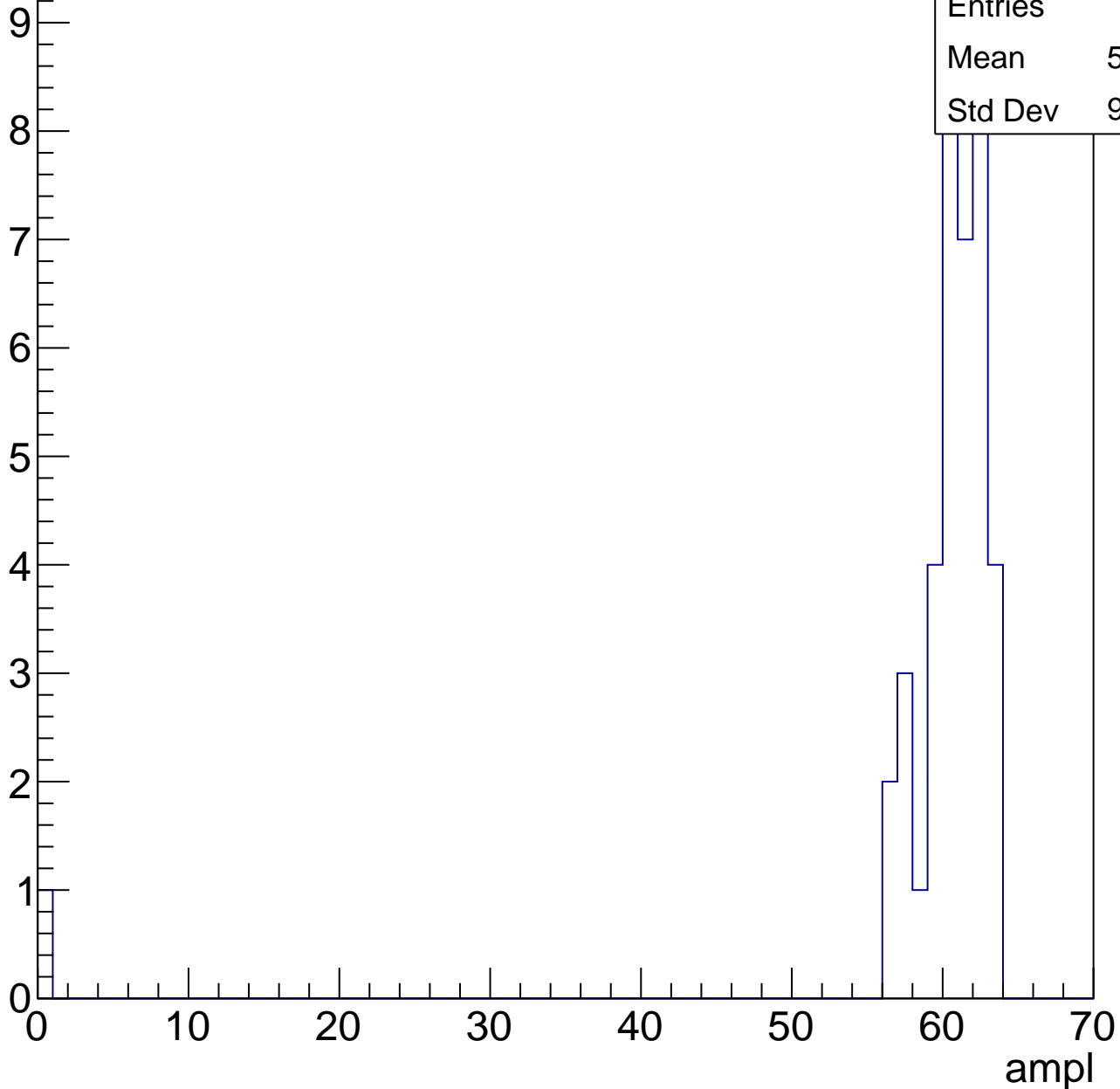
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch19, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

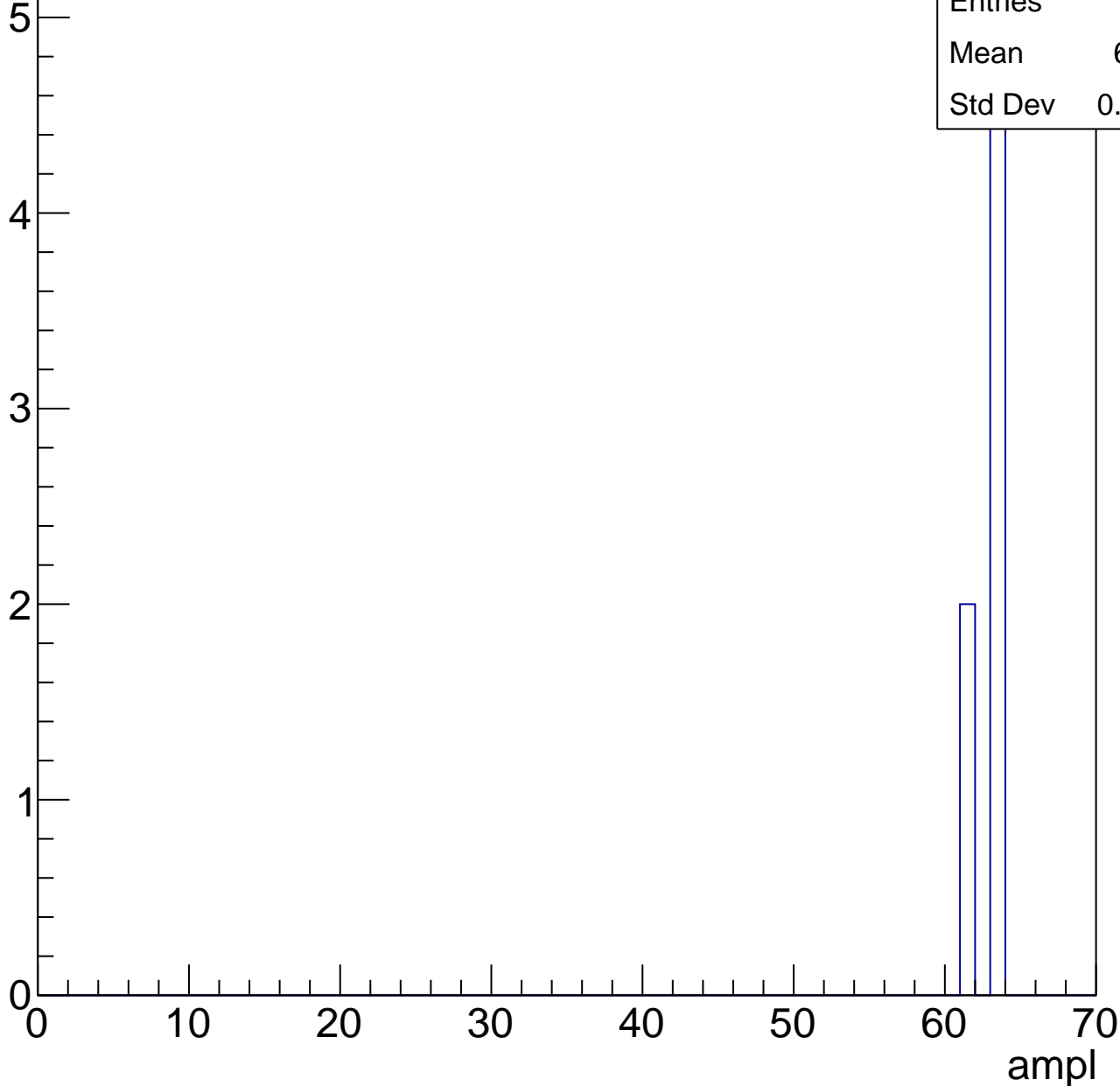


B1L100S, U1-ch19, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	7
Mean	62.43
Std Dev	0.9035



B1L100S, U1-ch19, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch20, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	28.9
Std Dev	3.712

Gaus mean : 29.4055

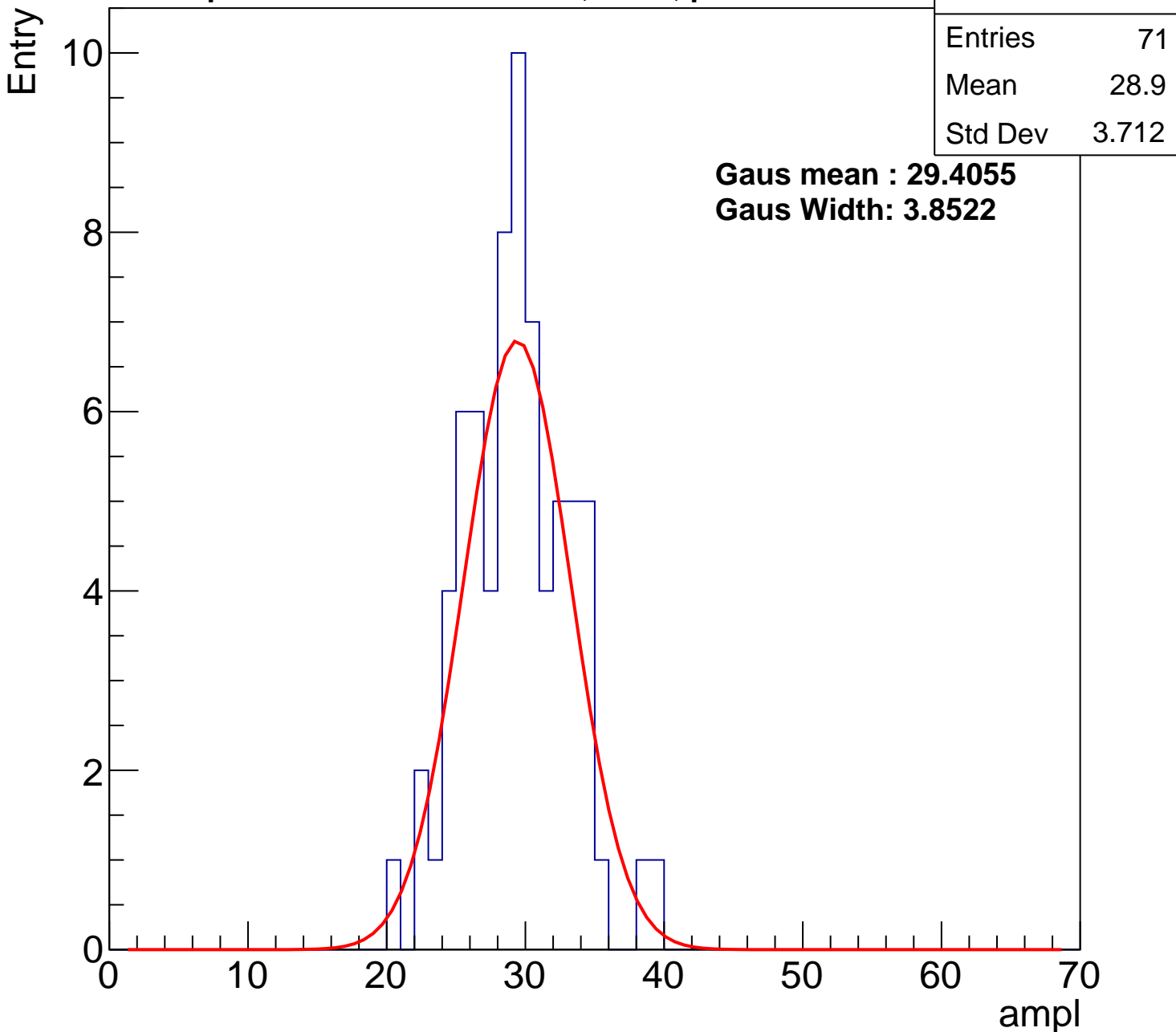
Gaus Width: 3.8522

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch20, adc1

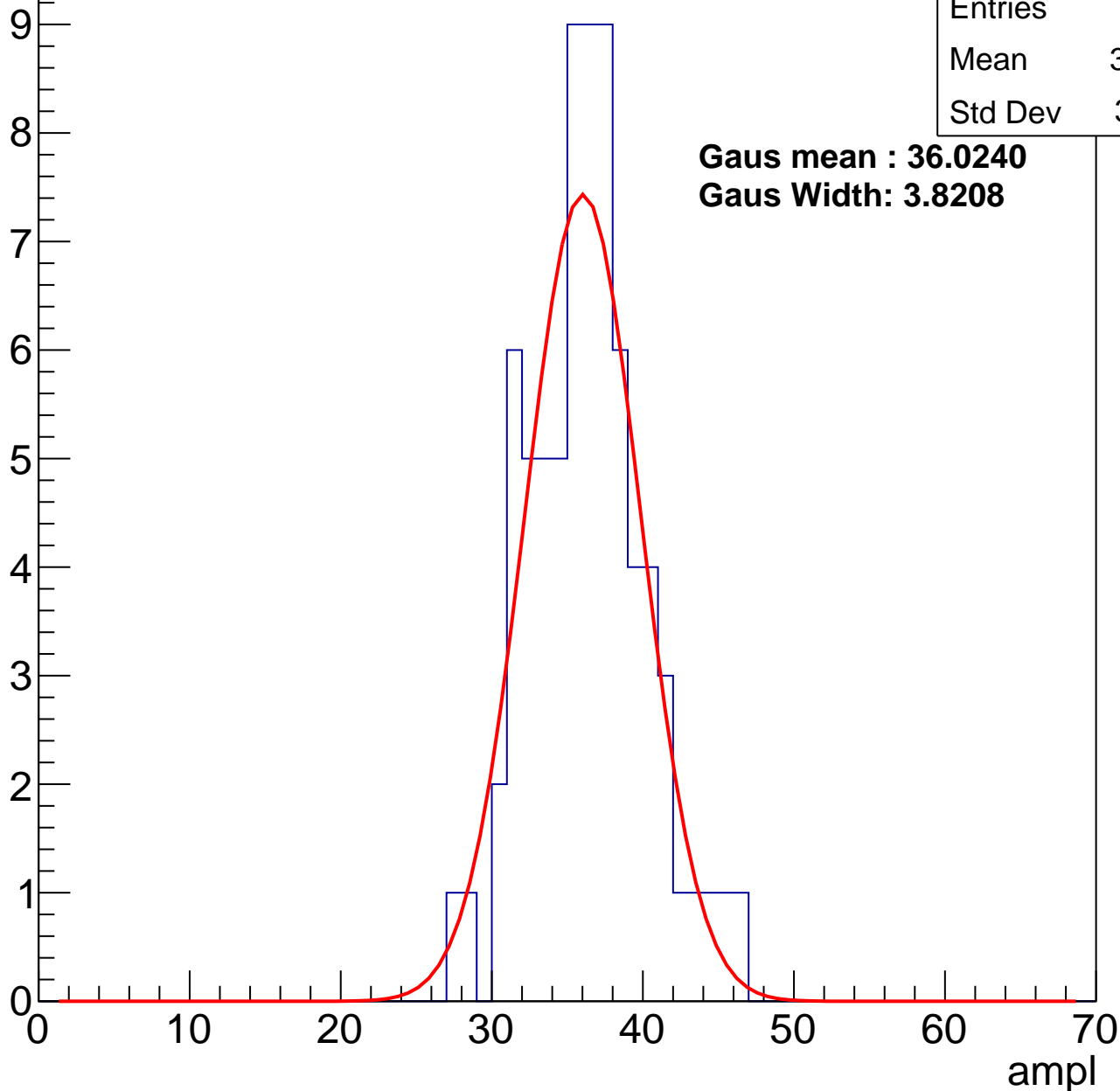
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.88
Std Dev	3.781

Gaus mean : 36.0240

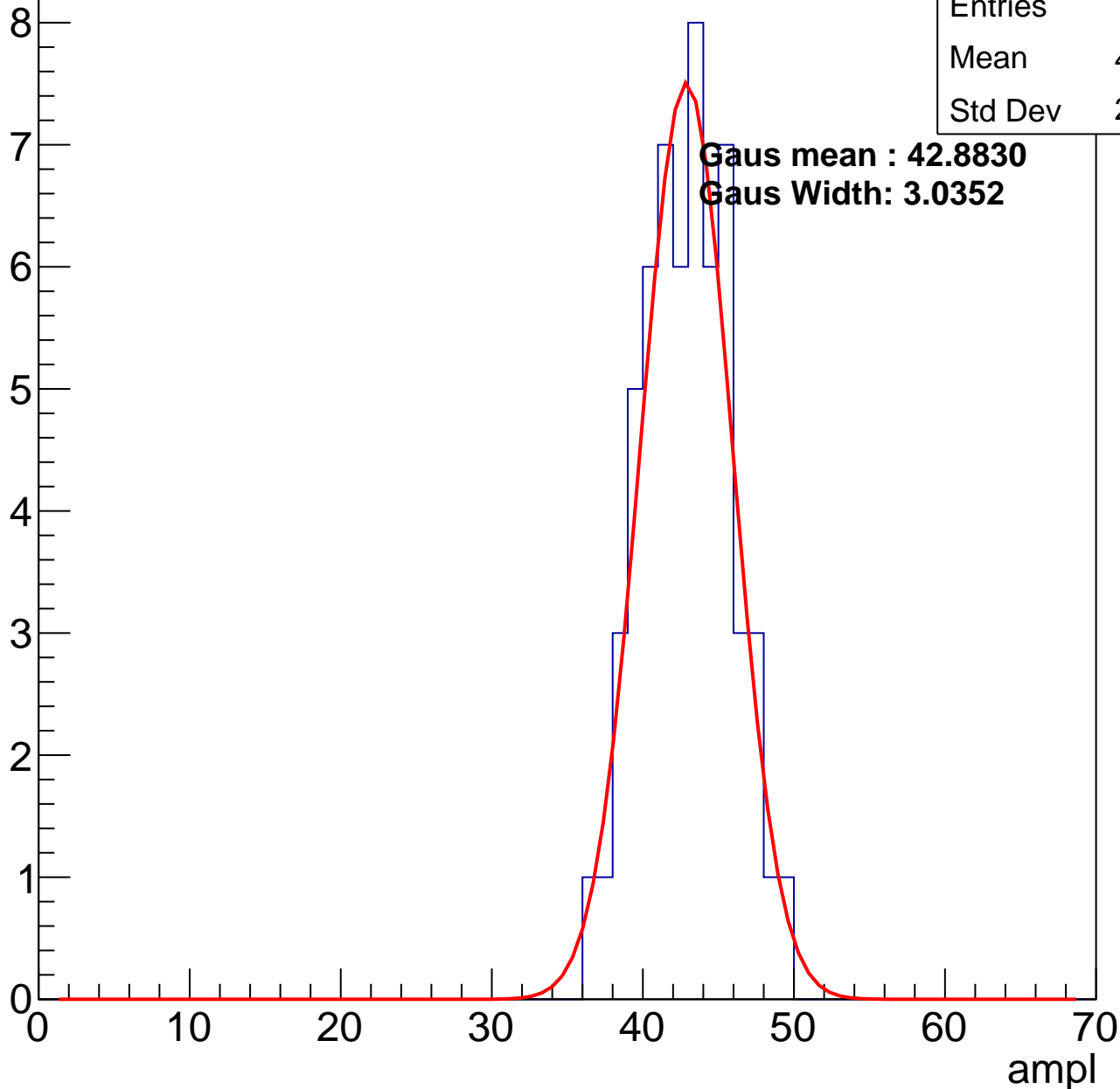
Gaus Width: 3.8208



B1L100S, U1-ch20, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch20, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	48.9
Std Dev	3.319

Entry

10

8

6

4

2

0

0

10

20

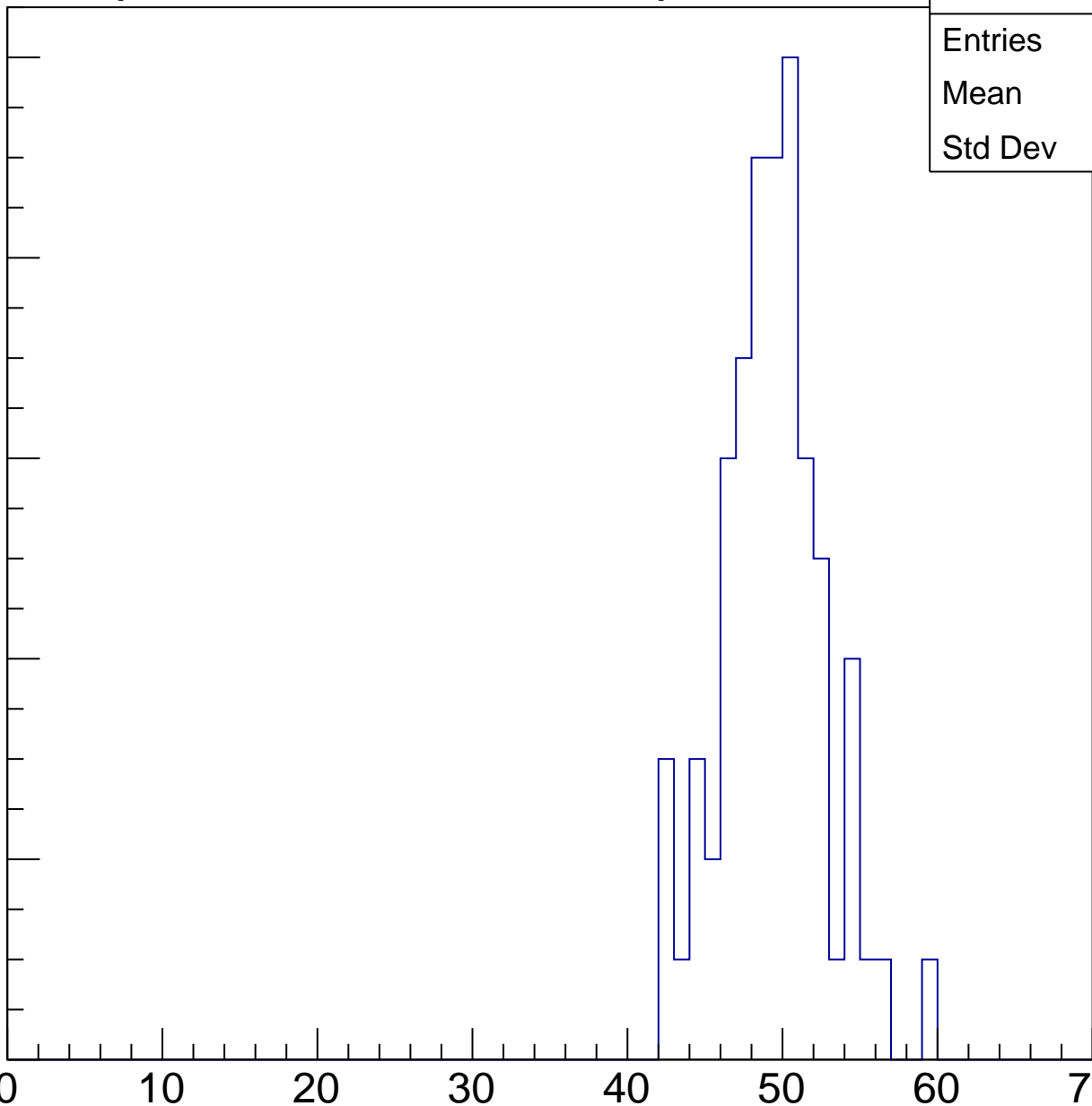
30

40

50

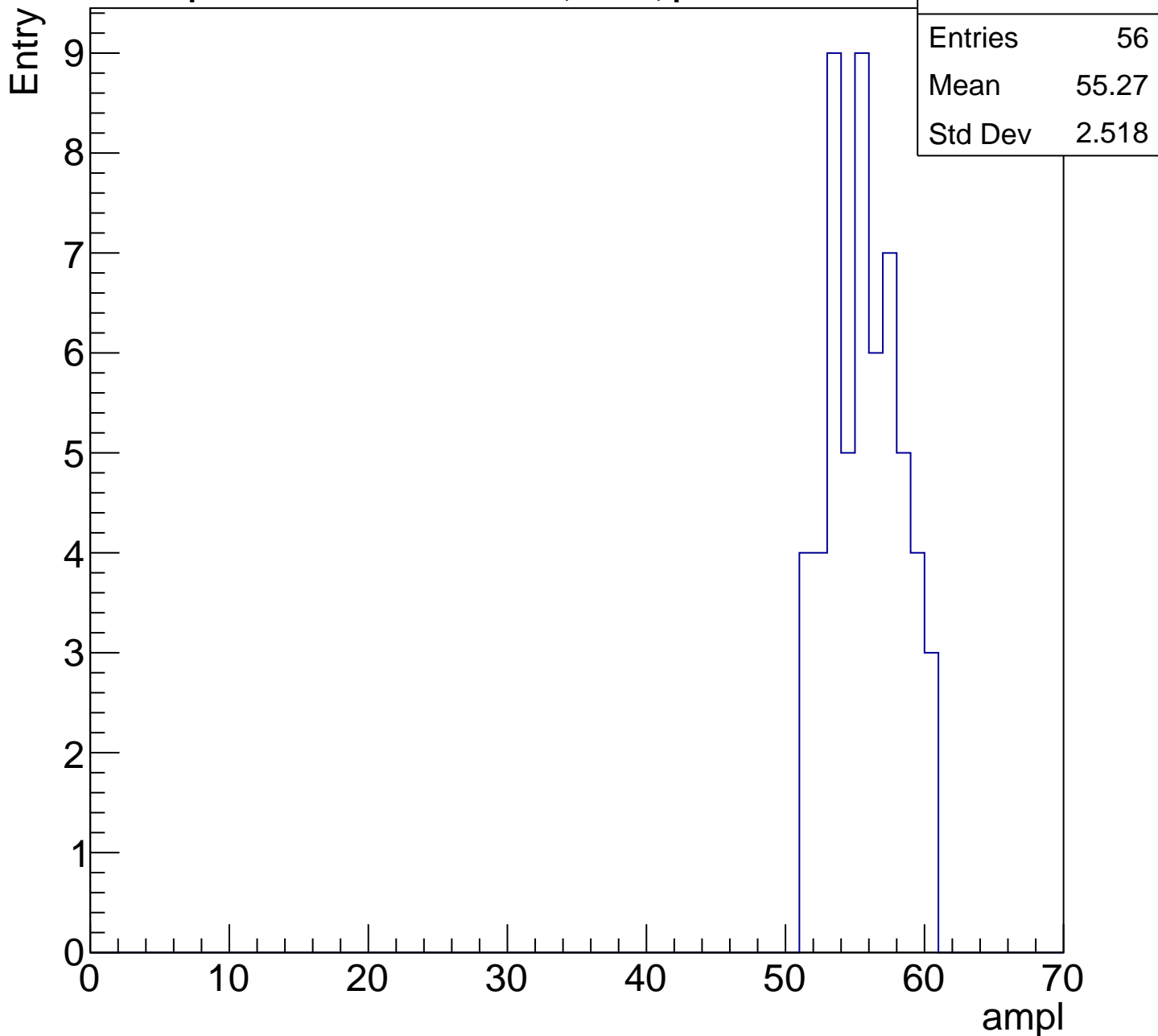
60

ampl



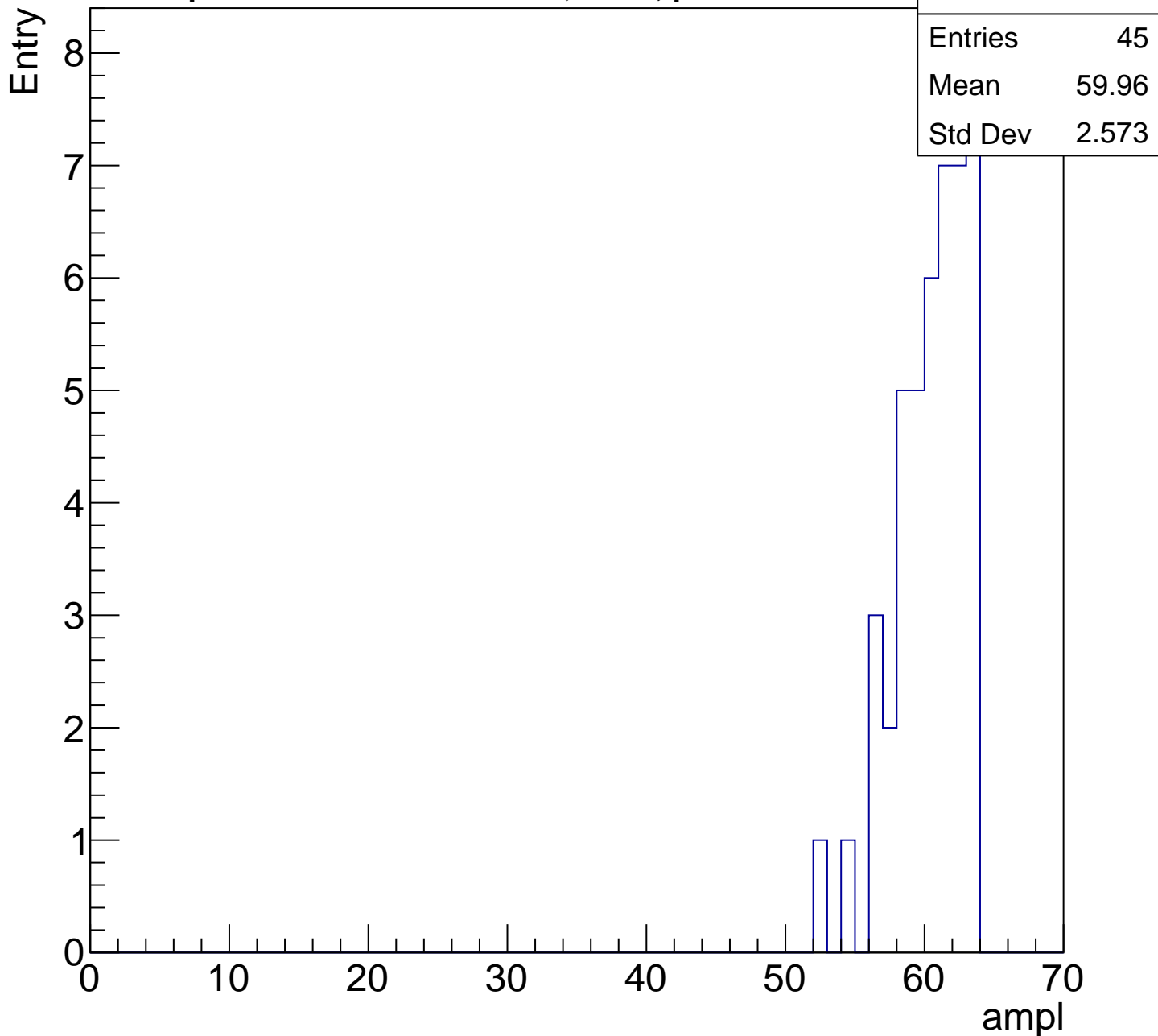
B1L100S, U1-ch20, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch20, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch20, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

3

2.5

2

1.5

1

0.5

0

Entries

11

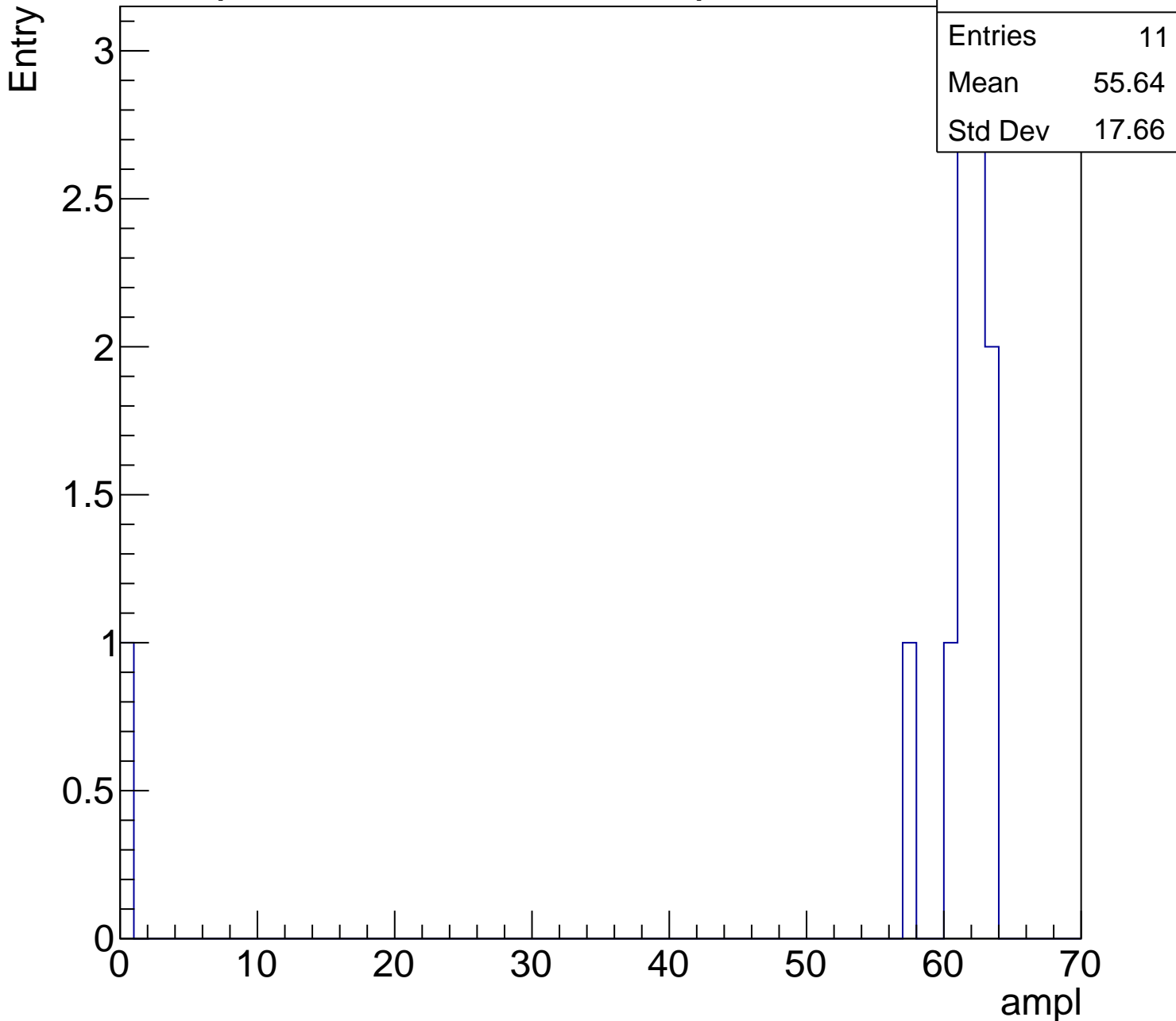
Mean

55.64

Std Dev

17.66

ampl



B1L100S, U1-ch20, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch21, adc0

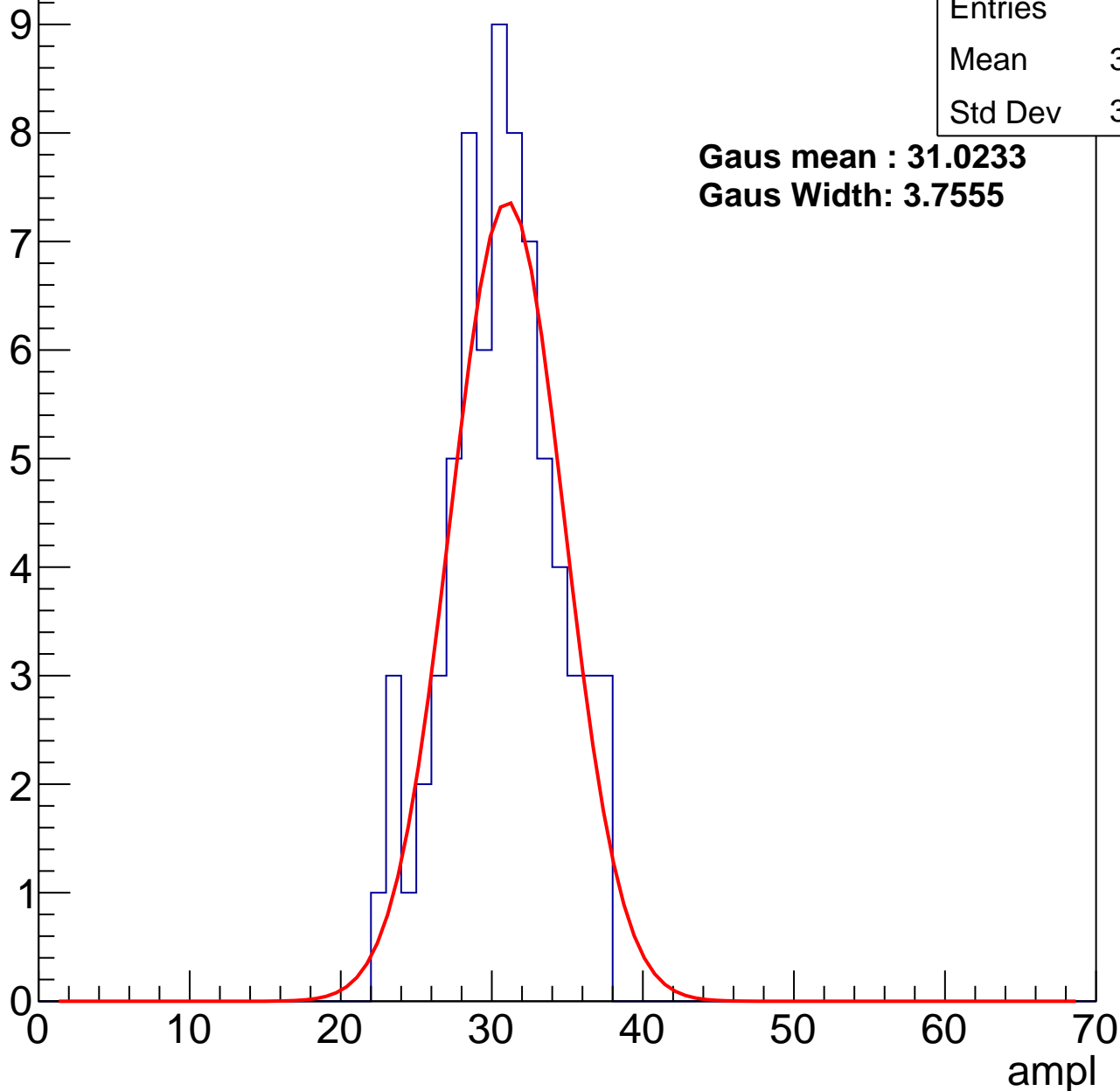
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.18
Std Dev	3.562

Gaus mean : 31.0233

Gaus Width: 3.7555



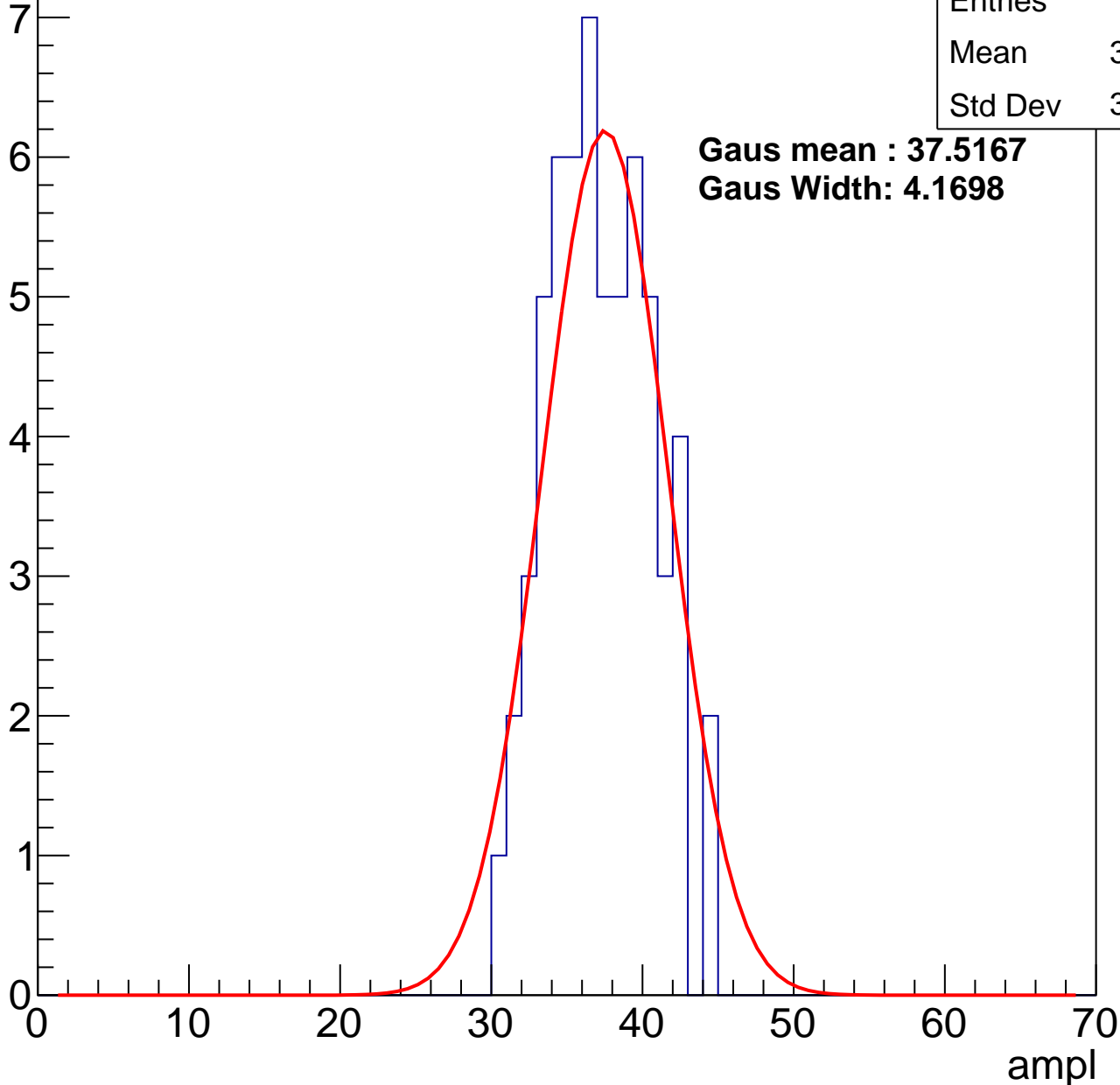
B1L100S, U1-ch21, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	36.78
Std Dev	3.362

Gaus mean : 37.5167
Gaus Width: 4.1698



B1L100S, U1-ch21, adc2

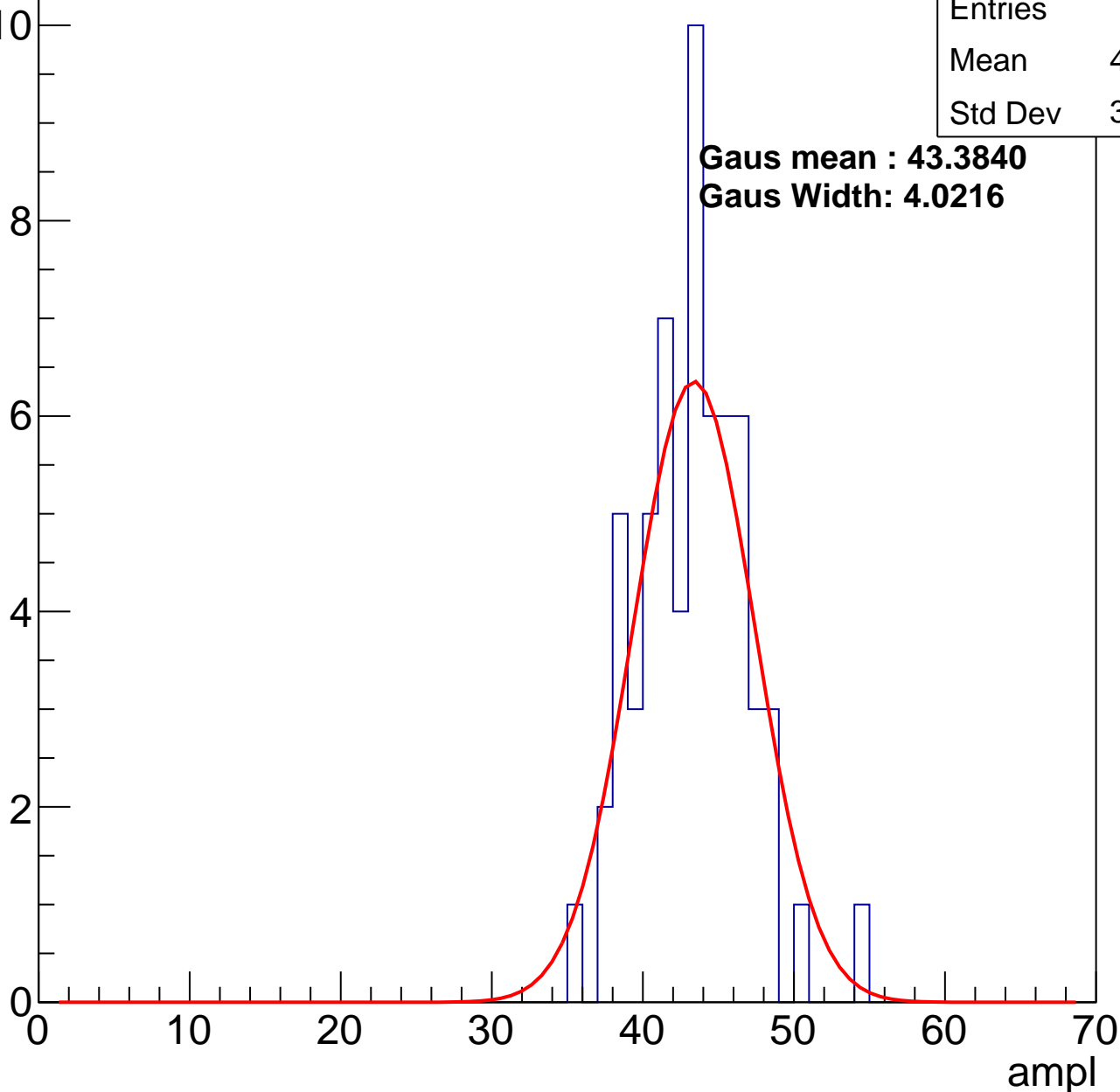
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	42.86
Std Dev	3.477

Gaus mean : 43.3840

Gaus Width: 4.0216

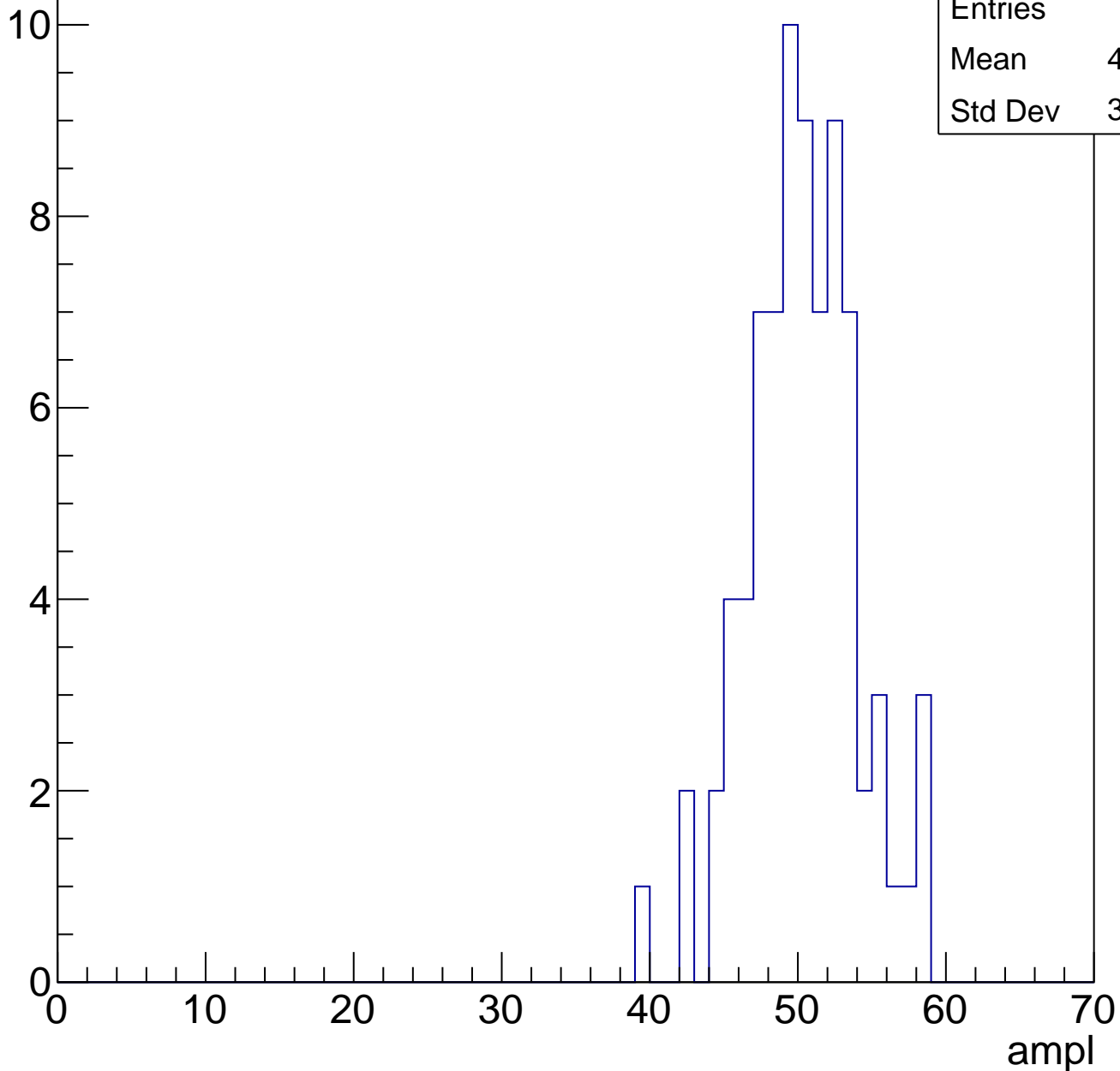


B1L100S, U1-ch21, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	49.82
Std Dev	3.686

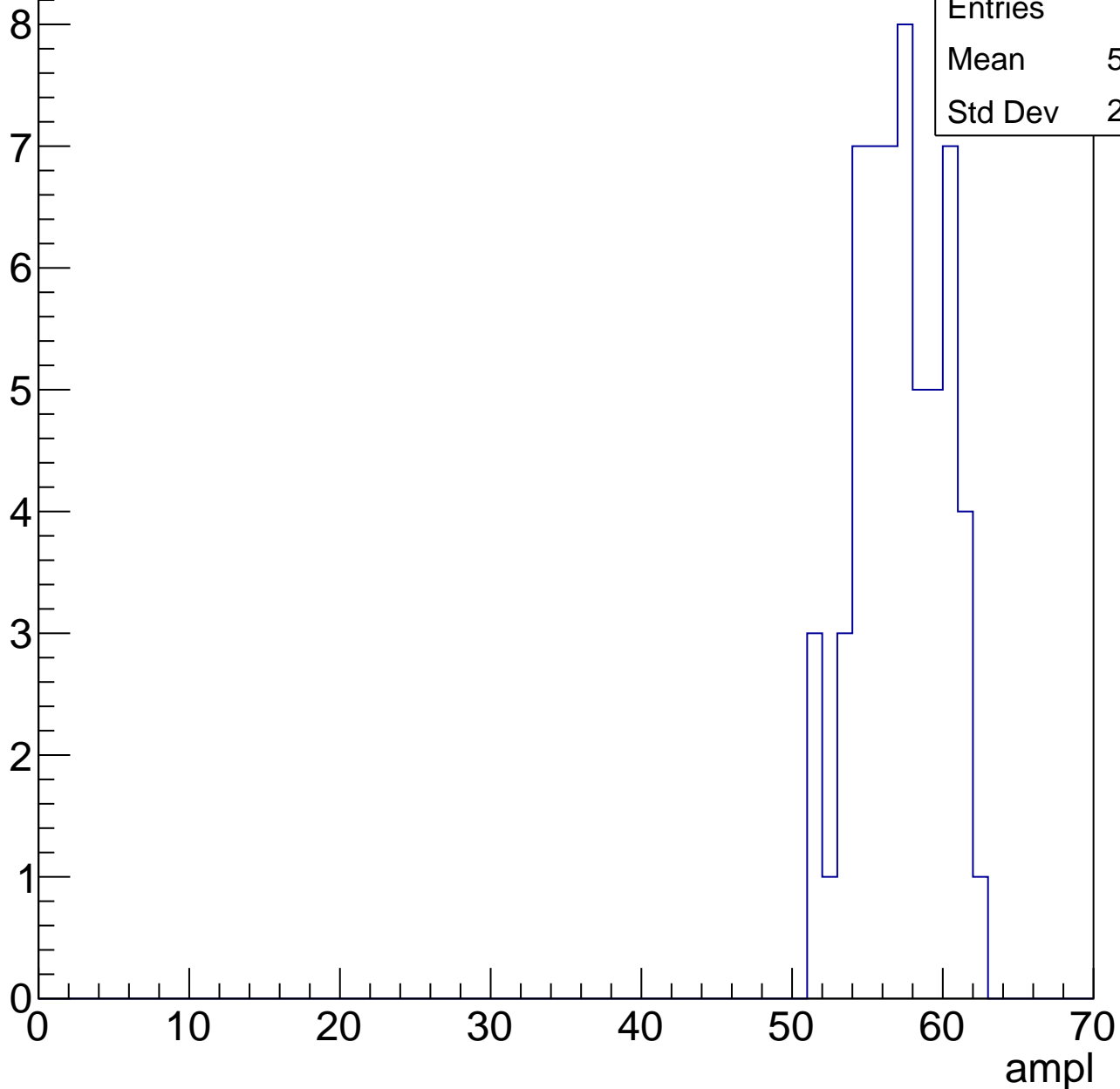
Entry



B1L100S, U1-ch21, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

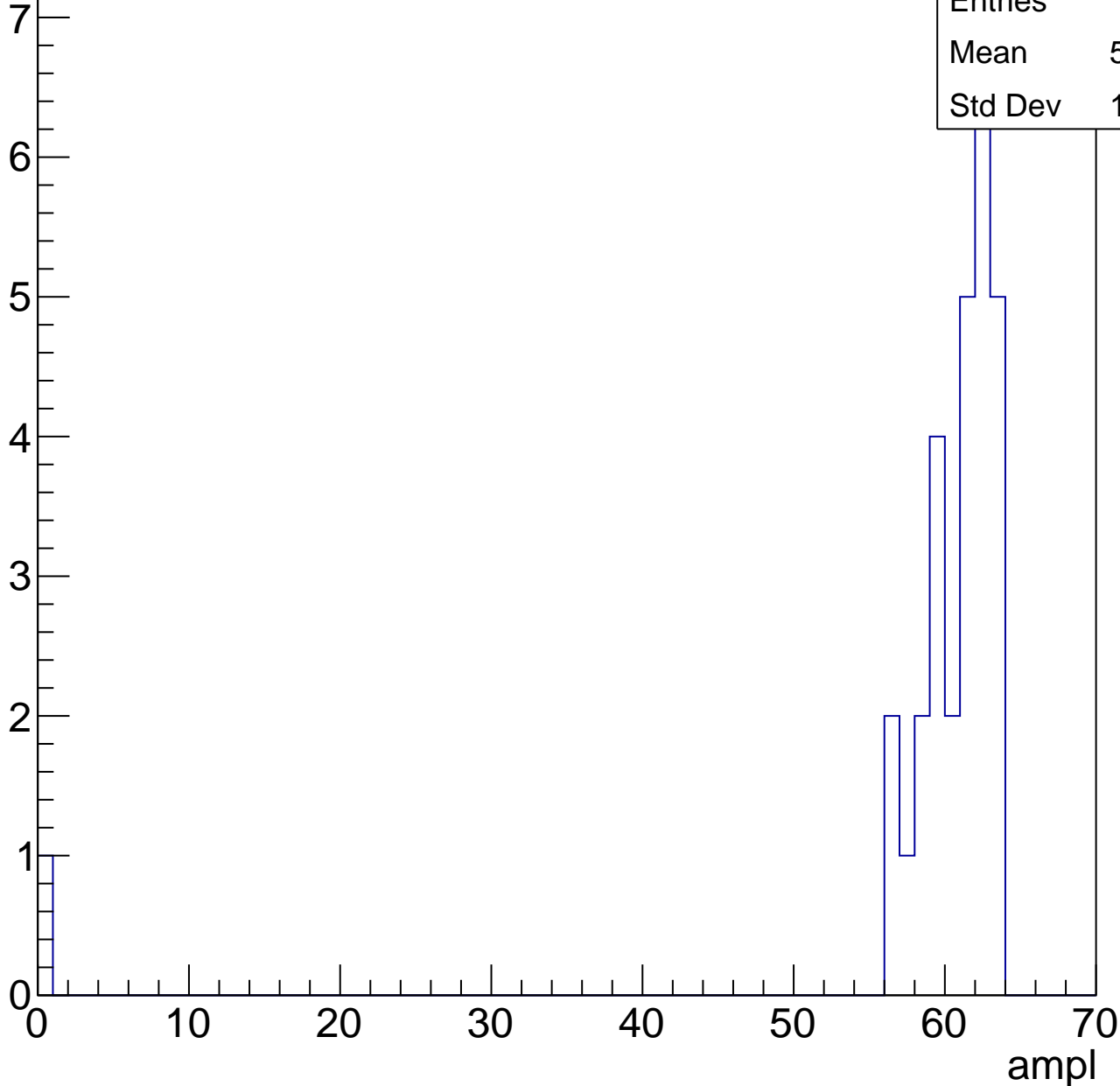


B1L100S, U1-ch21, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	29
Mean	58.45
Std Dev	11.24

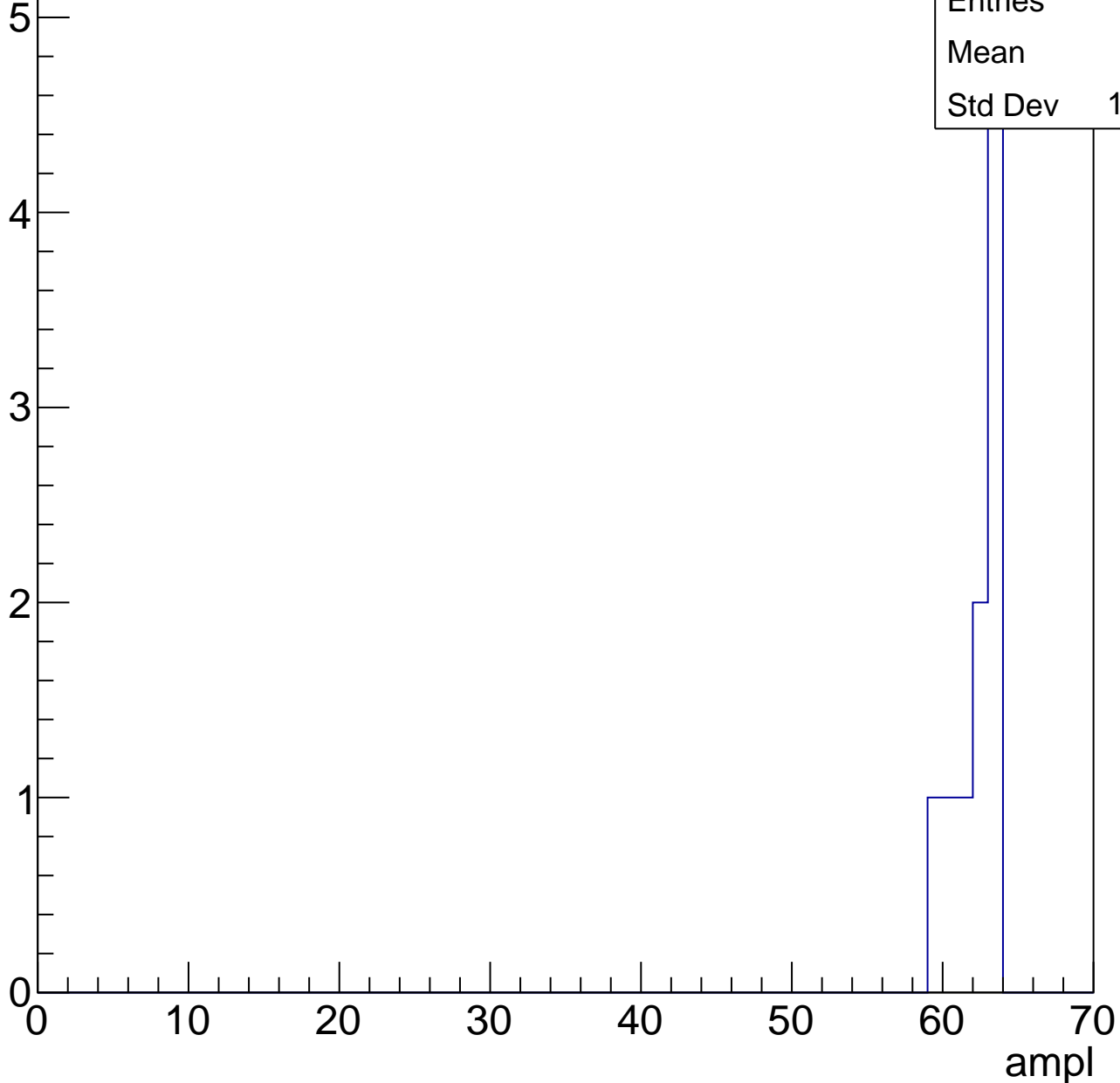


B1L100S, U1-ch21, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	61.9
Std Dev	1.375



B1L100S, U1-ch21, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch22, adc0

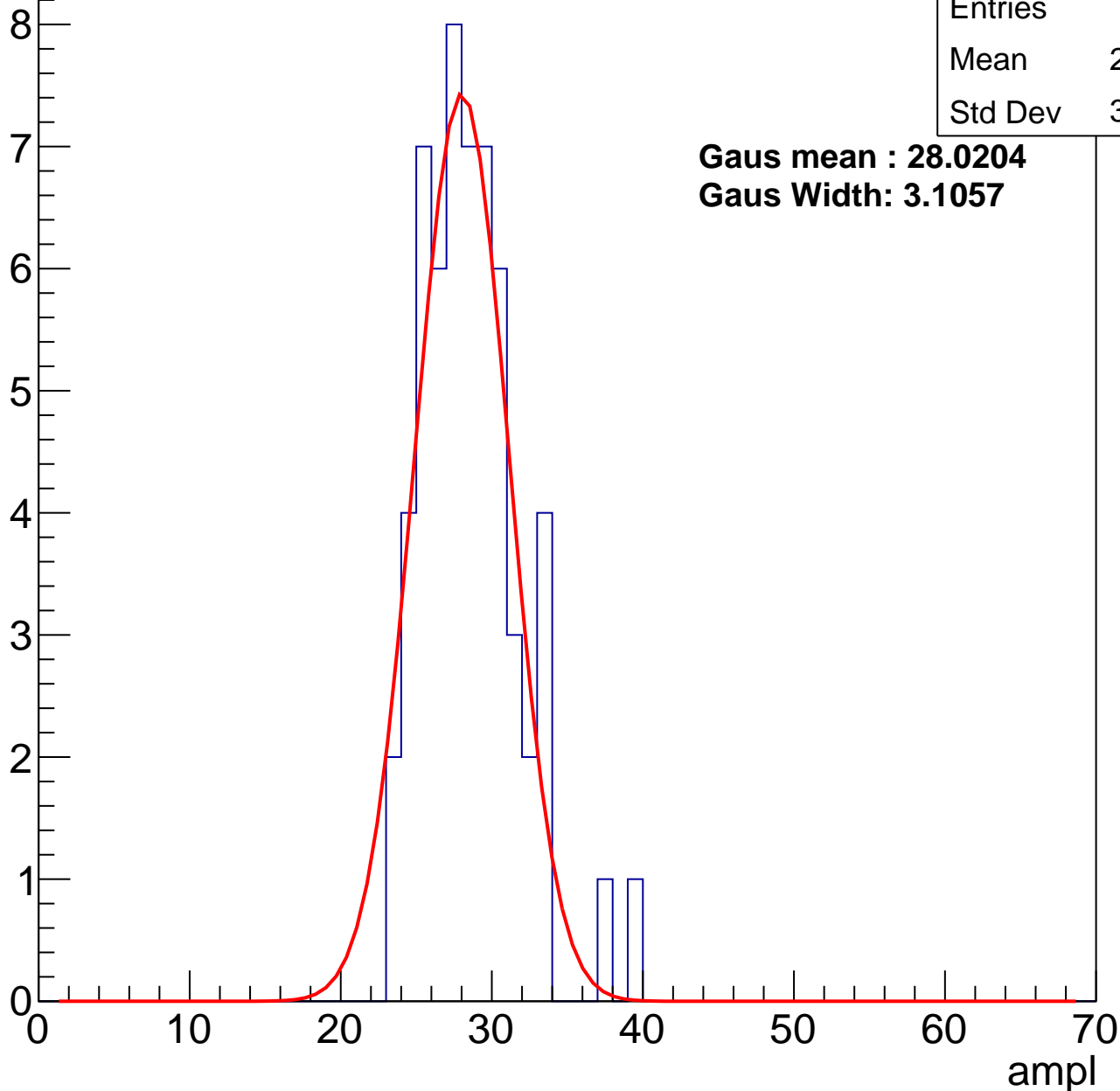
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	28.16
Std Dev	3.215

Gaus mean : 28.0204

Gaus Width: 3.1057



B1L100S, U1-ch22, adc1

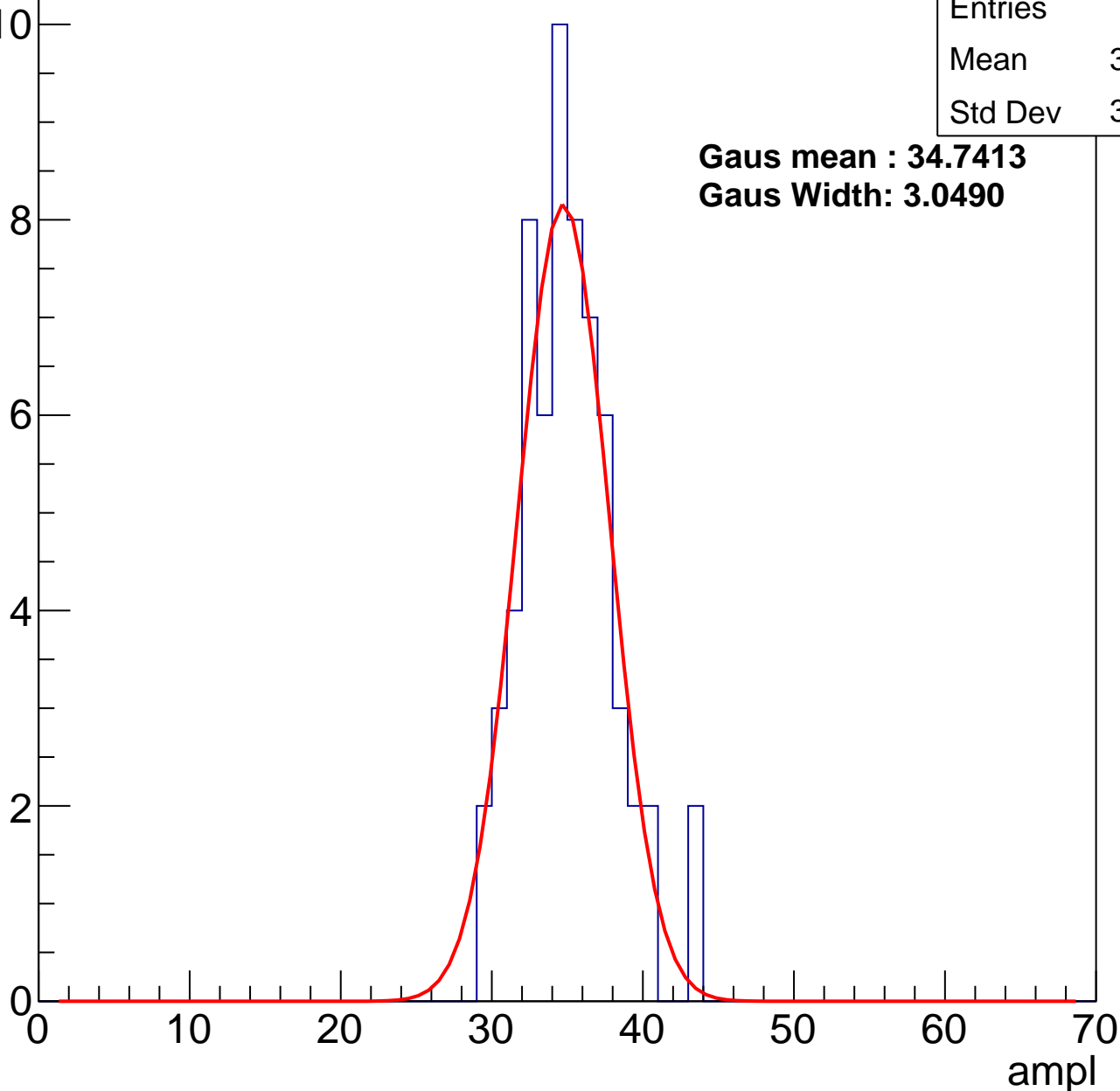
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	34.57
Std Dev	3.017

Gaus mean : 34.7413

Gaus Width: 3.0490



B1L100S, U1-ch22, adc2

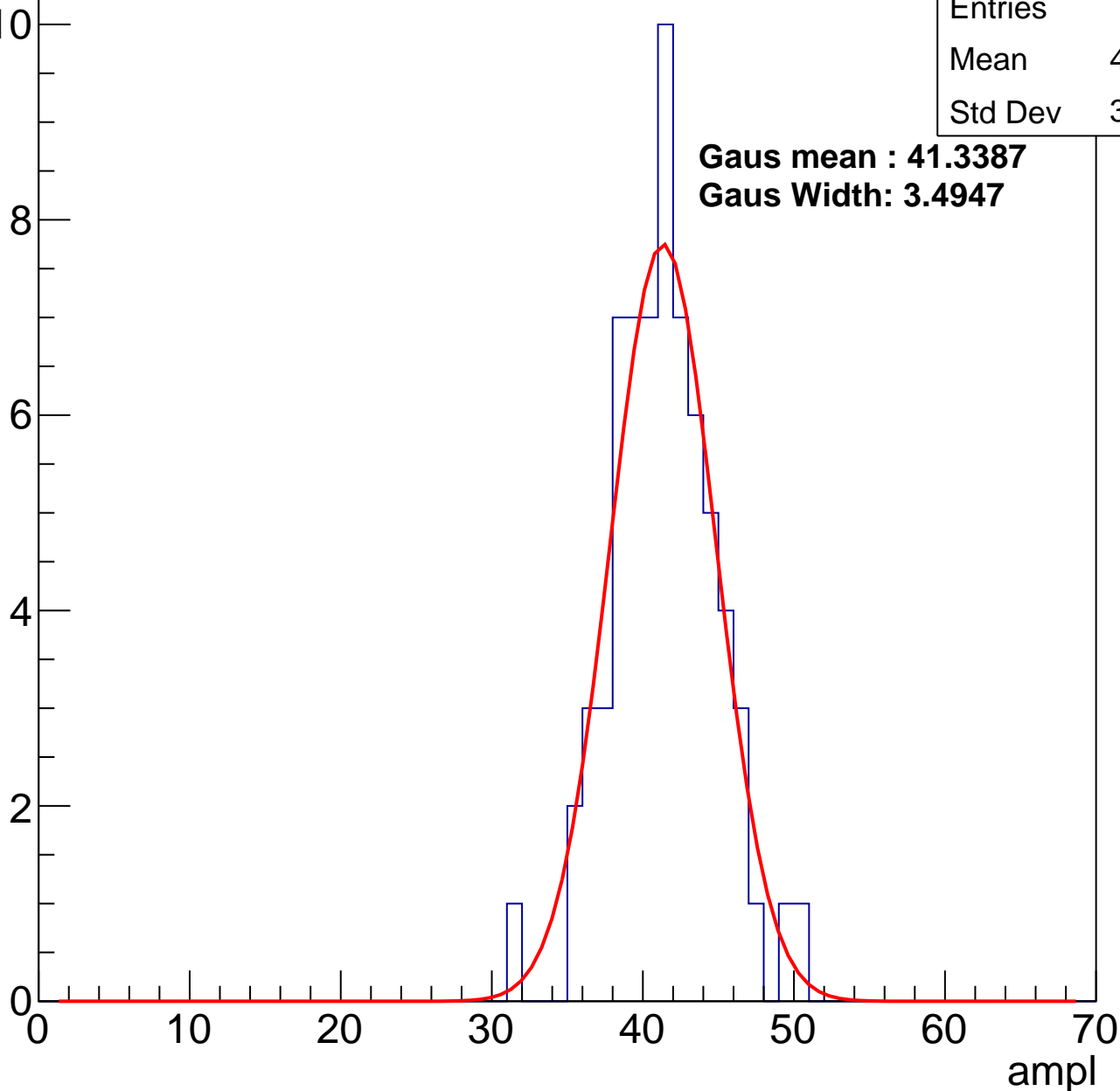
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	40.96
Std Dev	3.406

Gaus mean : 41.3387

Gaus Width: 3.4947

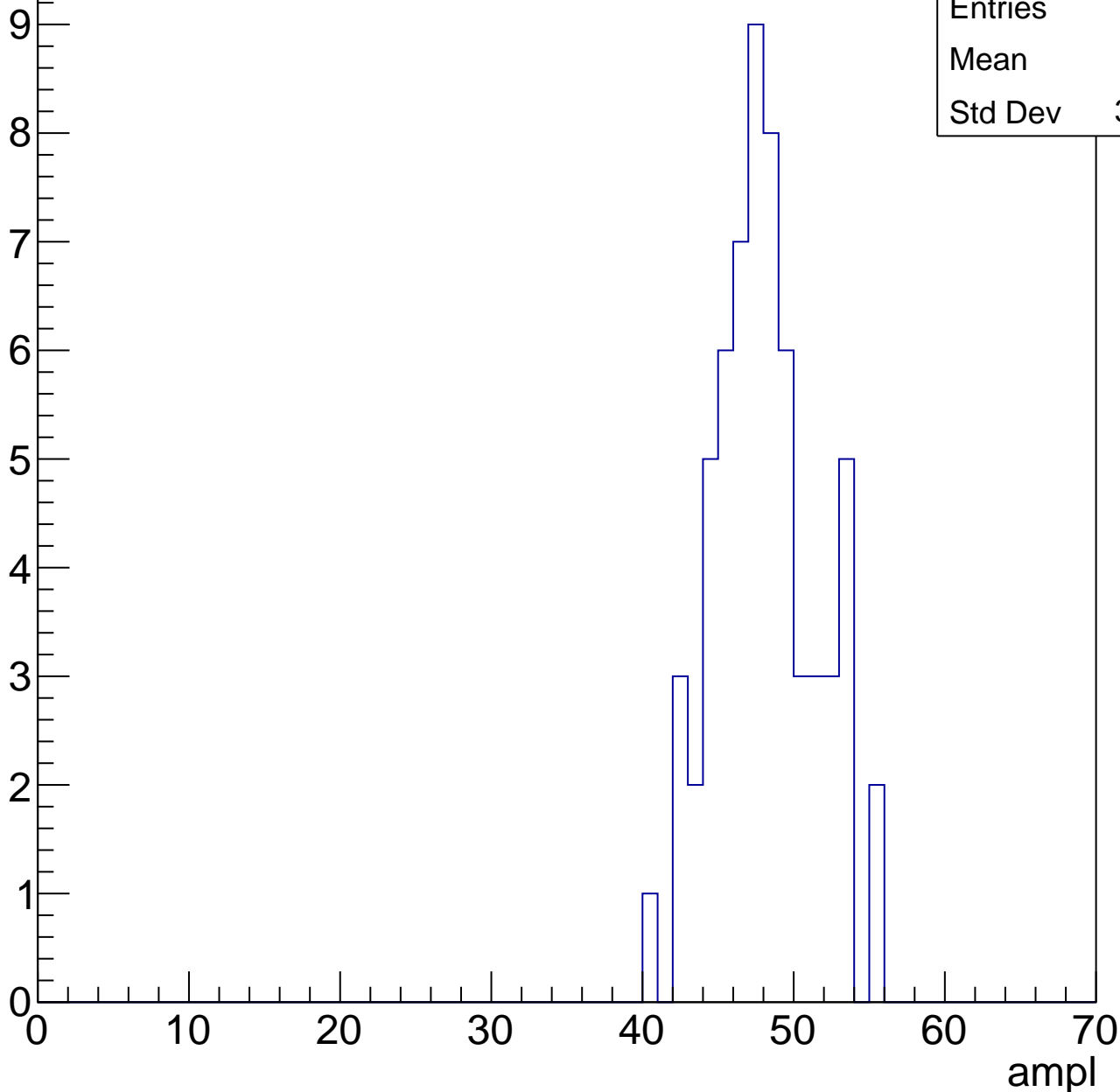


B1L100S, U1-ch22, adc3

calib_packv5_042523_0143.root, FC#4, port A2

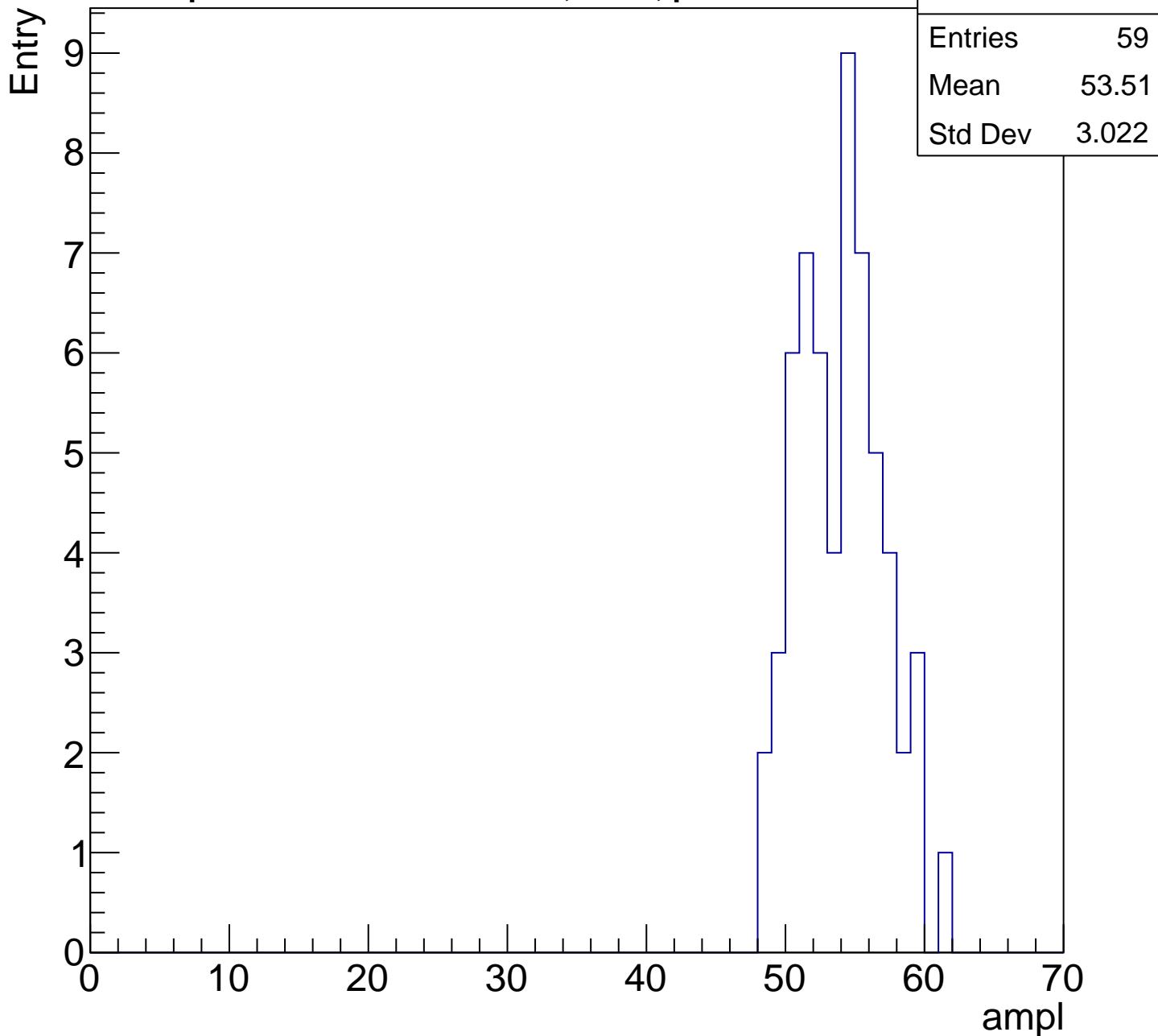
Entry

Entries	63
Mean	47.6
Std Dev	3.331



B1L100S, U1-ch22, adc4

calib_packv5_042523_0143.root, FC#4, port A2

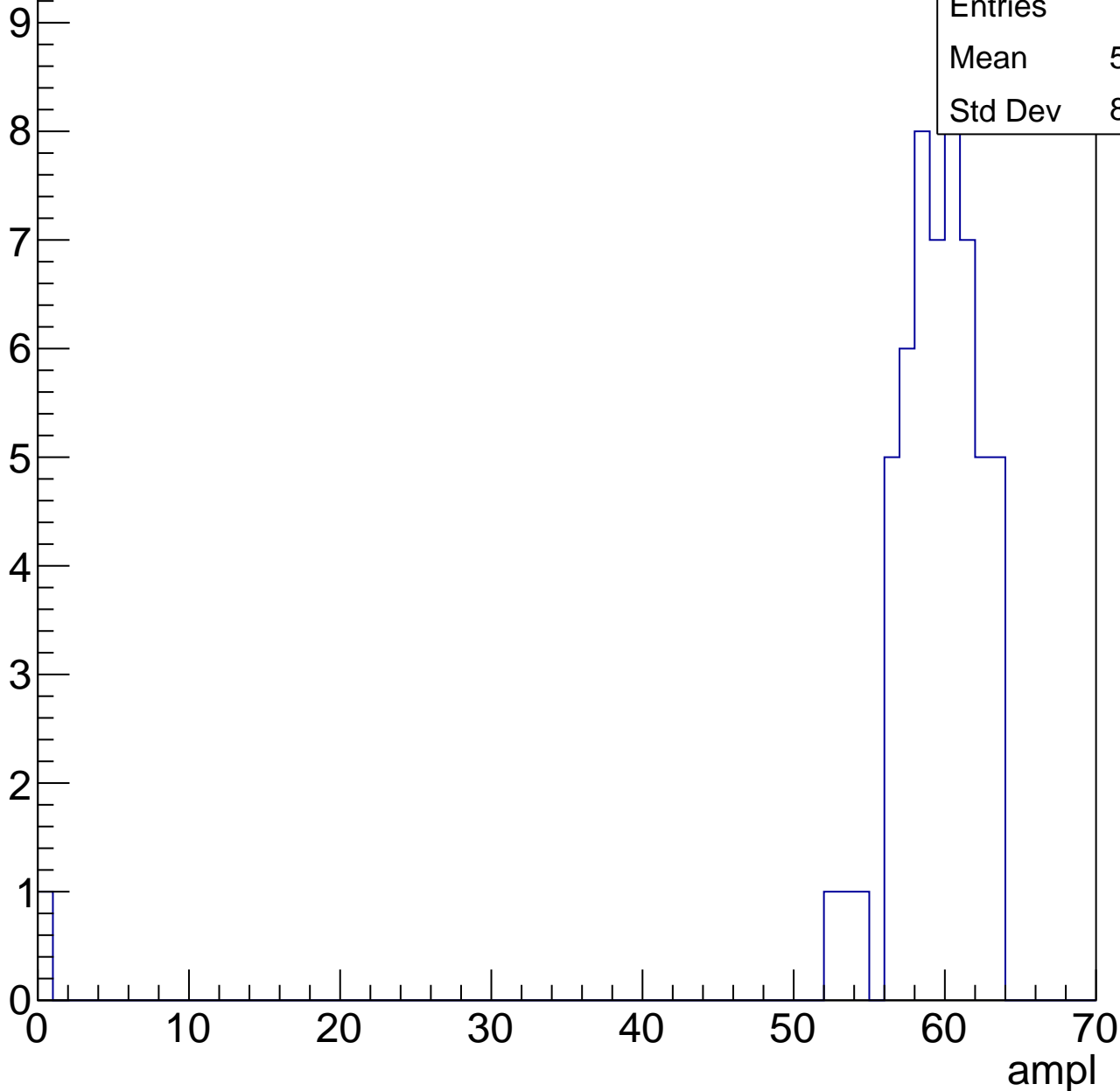


B1L100S, U1-ch22, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	58.04
Std Dev	8.214

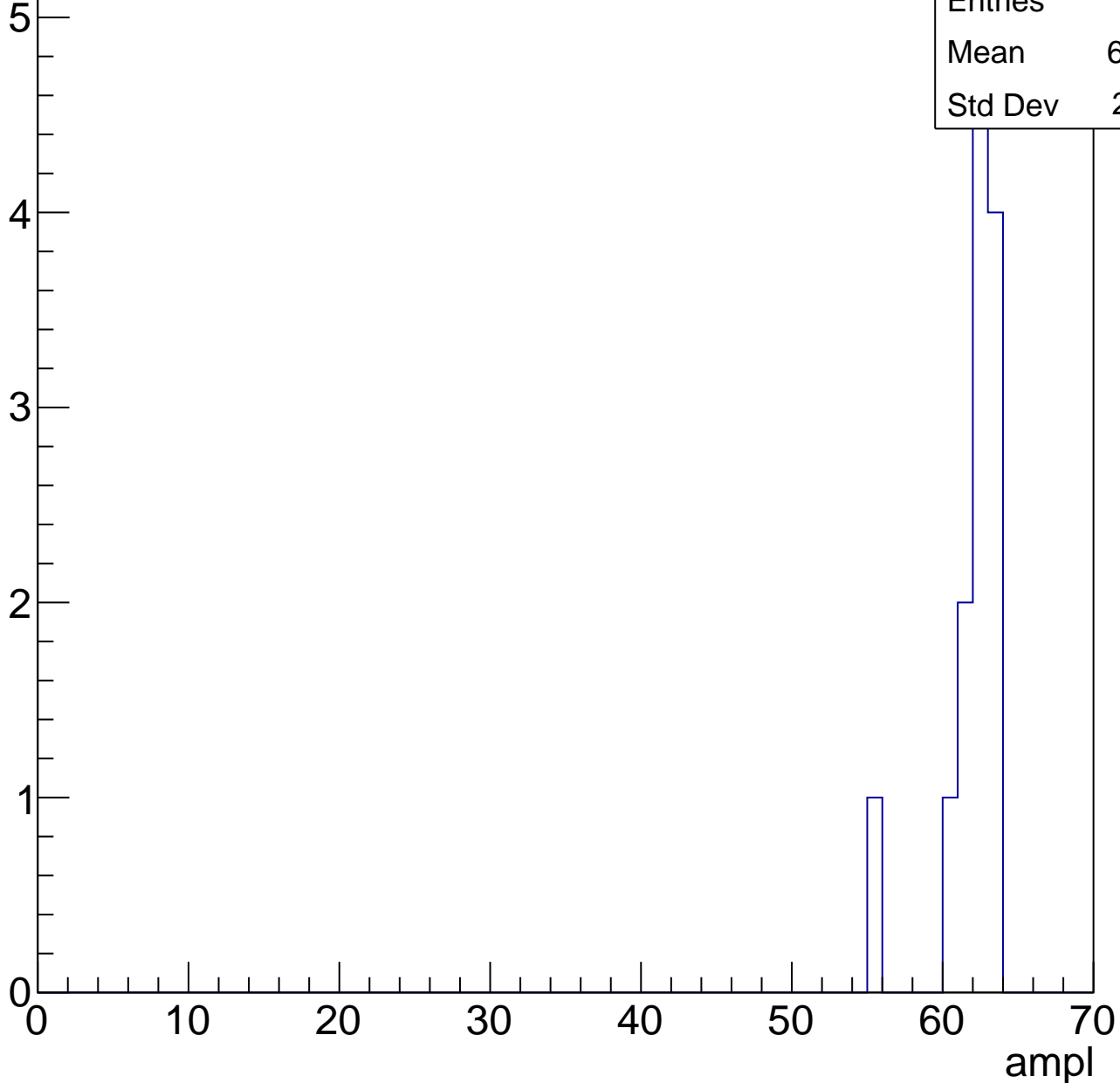


B1L100S, U1-ch22, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	61.46
Std Dev	2.061



B1L100S, U1-ch22, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch23, adc0

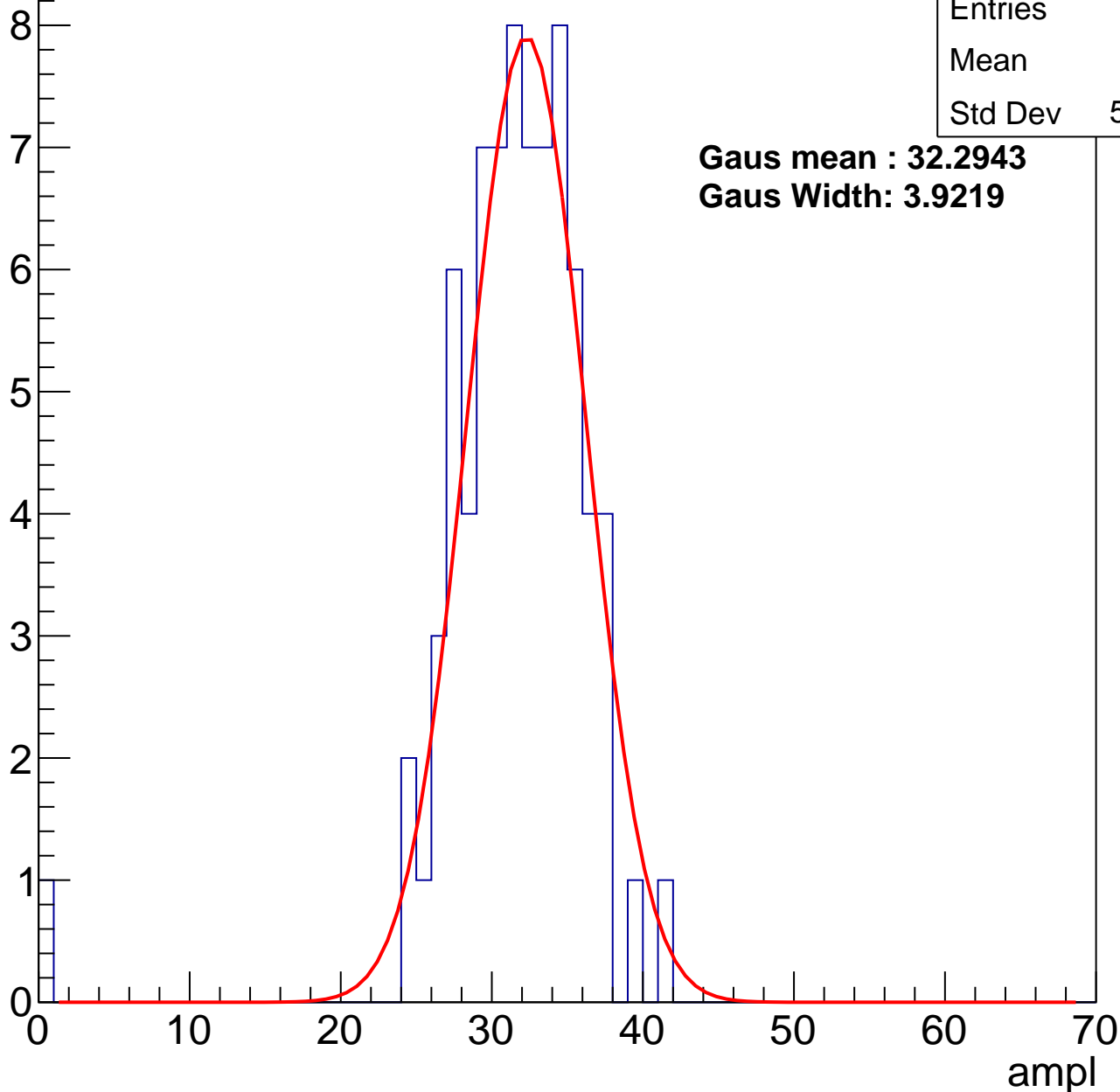
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	31.1
Std Dev	5.029

Gaus mean : 32.2943

Gaus Width: 3.9219



B1L100S, U1-ch23, adc1

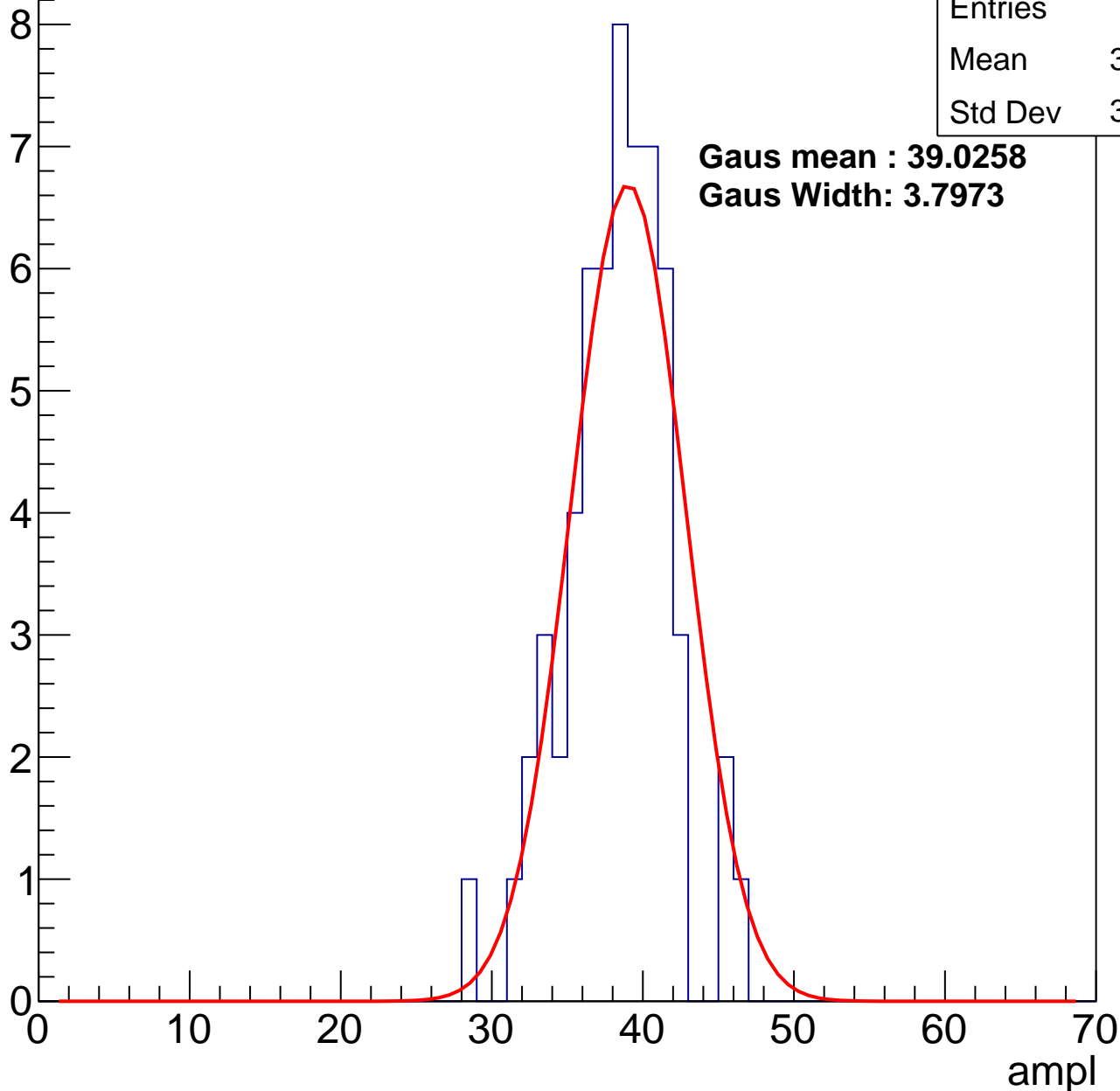
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	37.85
Std Dev	3.424

Gaus mean : 39.0258

Gaus Width: 3.7973



B1L100S, U1-ch23, adc2

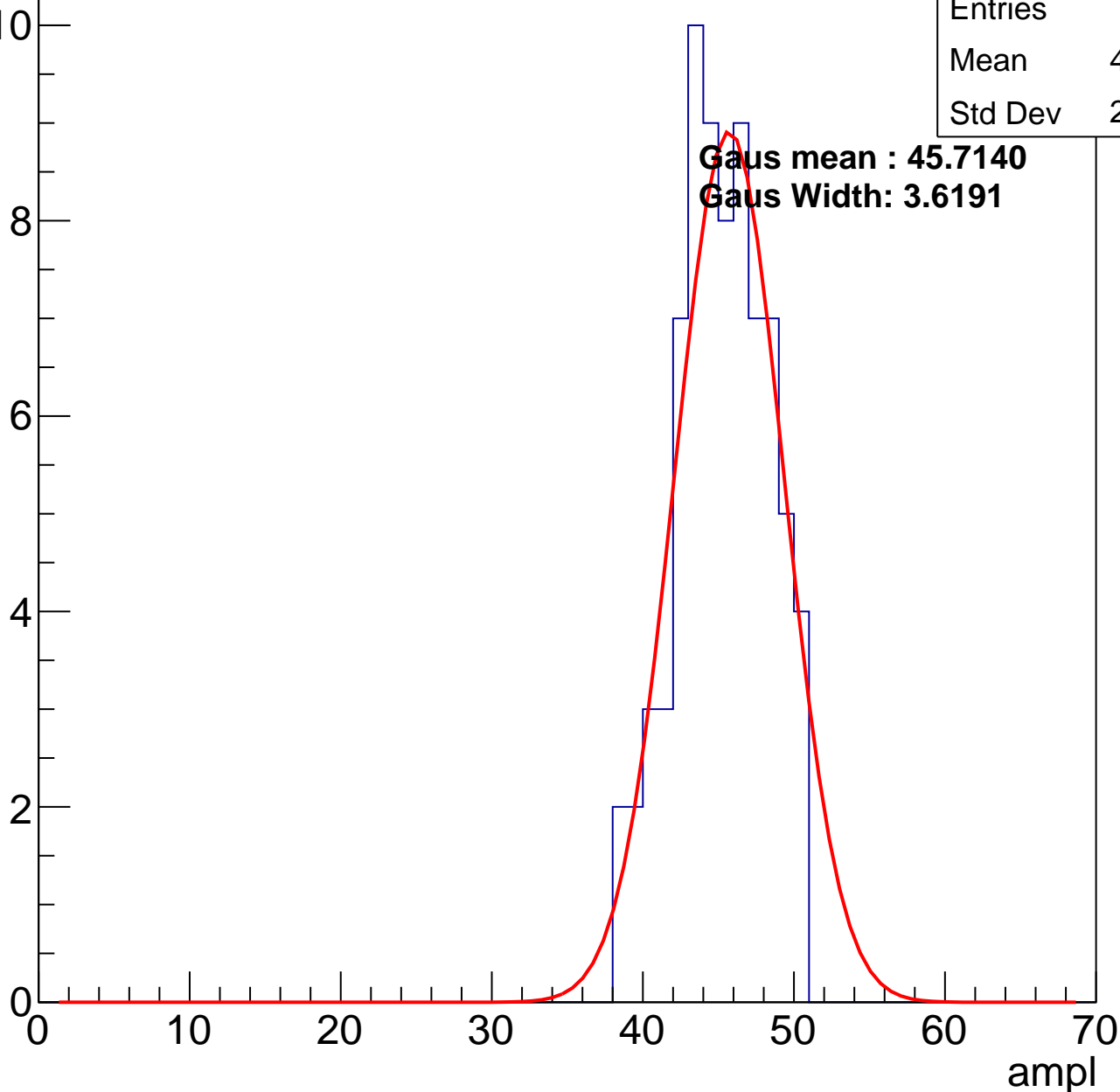
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	44.75
Std Dev	2.992

Gaus mean : 45.7140

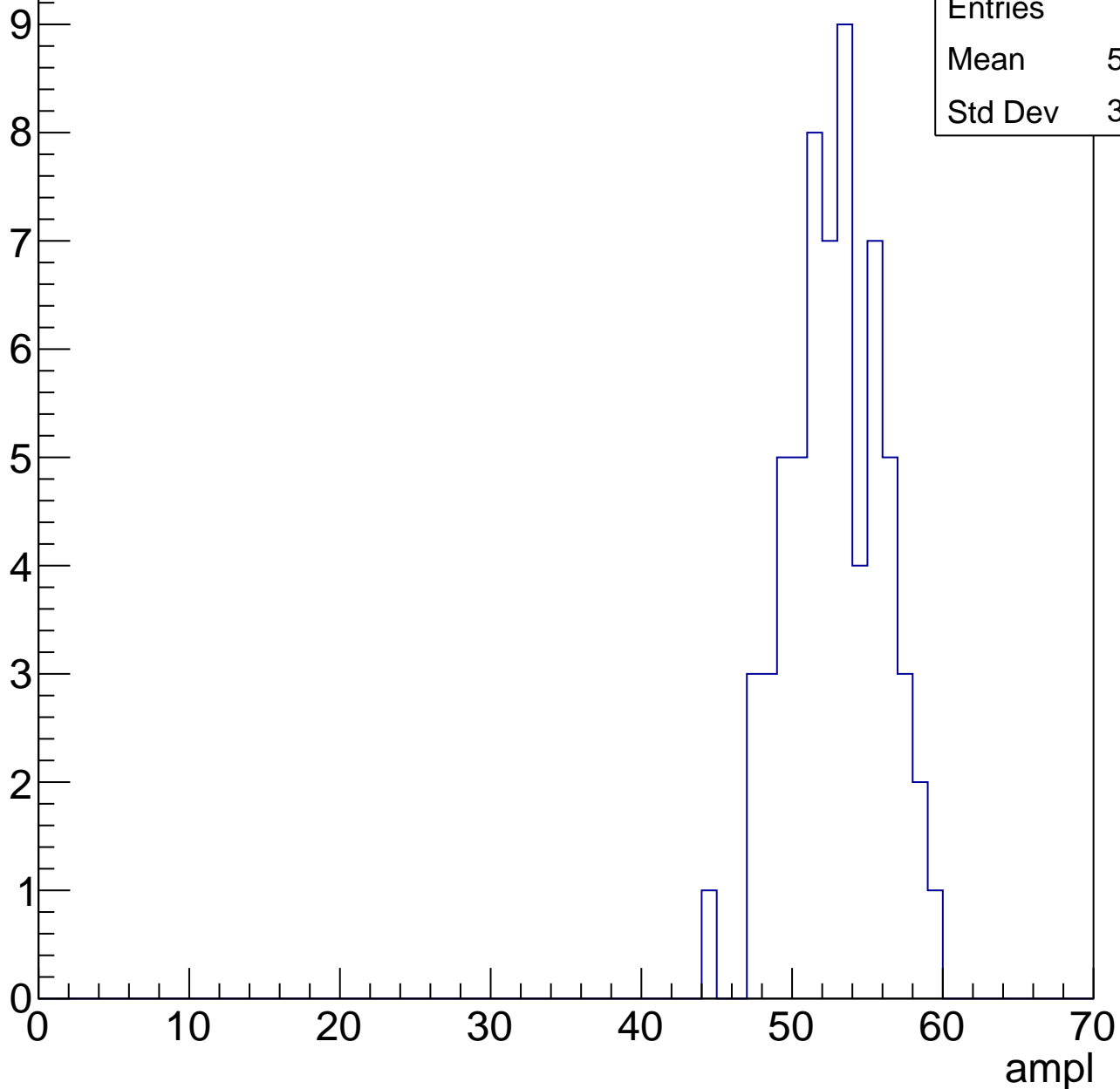
Gaus Width: 3.6191



B1L100S, U1-ch23, adc3

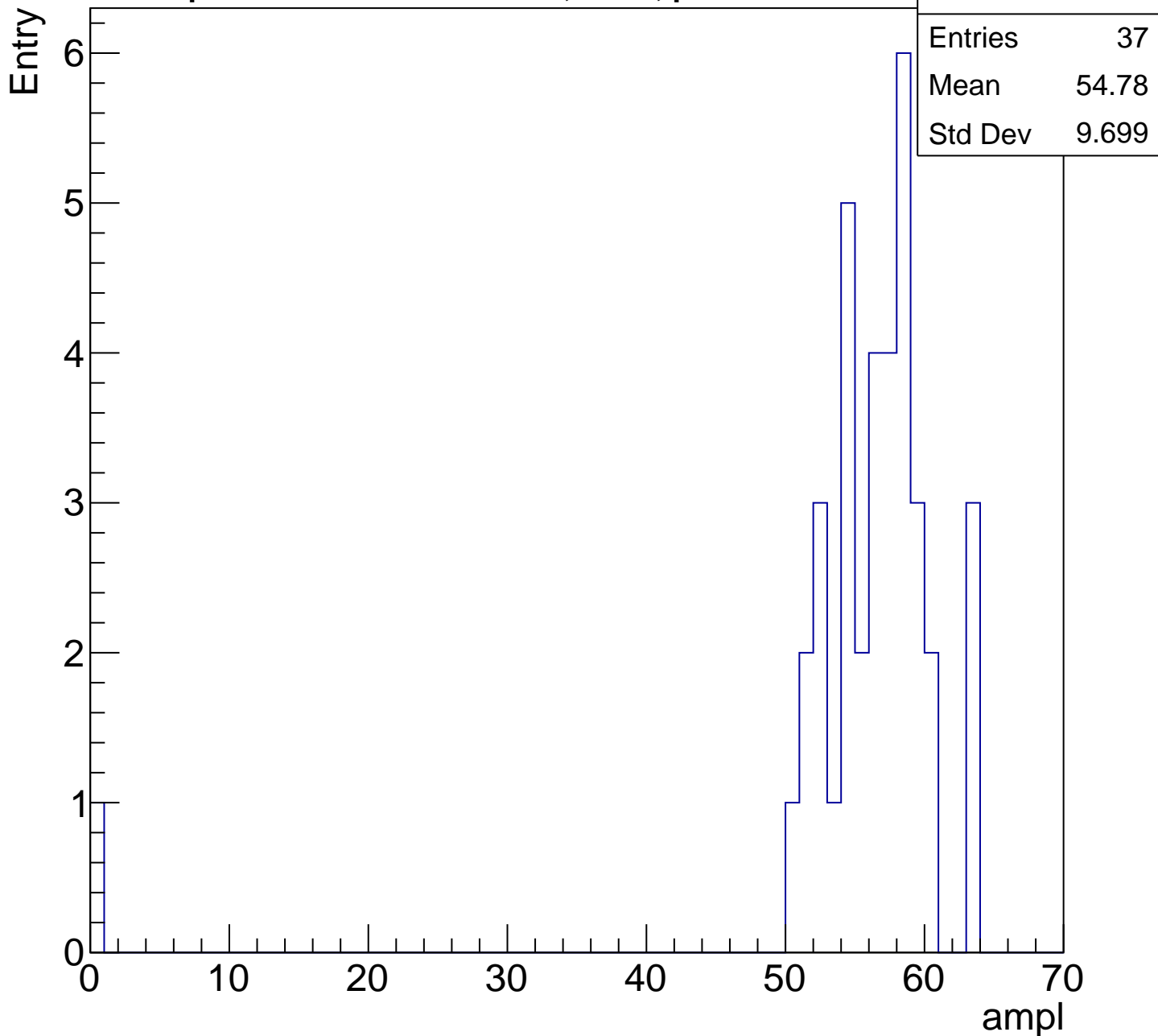
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch23, adc4

calib_packv5_042523_0143.root, FC#4, port A2

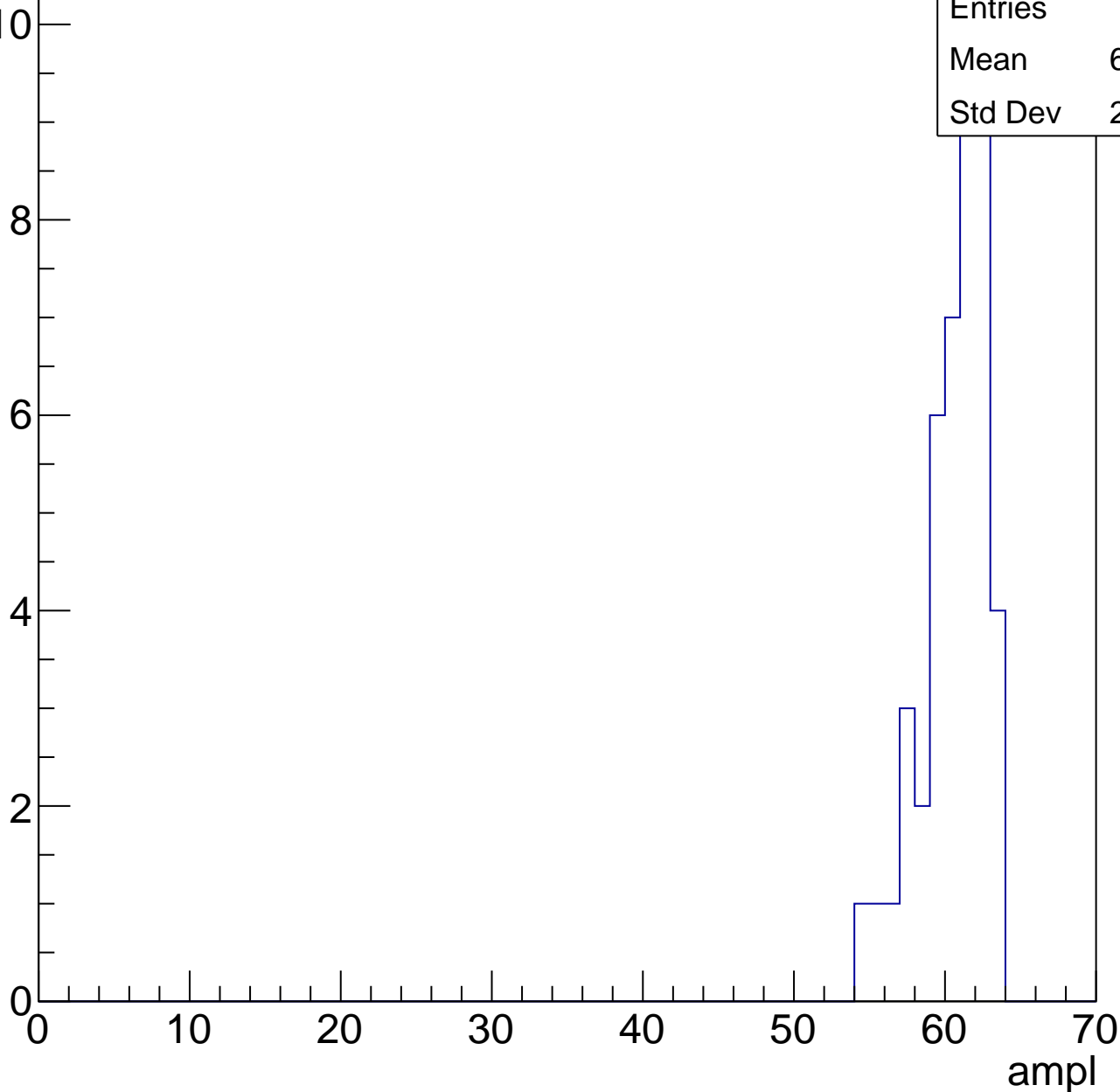


B1L100S, U1-ch23, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	60.18
Std Dev	2.122



B1L100S, U1-ch23, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch23, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch24, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	27.69
Std Dev	7.284

Gaus mean : 29.6072

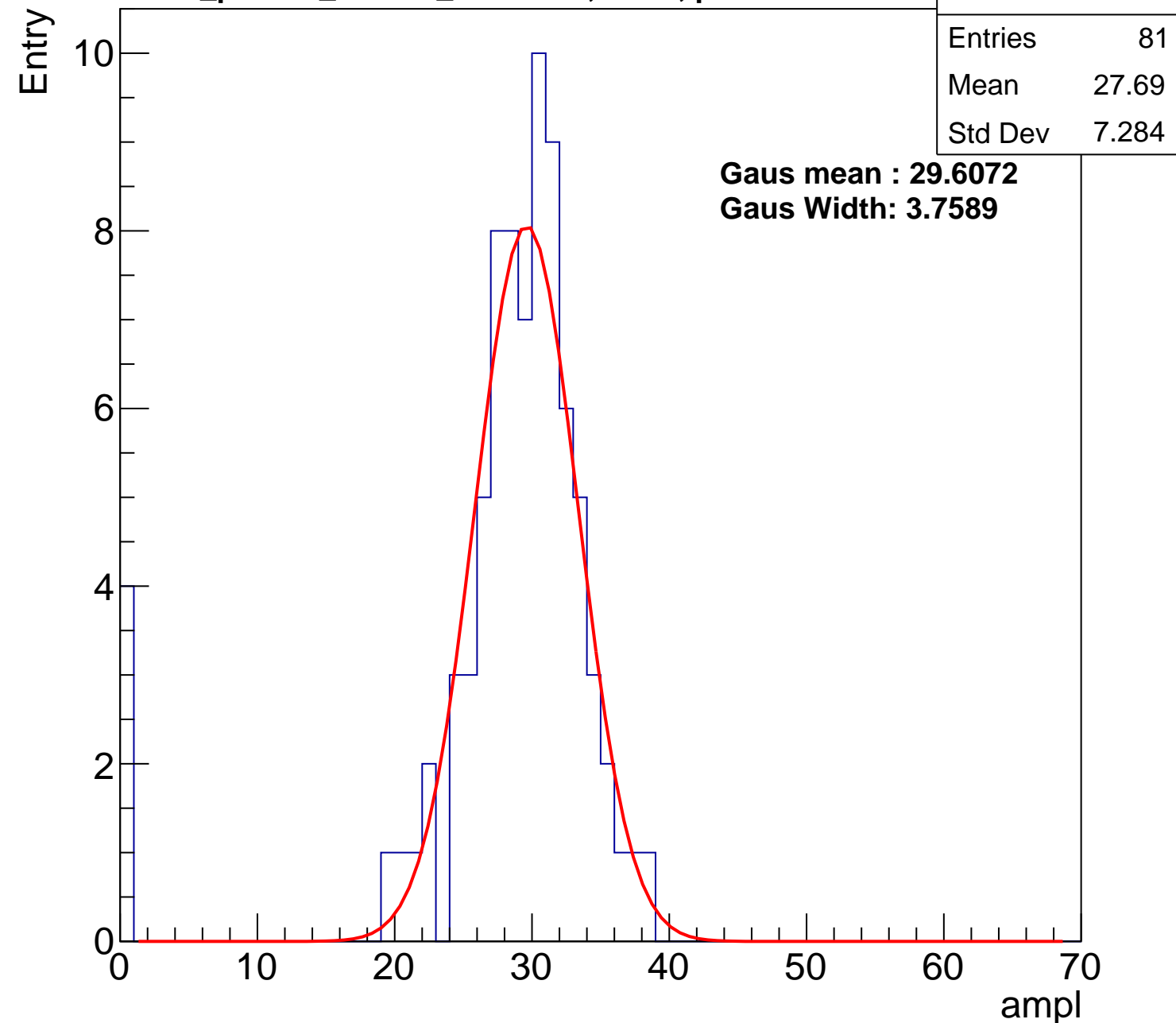
Gaus Width: 3.7589

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch24, adc1

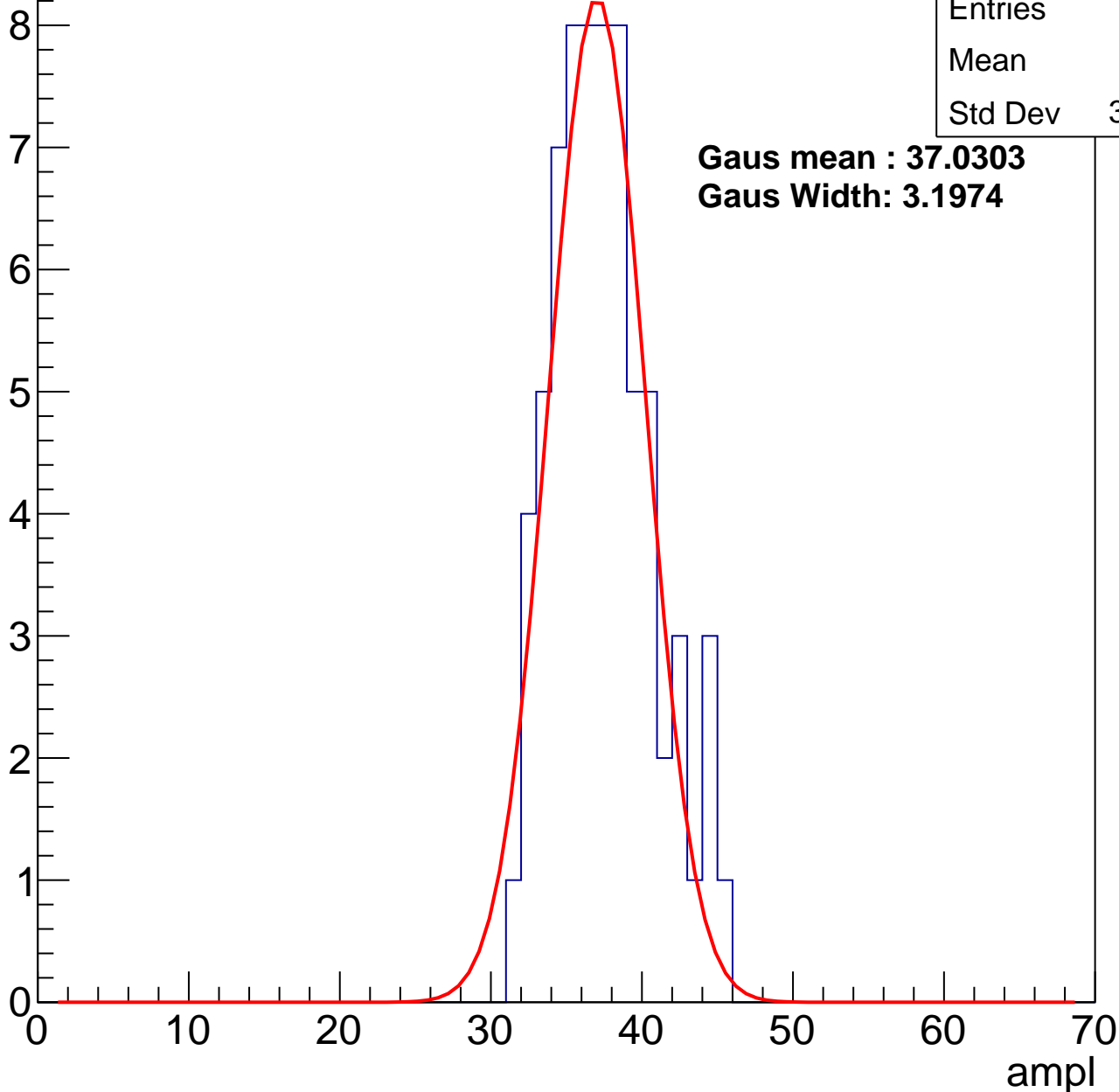
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	37
Std Dev	3.288

Gaus mean : 37.0303

Gaus Width: 3.1974



B1L100S, U1-ch24, adc2

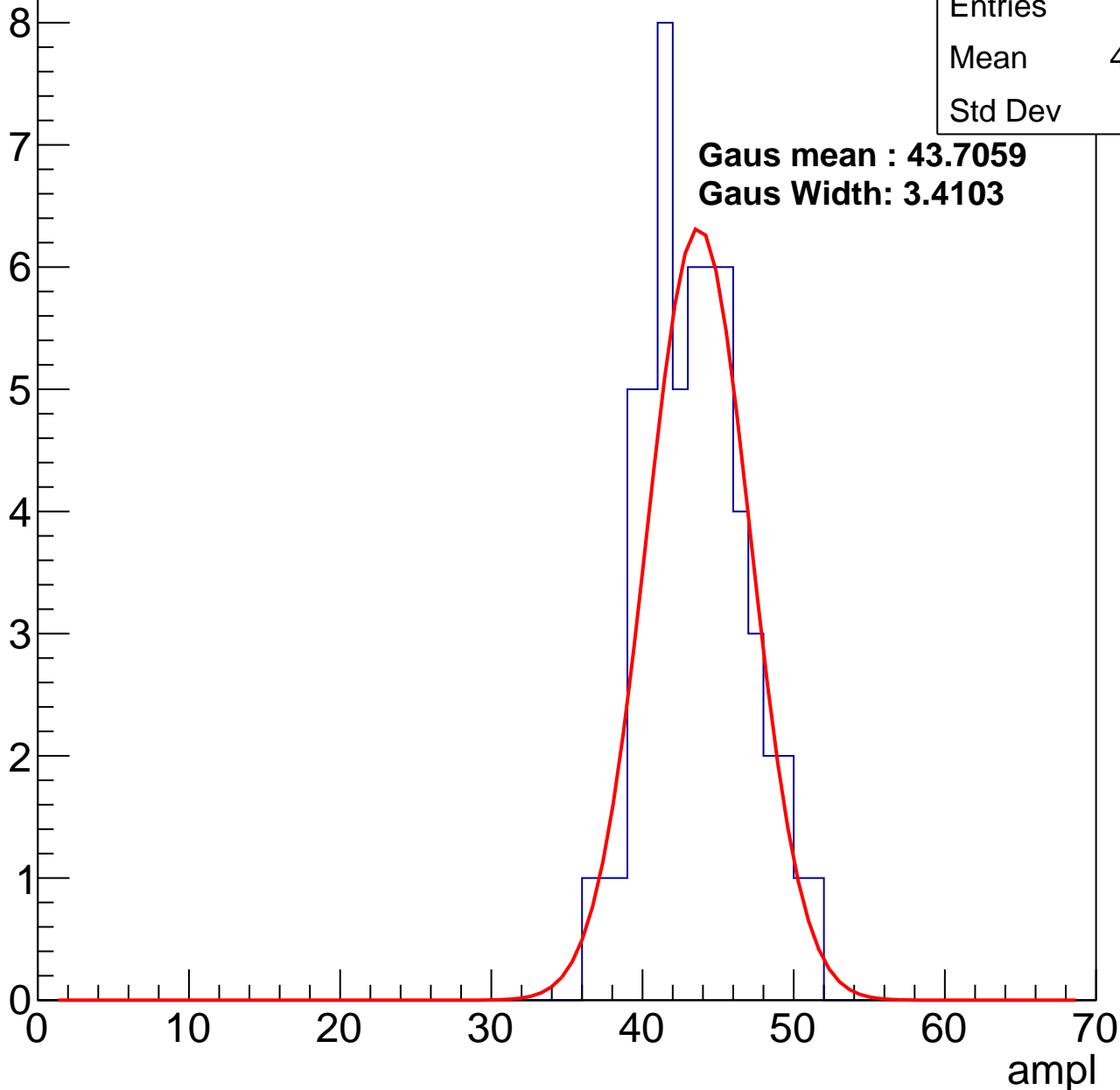
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	43.09
Std Dev	3.3

Gaus mean : 43.7059

Gaus Width: 3.4103

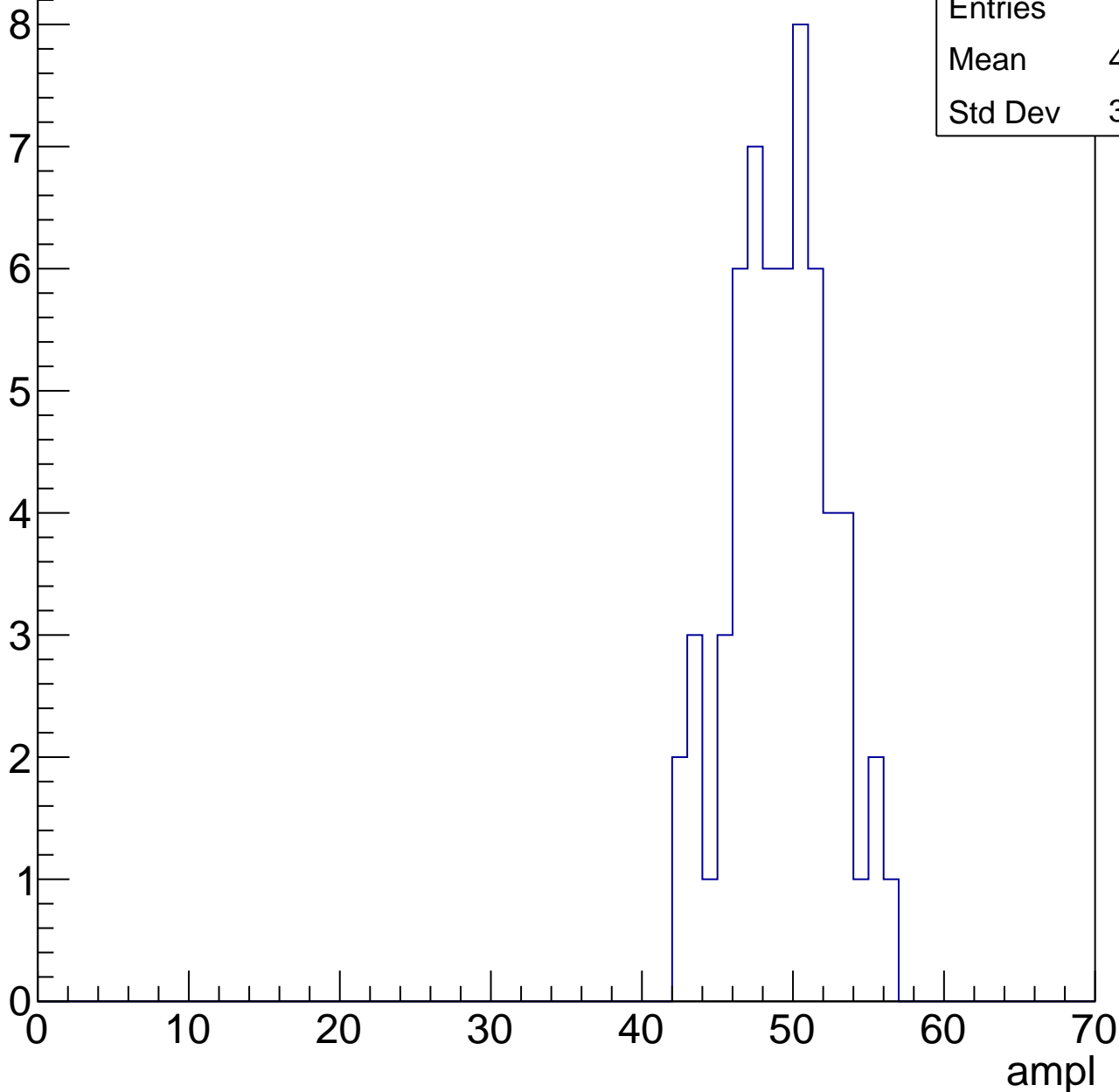


B1L100S, U1-ch24, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	48.75
Std Dev	3.269

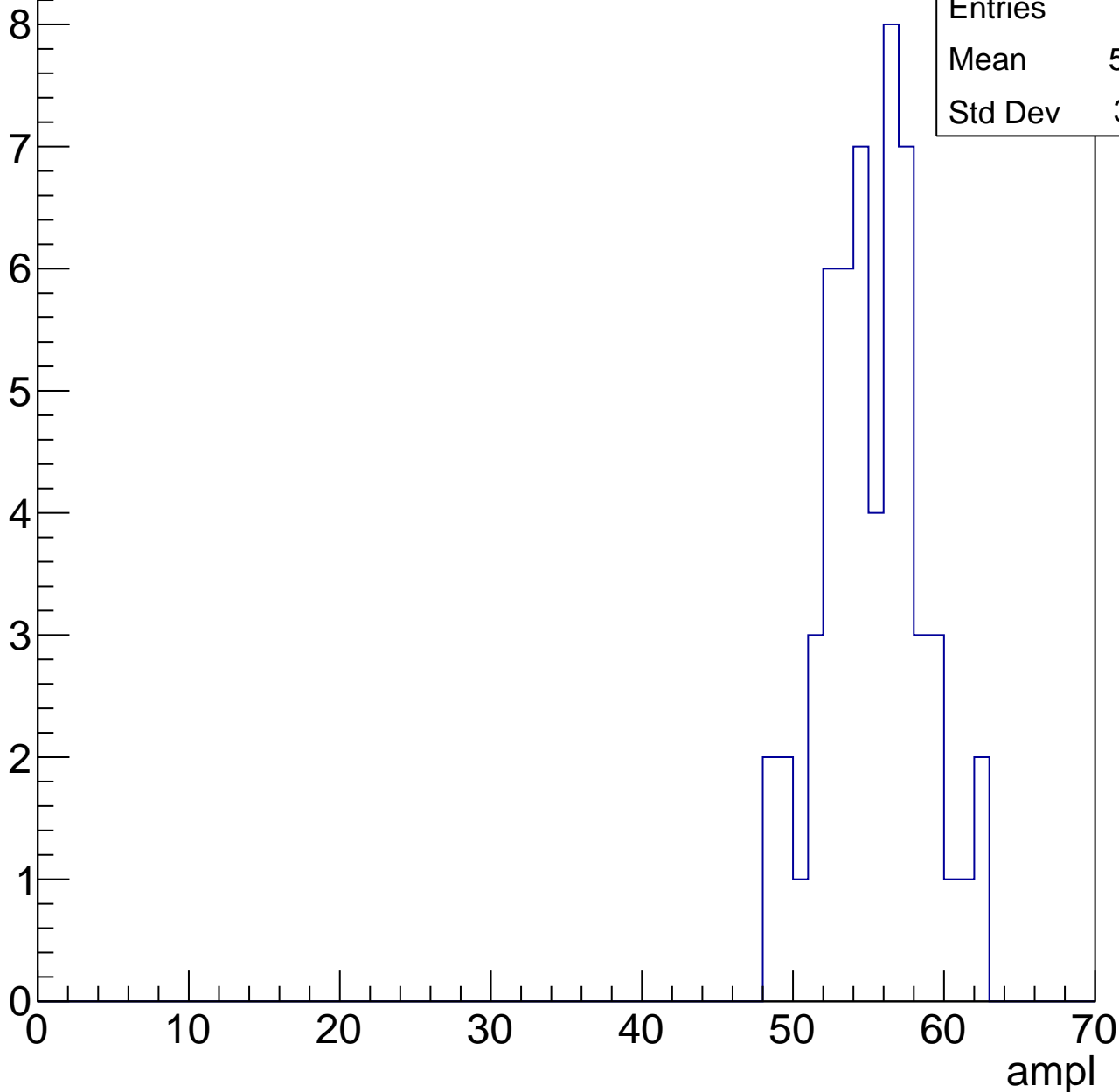


B1L100S, U1-ch24, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	54.79
Std Dev	3.261



B1L100S, U1-ch24, adc5

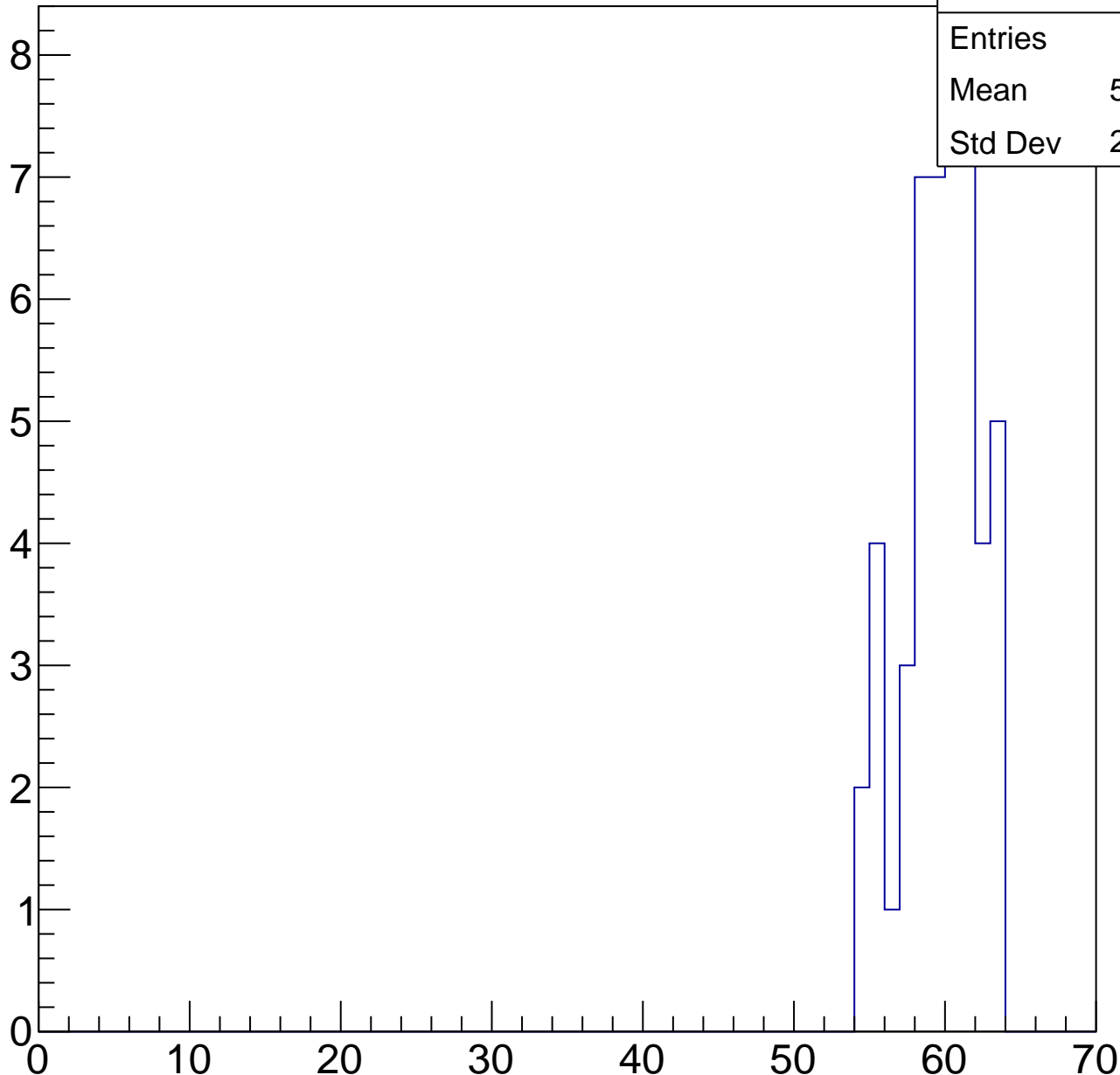
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	49
Mean	59.29
Std Dev	2.449

ampl

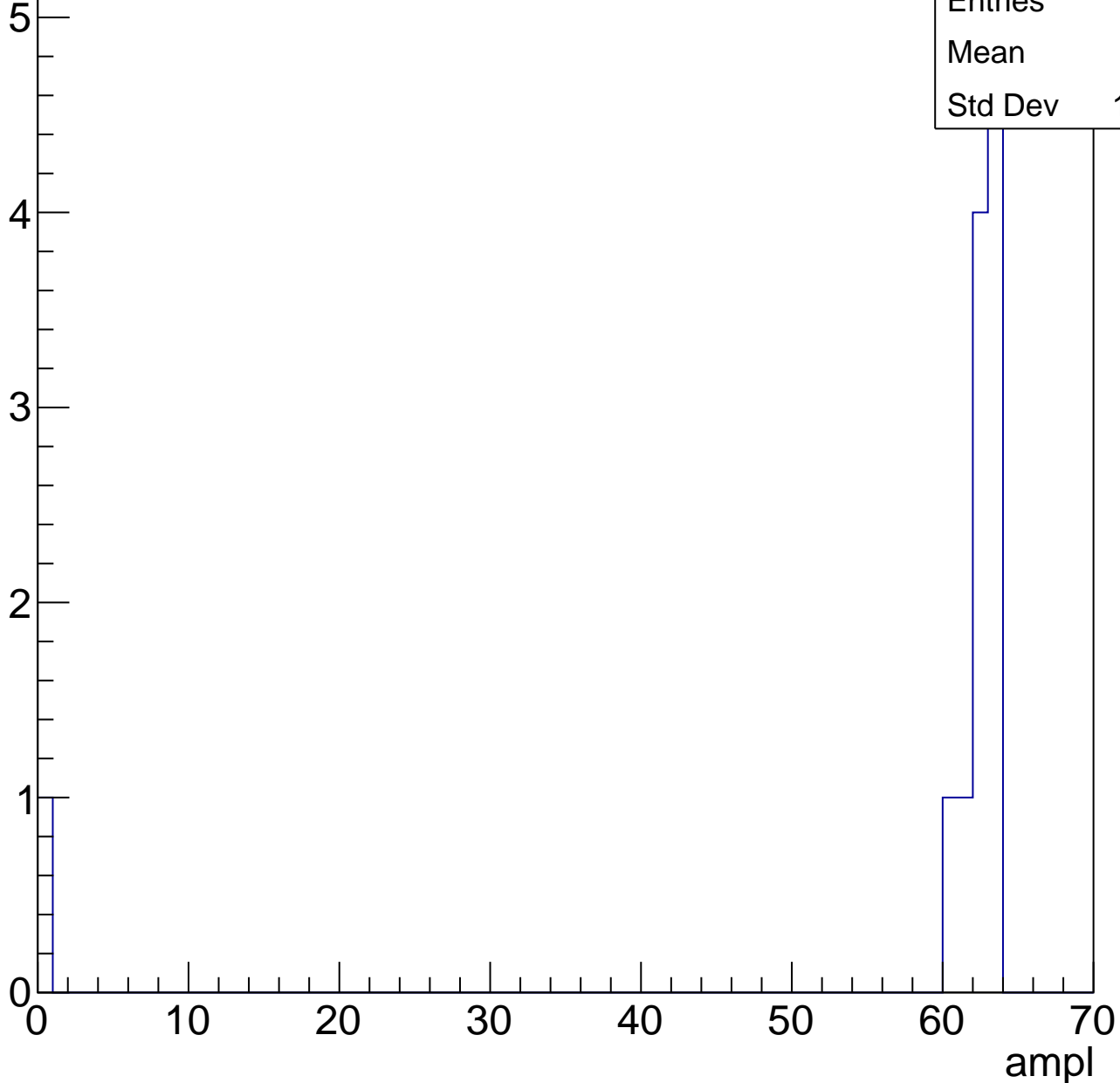


B1L100S, U1-ch24, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	57
Std Dev	17.21



B1L100S, U1-ch24, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch25, adc0

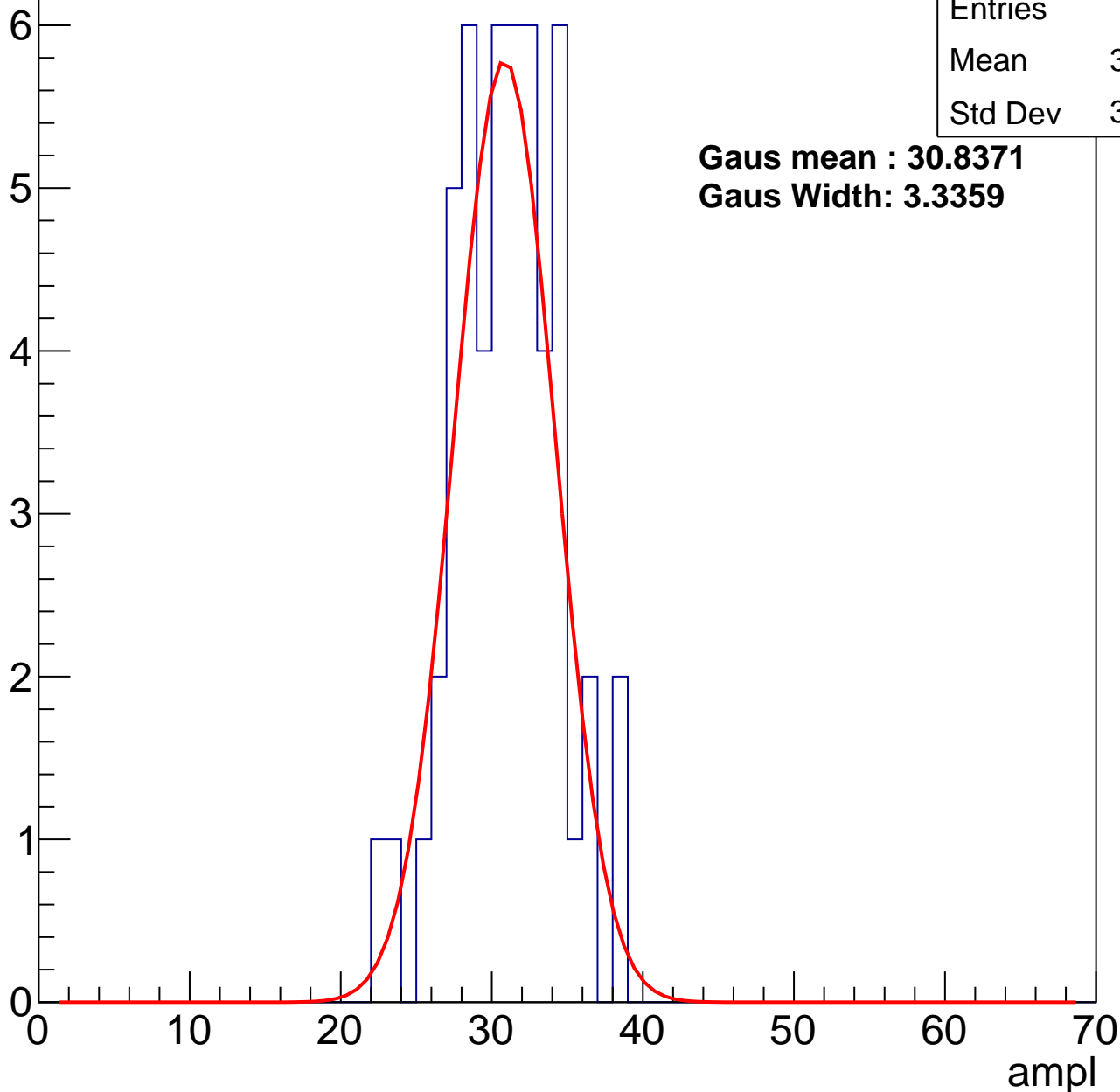
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	30.53
Std Dev	3.407

Gaus mean : 30.8371

Gaus Width: 3.3359



B1L100S, U1-ch25, adc1

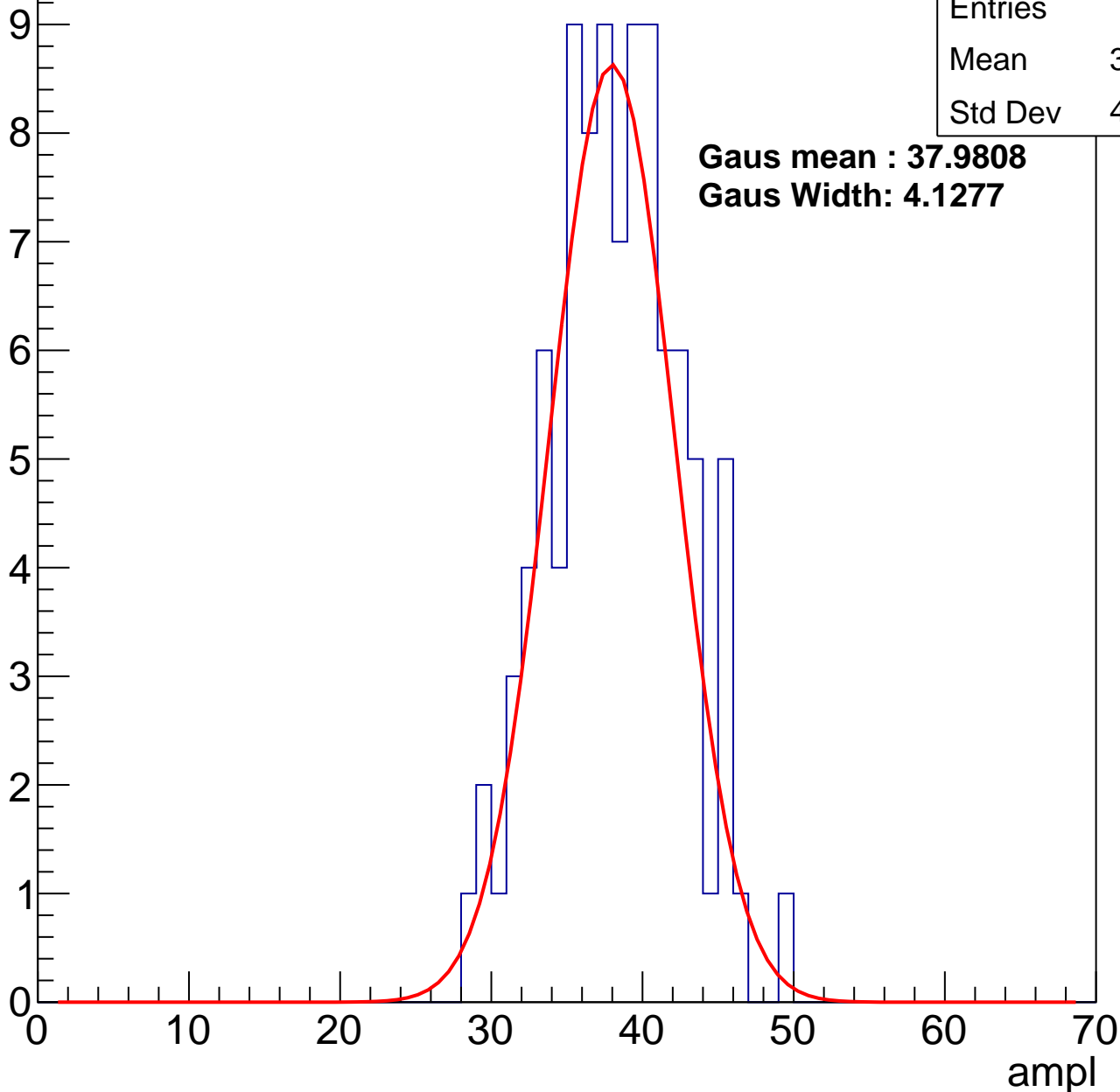
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	97
Mean	37.74
Std Dev	4.226

Gaus mean : 37.9808

Gaus Width: 4.1277



B1L100S, U1-ch25, adc2

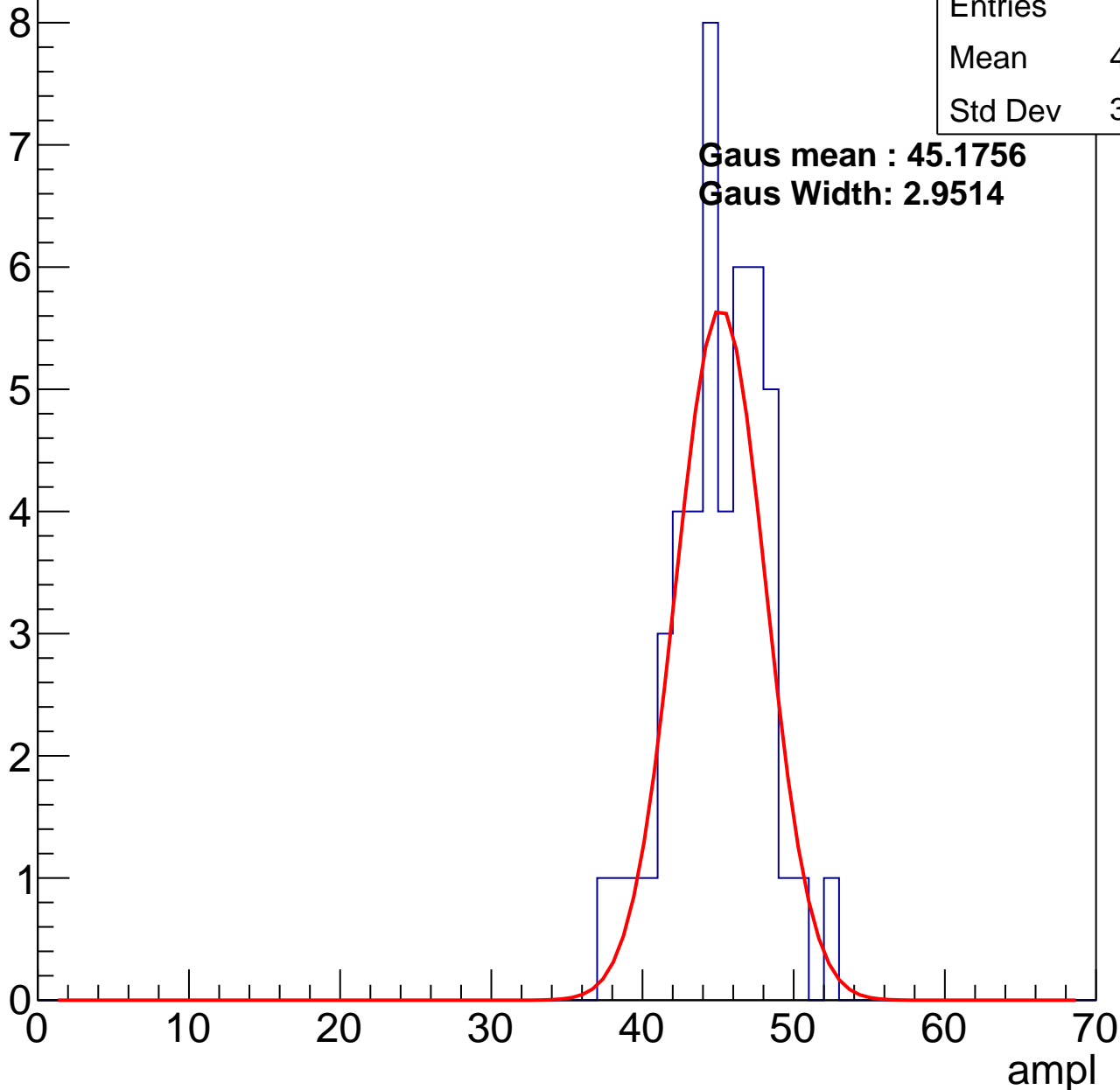
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	44.64
Std Dev	3.063

Gaus mean : 45.1756

Gaus Width: 2.9514

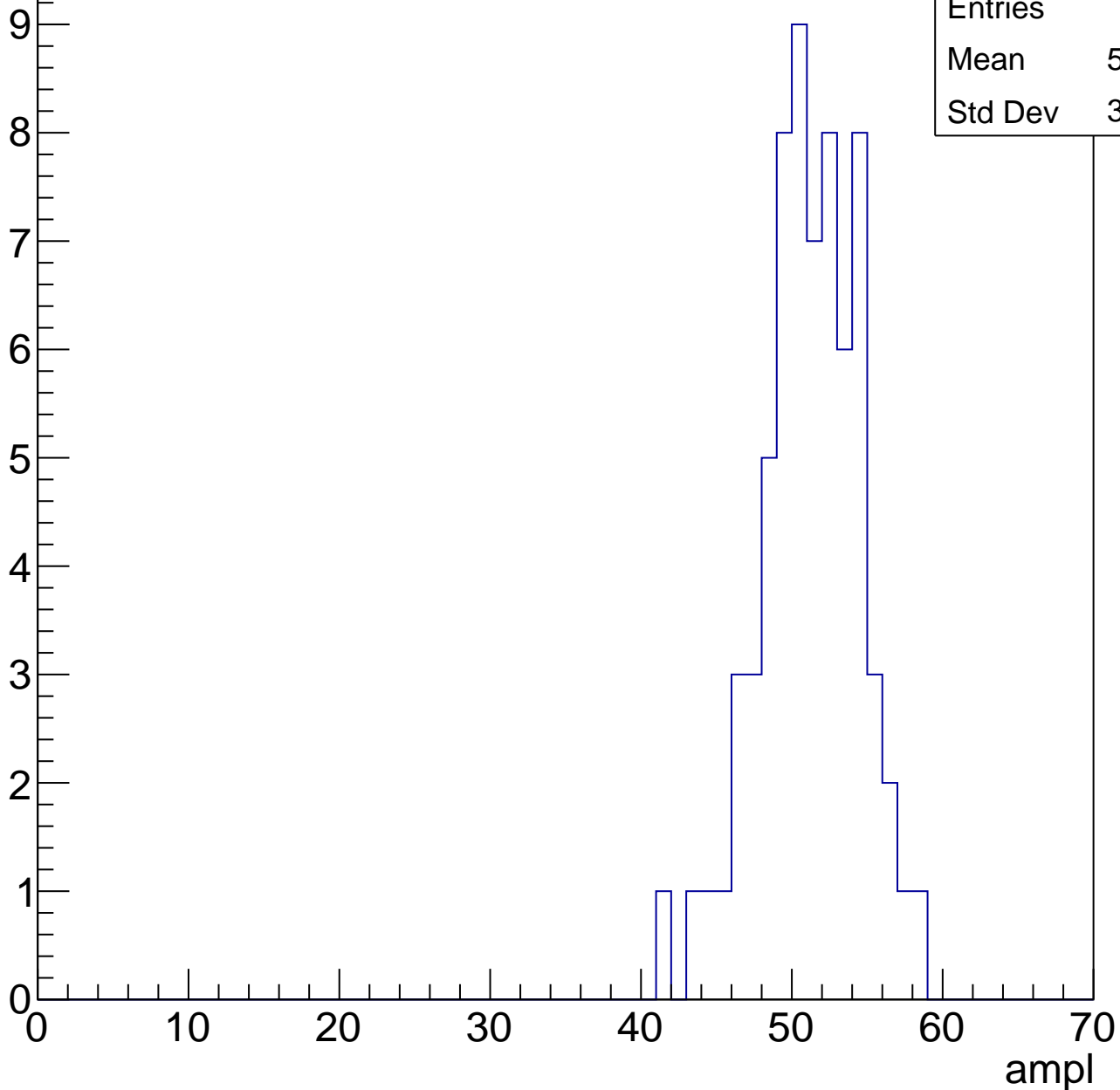


B1L100S, U1-ch25, adc3

calib_packv5_042523_0143.root, FC#4, port A2

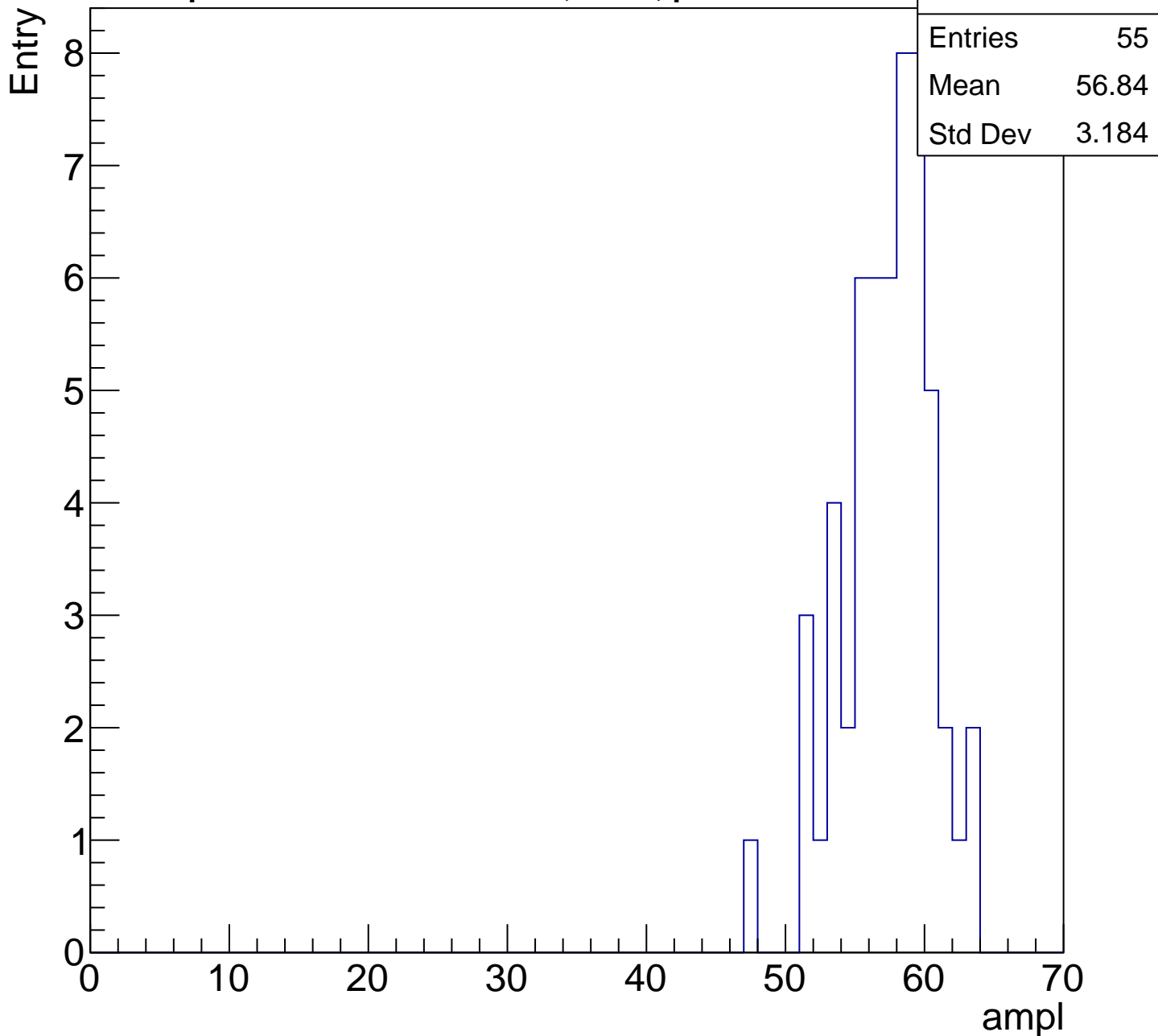
Entry

Entries	68
Mean	50.72
Std Dev	3.298



B1L100S, U1-ch25, adc4

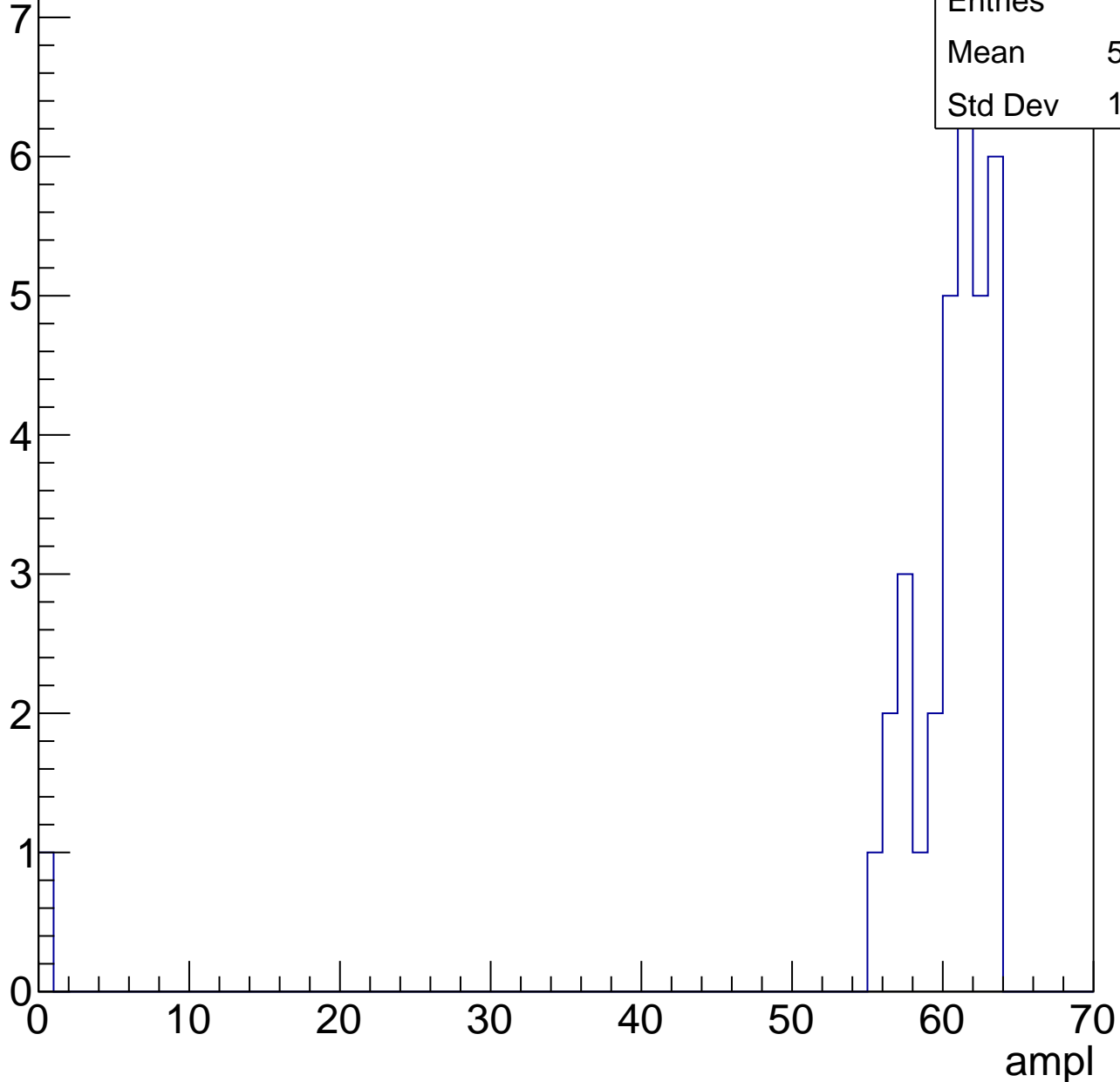
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch25, adc5

calib_packv5_042523_0143.root, FC#4, port A2

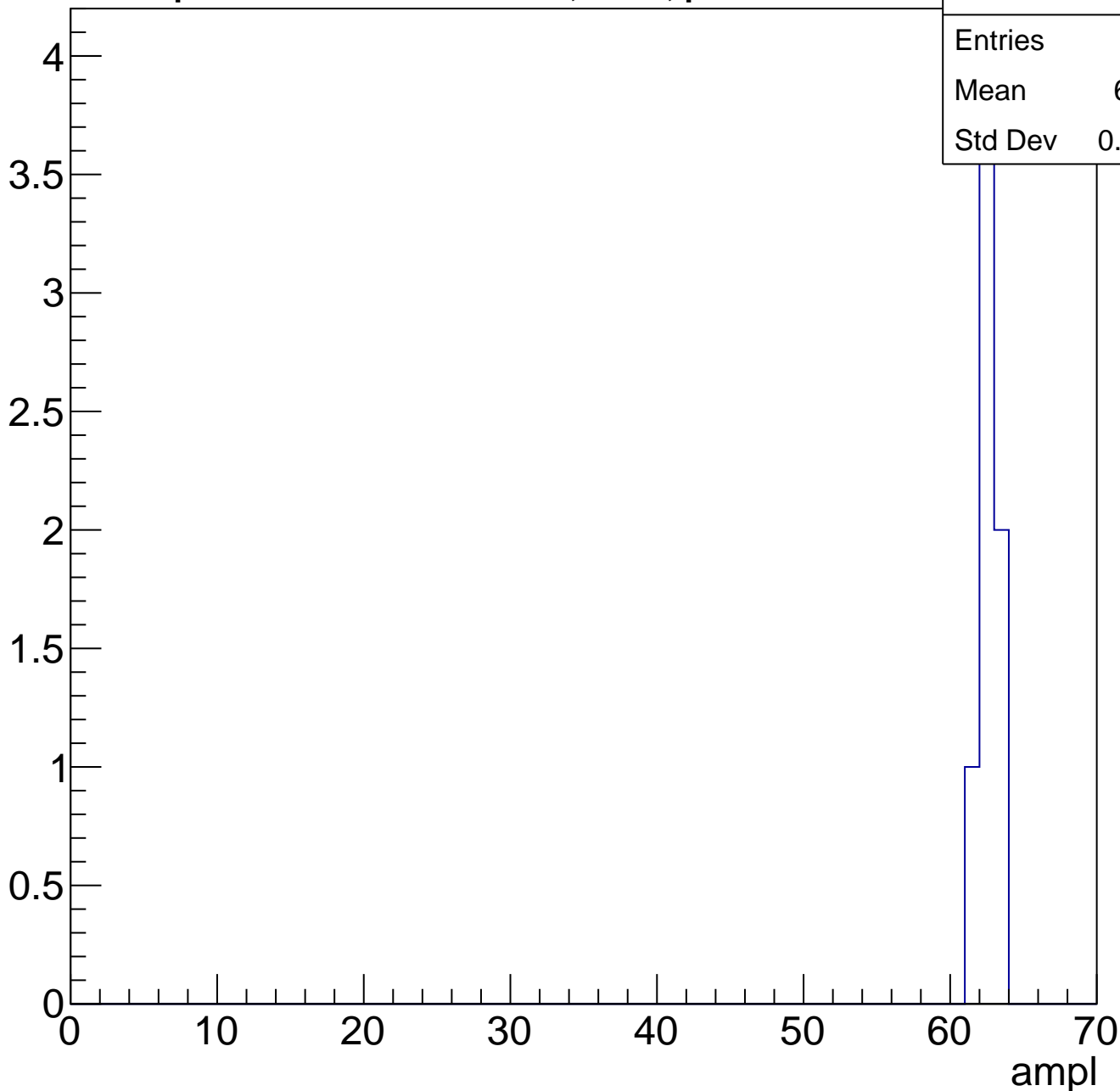
Entry



B1L100S, U1-ch25, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch25, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch26, adc0

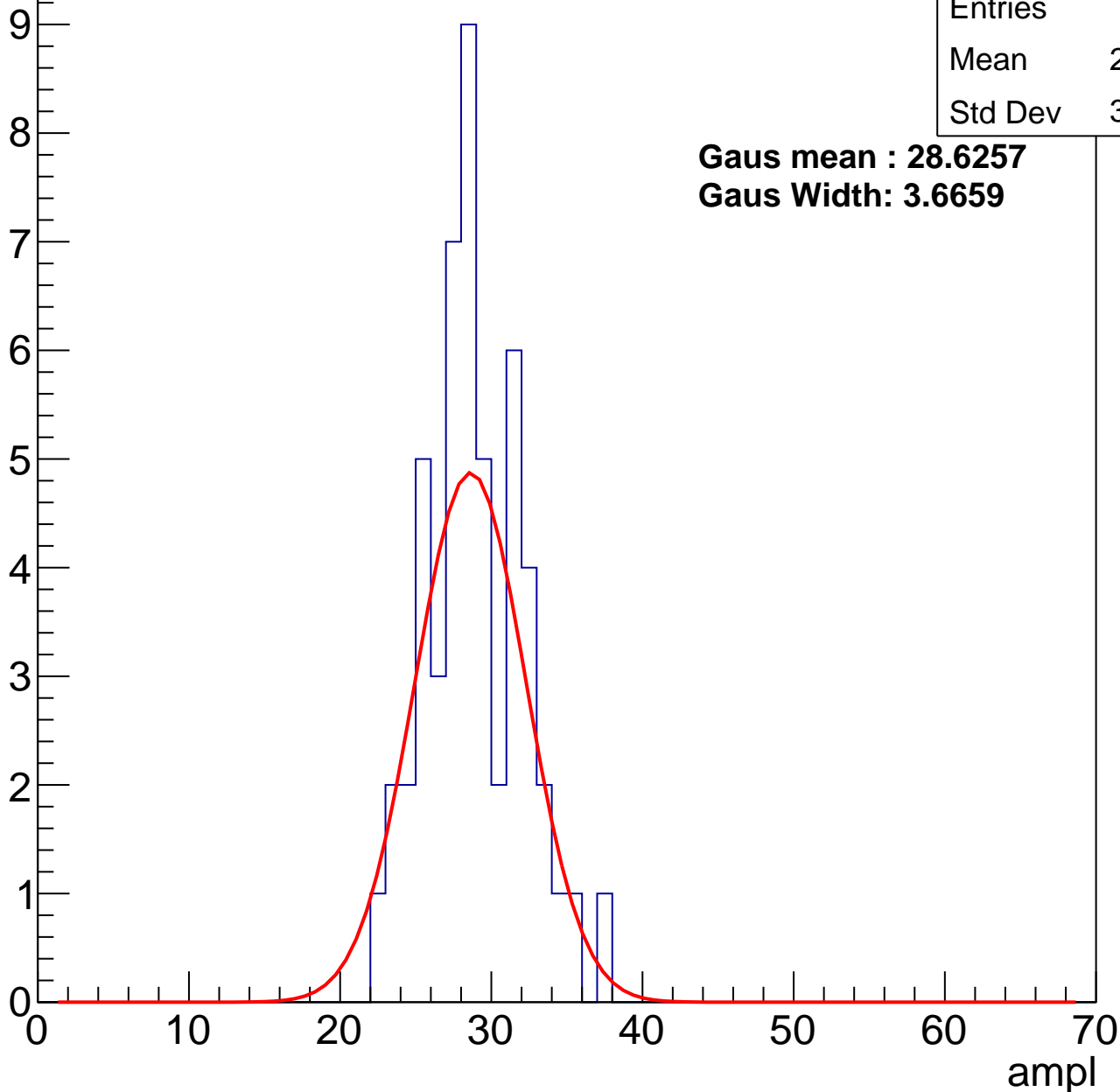
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.45
Std Dev	3.189

Gaus mean : 28.6257

Gaus Width: 3.6659



B1L100S, U1-ch26, adc1

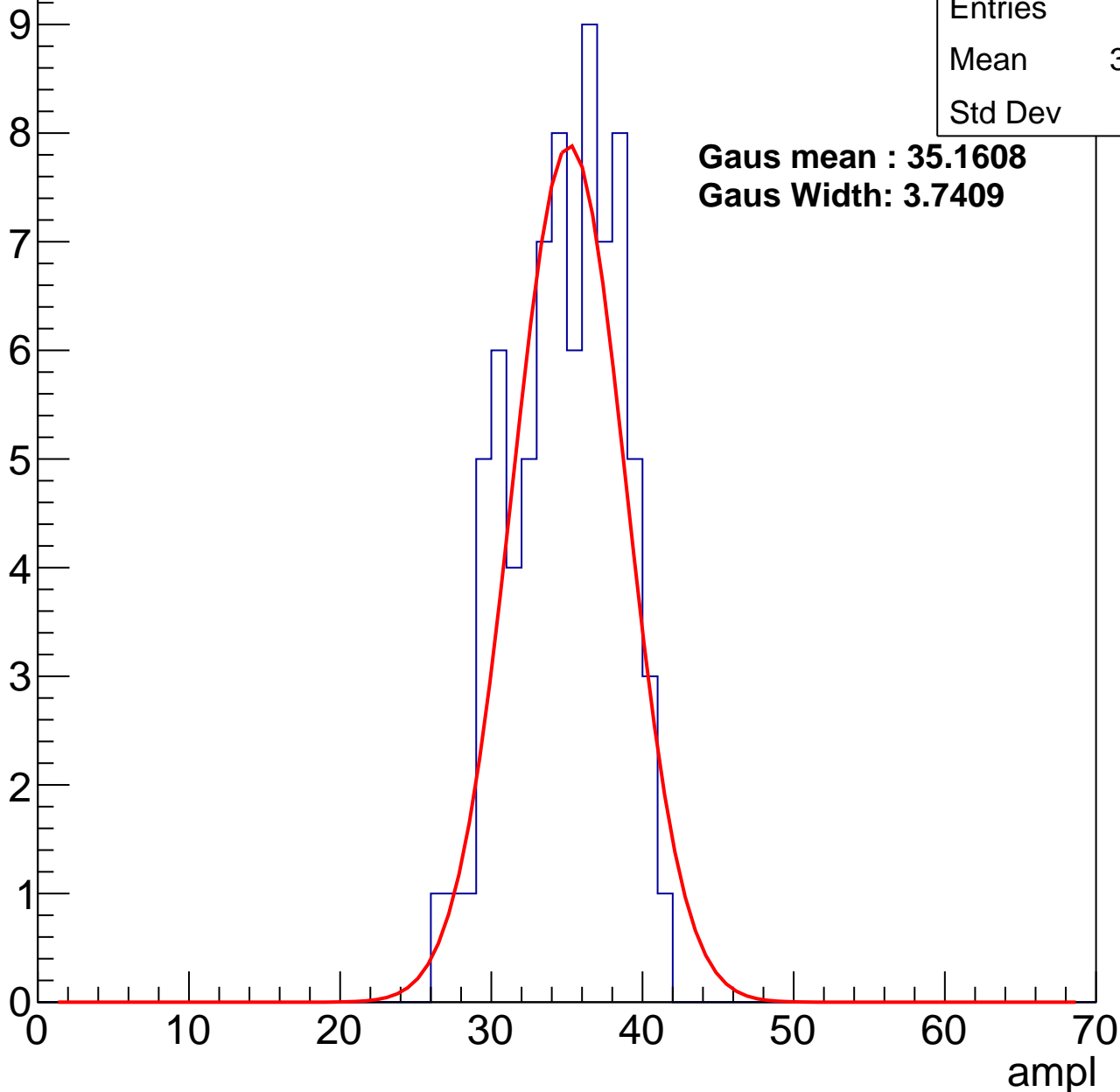
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	34.36
Std Dev	3.49

Gaus mean : 35.1608

Gaus Width: 3.7409



B1L100S, U1-ch26, adc2

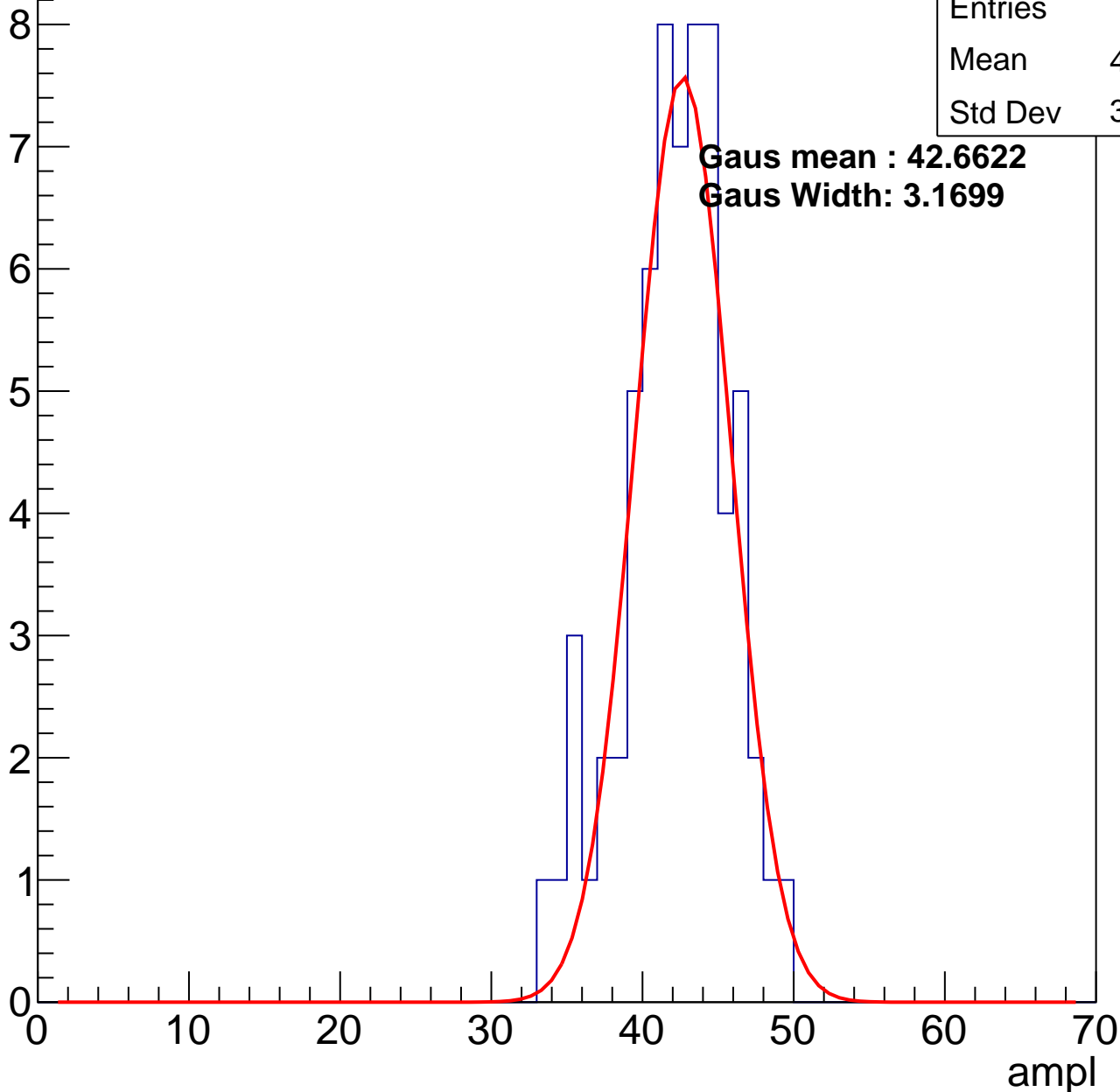
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.72
Std Dev	3.449

Gaus mean : 42.6622

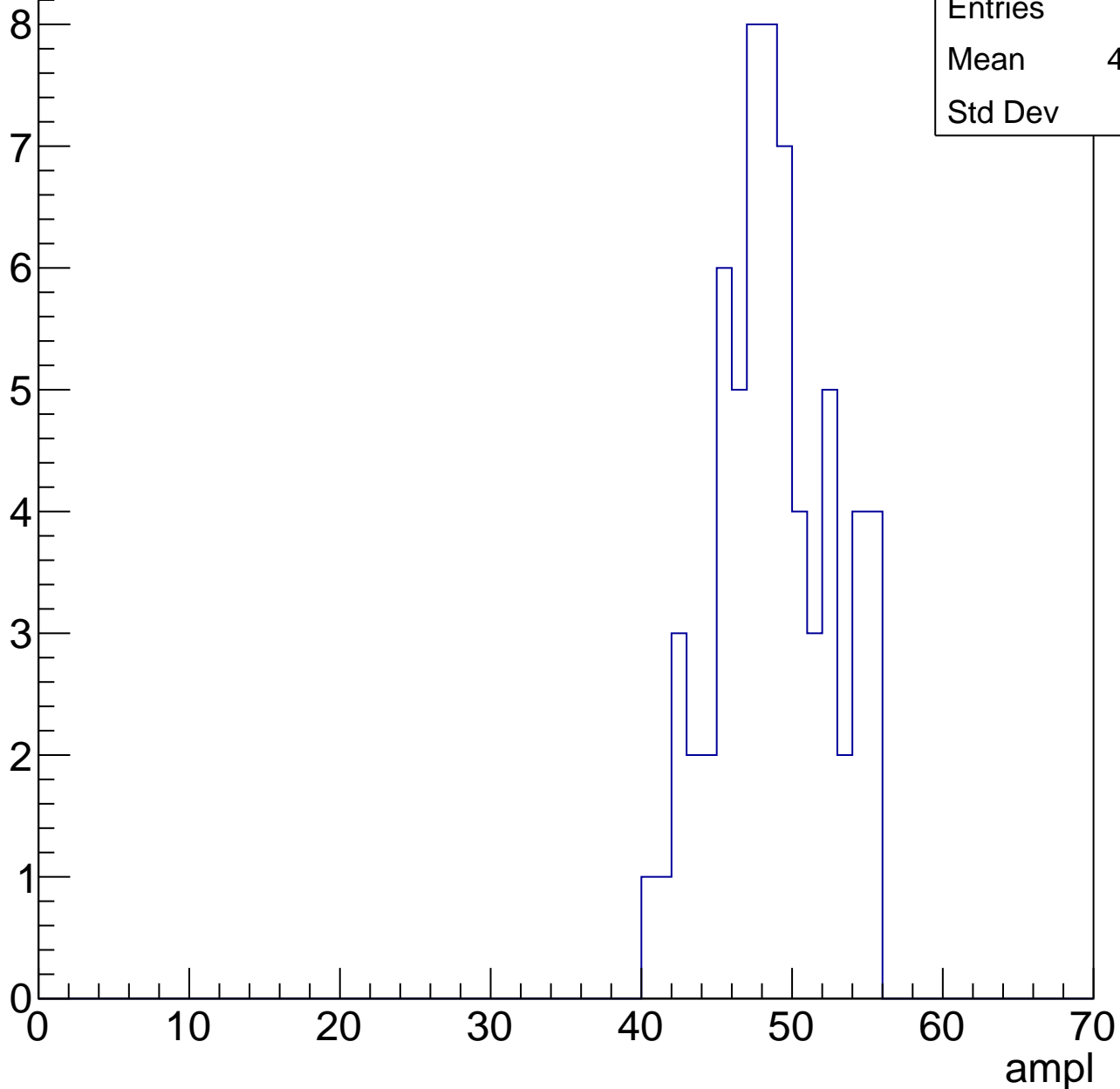
Gaus Width: 3.1699



B1L100S, U1-ch26, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	65
Mean	48.29
Std Dev	3.72

B1L100S, U1-ch26, adc4

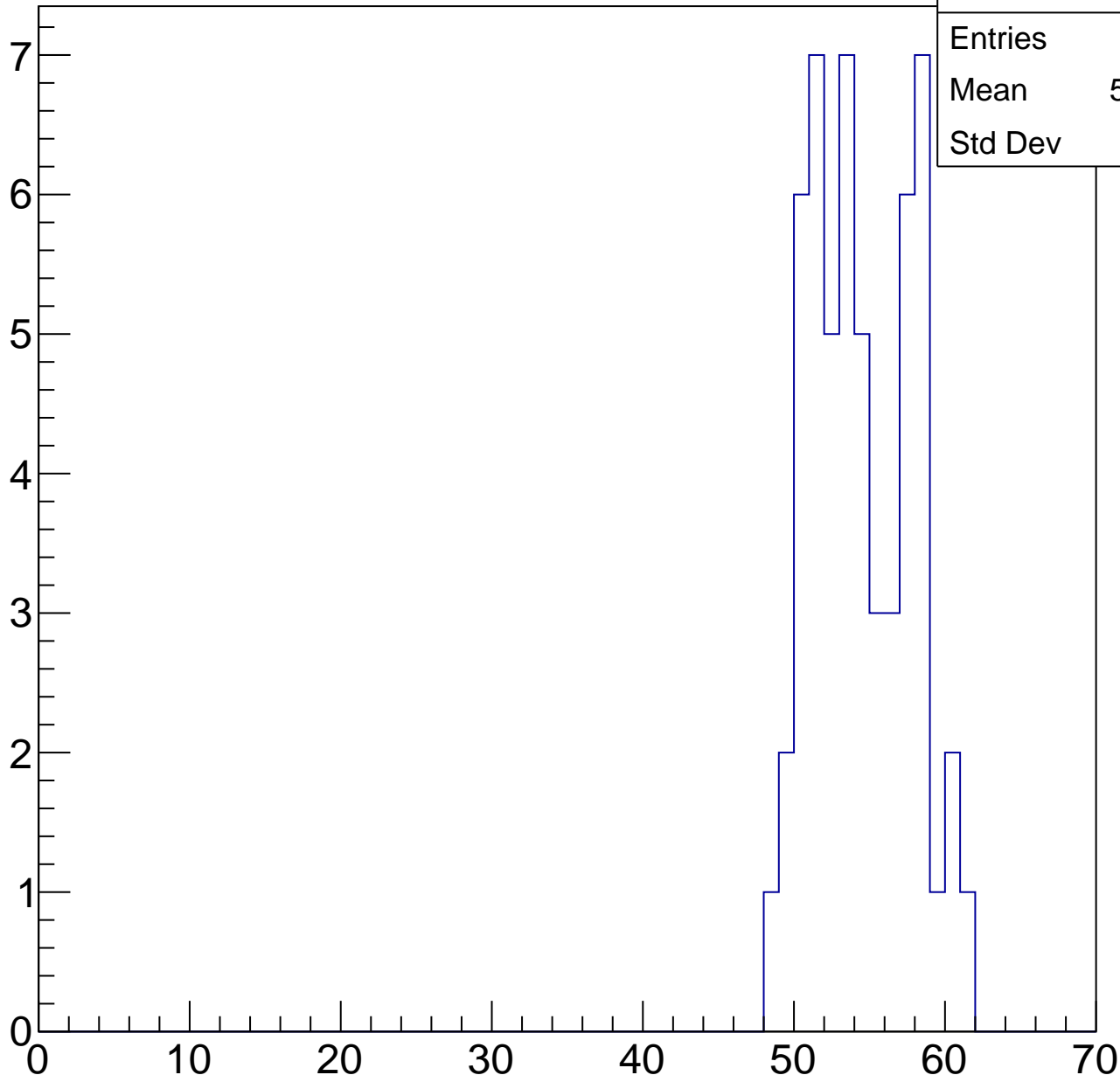
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	56
Mean	54.02
Std Dev	3.27

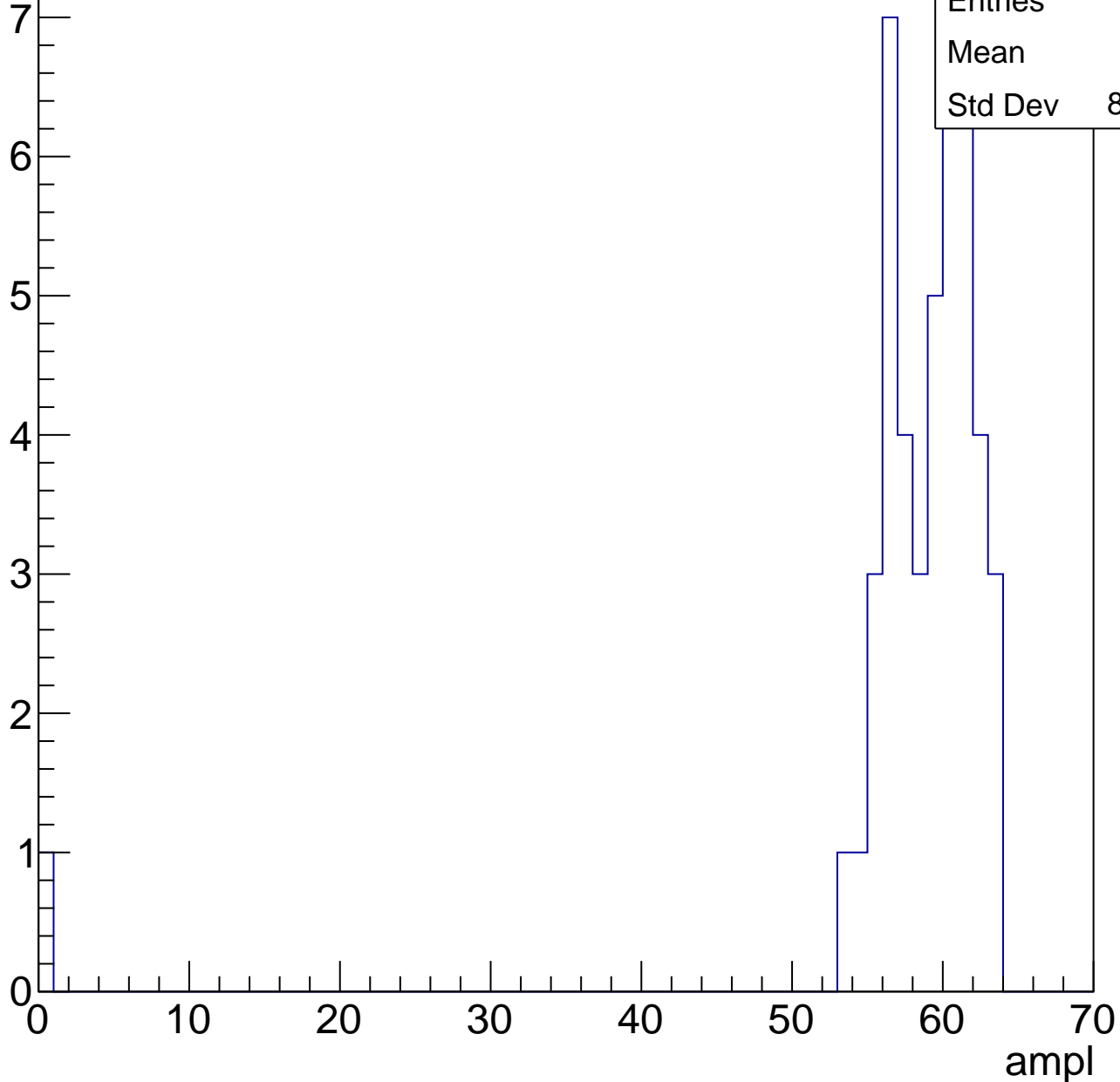
ampl



B1L100S, U1-ch26, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

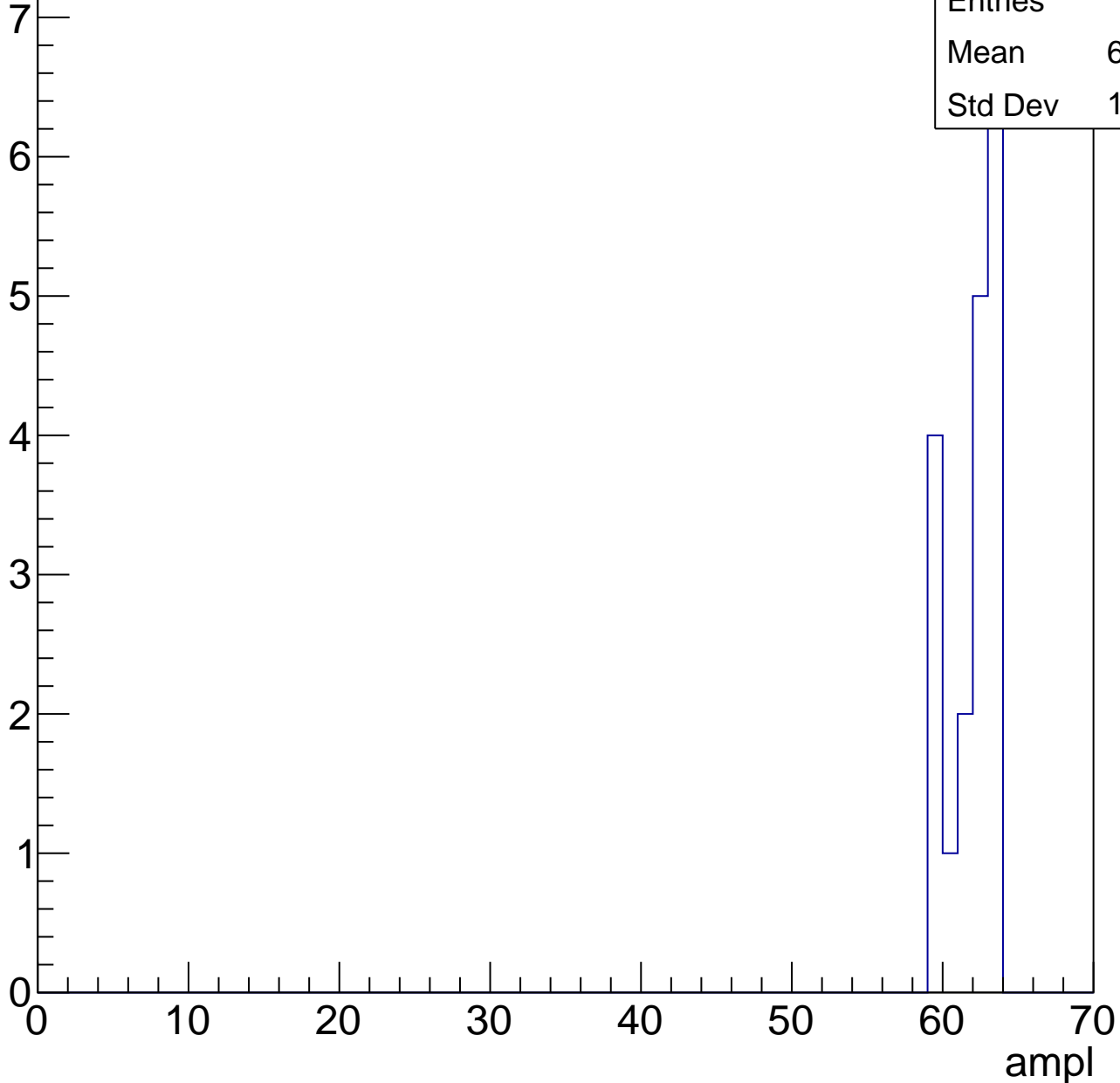


B1L100S, U1-ch26, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

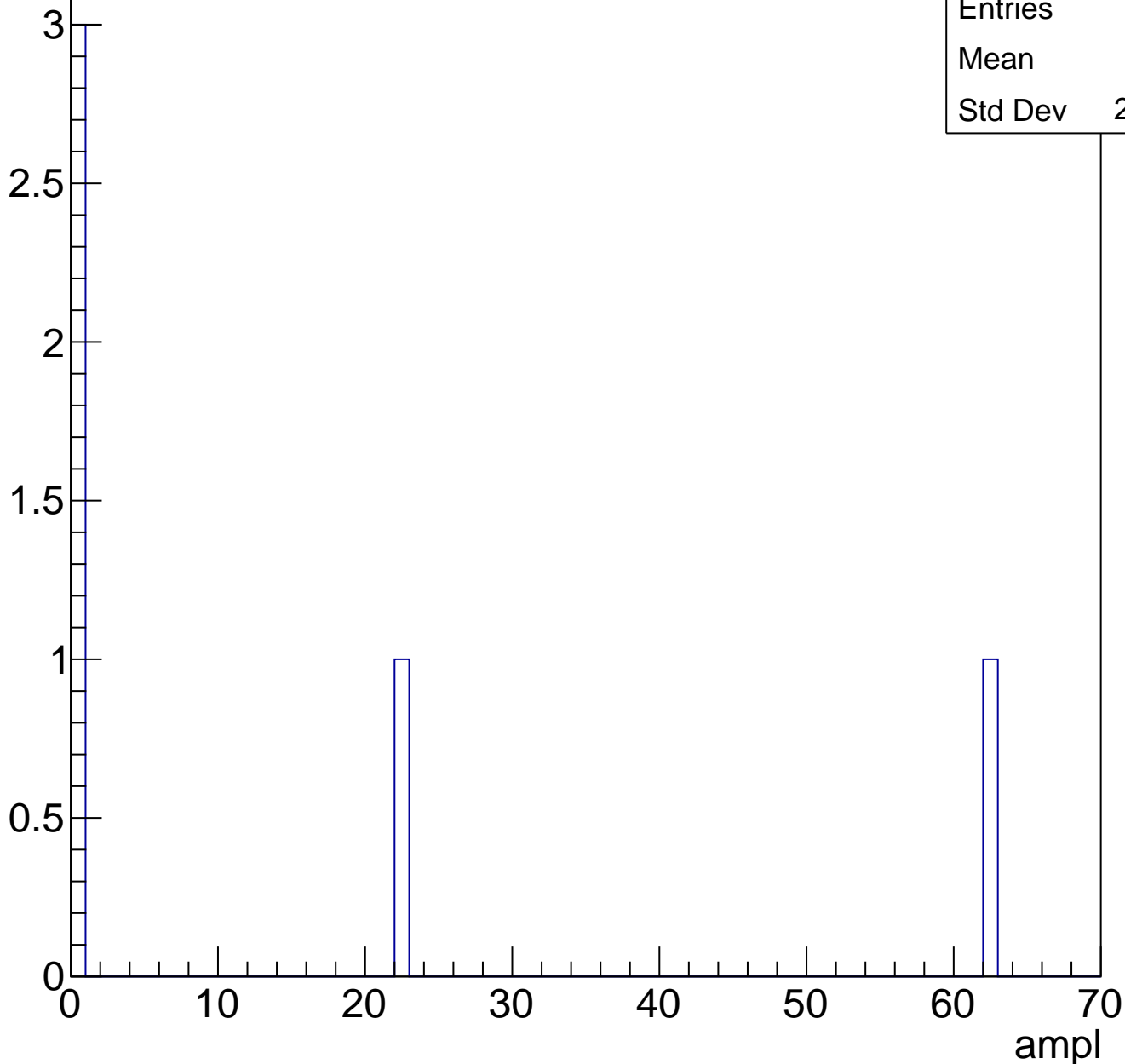
Entries	19
Mean	61.53
Std Dev	1.534



B1L100S, U1-ch26, adc7

calib_packv5_042523_0143.root, FC#4, port A2

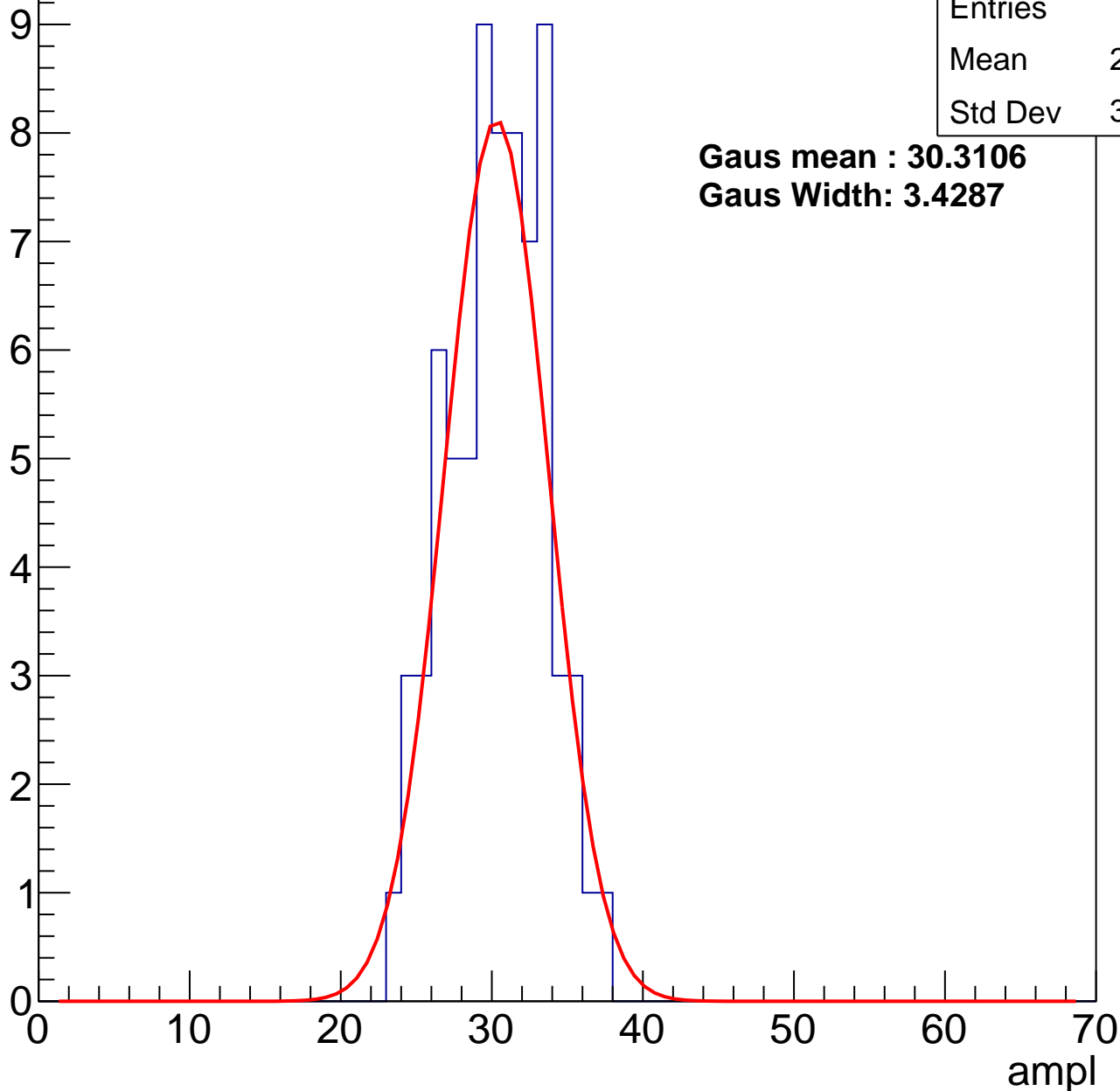
Entry



B1L100S, U1-ch27, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch27, adc1

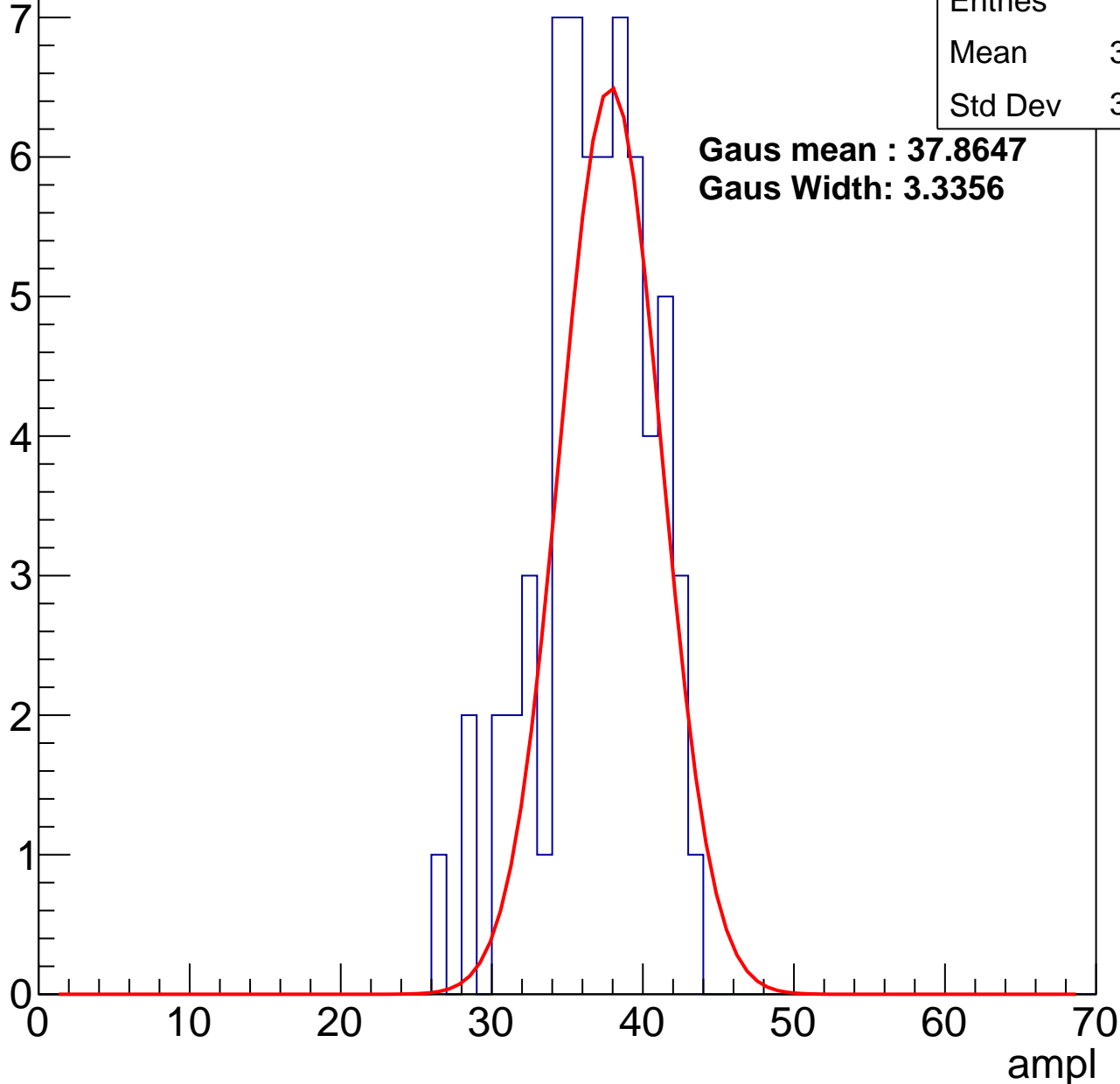
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	36.32
Std Dev	3.715

Gaus mean : 37.8647

Gaus Width: 3.3356



B1L100S, U1-ch27, adc2

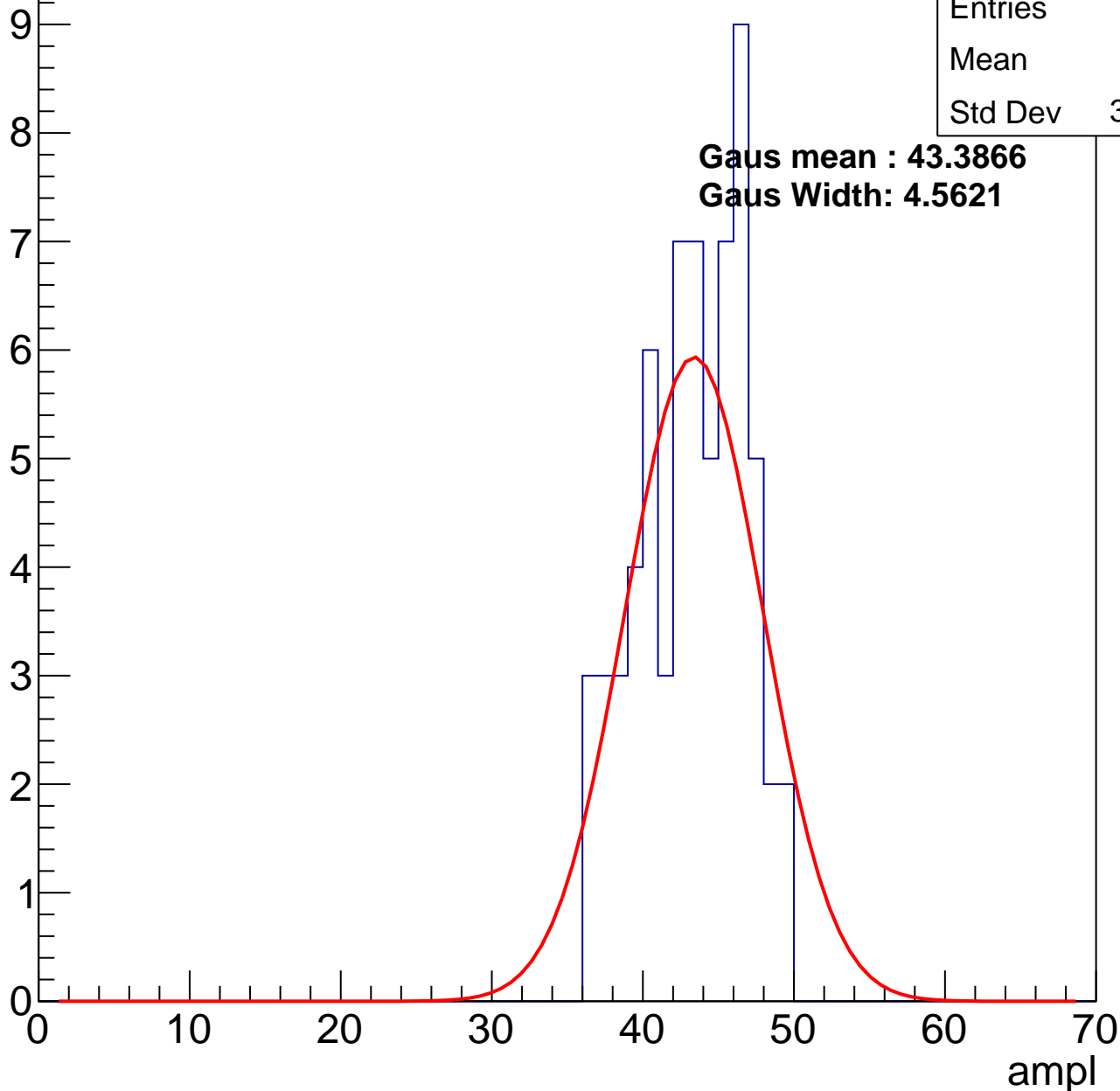
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	42.8
Std Dev	3.439

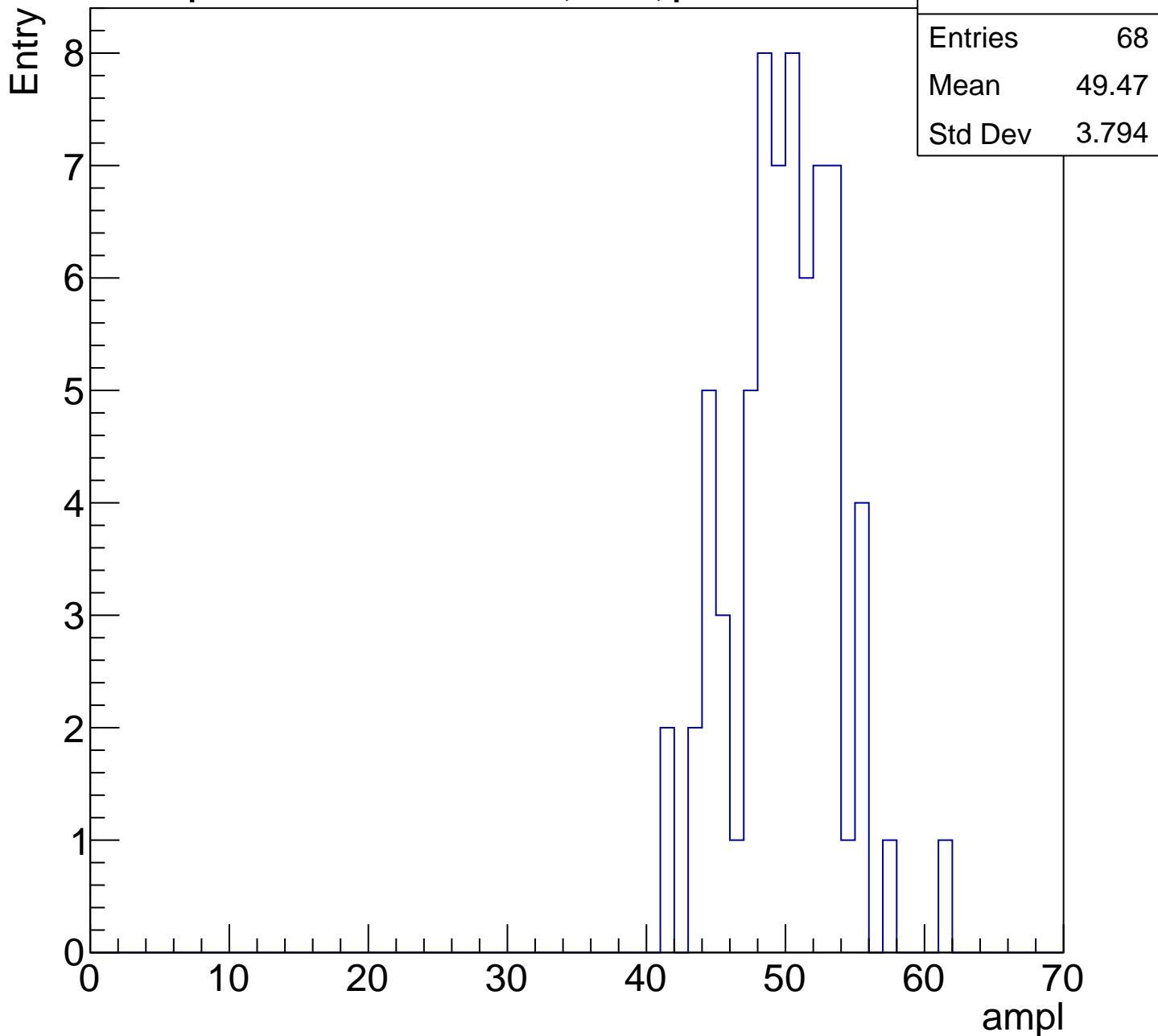
Gaus mean : 43.3866

Gaus Width: 4.5621



B1L100S, U1-ch27, adc3

calib_packv5_042523_0143.root, FC#4, port A2

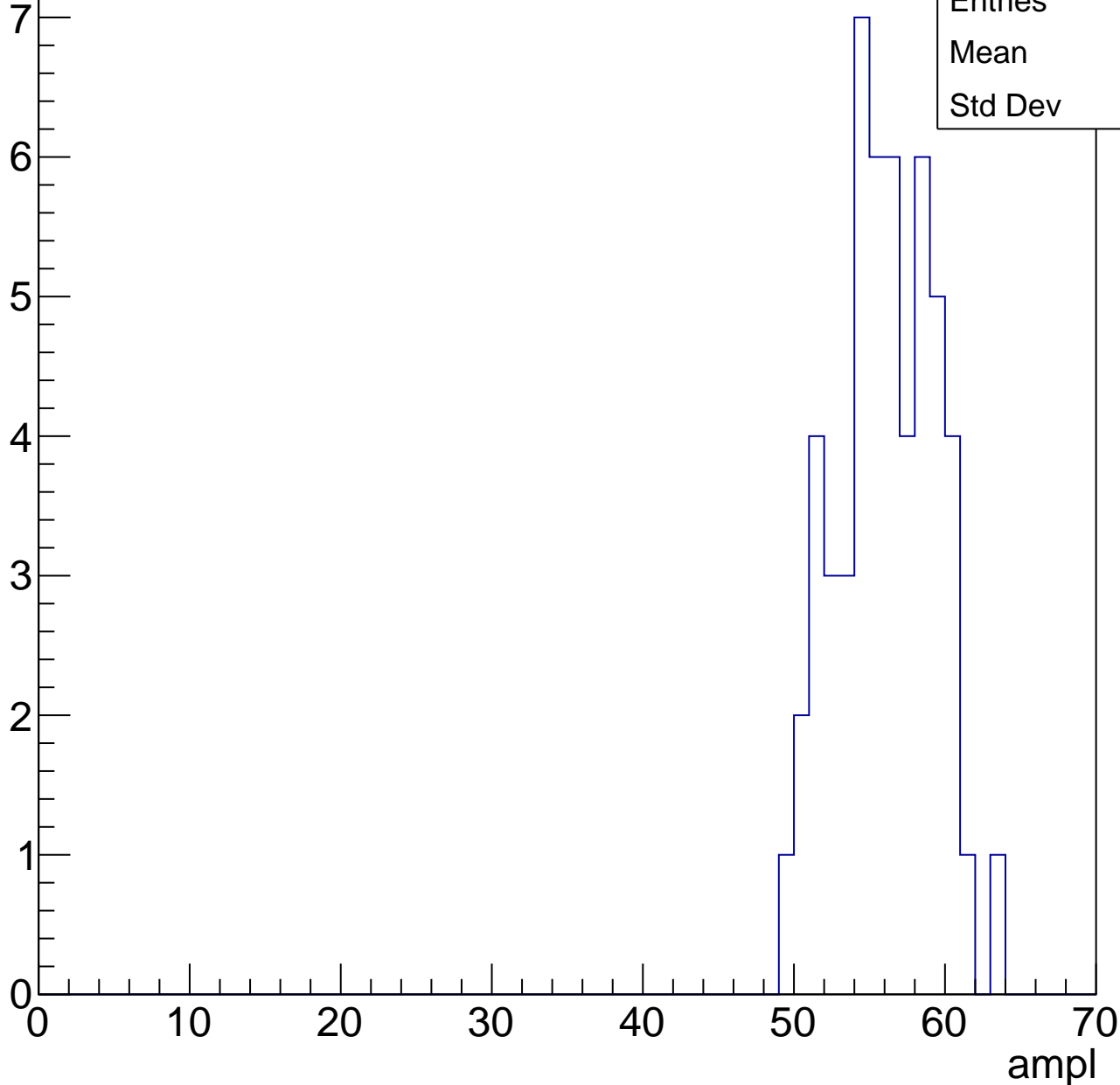


B1L100S, U1-ch27, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.6
Std Dev	3.17

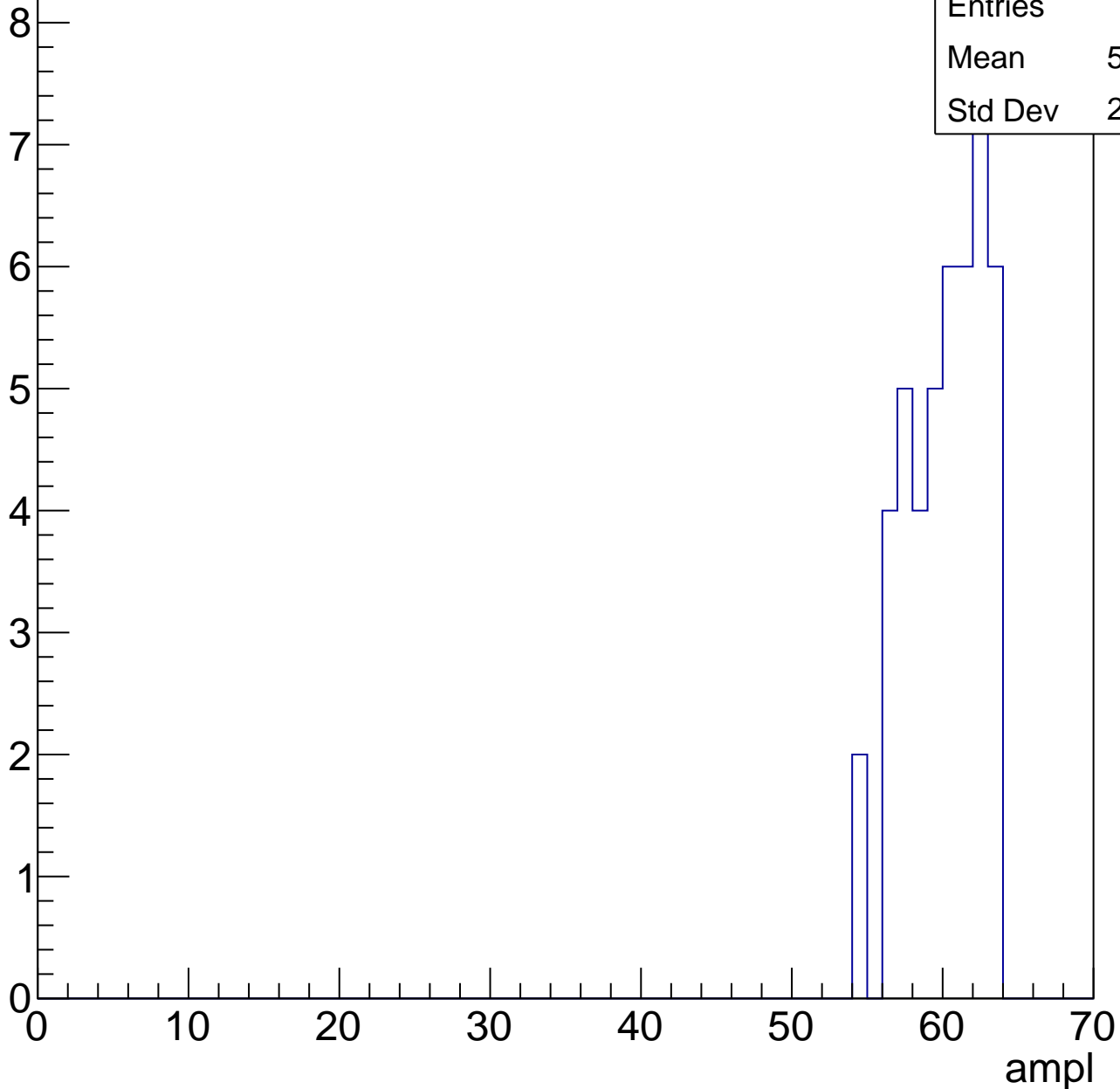


B1L100S, U1-ch27, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

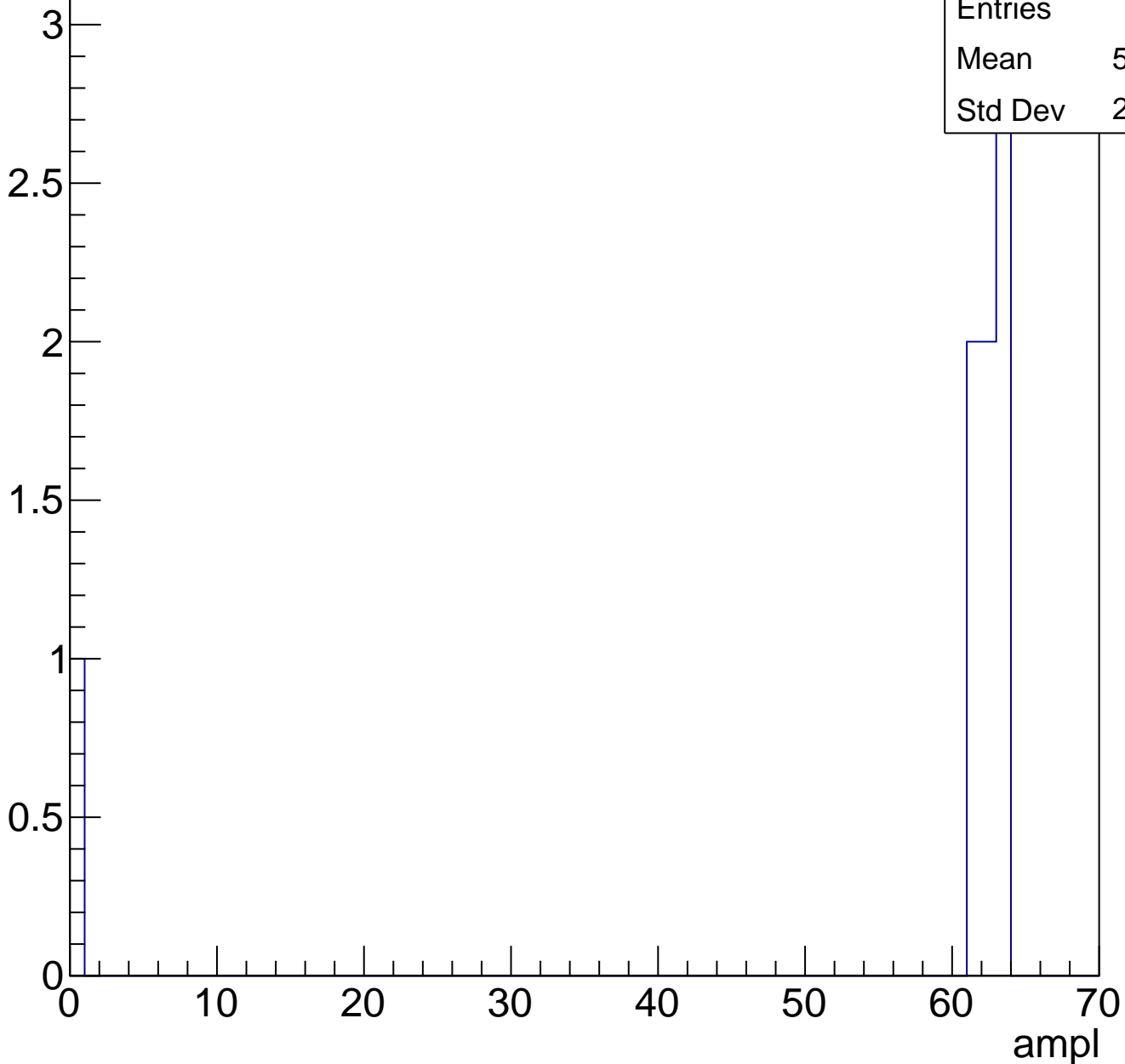
Entries	46
Mean	59.65
Std Dev	2.504



B1L100S, U1-ch27, adc6

calib_packv5_042523_0143.root, FC#4, port A2

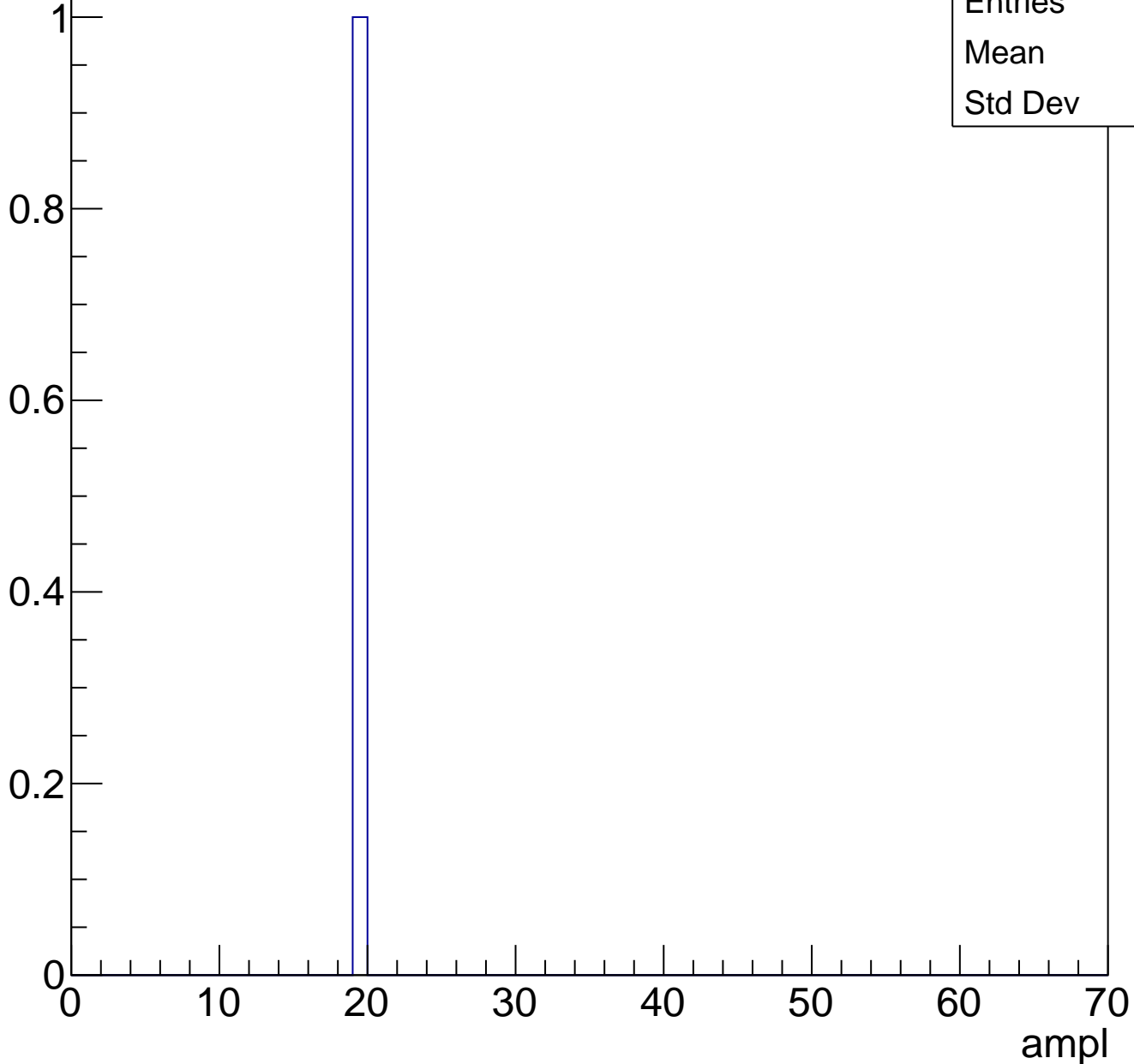
Entry



B1L100S, U1-ch27, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	19
Std Dev	0

B1L100S, U1-ch28, adc0

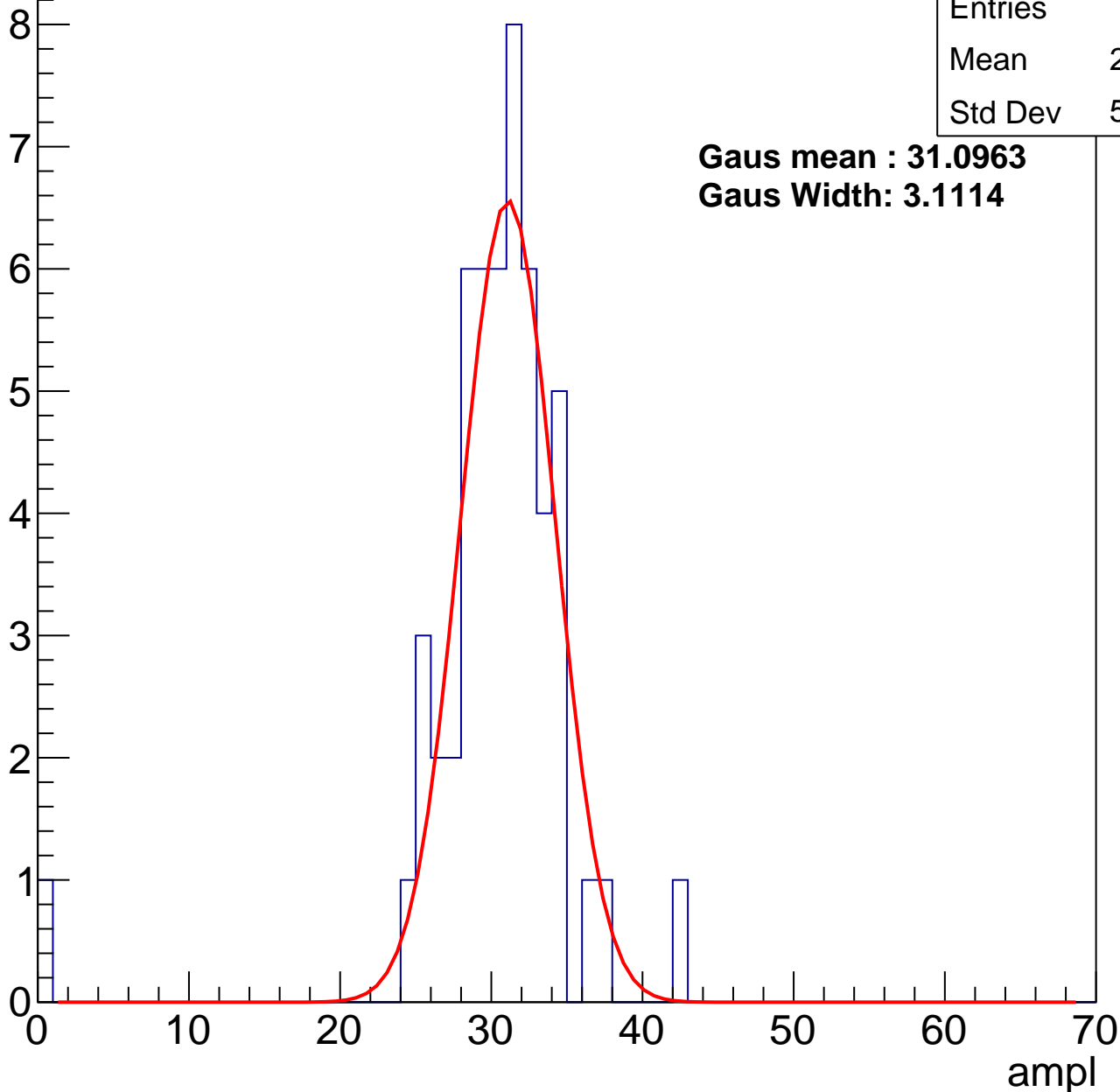
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	29.89
Std Dev	5.262

Gaus mean : 31.0963

Gaus Width: 3.1114



B1L100S, U1-ch28, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	35.83
Std Dev	3.617

Gaus mean : 37.0508

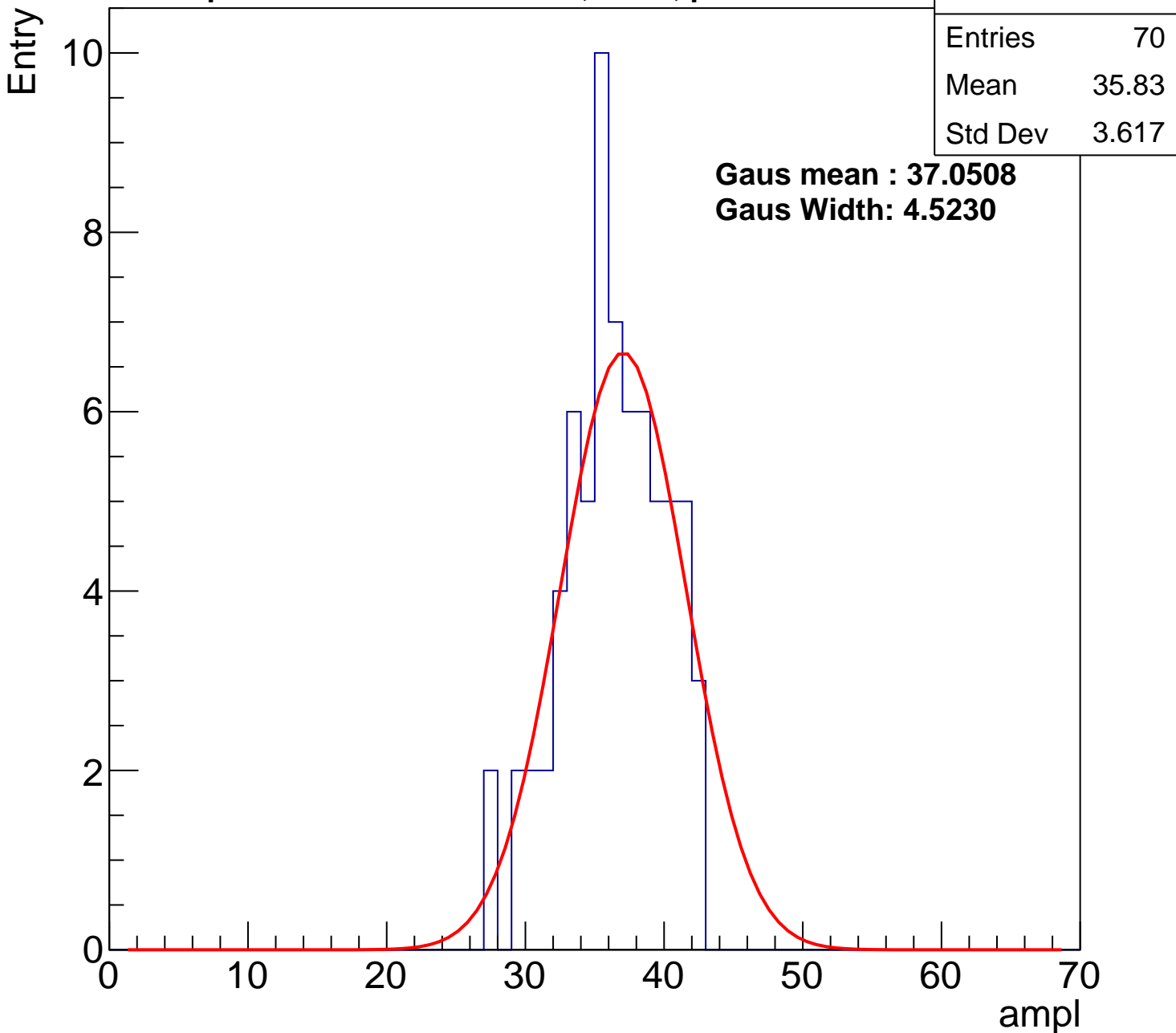
Gaus Width: 4.5230

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch28, adc2

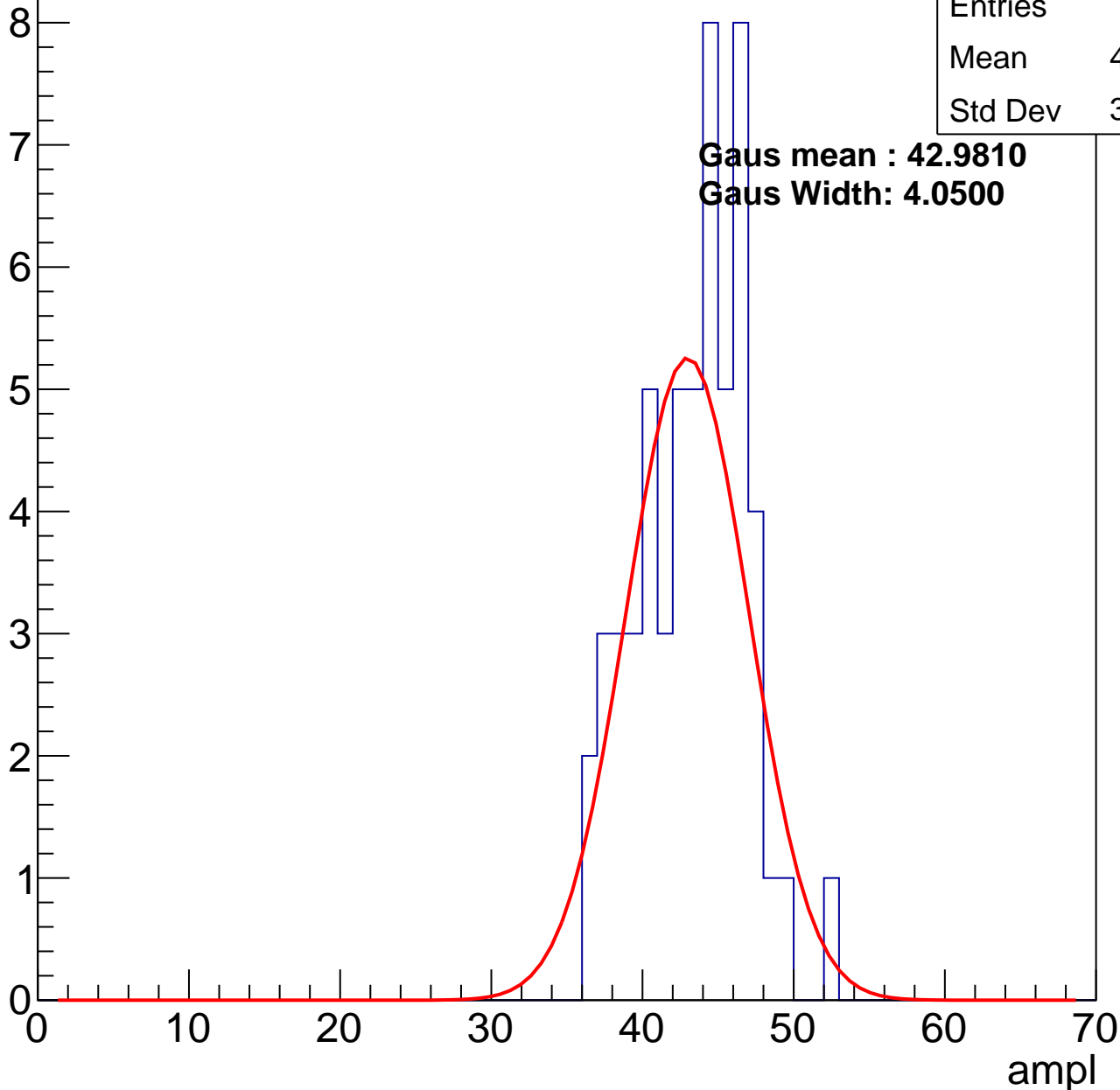
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.88
Std Dev	3.495

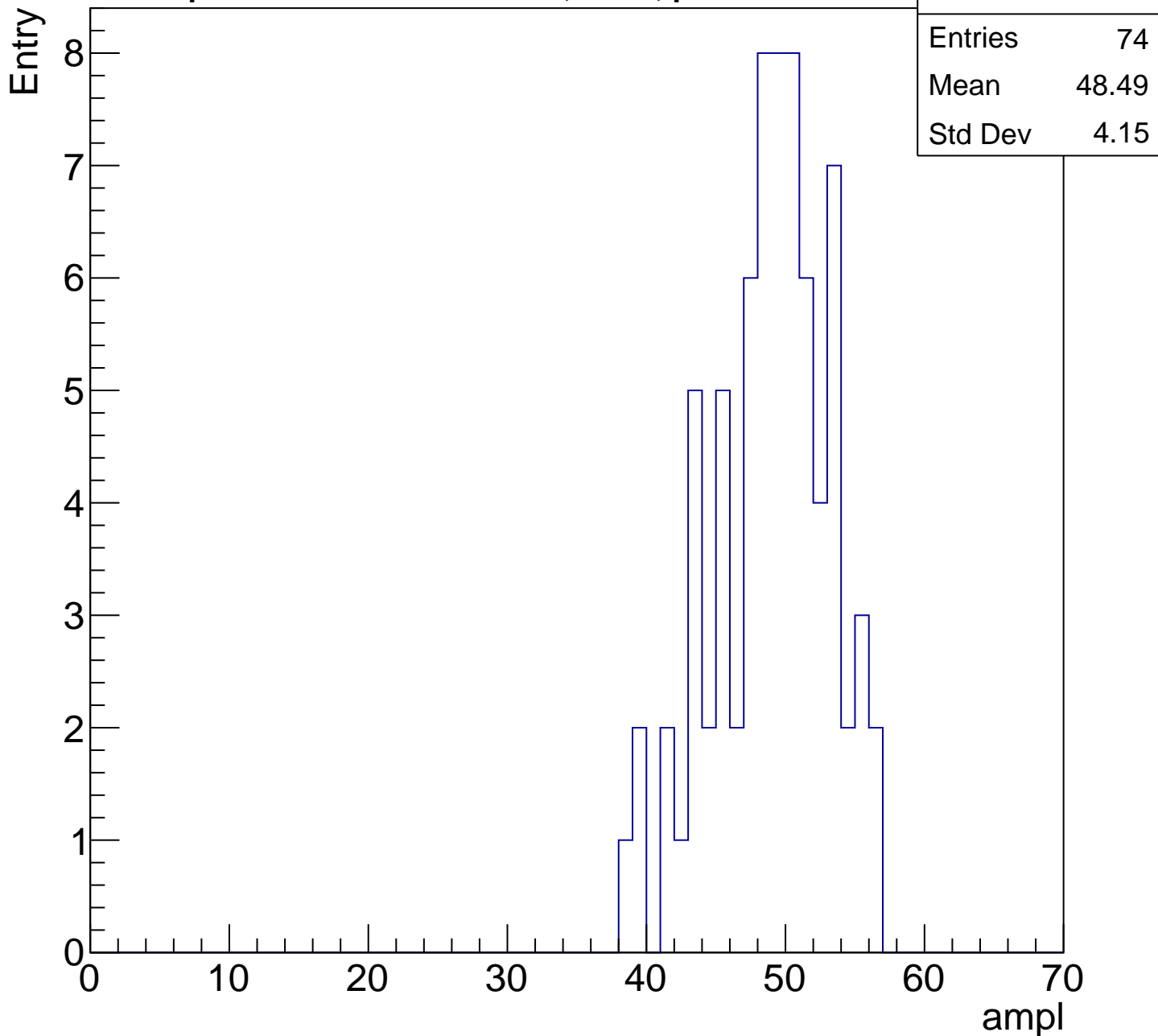
Gaus mean : 42.9810

Gaus Width: 4.0500



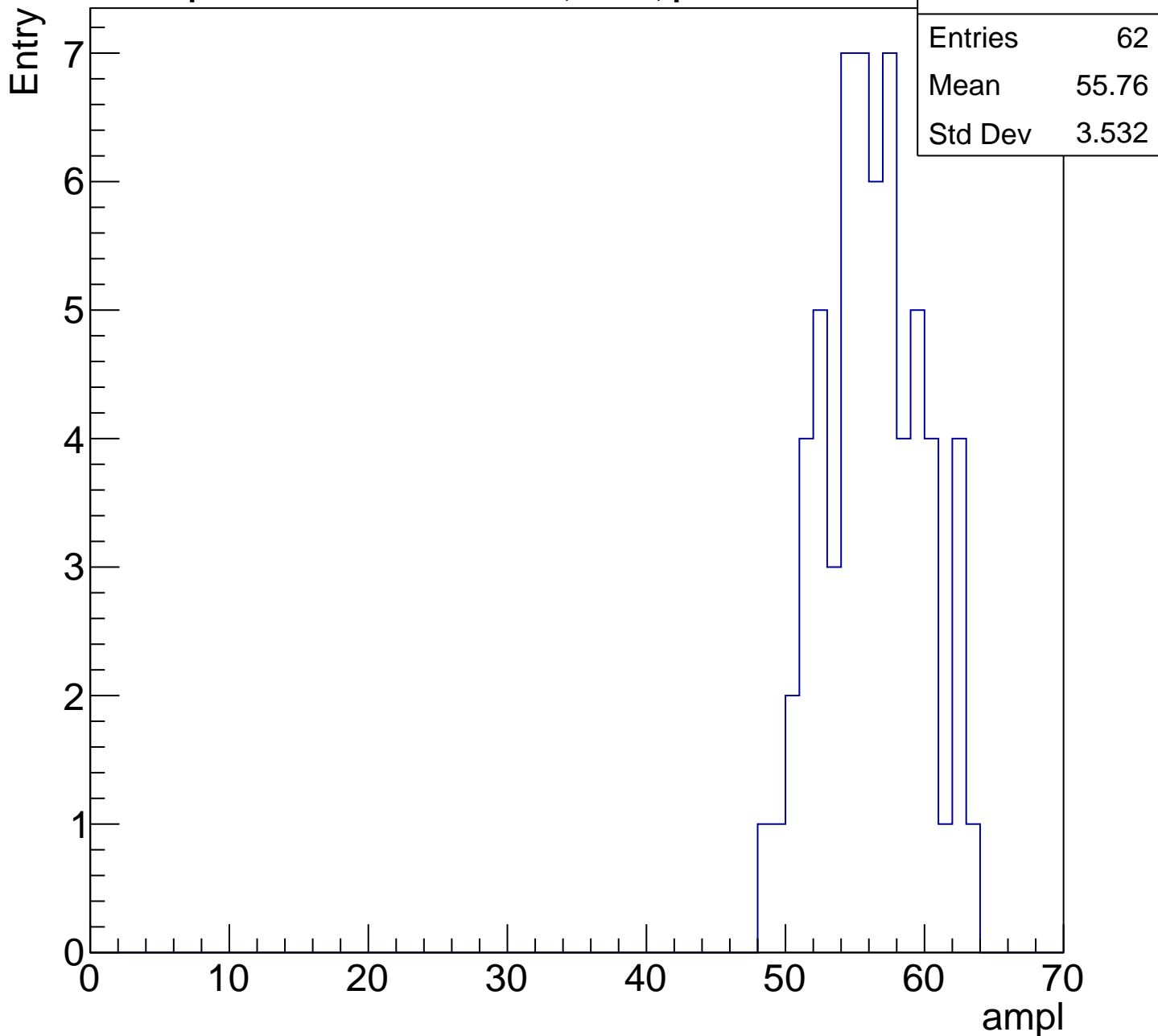
B1L100S, U1-ch28, adc3

calib_packv5_042523_0143.root, FC#4, port A2



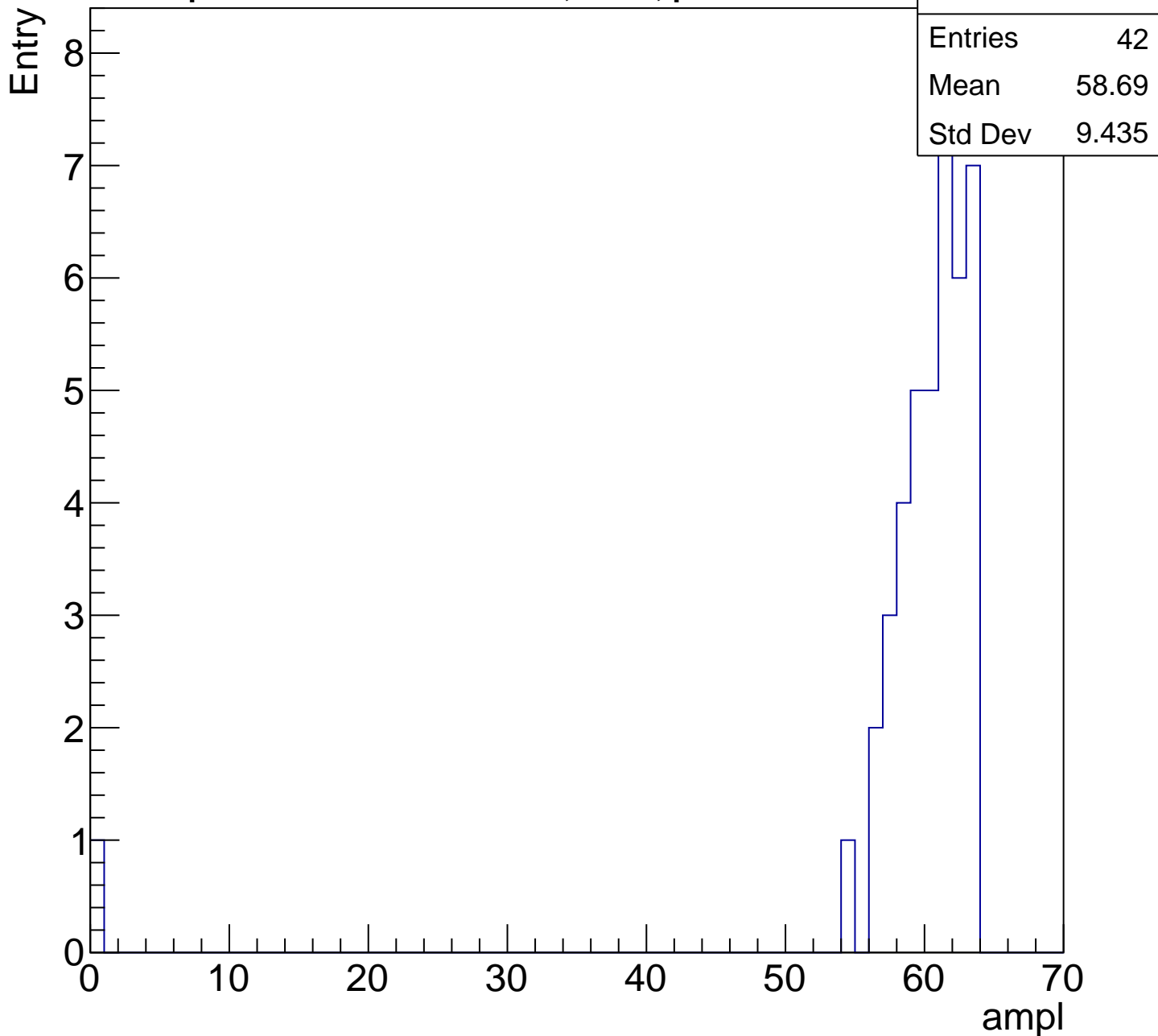
B1L100S, U1-ch28, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch28, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch28, adc6

calib_packv5_042523_0143.root, FC#4, port A2

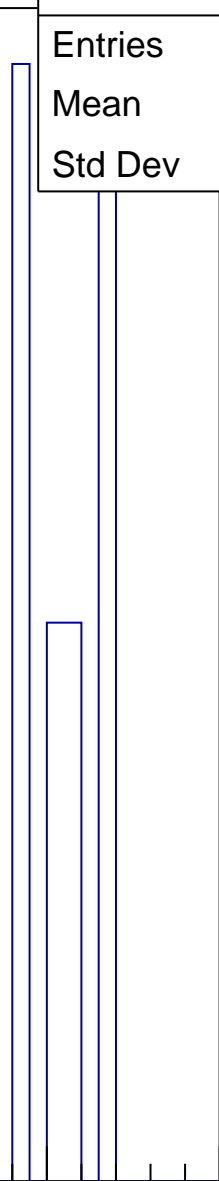
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	6
Mean	60.5
Std Dev	2.062

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch28, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch29, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	28.49
Std Dev	5.017

Gaus mean : 28.2998

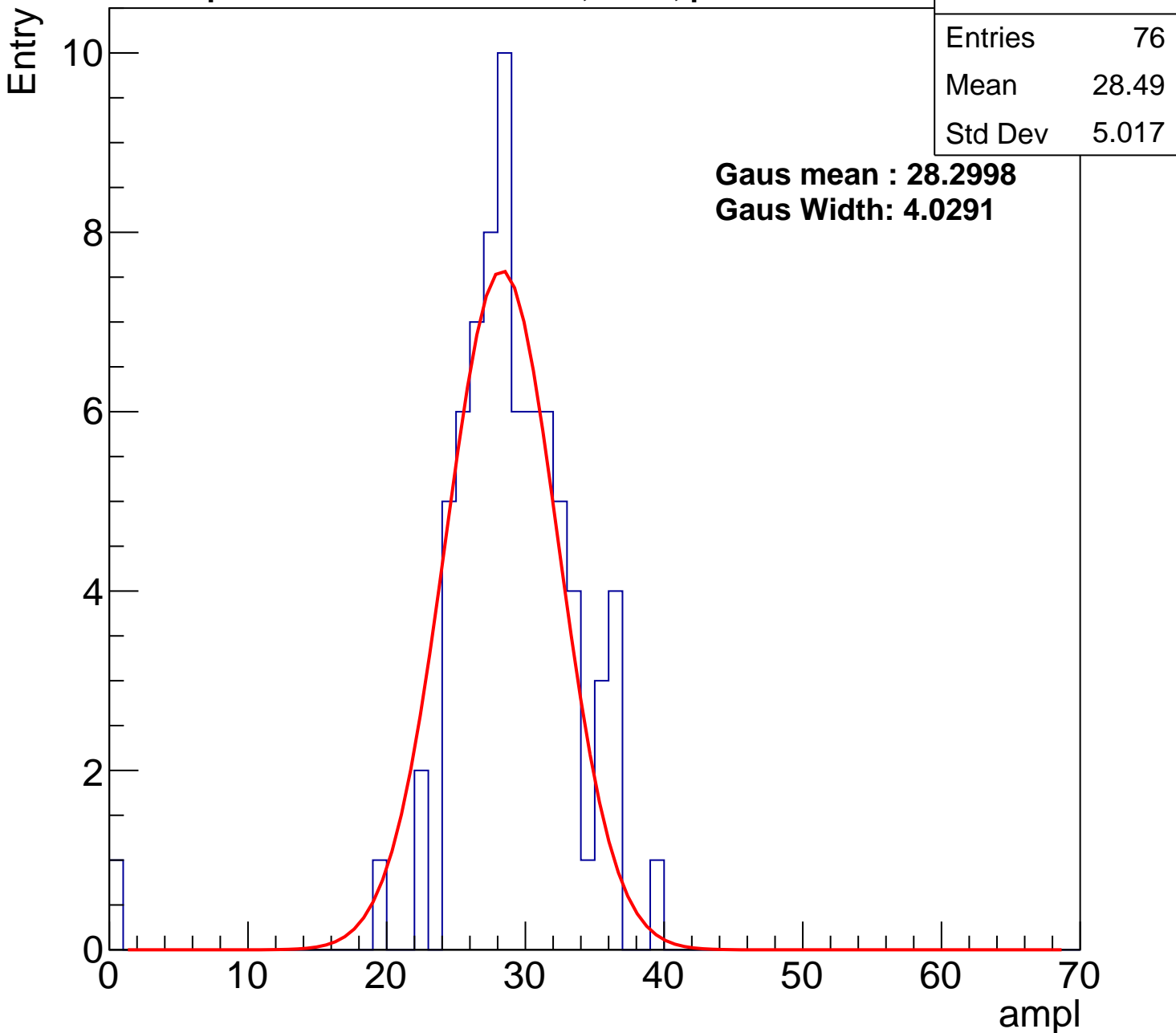
Gaus Width: 4.0291

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch29, adc1

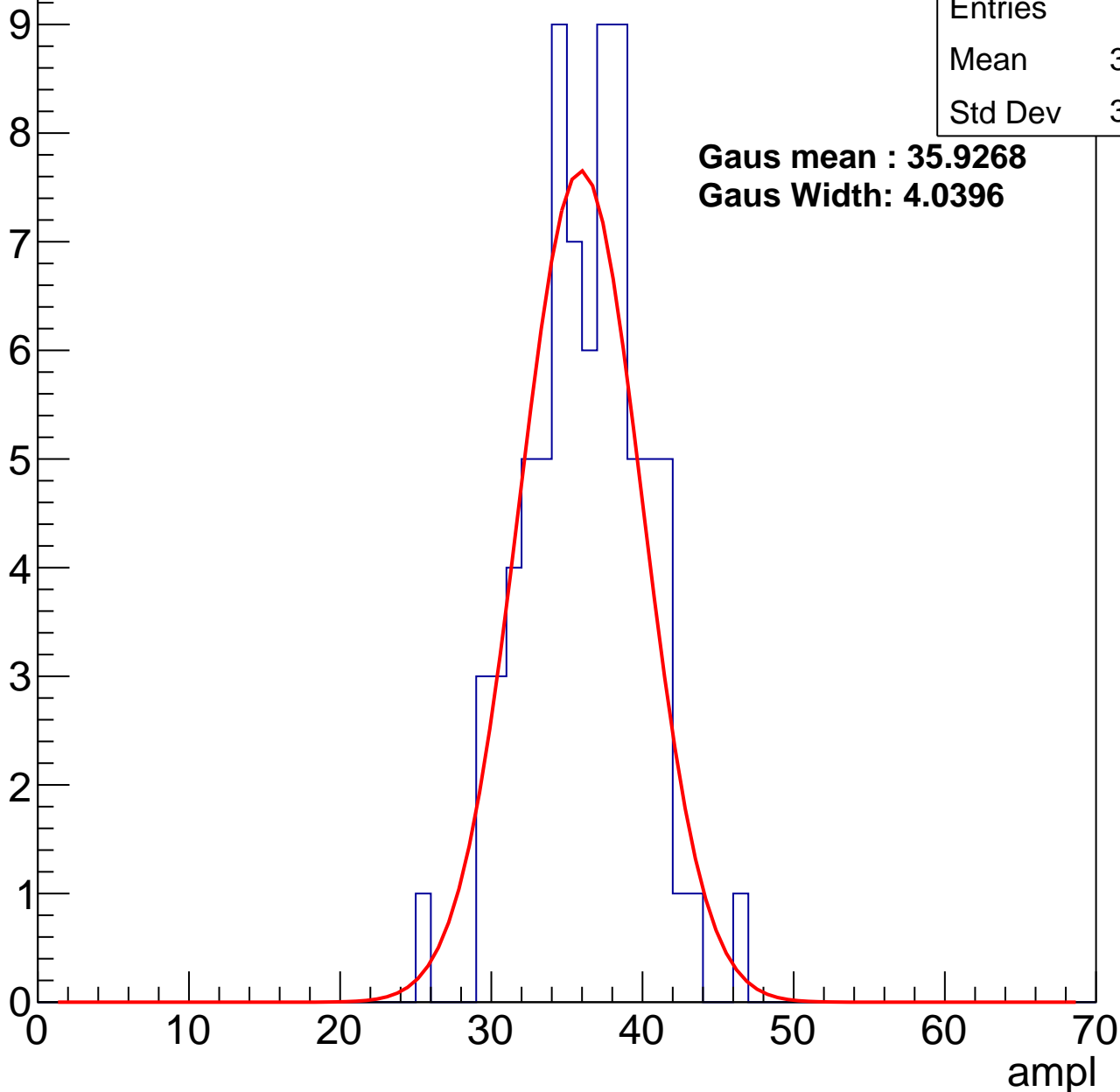
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.75
Std Dev	3.763

Gaus mean : 35.9268

Gaus Width: 4.0396



B1L100S, U1-ch29, adc2

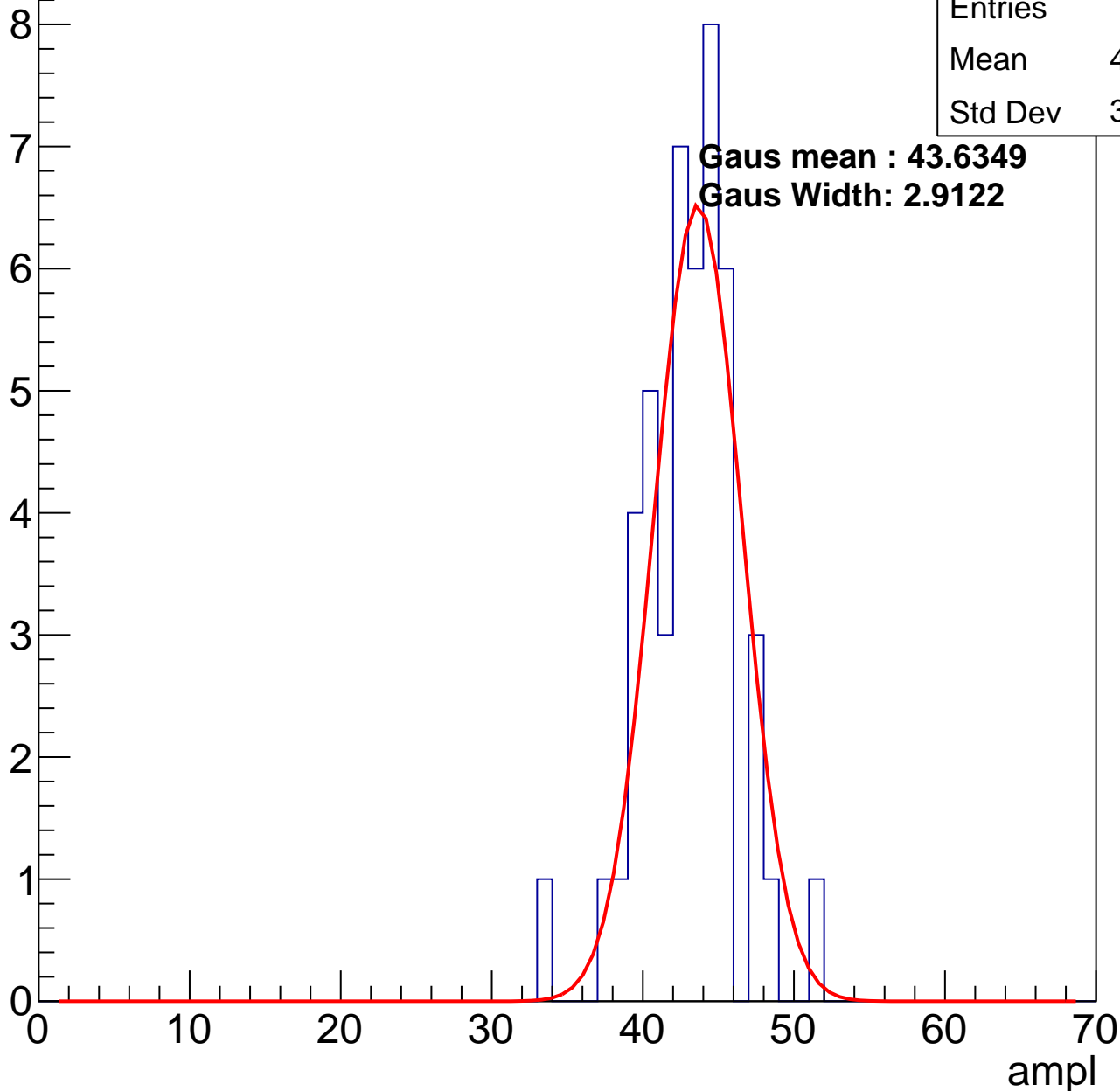
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	42.57
Std Dev	3.093

Gaus mean : 43.6349

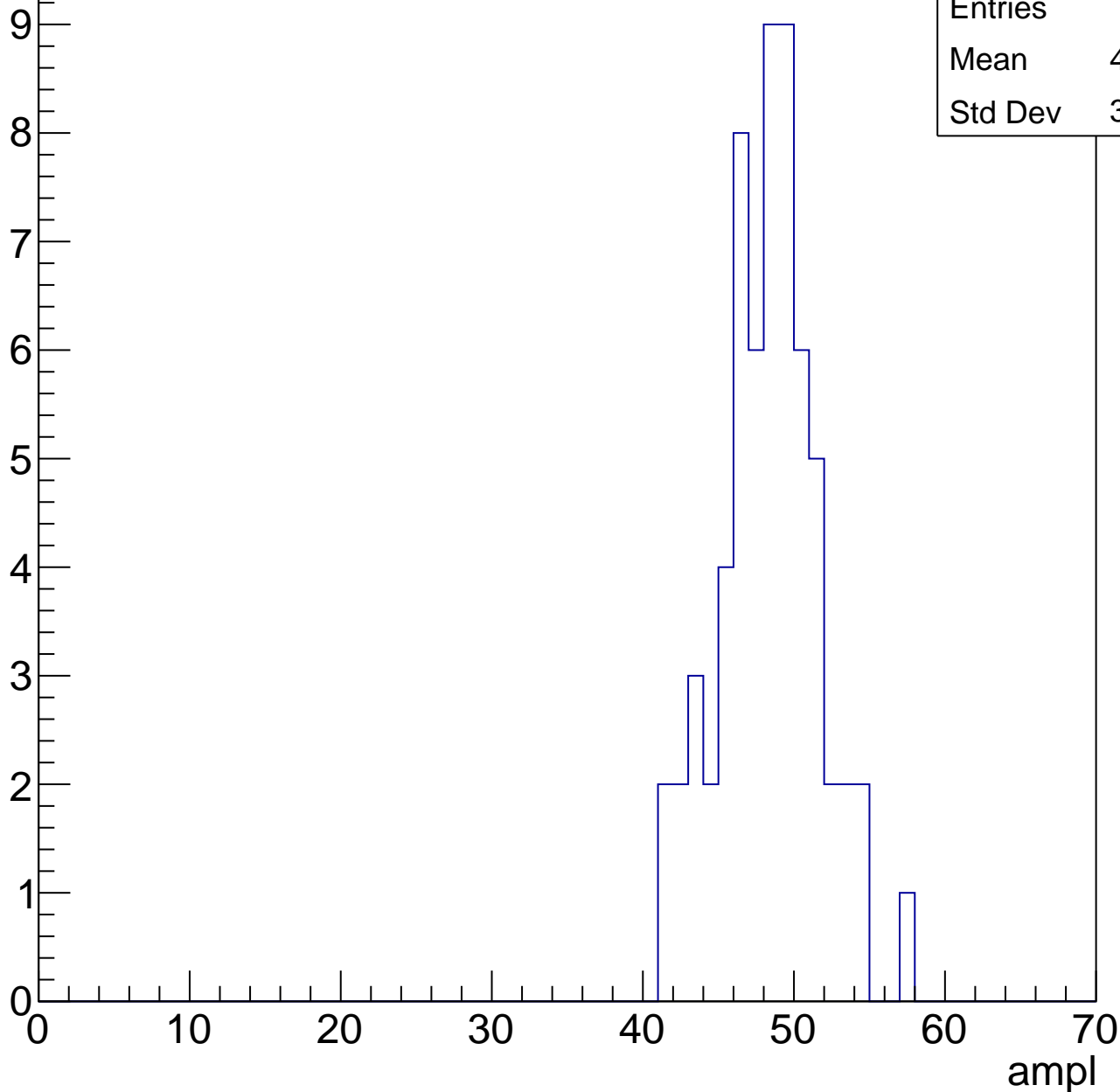
Gaus Width: 2.9122



B1L100S, U1-ch29, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

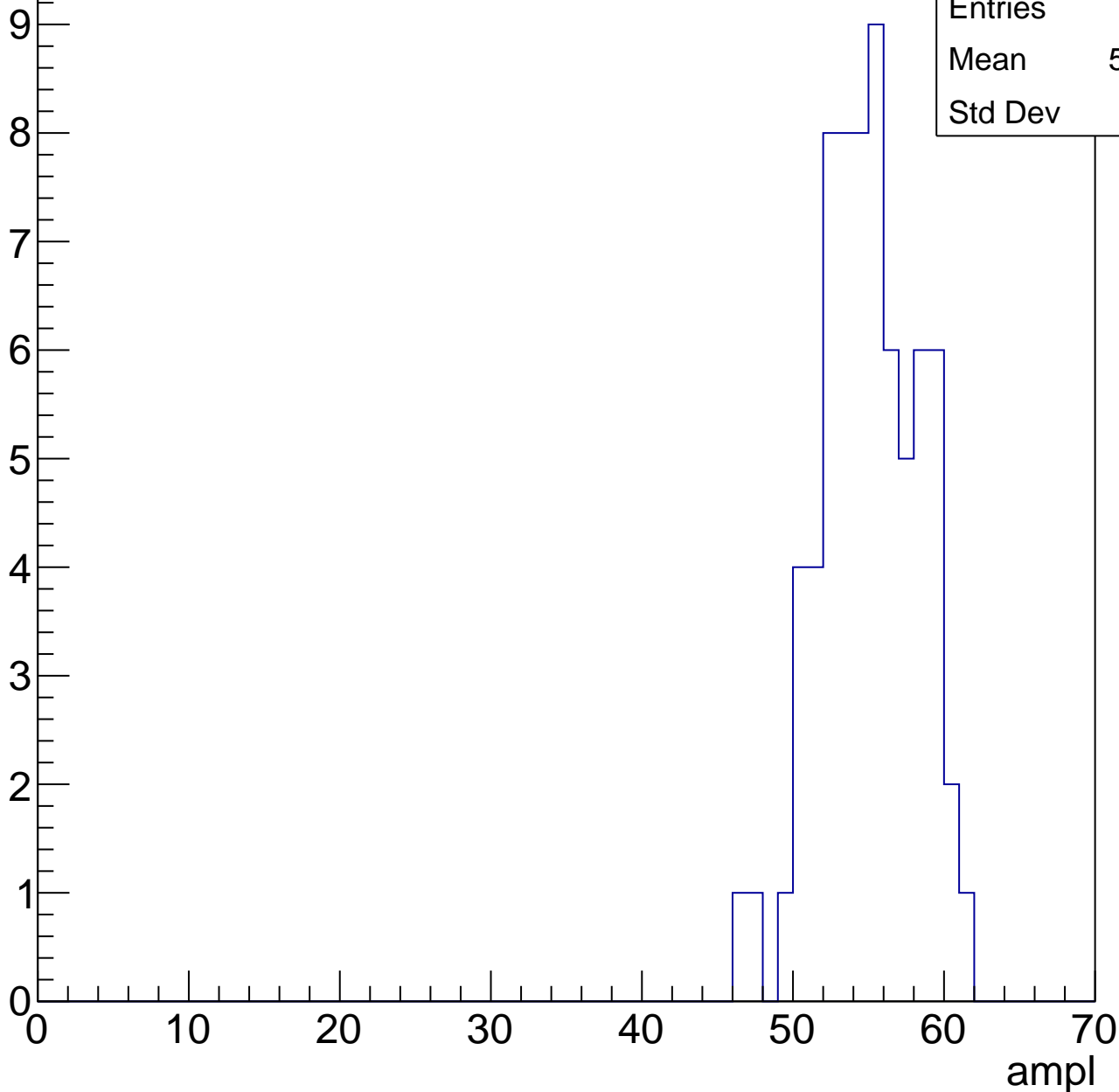


B1L100S, U1-ch29, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	54.53
Std Dev	3.17

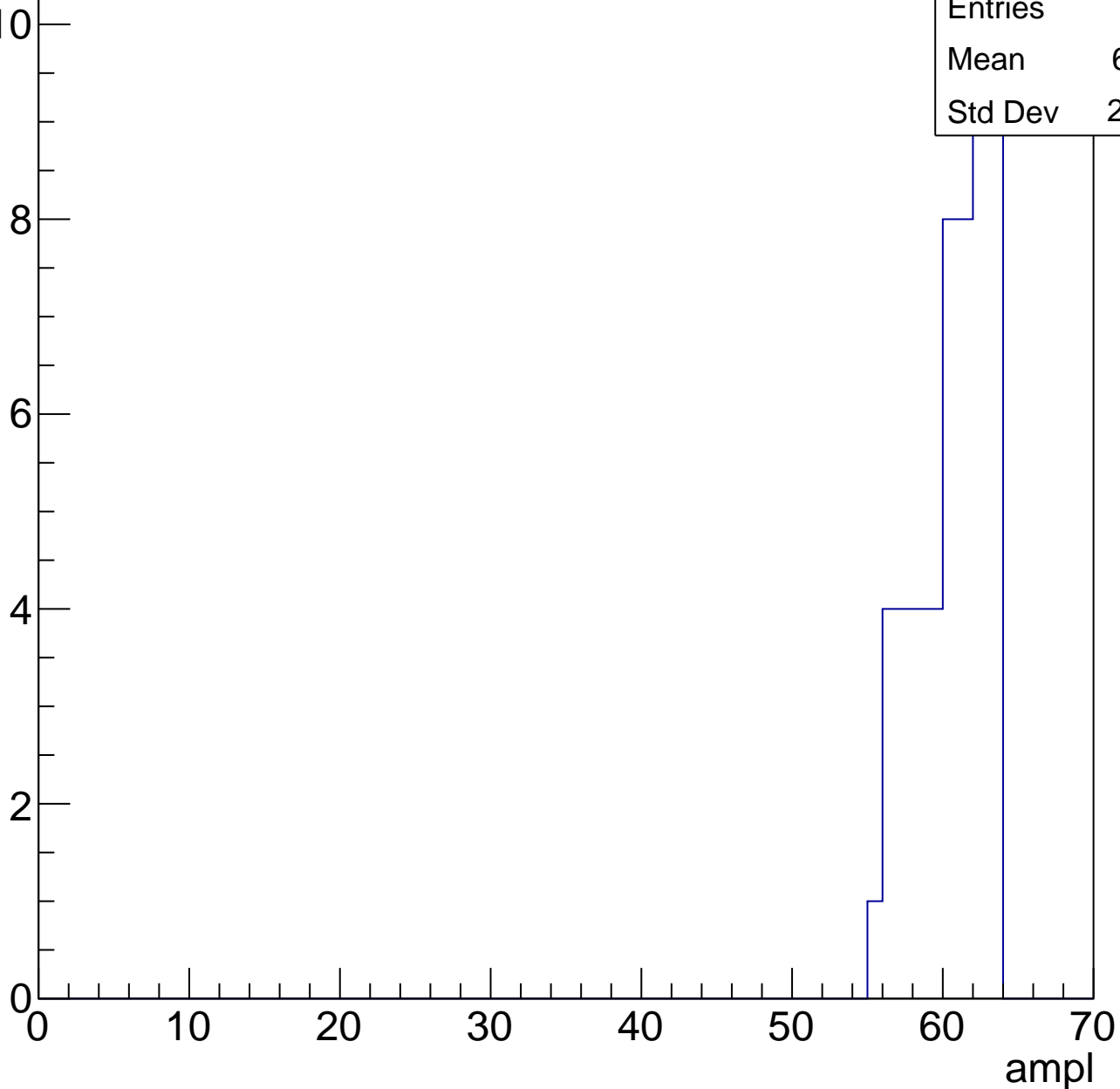


B1L100S, U1-ch29, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	60.21
Std Dev	2.307



B1L100S, U1-ch29, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch29, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch30, adc0

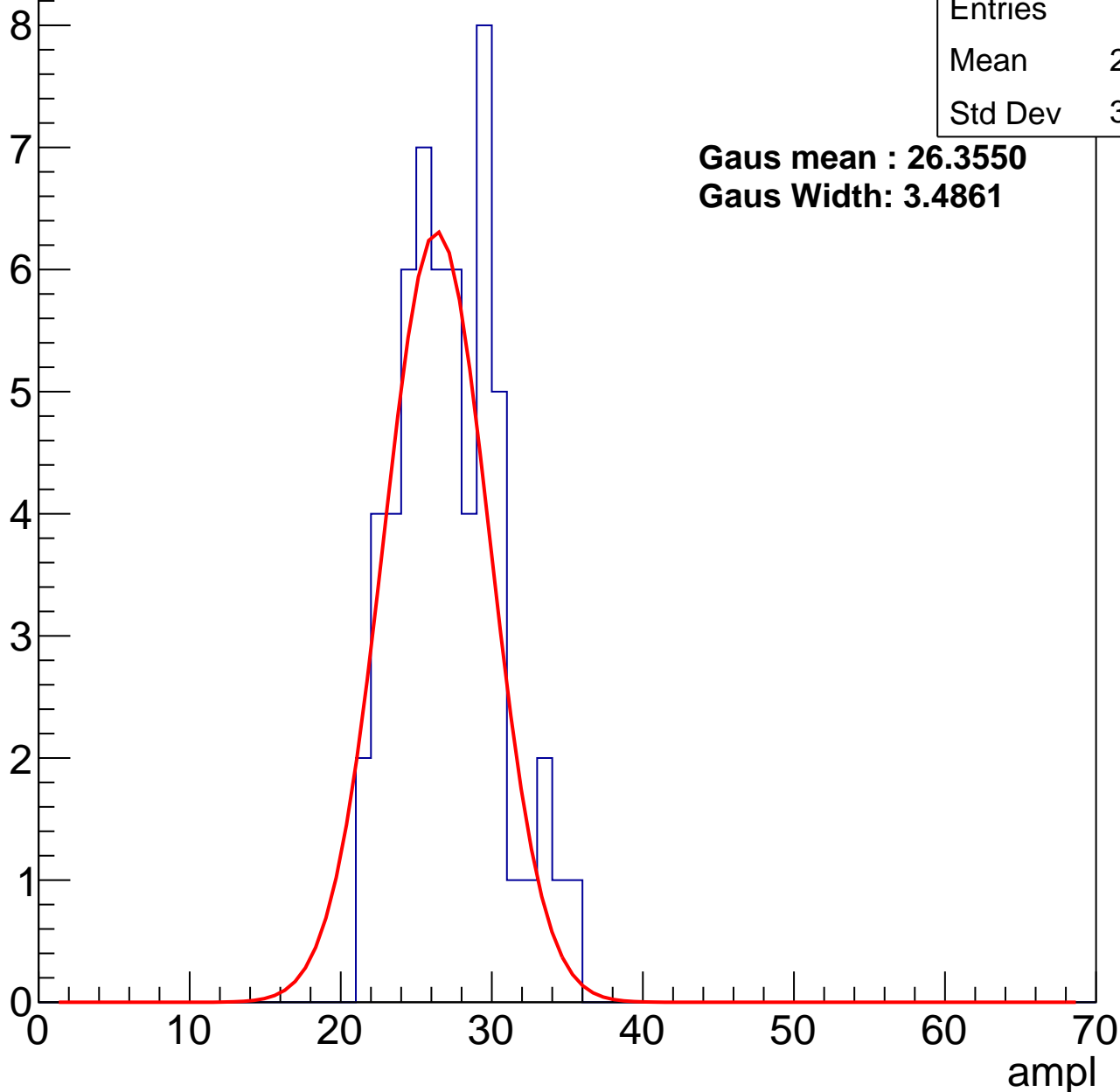
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	26.74
Std Dev	3.288

Gaus mean : 26.3550

Gaus Width: 3.4861



B1L100S, U1-ch30, adc1

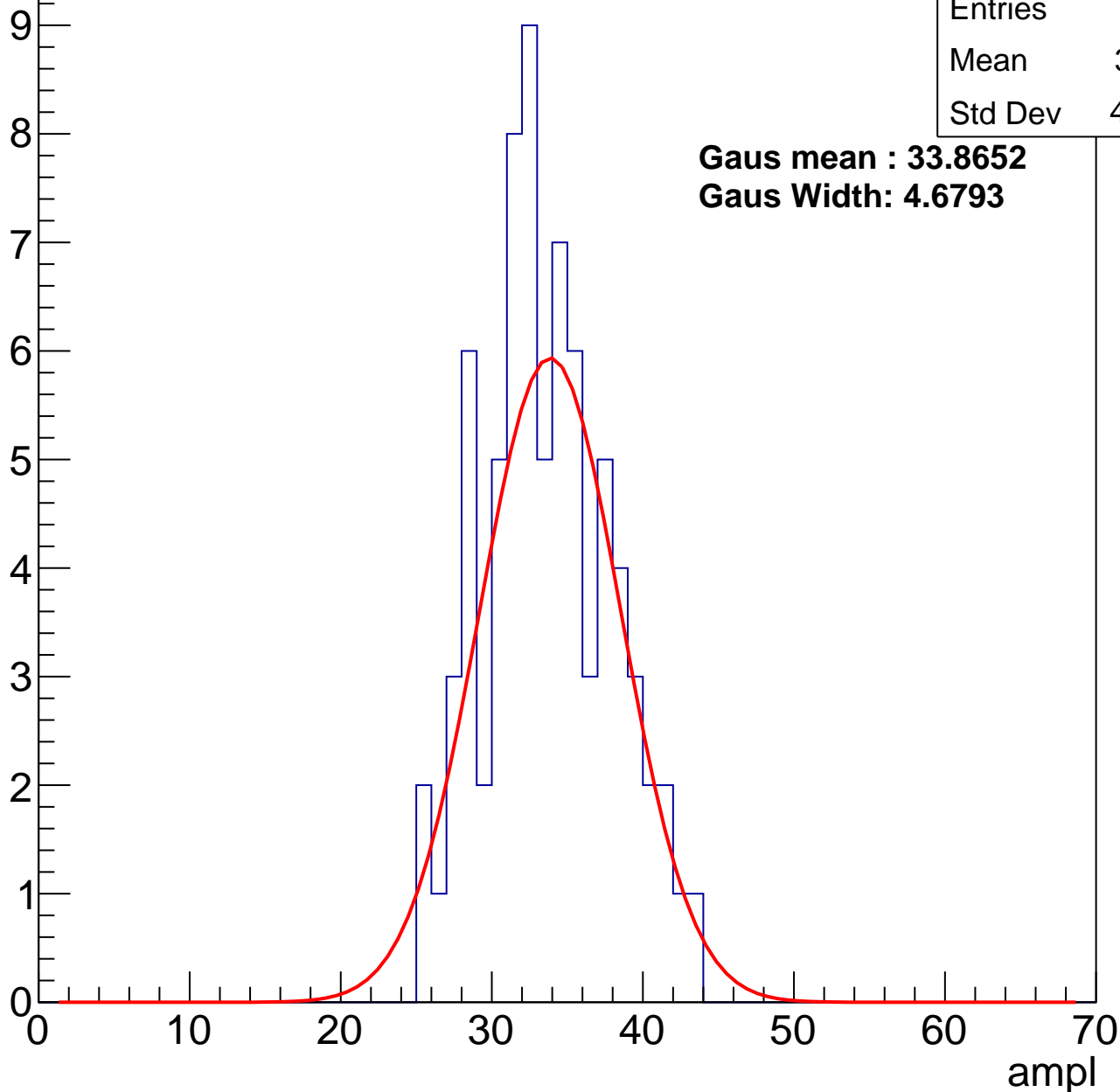
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	33.21
Std Dev	4.167

Gaus mean : 33.8652

Gaus Width: 4.6793



B1L100S, U1-ch30, adc2

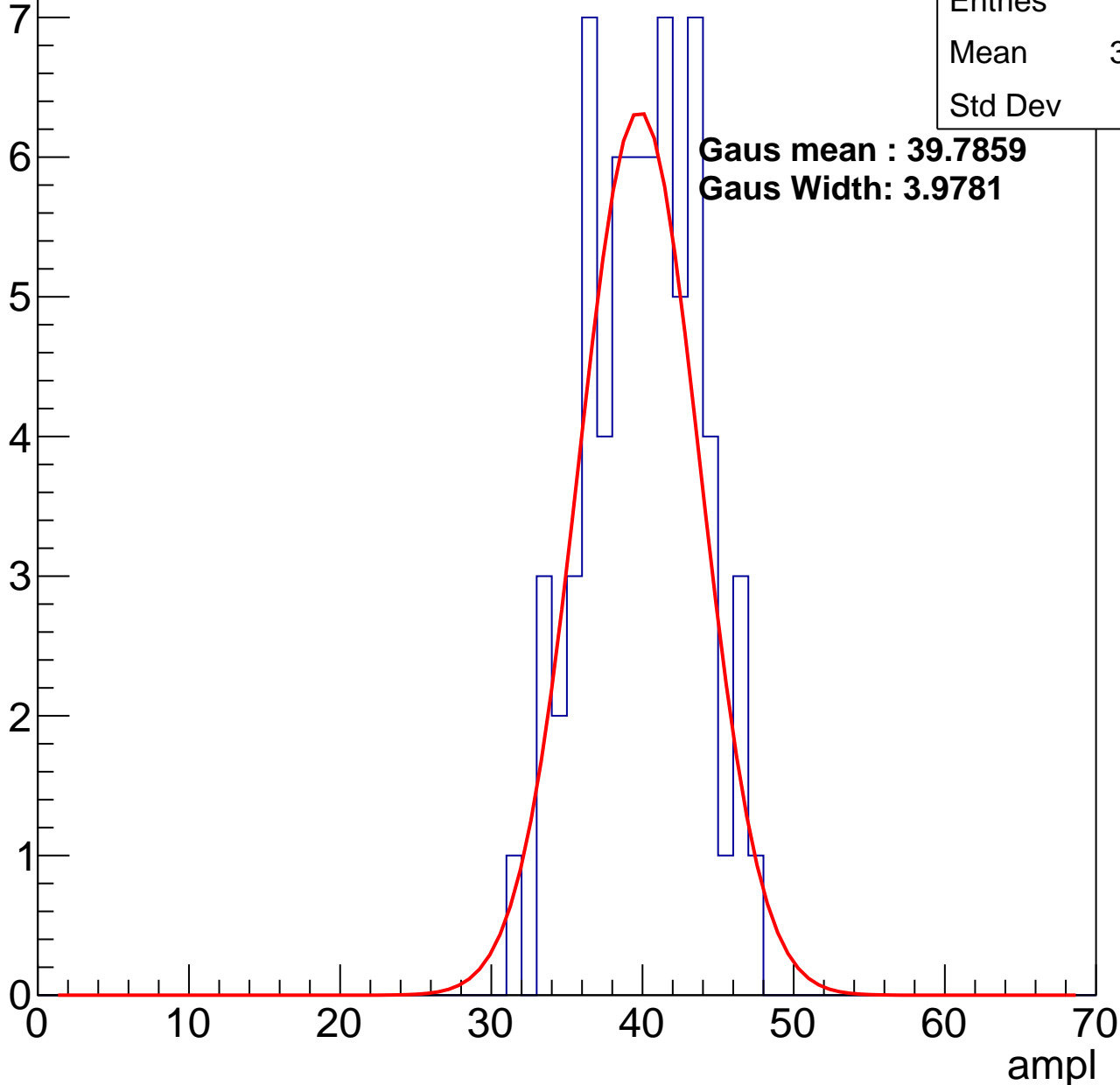
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	39.53
Std Dev	3.64

Gaus mean : 39.7859

Gaus Width: 3.9781

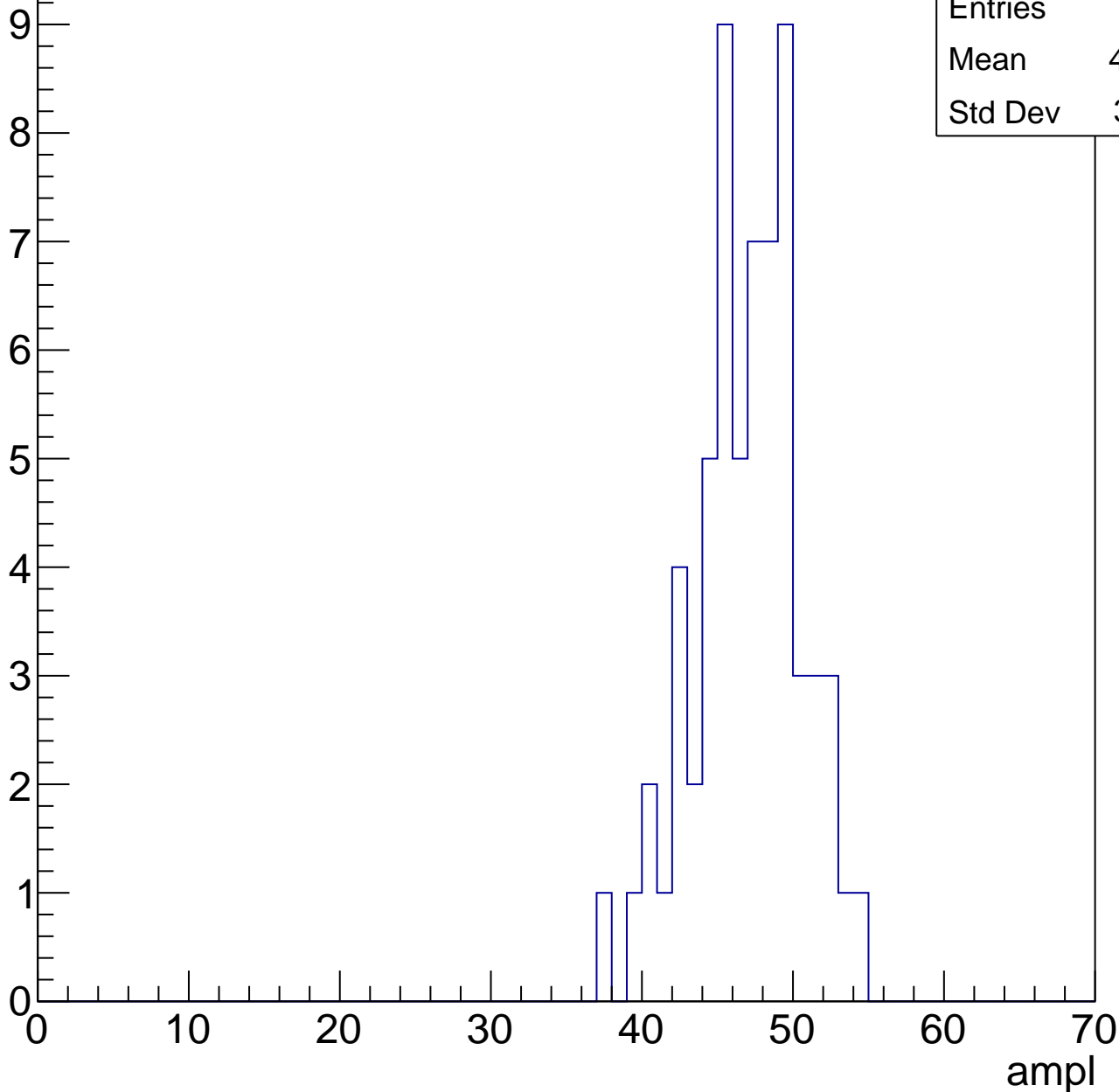


B1L100S, U1-ch30, adc3

calib_packv5_042523_0143.root, FC#4, port A2

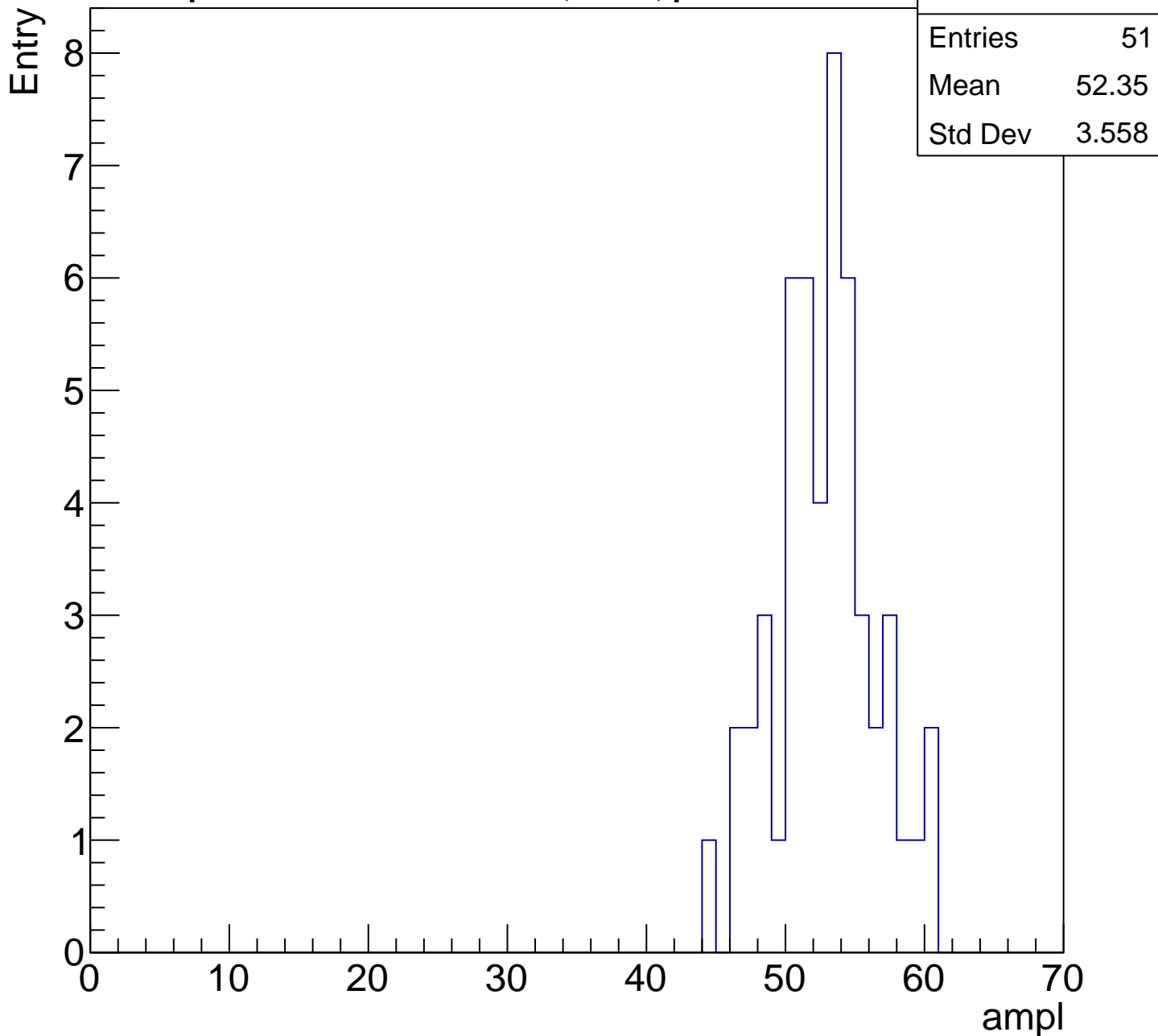
Entry

Entries	64
Mean	46.53
Std Dev	3.491



B1L100S, U1-ch30, adc4

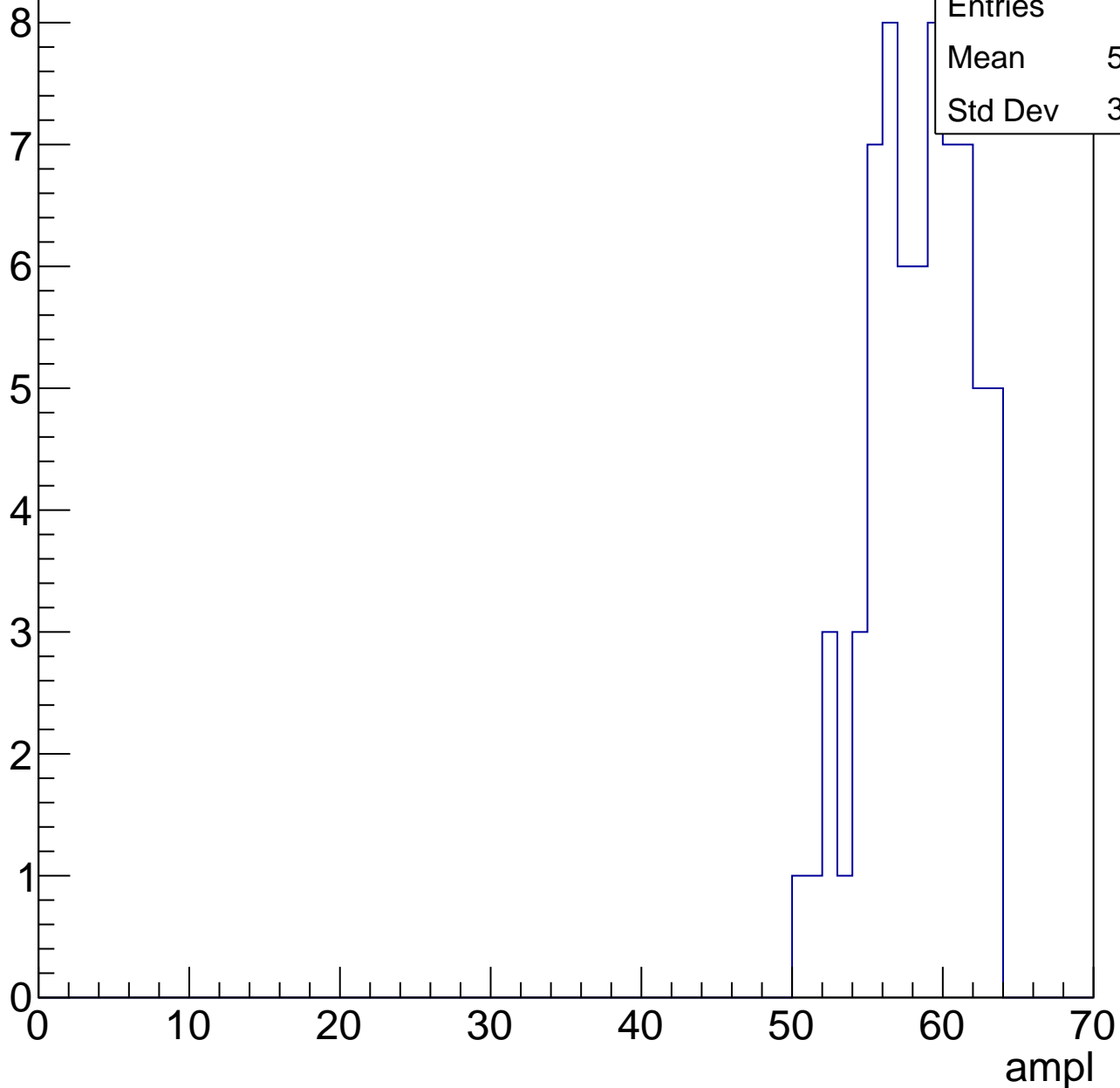
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch30, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



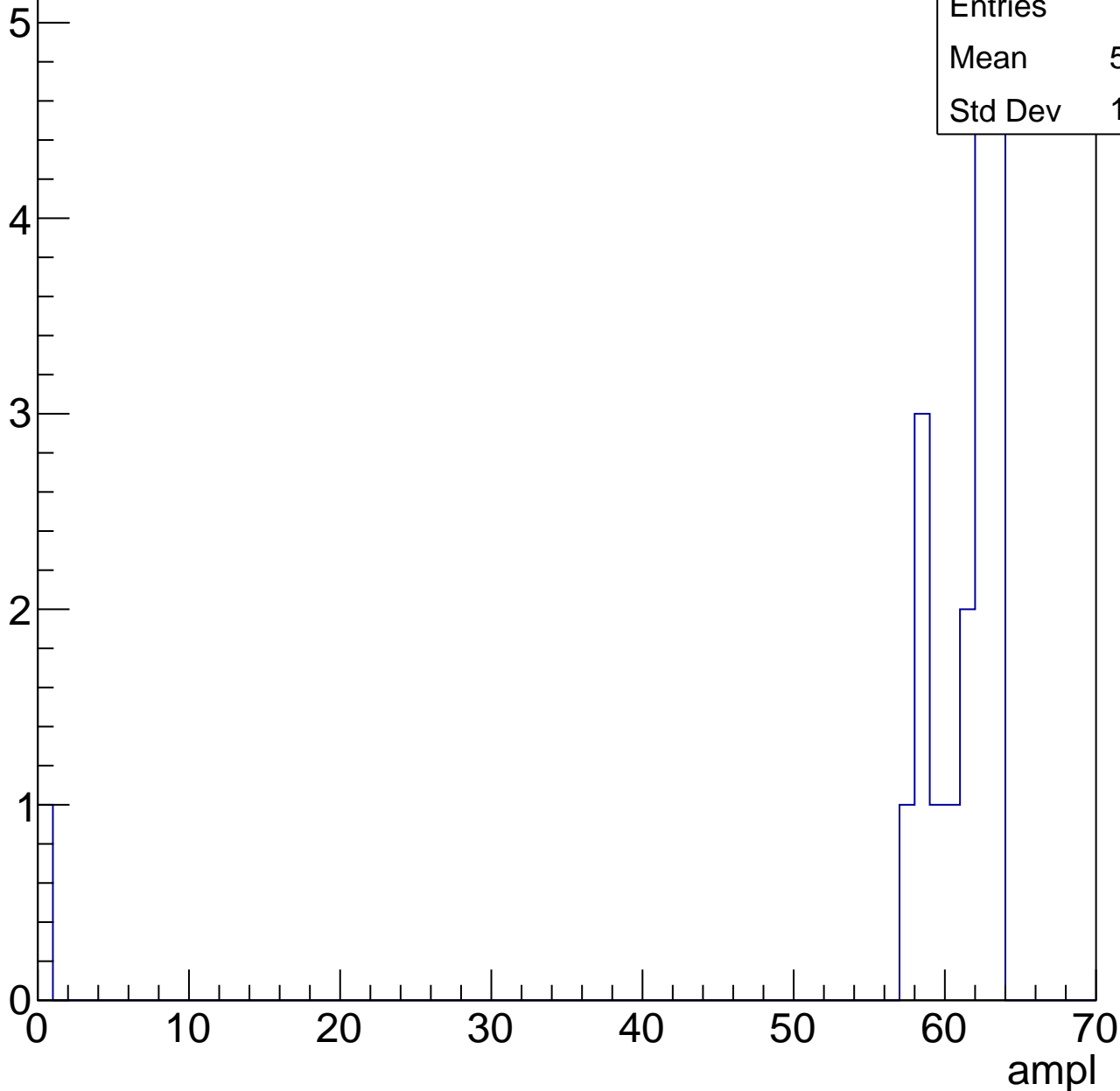
Entries	68
Mean	57.93
Std Dev	3.205

B1L100S, U1-ch30, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	57.74
Std Dev	13.75



B1L100S, U1-ch30, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.33
Std Dev	28.76

B1L100S, U1-ch31, adc0

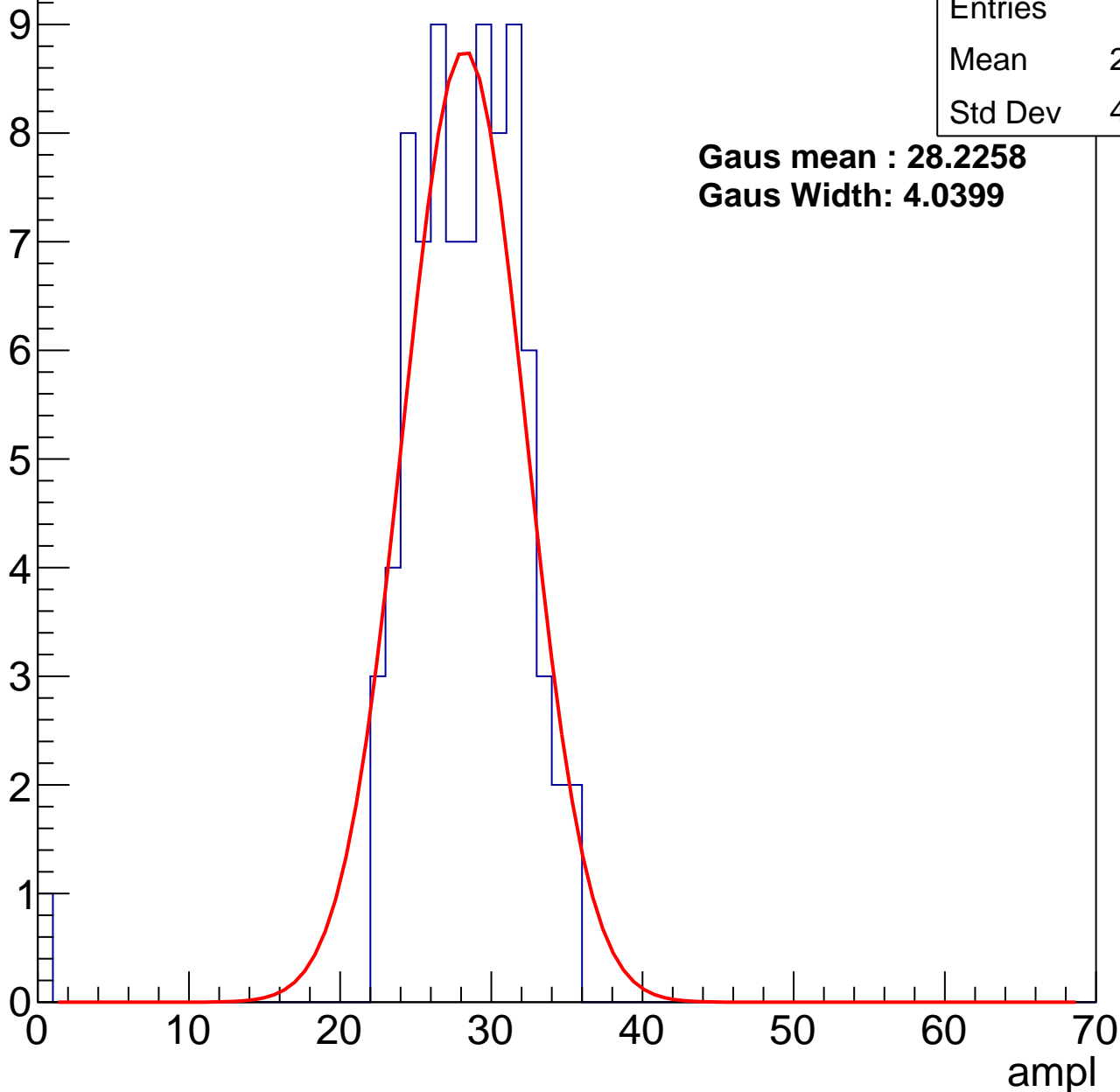
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	27.68
Std Dev	4.446

Gaus mean : 28.2258

Gaus Width: 4.0399



B1L100S, U1-ch31, adc1

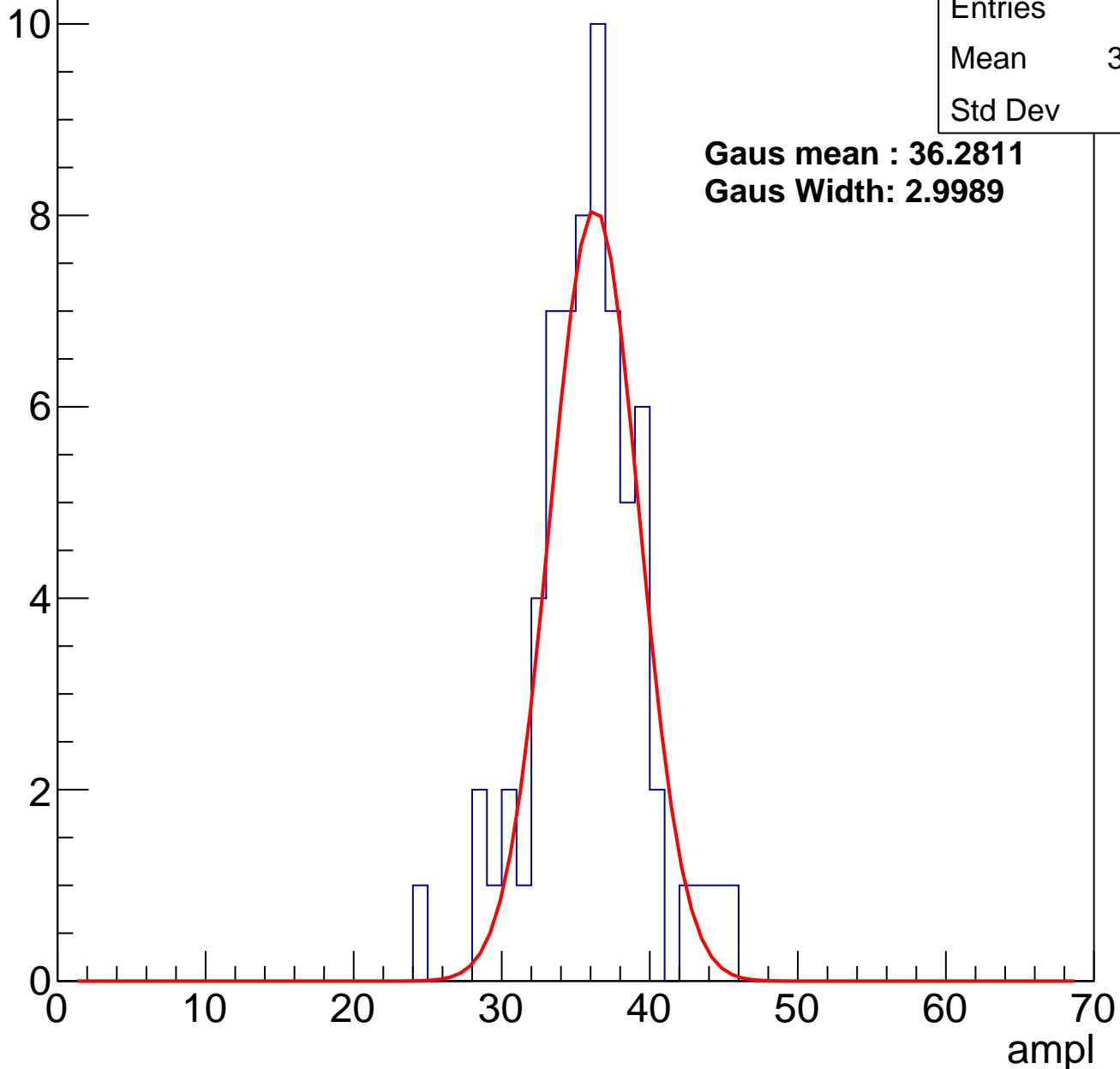
calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	35.43
Std Dev	3.67

Gaus mean : 36.2811

Gaus Width: 2.9989

Entry



B1L100S, U1-ch31, adc2

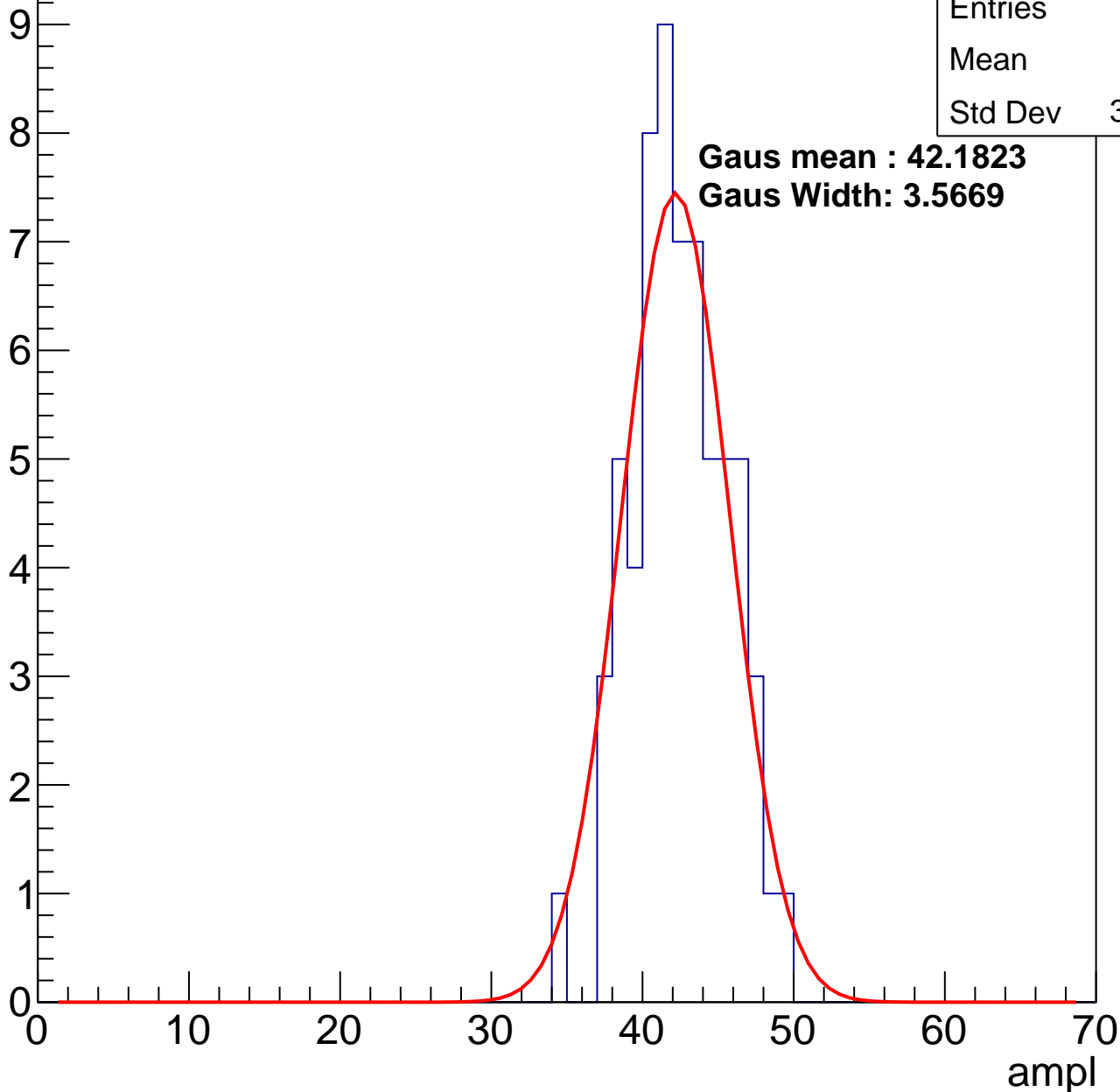
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42
Std Dev	3.082

Gaus mean : 42.1823

Gaus Width: 3.5669

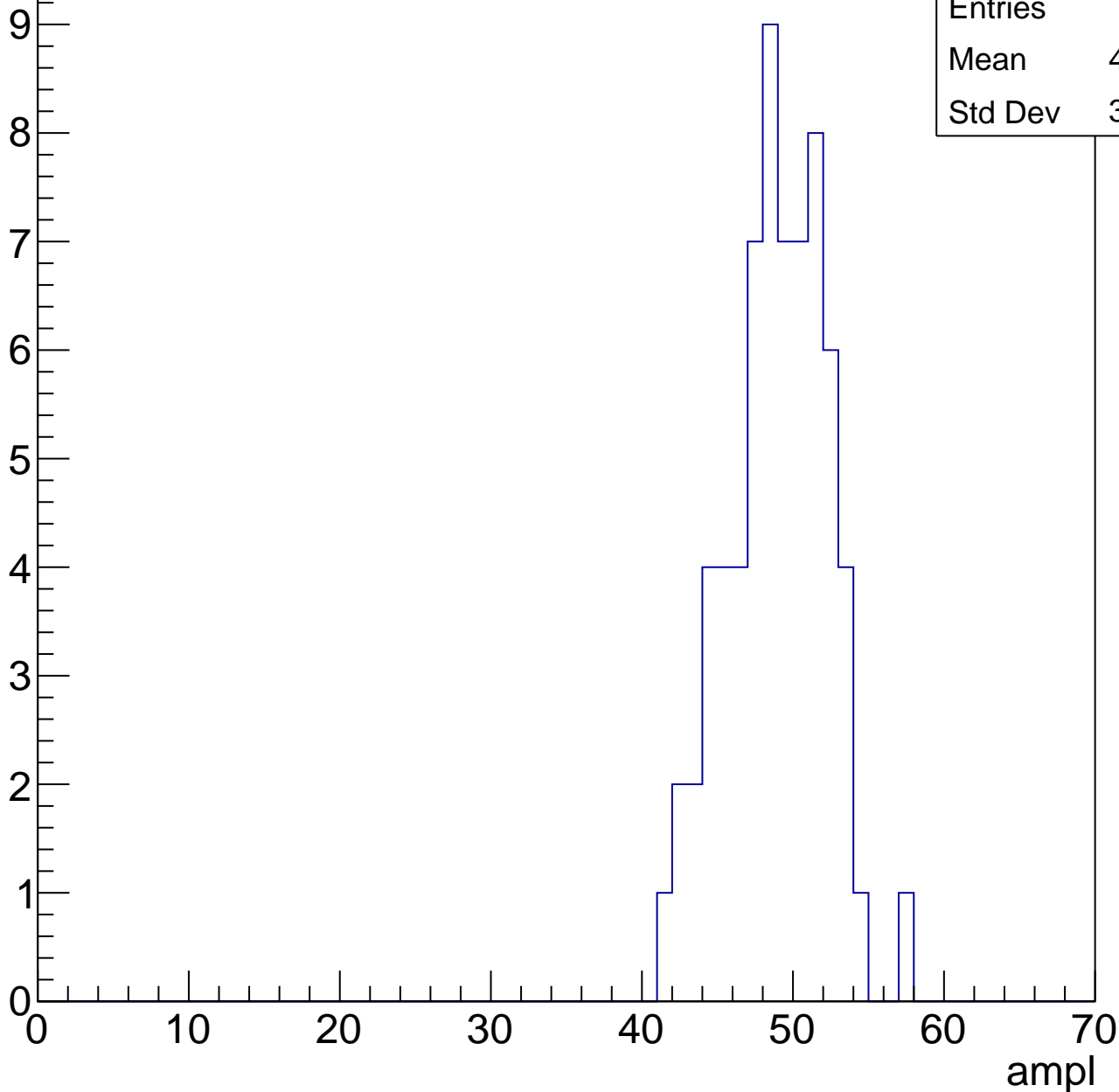


B1L100S, U1-ch31, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

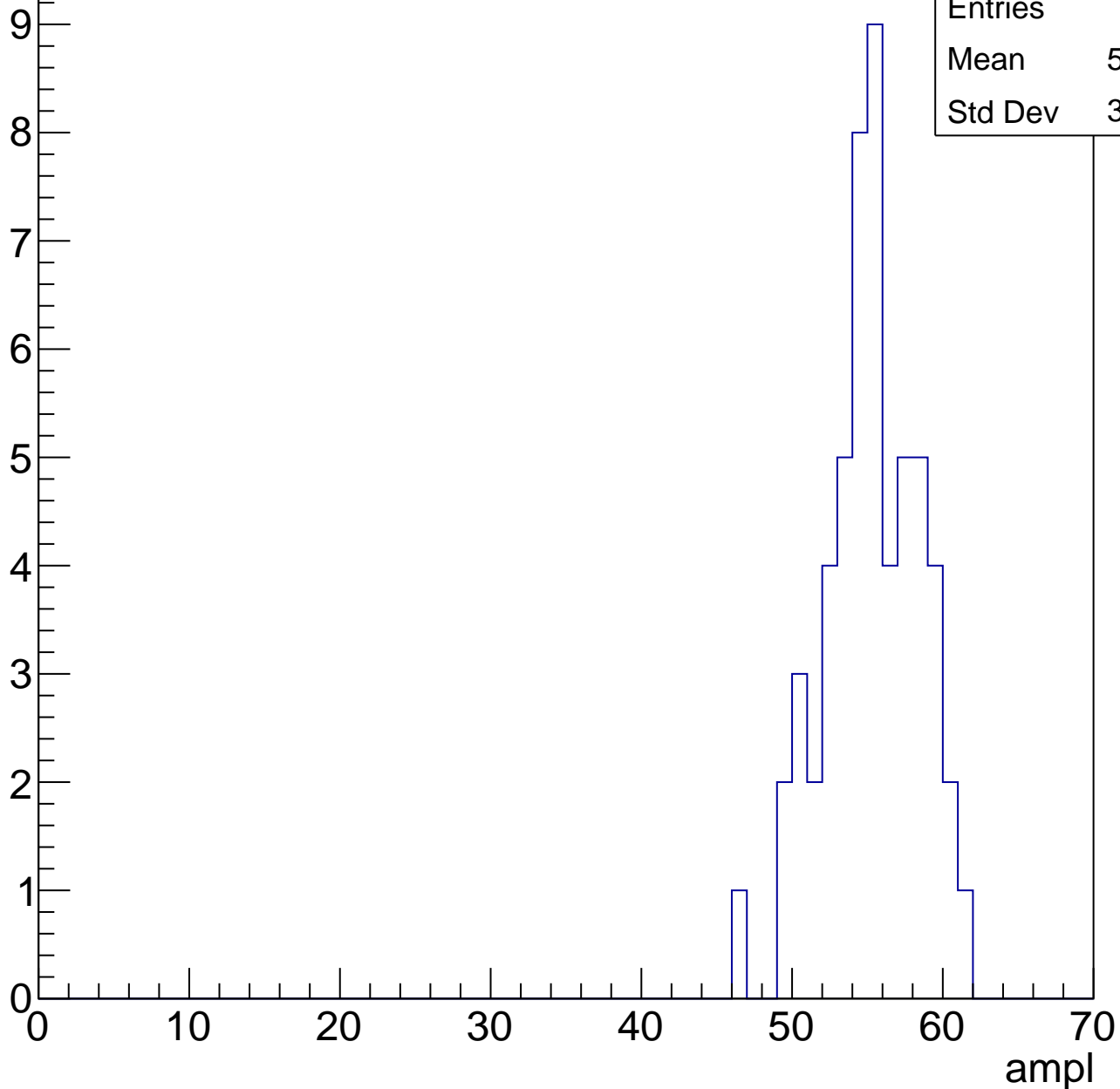
Entries	67
Mean	48.48
Std Dev	3.229



B1L100S, U1-ch31, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



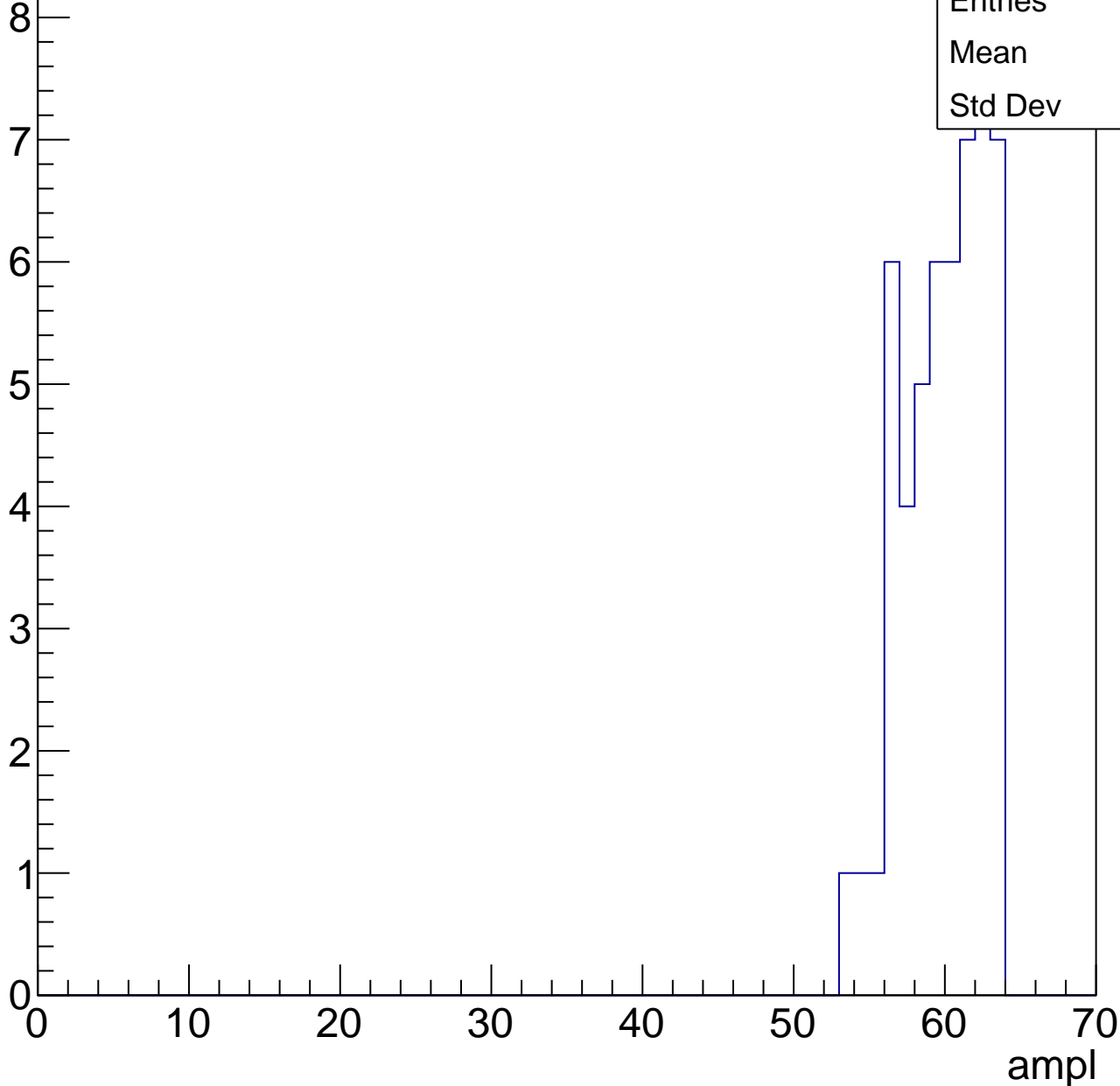
Entries	55
Mean	54.76
Std Dev	3.133

B1L100S, U1-ch31, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

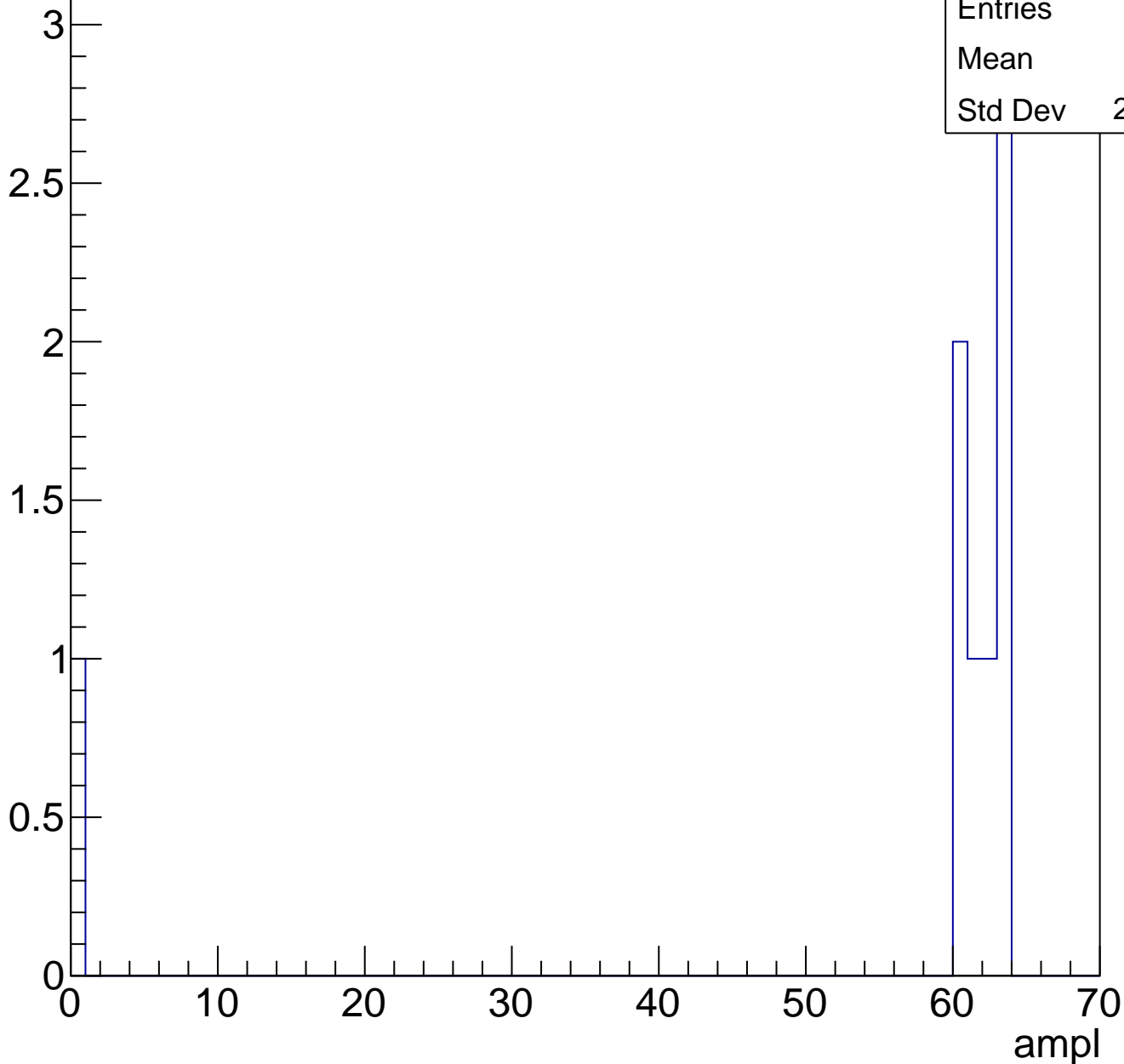
Entries	52
Mean	59.5
Std Dev	2.62



B1L100S, U1-ch31, adc6

calib_packv5_042523_0143.root, FC#4, port A2

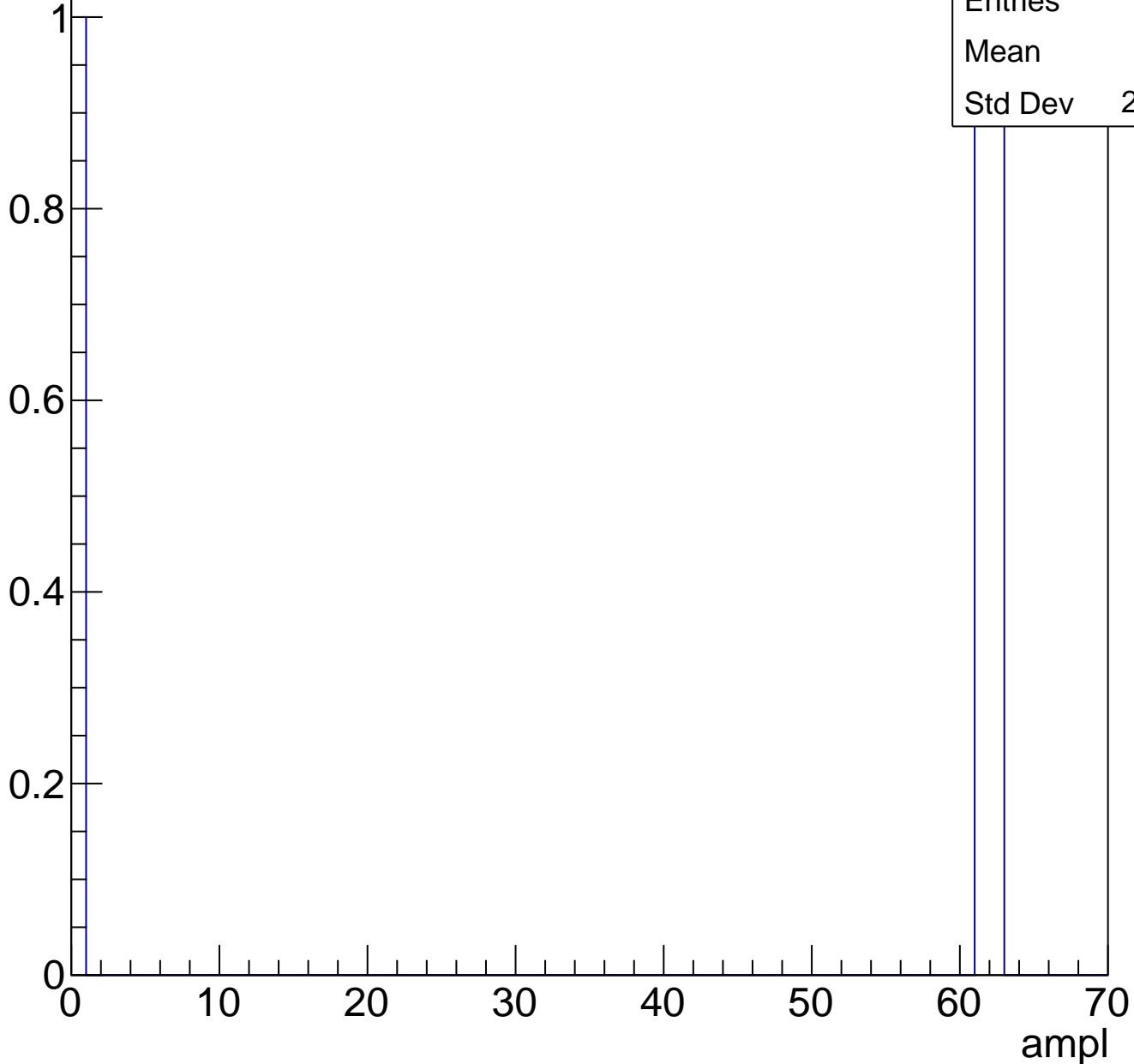
Entry



B1L100S, U1-ch31, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch32, adc0

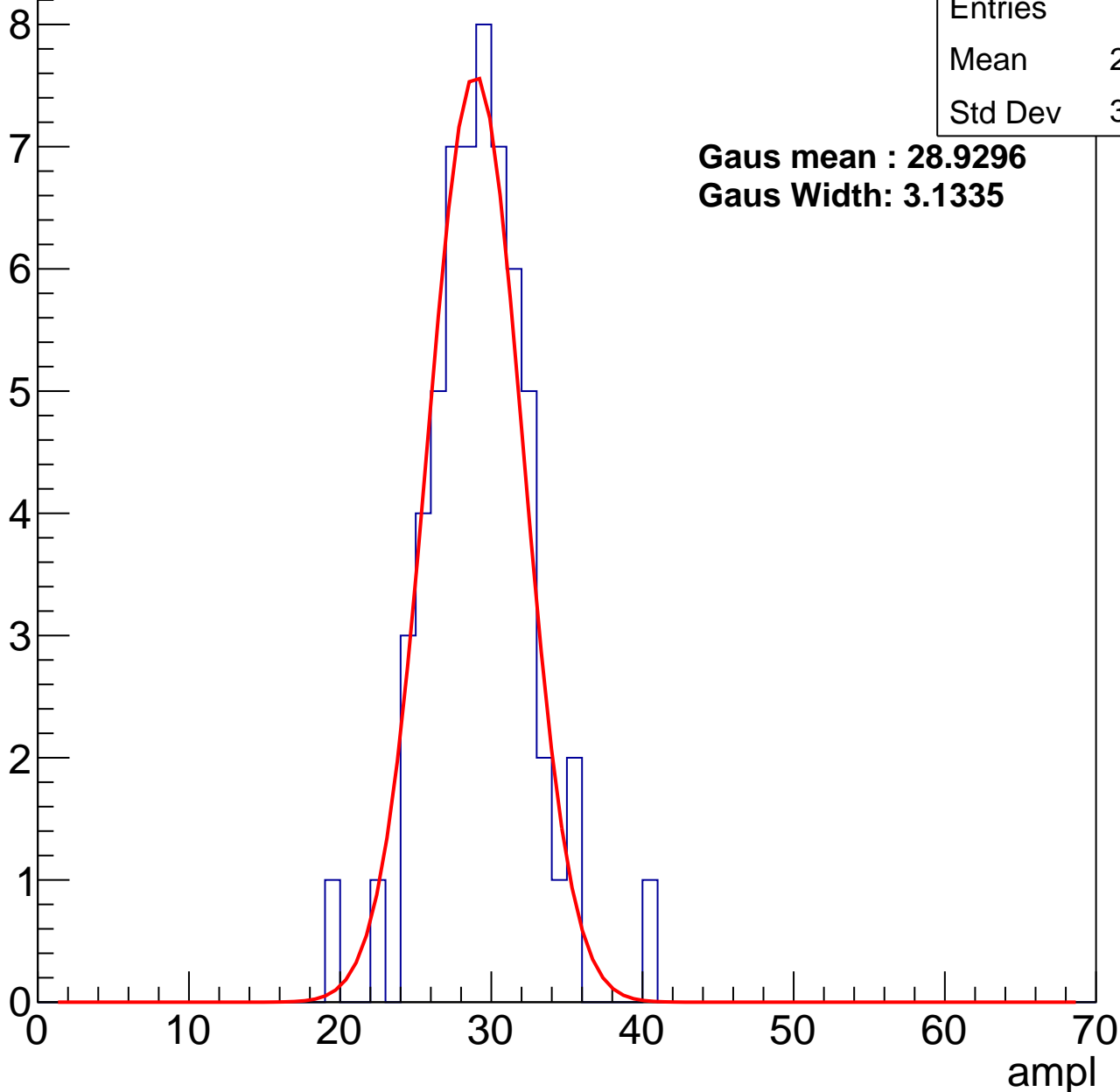
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	28.77
Std Dev	3.393

Gaus mean : 28.9296

Gaus Width: 3.1335



B1L100S, U1-ch32, adc1

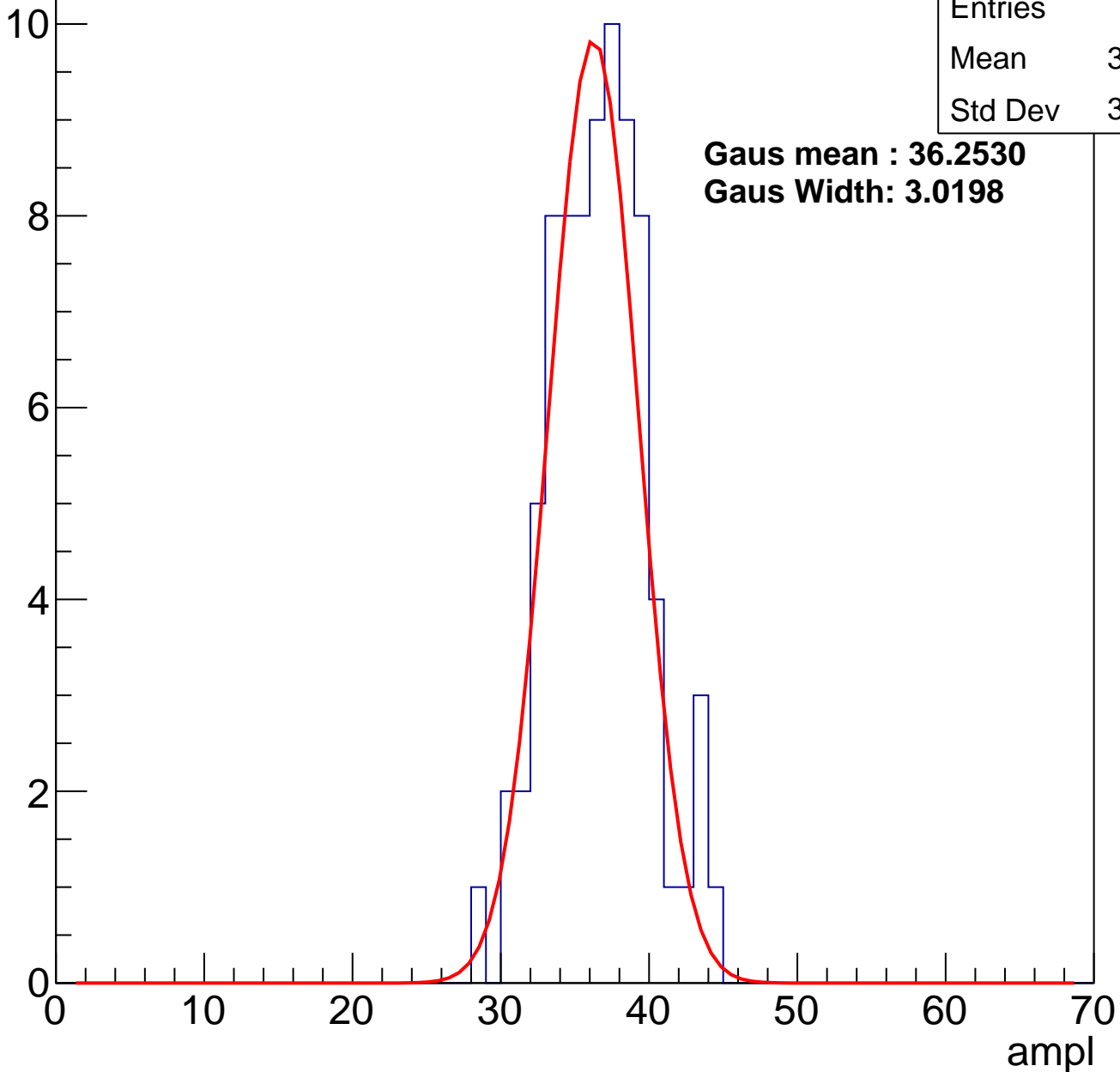
calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	36.12
Std Dev	3.222

Gaus mean : 36.2530

Gaus Width: 3.0198

Entry



B1L100S, U1-ch32, adc2

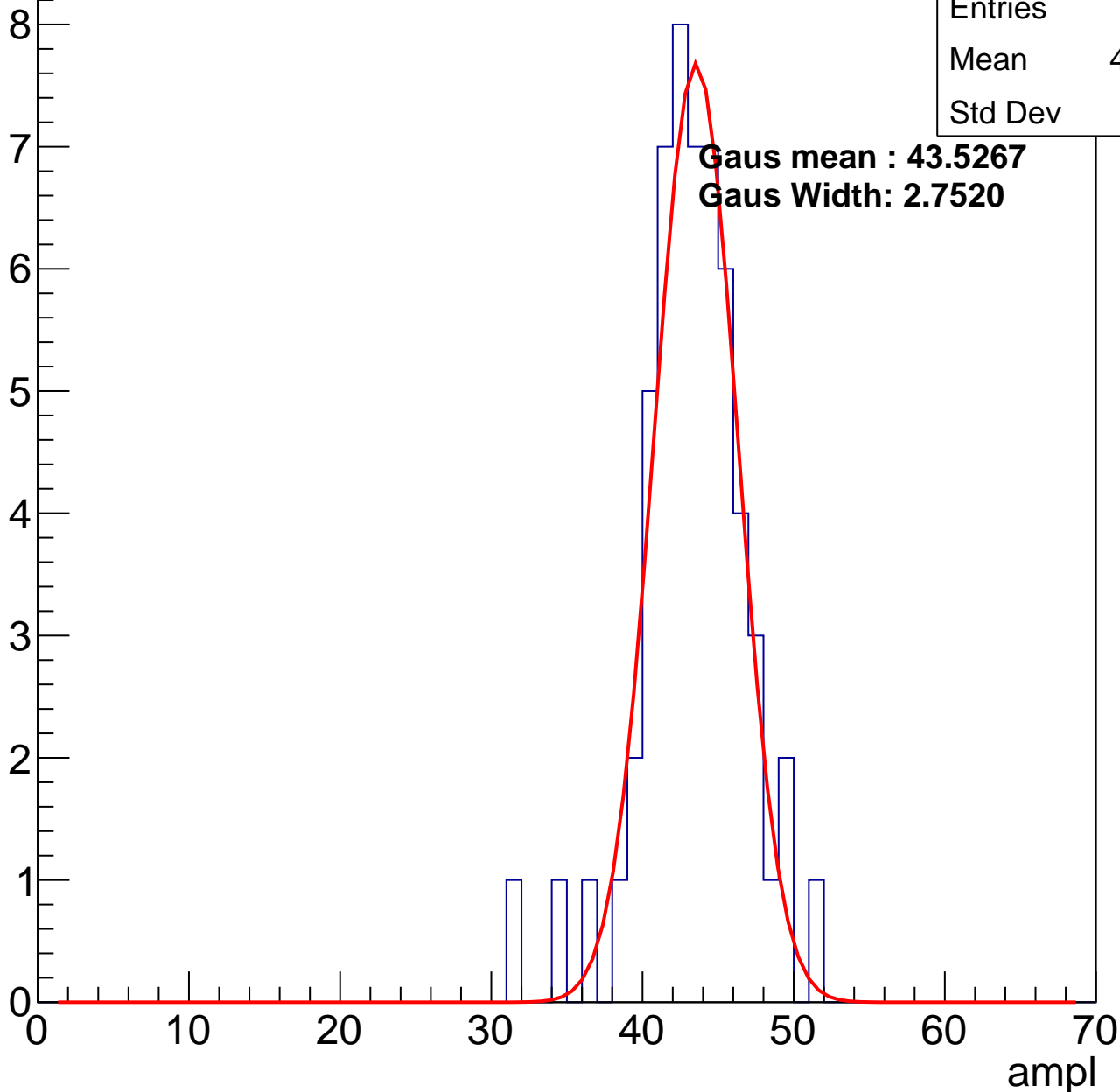
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	42.82
Std Dev	3.47

Gaus mean : 43.5267

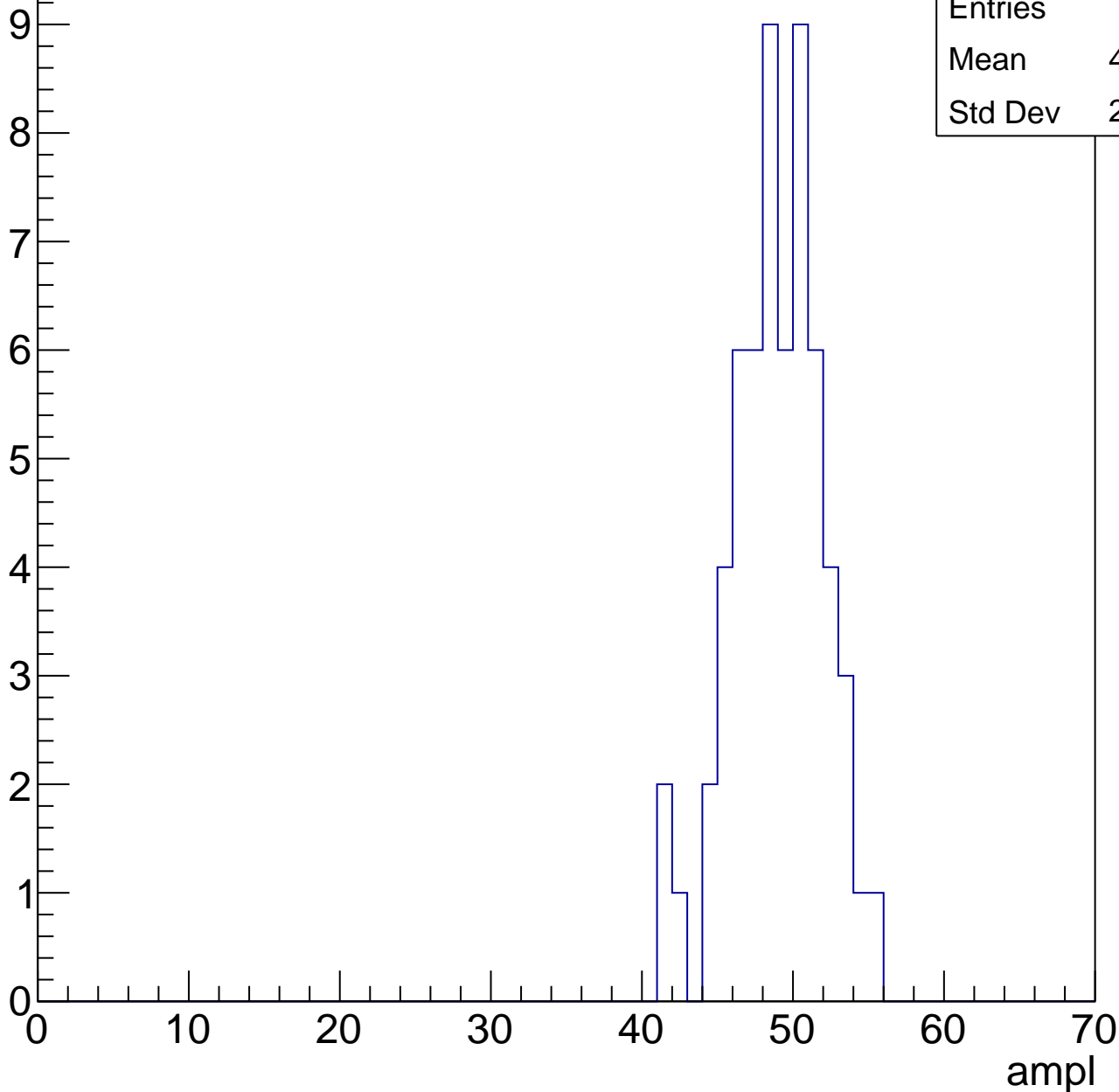
Gaus Width: 2.7520



B1L100S, U1-ch32, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

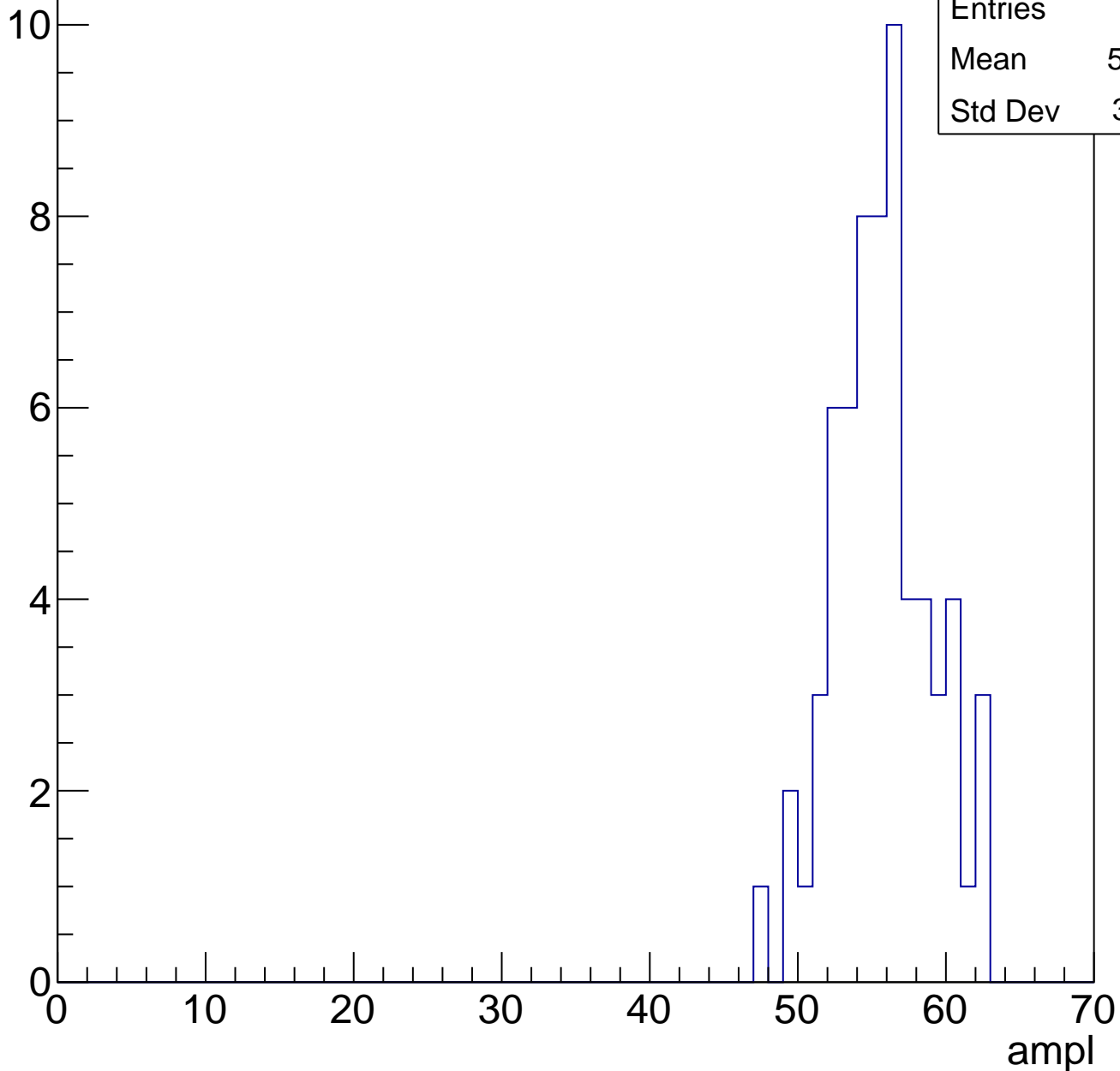


Entries	60
Mean	48.47
Std Dev	2.997

B1L100S, U1-ch32, adc4

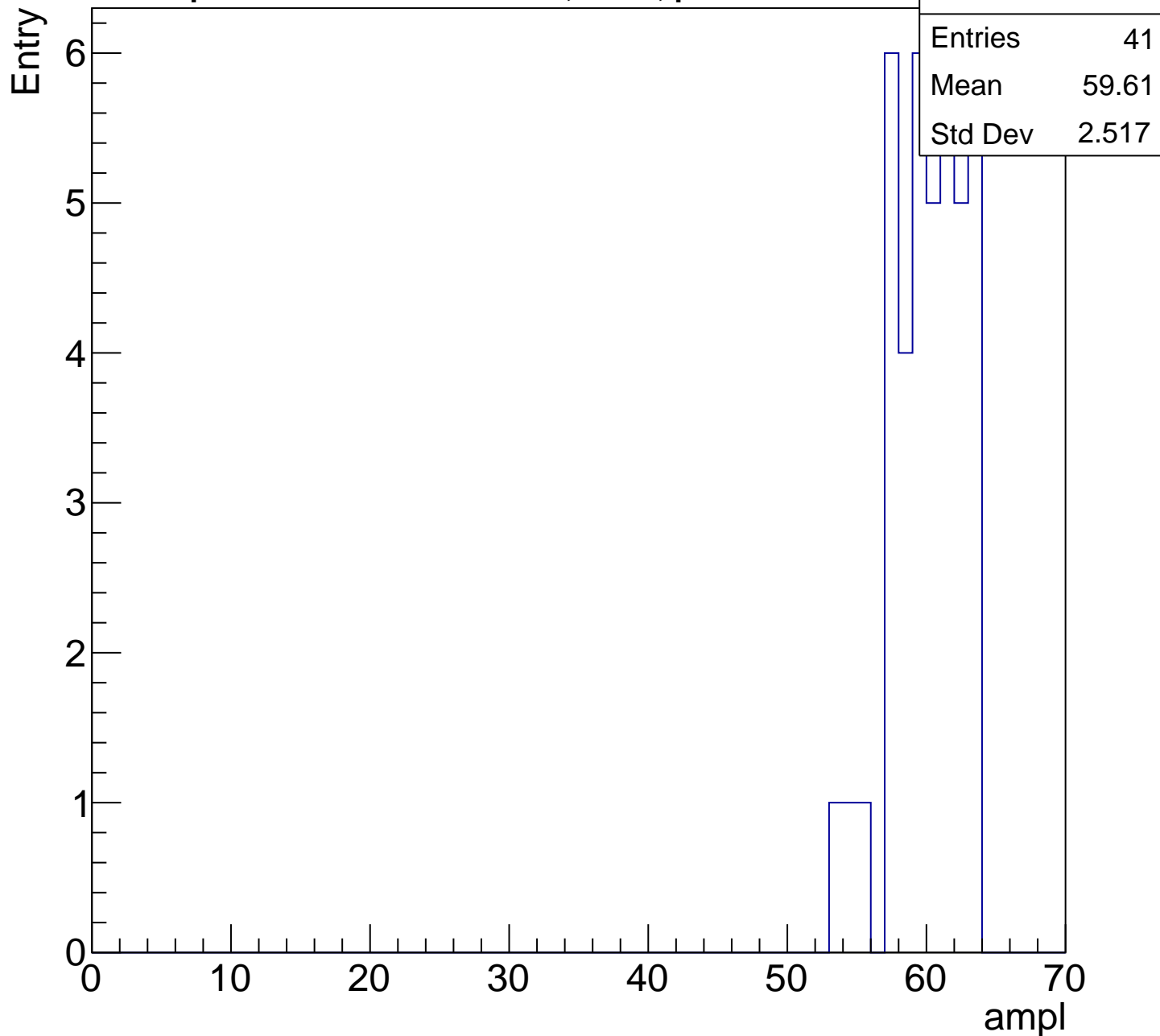
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch32, adc5

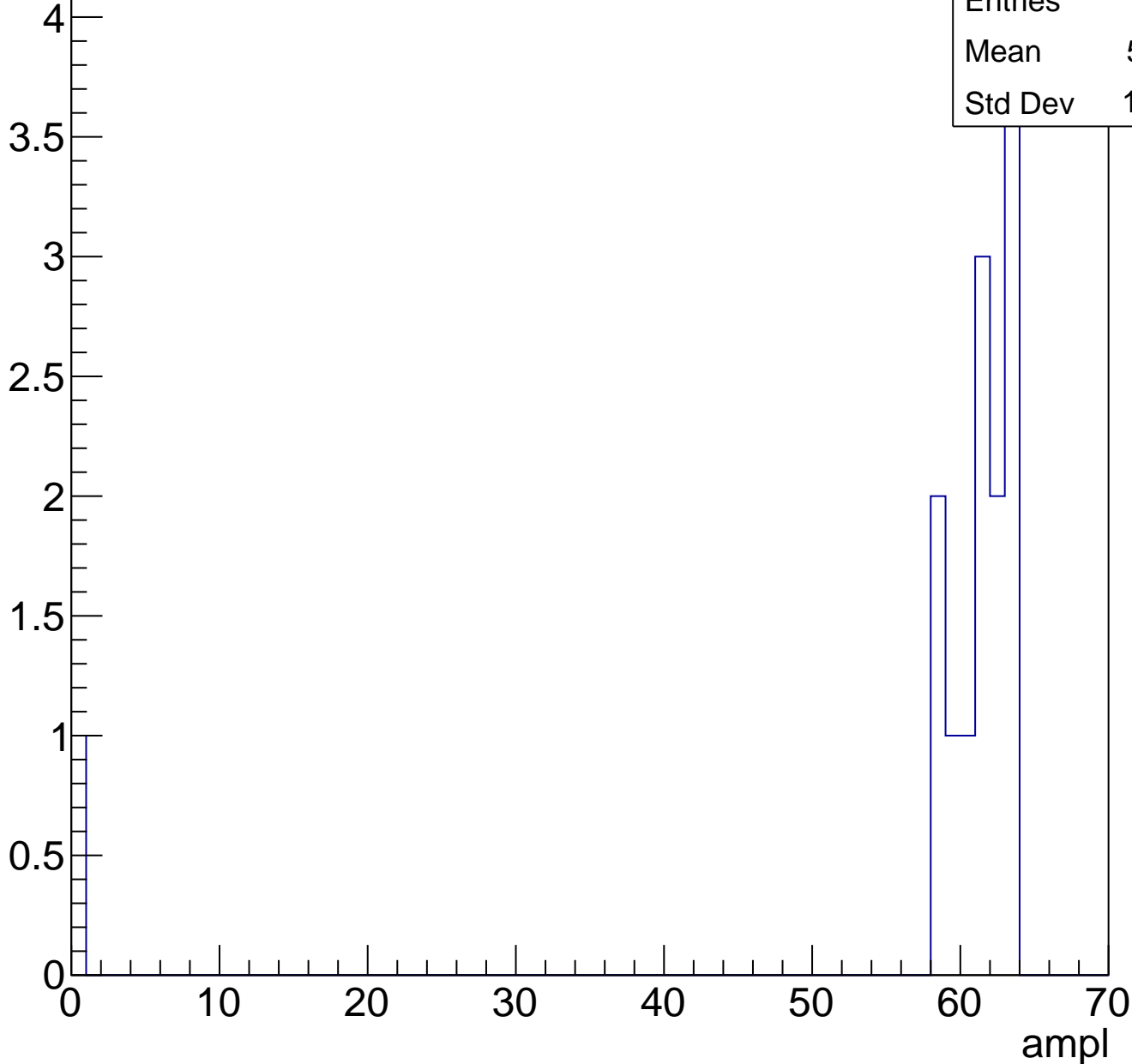
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch32, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch32, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch33, adc0

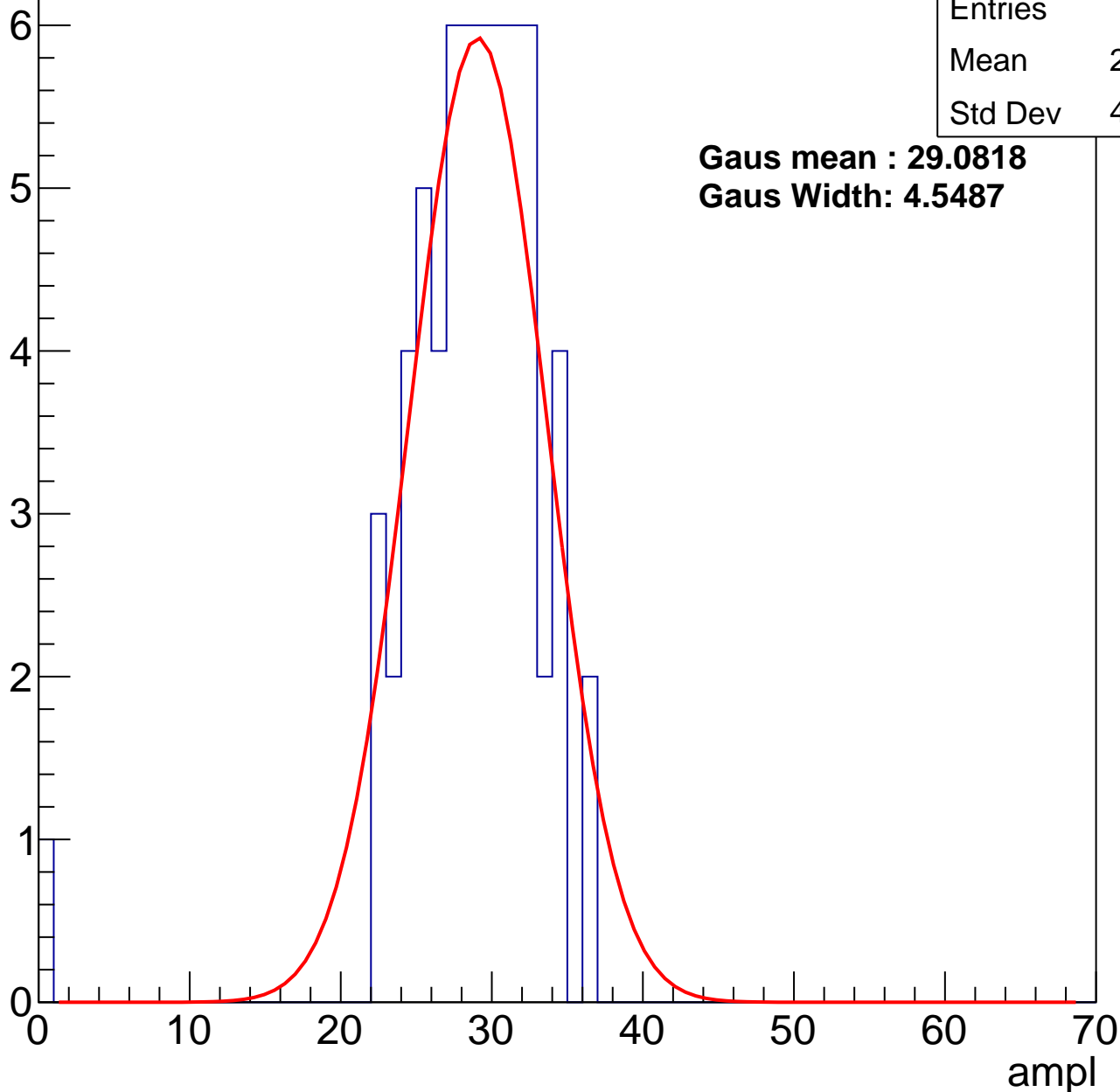
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.14
Std Dev	4.998

Gaus mean : 29.0818

Gaus Width: 4.5487



B1L100S, U1-ch33, adc1

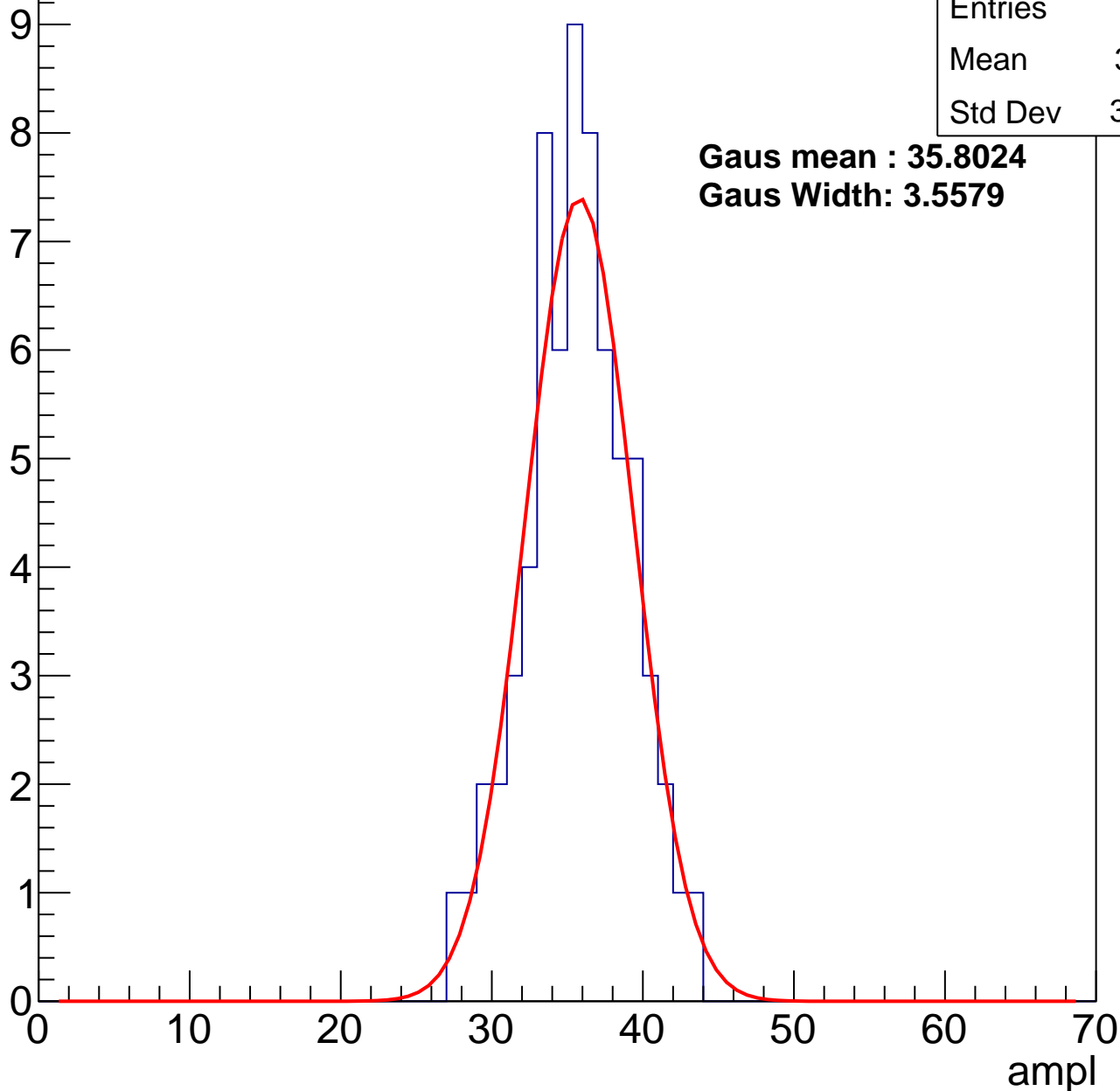
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	35.21
Std Dev	3.392

Gaus mean : 35.8024

Gaus Width: 3.5579



B1L100S, U1-ch33, adc2

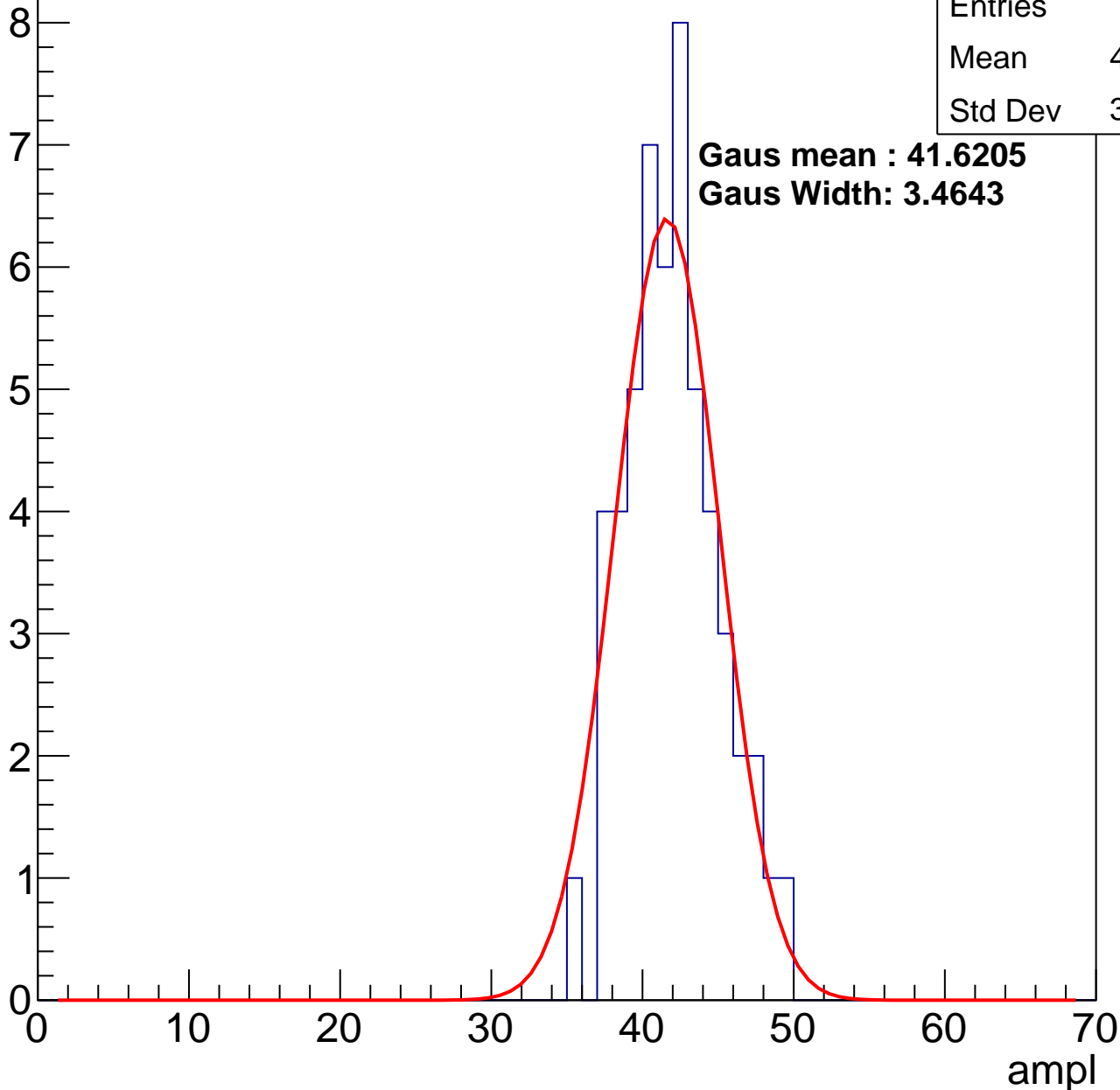
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	41.53
Std Dev	3.057

Gaus mean : 41.6205

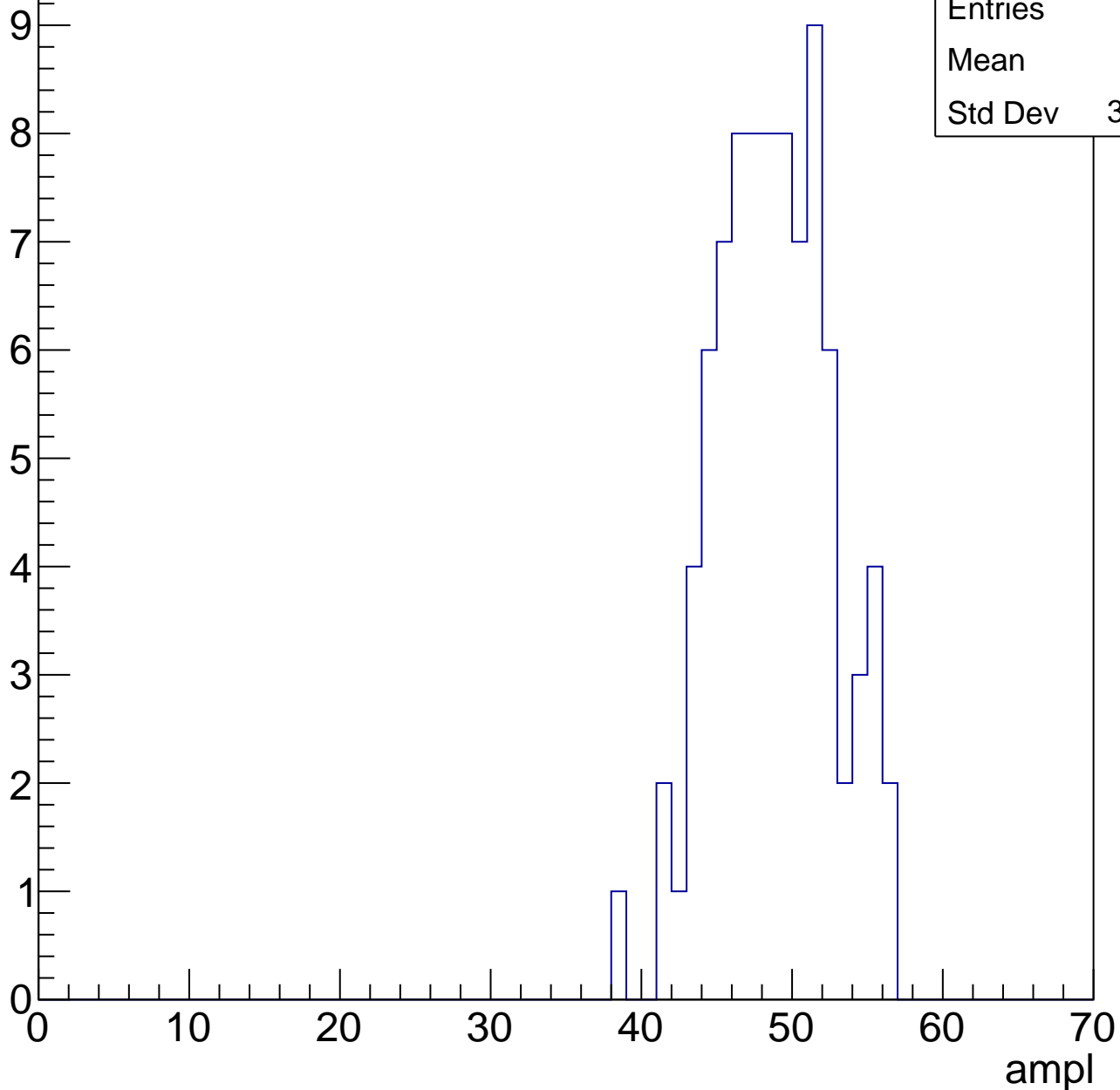
Gaus Width: 3.4643



B1L100S, U1-ch33, adc3

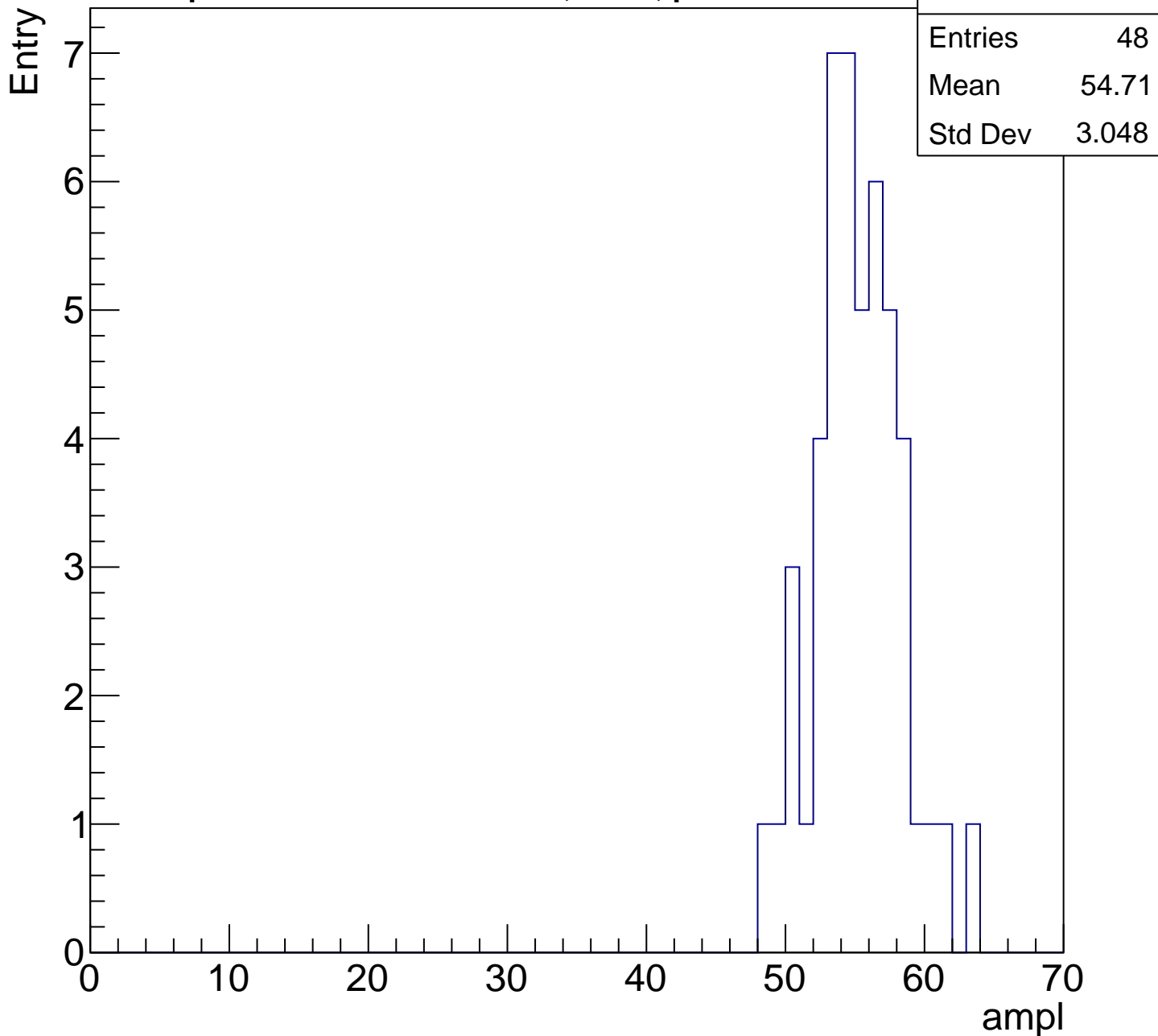
calib_packv5_042523_0143.root, FC#4, port A2

Entry



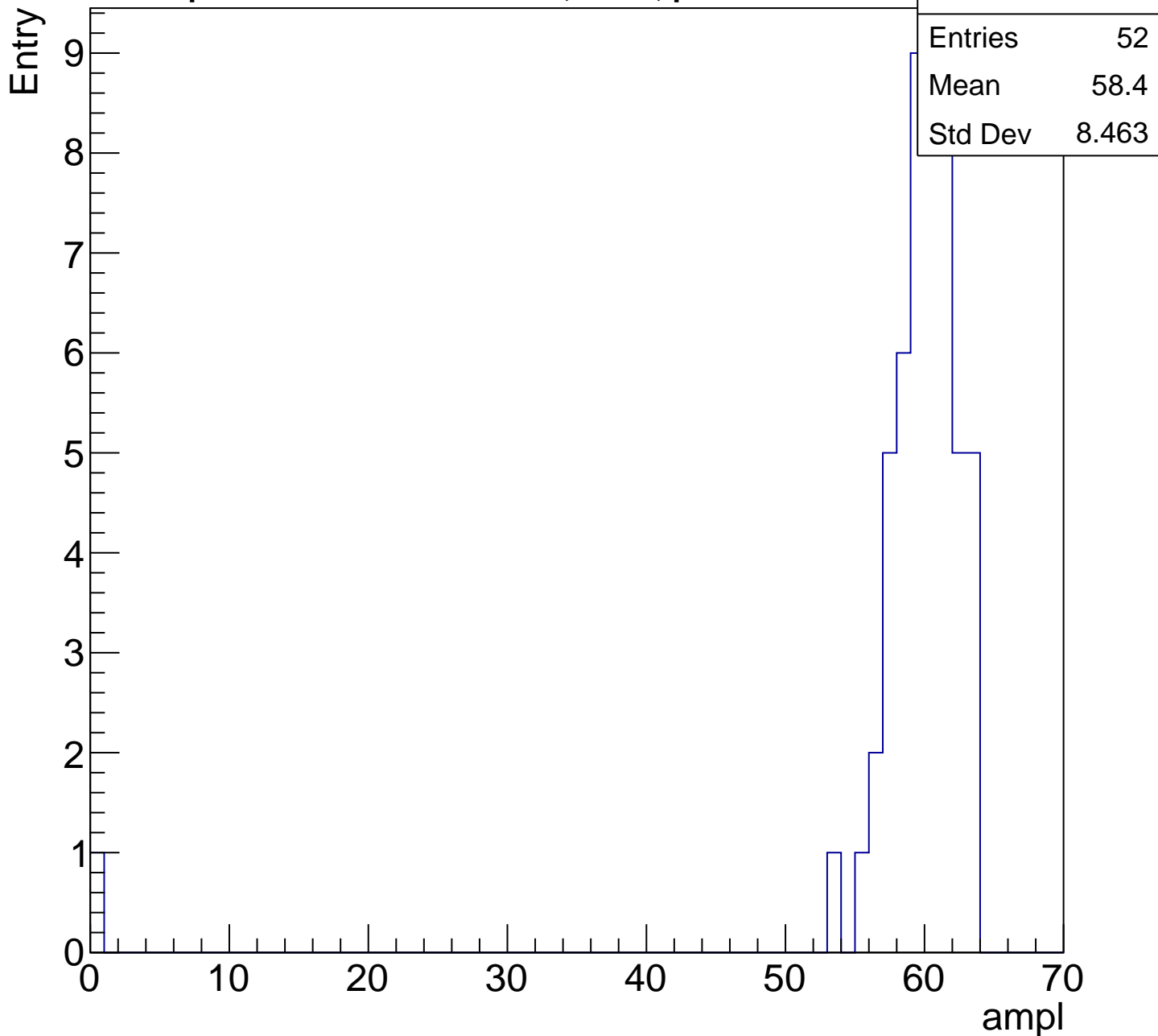
B1L100S, U1-ch33, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch33, adc5

calib_packv5_042523_0143.root, FC#4, port A2

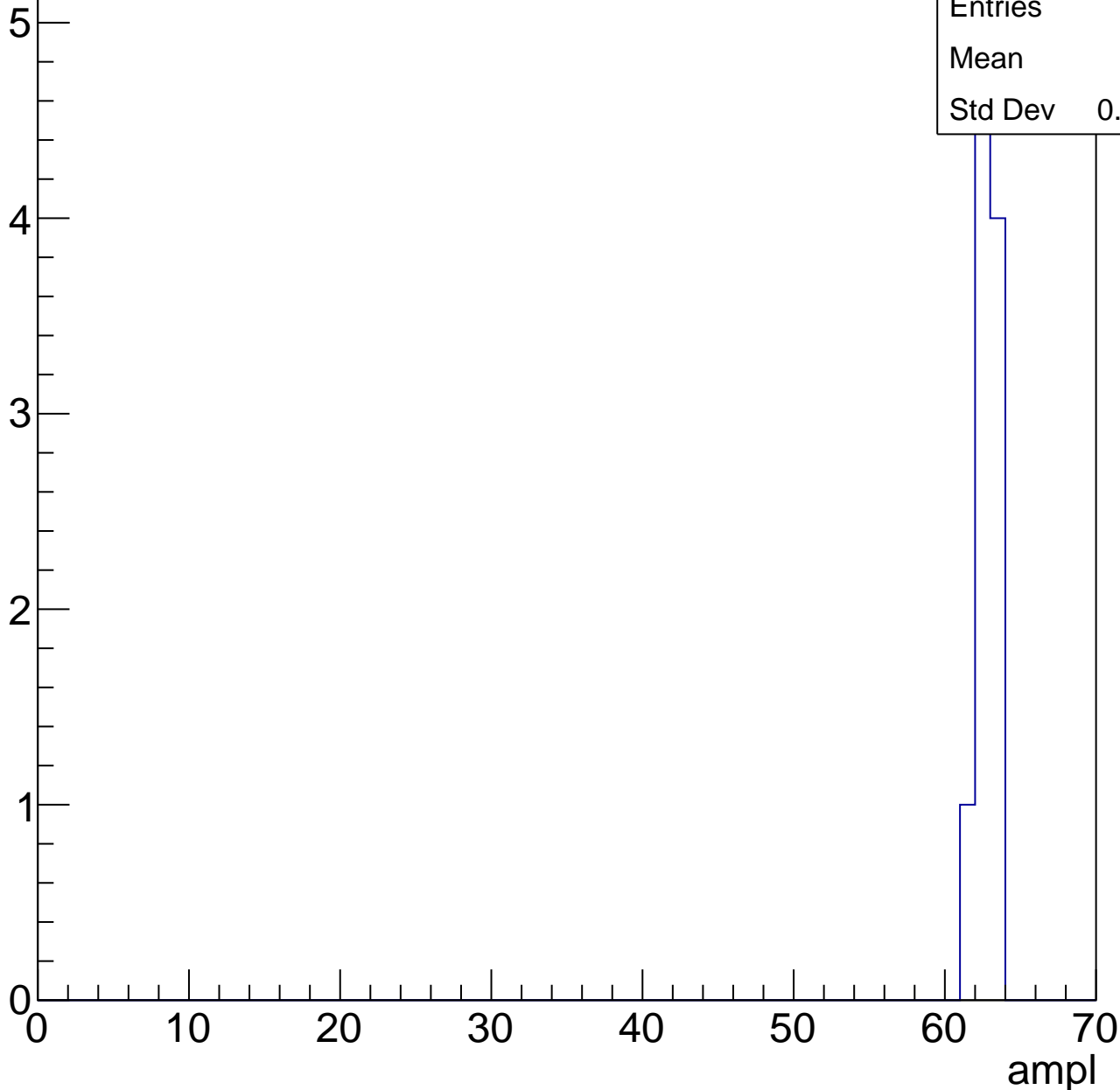


B1L100S, U1-ch33, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	62.3
Std Dev	0.6403



B1L100S, U1-ch33, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch34, adc0

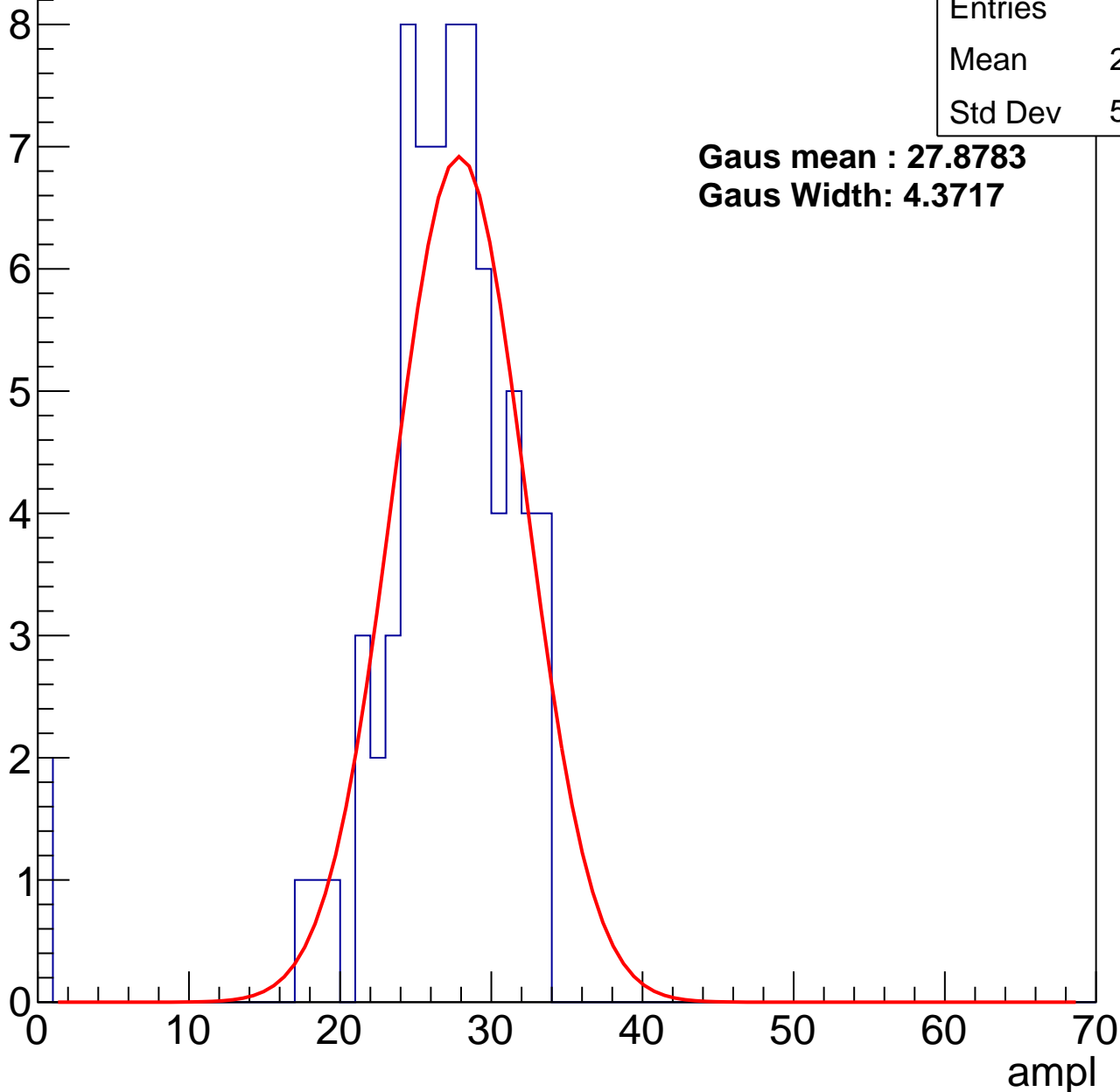
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	26.05
Std Dev	5.625

Gaus mean : 27.8783

Gaus Width: 4.3717



B1L100S, U1-ch34, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	80
Mean	34.23
Std Dev	3.602

Gaus mean : 34.3796

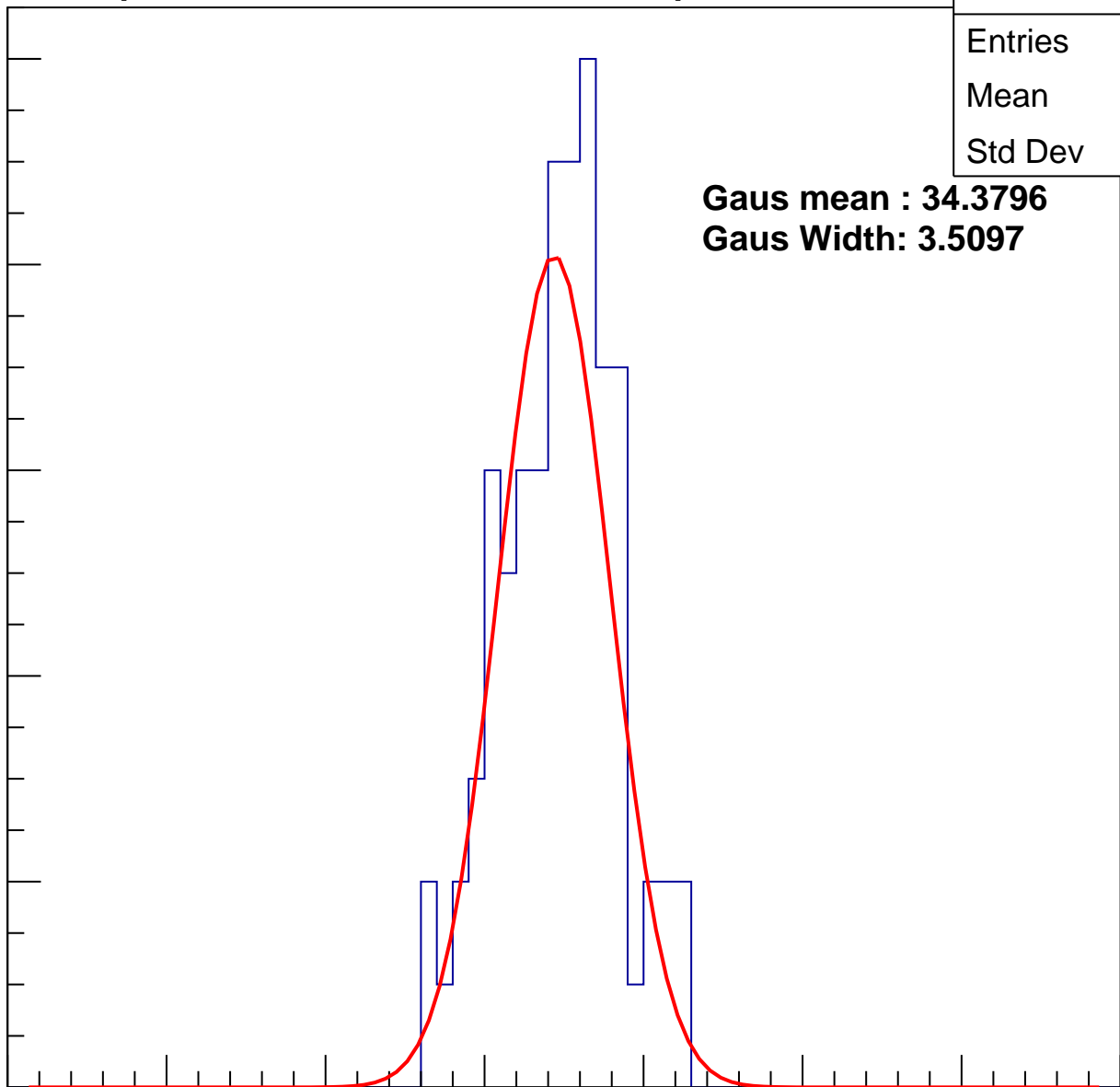
Gaus Width: 3.5097

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch34, adc2

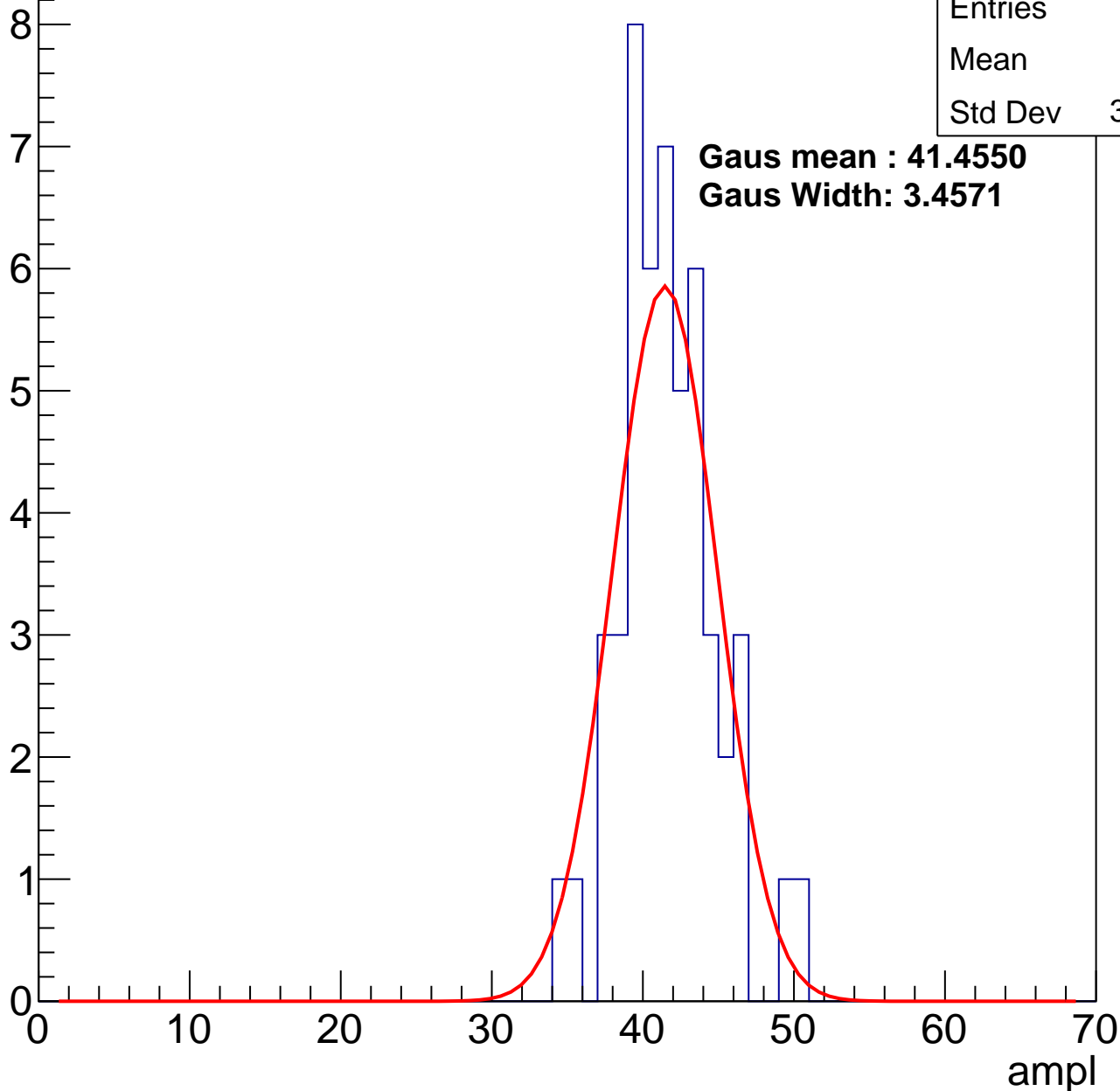
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	41.2
Std Dev	3.175

Gaus mean : 41.4550

Gaus Width: 3.4571



B1L100S, U1-ch34, adc3

calib_packv5_042523_0143.root, FC#4, port A2

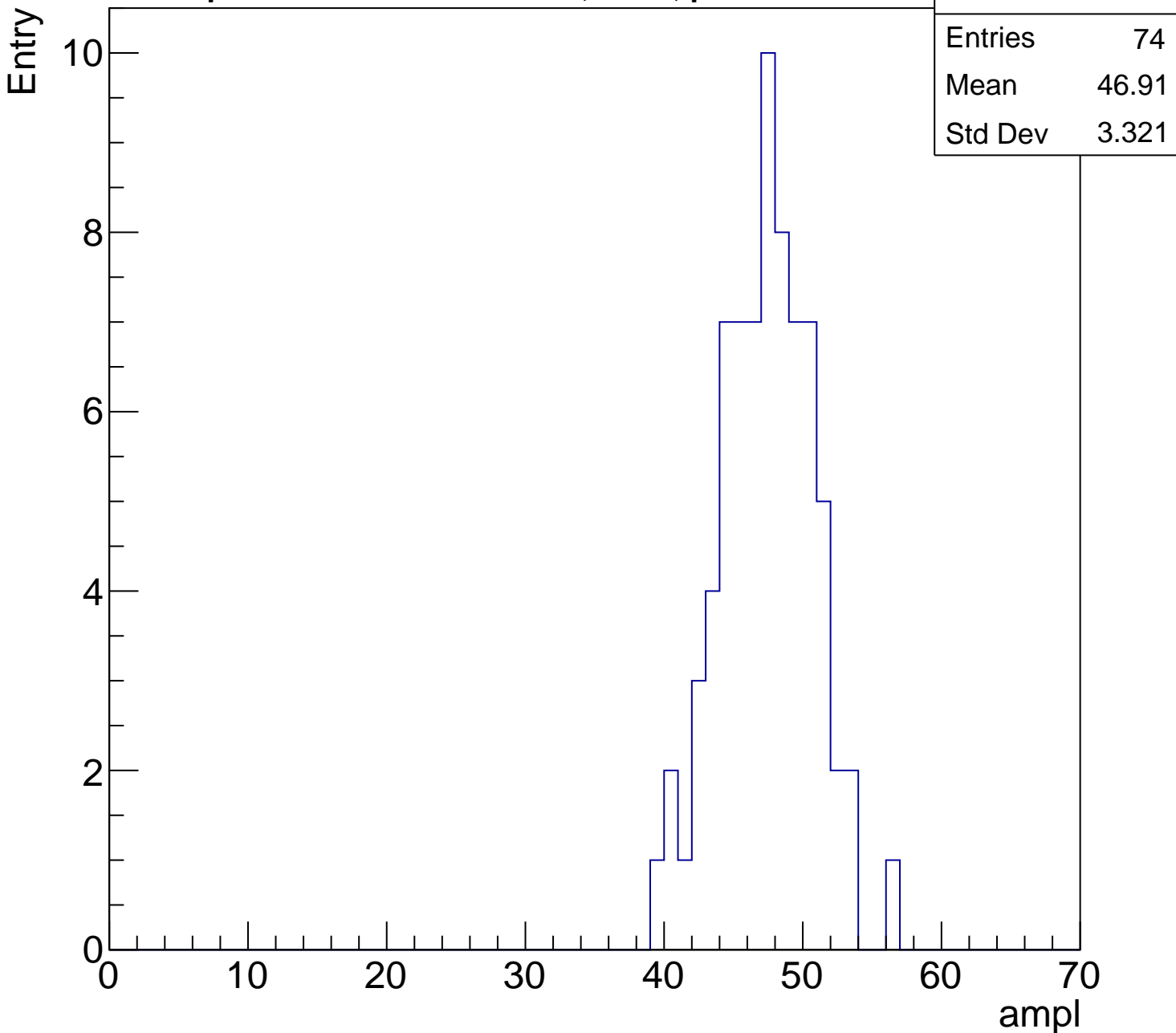
Entries	74
Mean	46.91
Std Dev	3.321

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

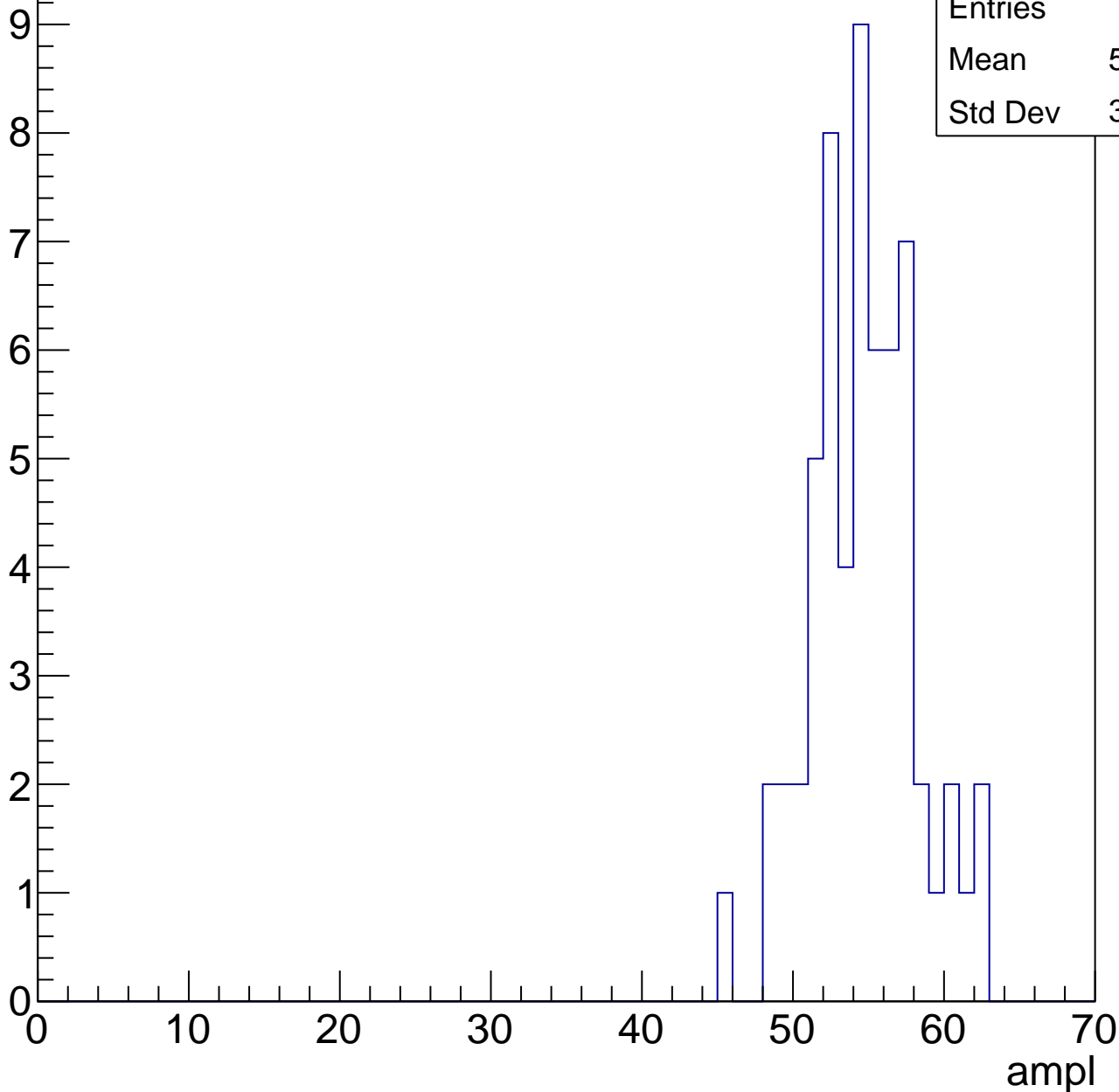


B1L100S, U1-ch34, adc4

calib_packv5_042523_0143.root, FC#4, port A2

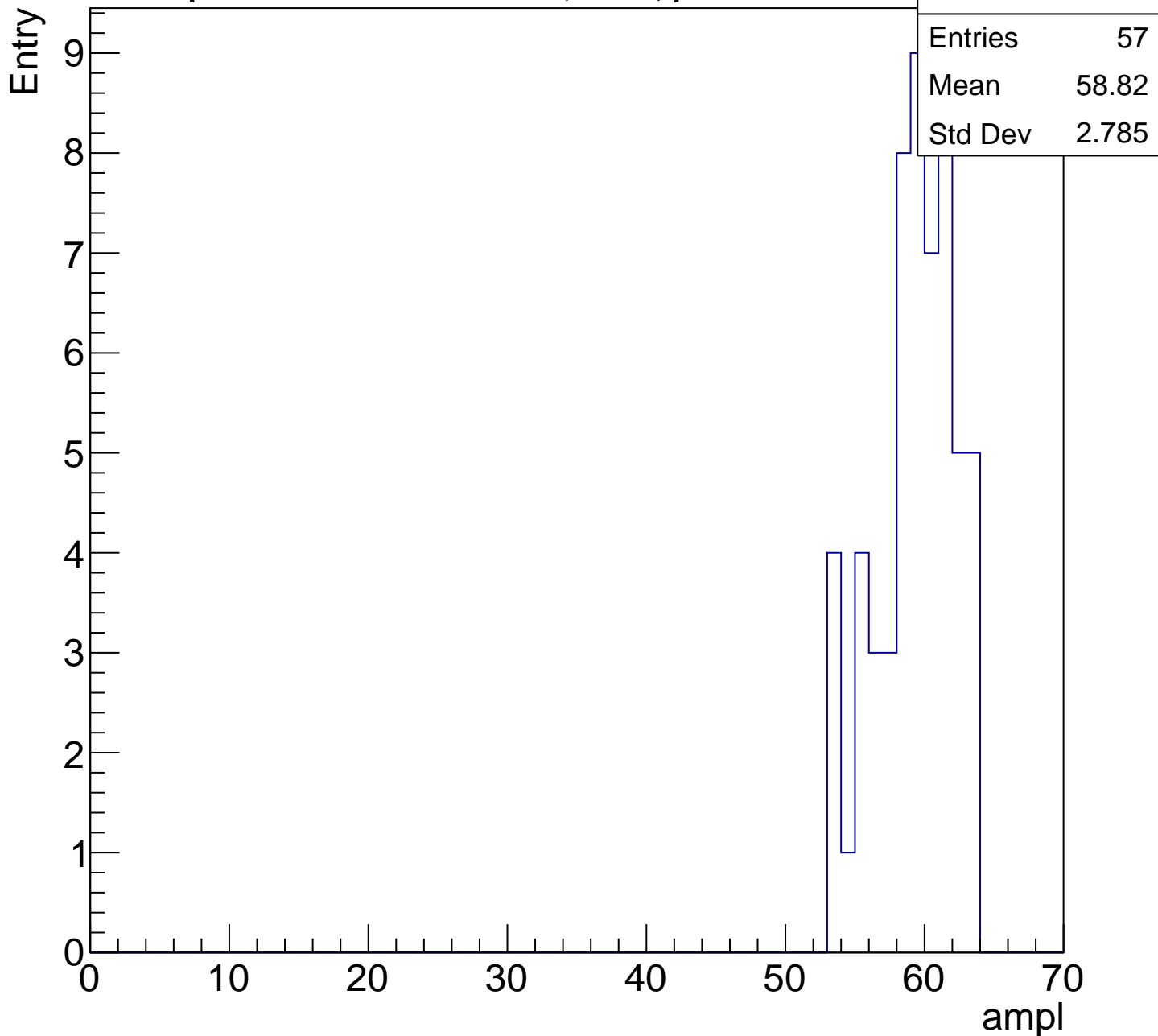
Entry

Entries	60
Mean	54.22
Std Dev	3.445



B1L100S, U1-ch34, adc5

calib_packv5_042523_0143.root, FC#4, port A2

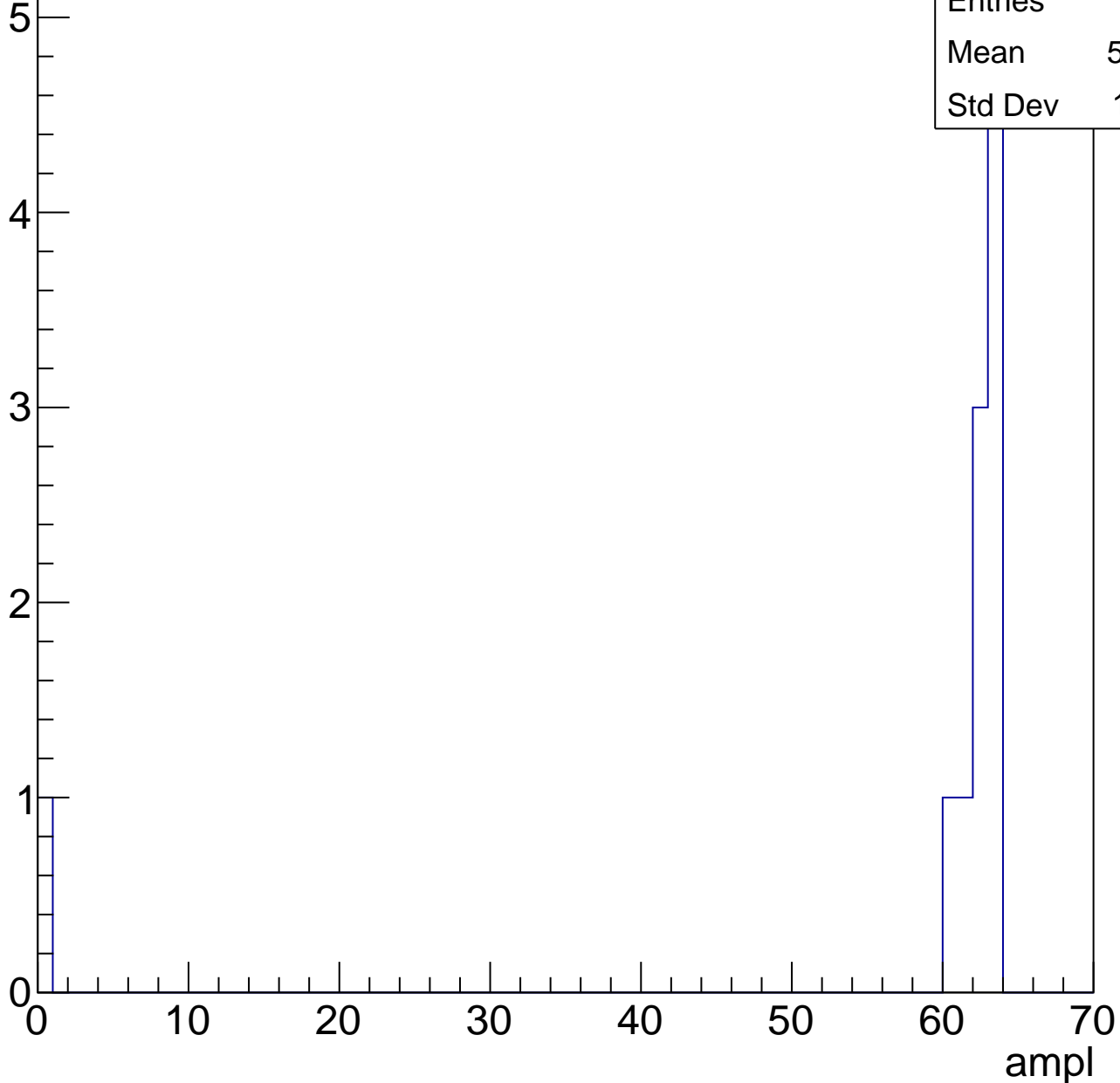


B1L100S, U1-ch34, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	56.55
Std Dev	17.91



B1L100S, U1-ch34, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch35, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	70
Mean	29.44
Std Dev	3.421

Gaus mean : 30.3501

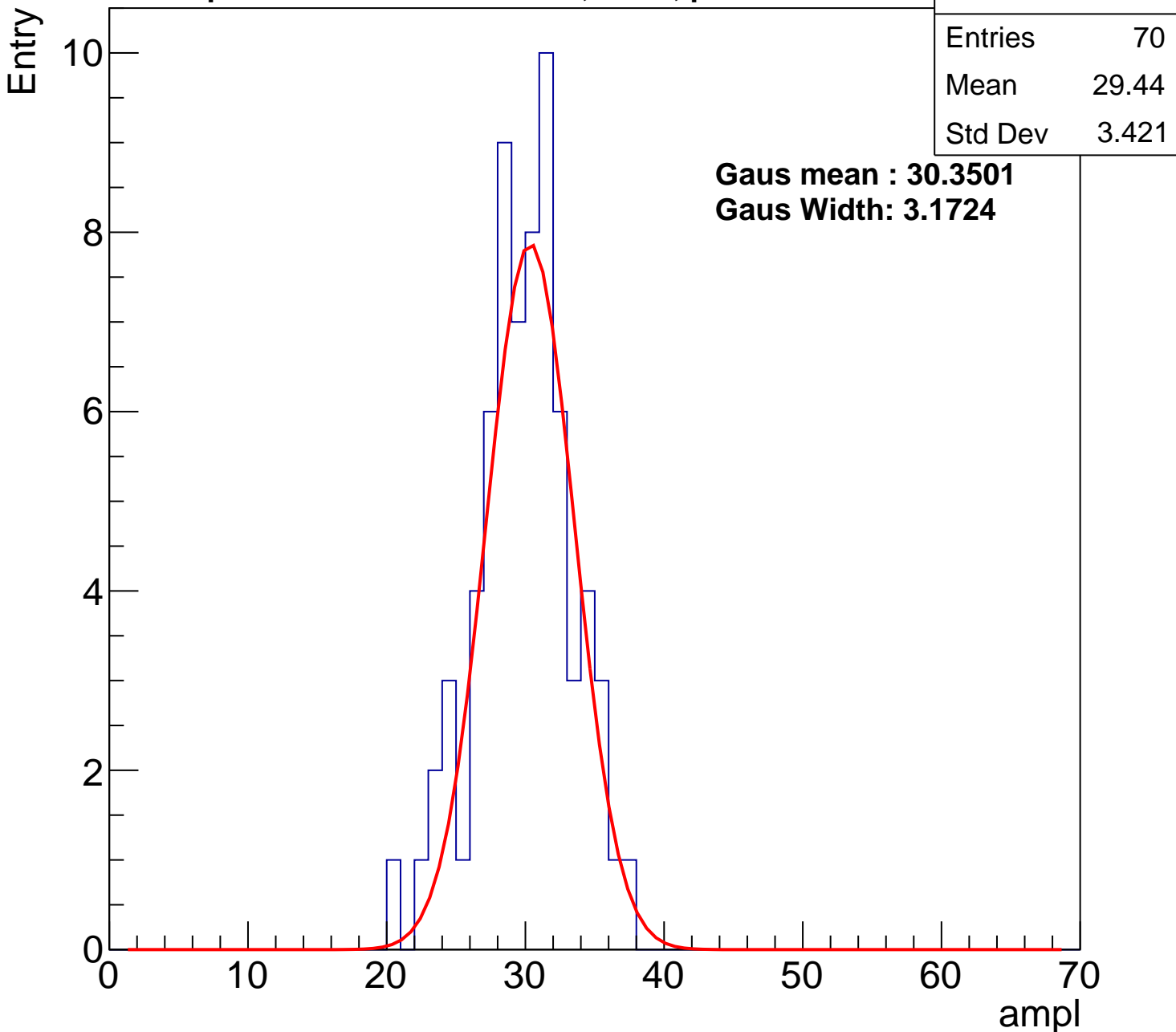
Gaus Width: 3.1724

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch35, adc1

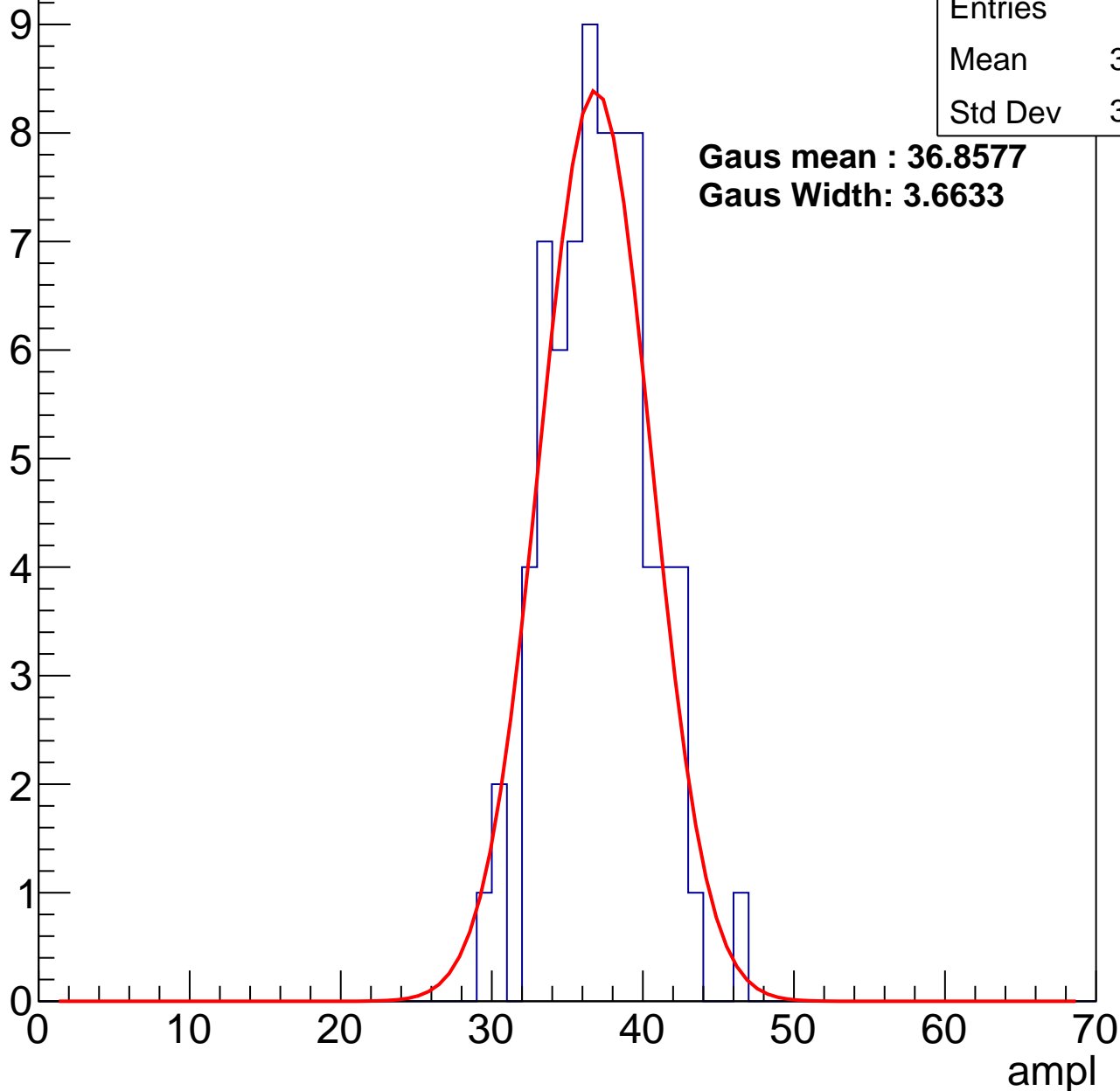
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	36.68
Std Dev	3.317

Gaus mean : 36.8577

Gaus Width: 3.6633



B1L100S, U1-ch35, adc2

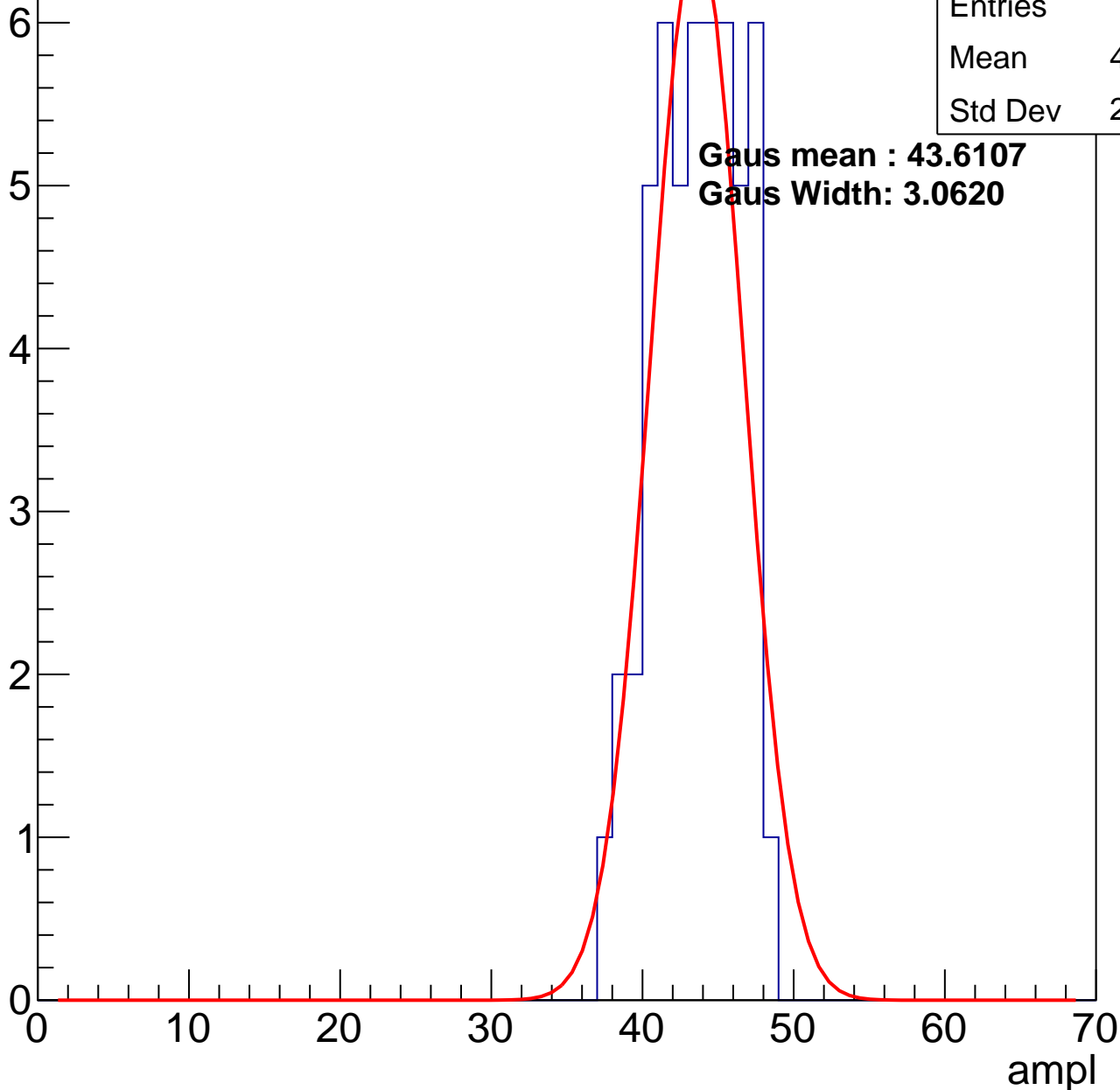
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	43.12
Std Dev	2.756

Gaus mean : 43.6107

Gaus Width: 3.0620

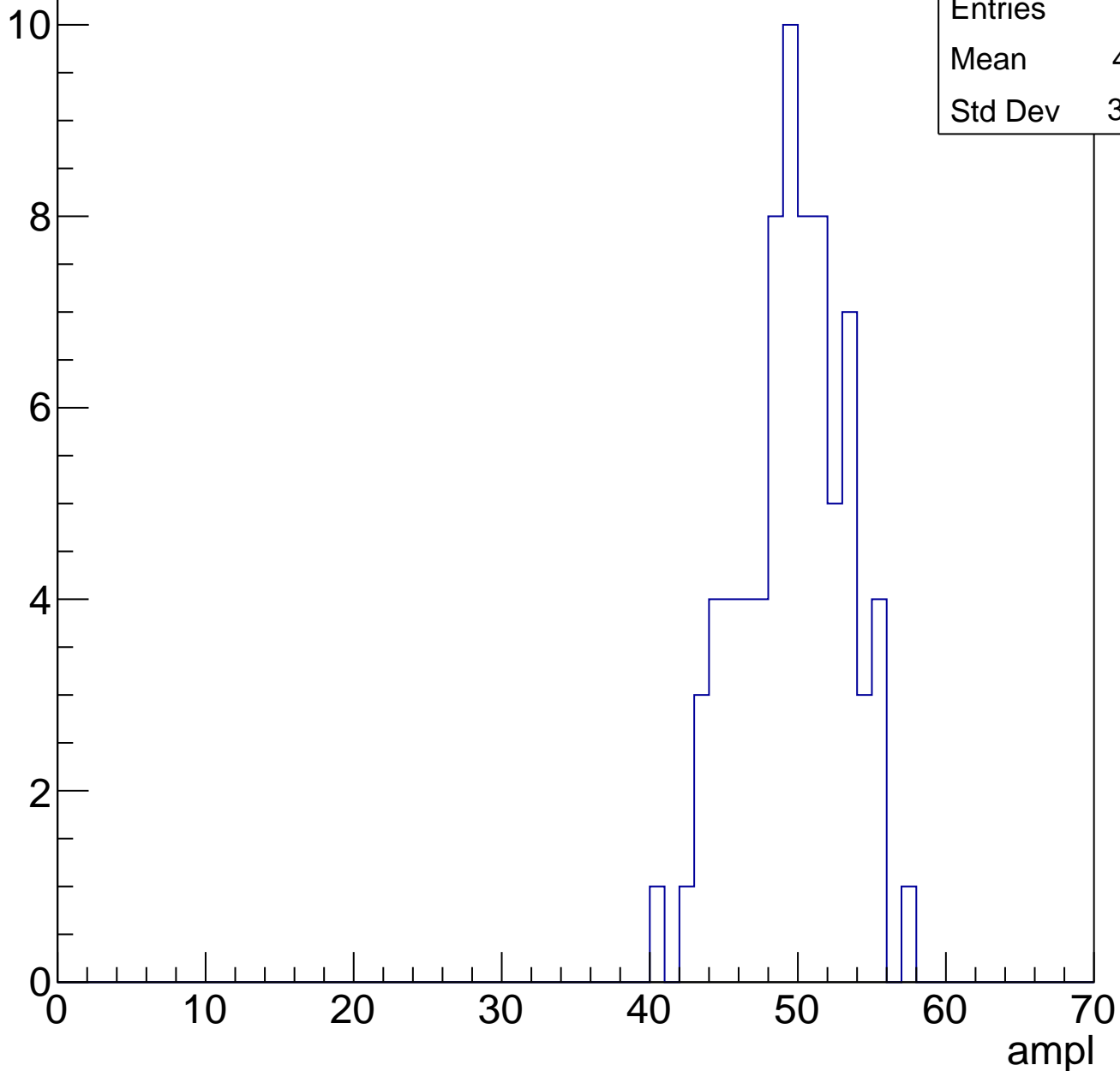


B1L100S, U1-ch35, adc3

calib_packv5_042523_0143.root, FC#4, port A2

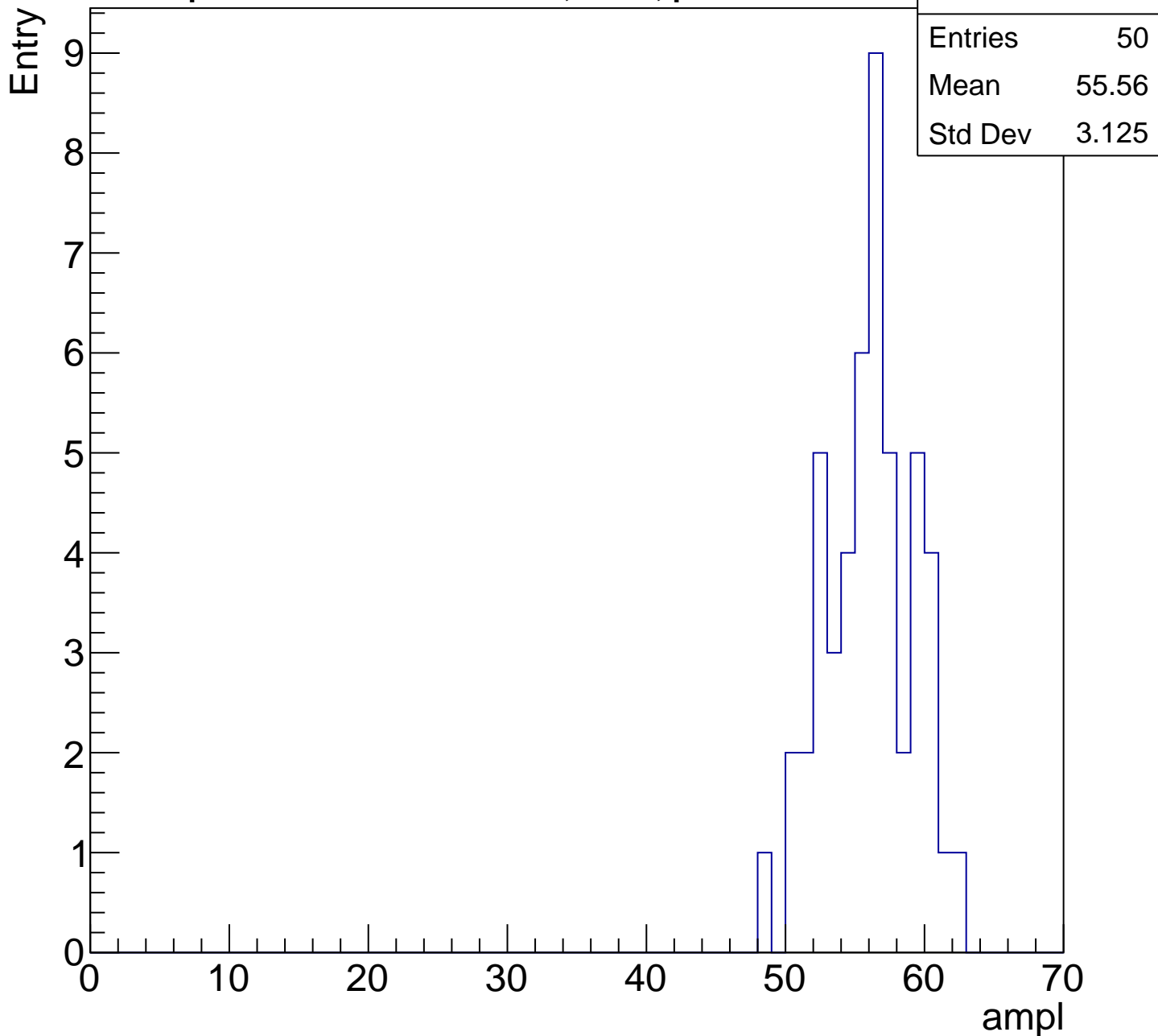
Entries	75
Mean	49.21
Std Dev	3.538

Entry



B1L100S, U1-ch35, adc4

calib_packv5_042523_0143.root, FC#4, port A2

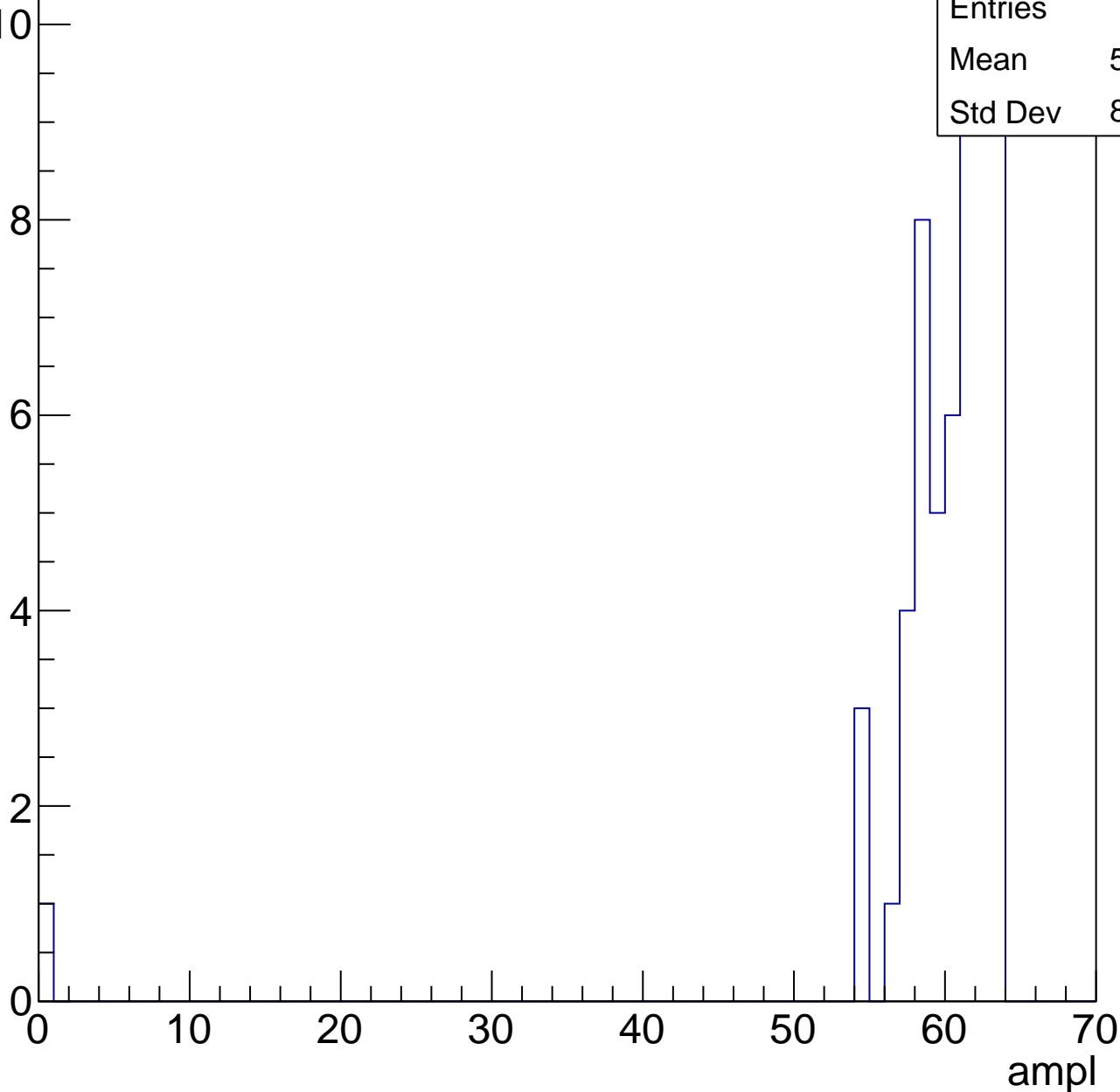


B1L100S, U1-ch35, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	58.96
Std Dev	8.315



B1L100S, U1-ch35, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch35, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch36, adc0

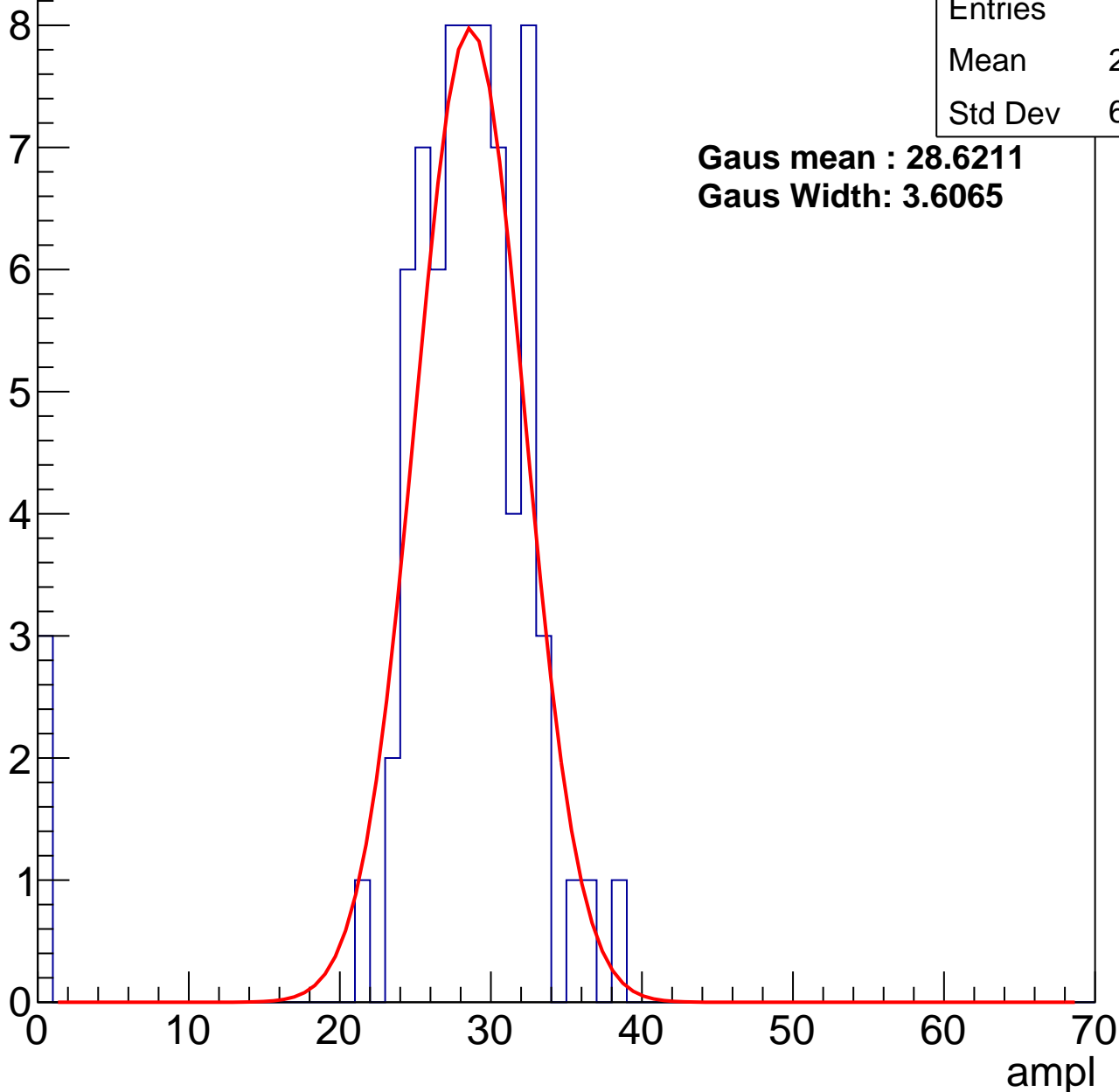
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	27.19
Std Dev	6.453

Gaus mean : 28.6211

Gaus Width: 3.6065



B1L100S, U1-ch36, adc1

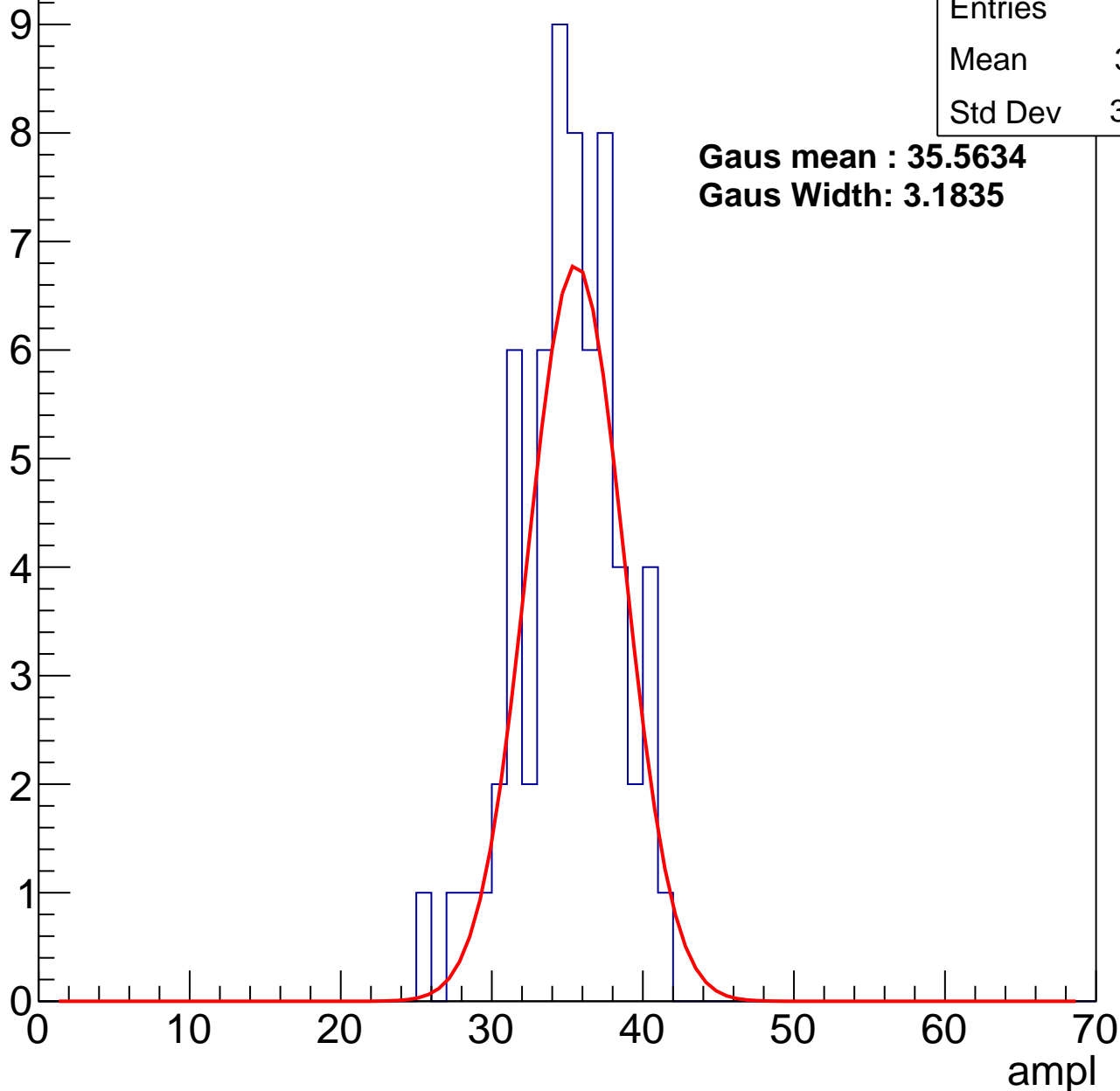
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	34.61
Std Dev	3.309

Gaus mean : 35.5634

Gaus Width: 3.1835



B1L100S, U1-ch36, adc2

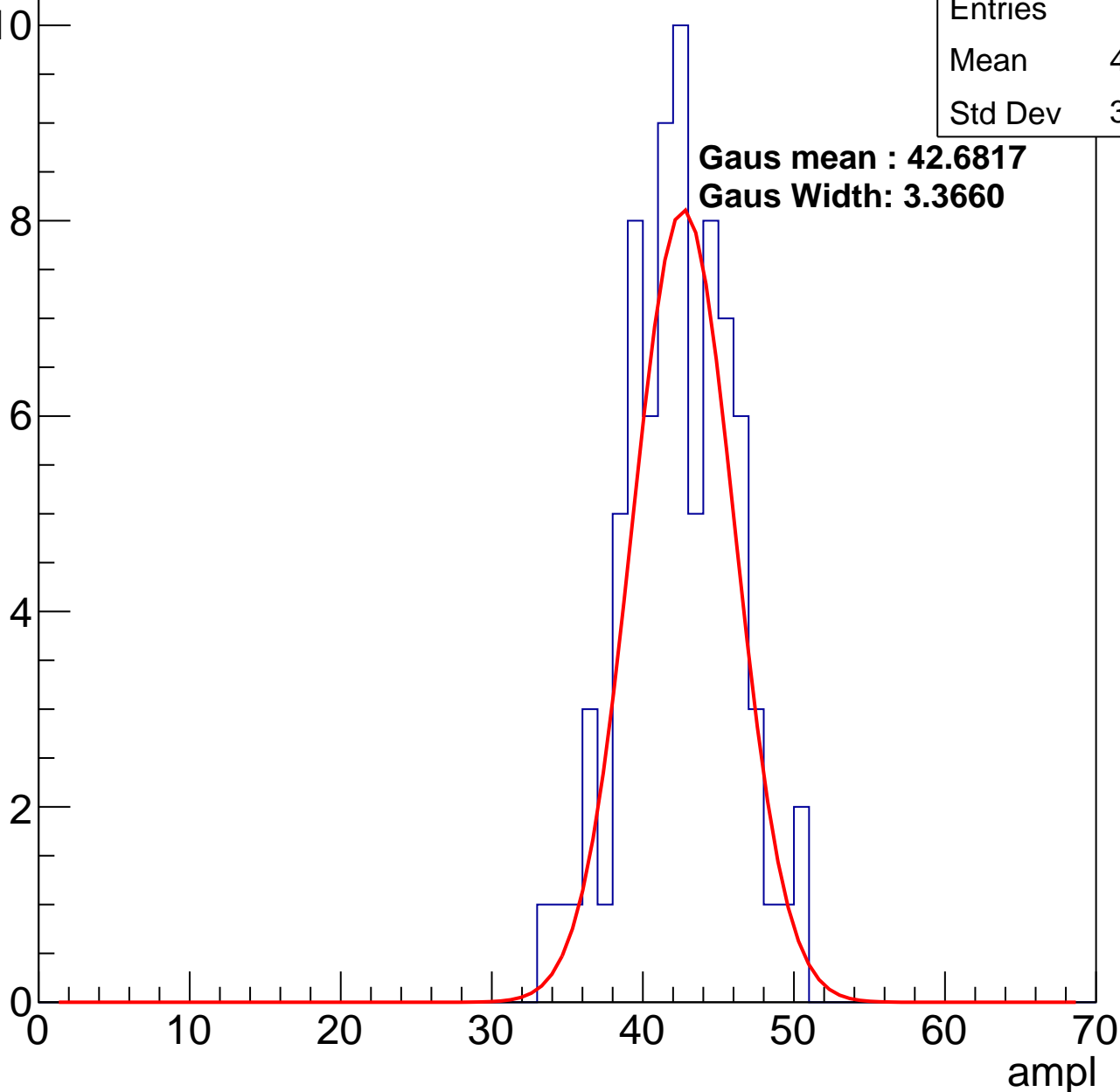
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	41.97
Std Dev	3.573

Gaus mean : 42.6817

Gaus Width: 3.3660

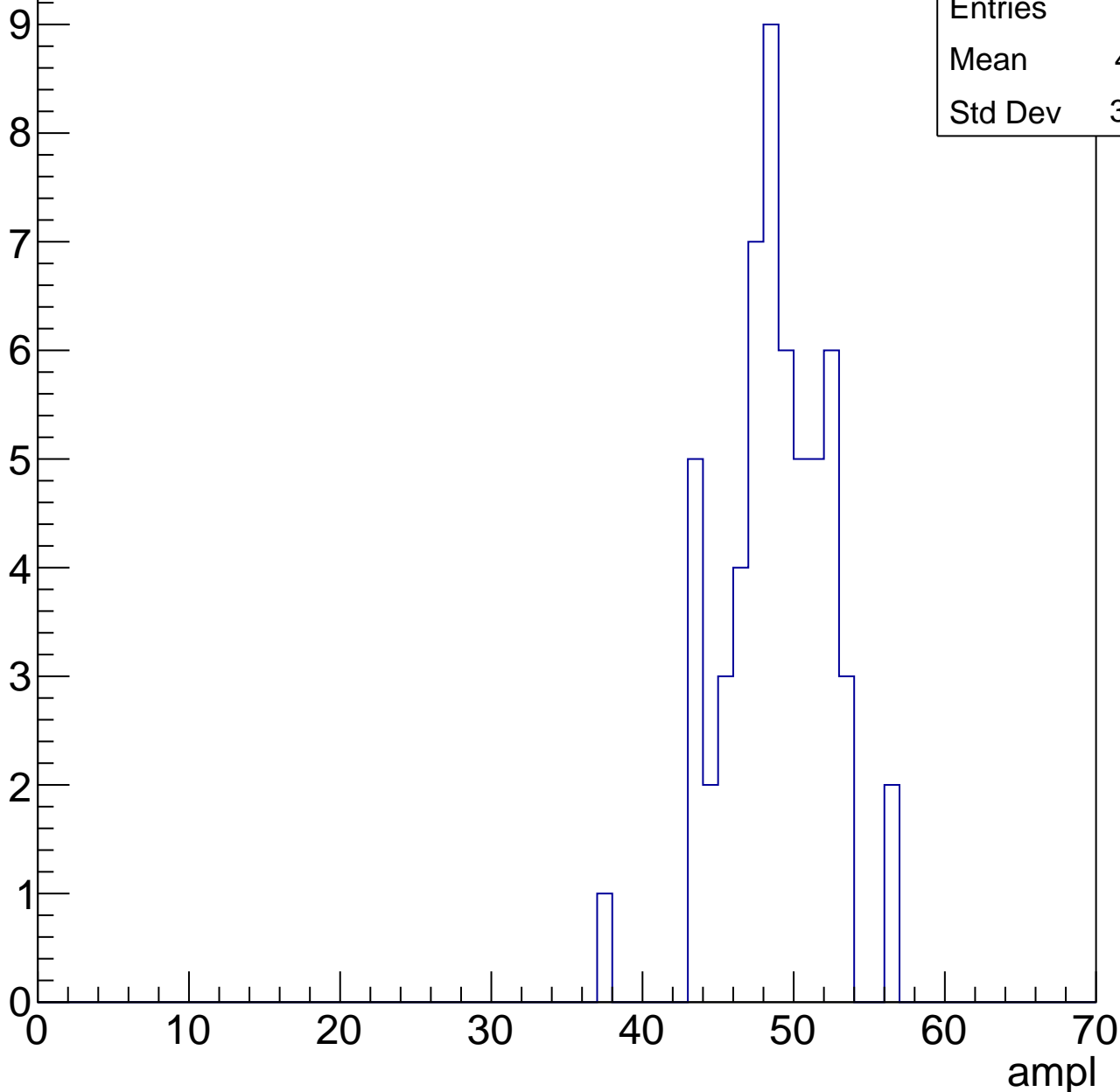


B1L100S, U1-ch36, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.31
Std Dev	3.455

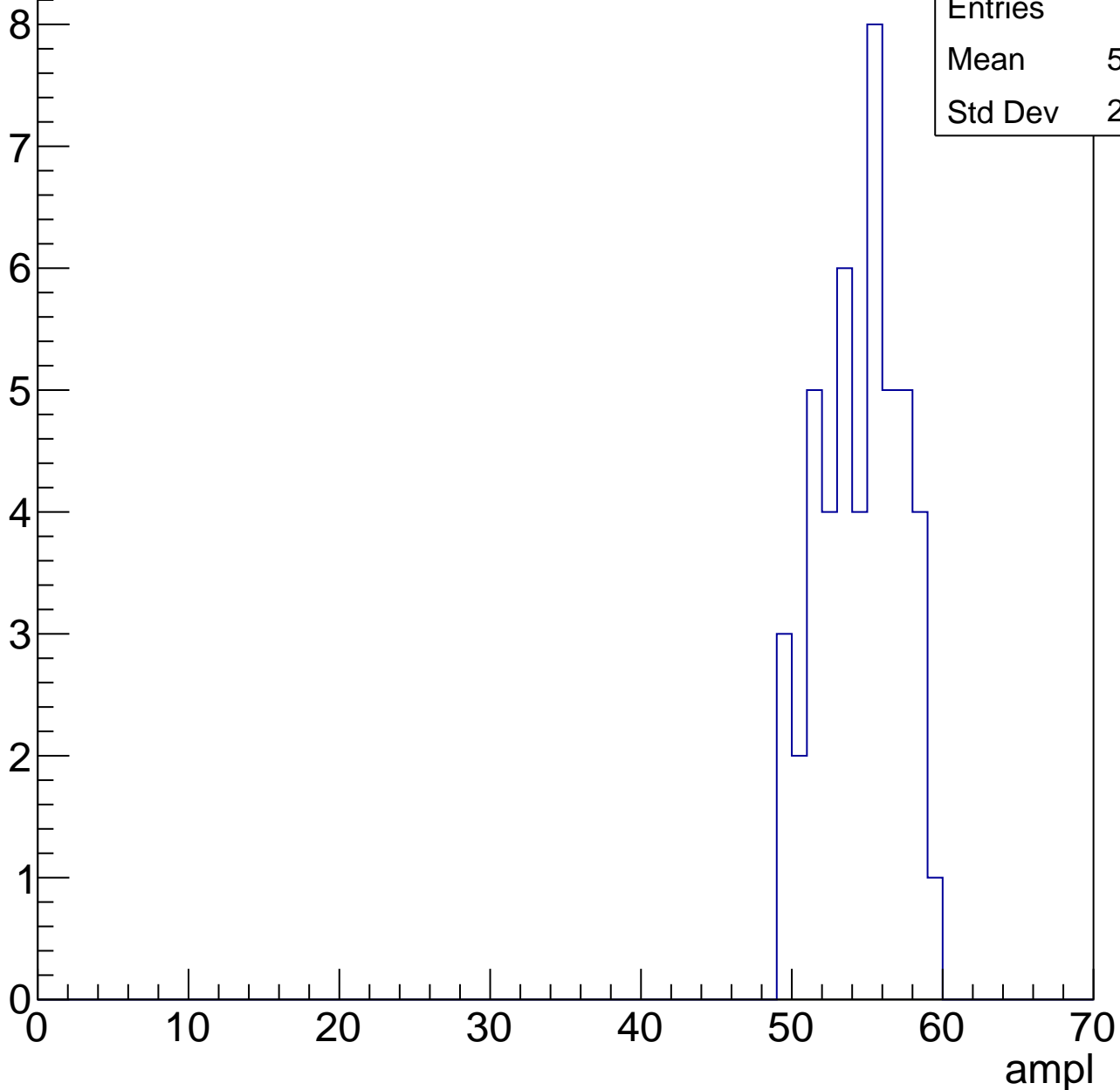


B1L100S, U1-ch36, adc4

calib_packv5_042523_0143.root, FC#4, port A2

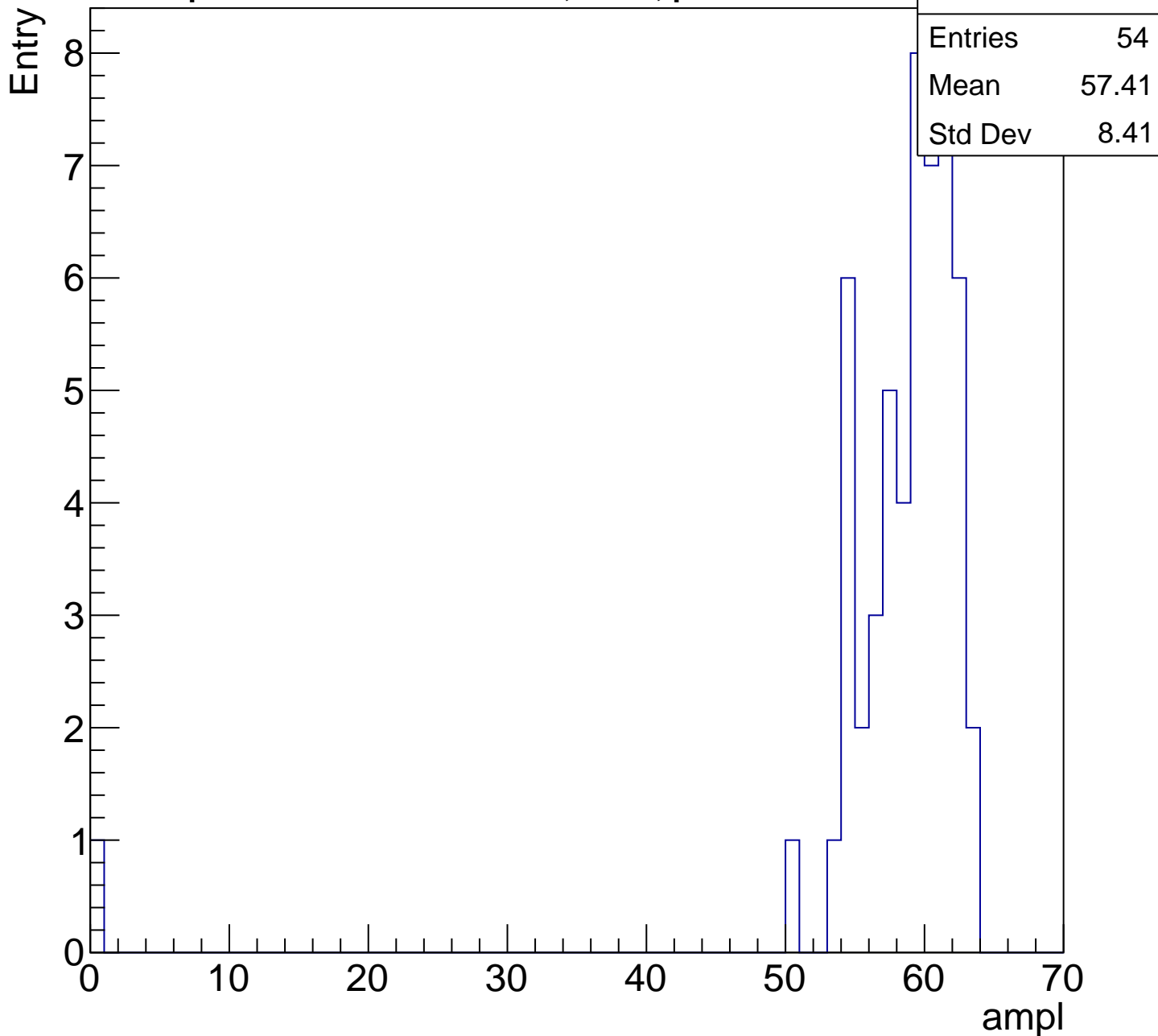
Entry

Entries	47
Mean	54.04
Std Dev	2.673



B1L100S, U1-ch36, adc5

calib_packv5_042523_0143.root, FC#4, port A2

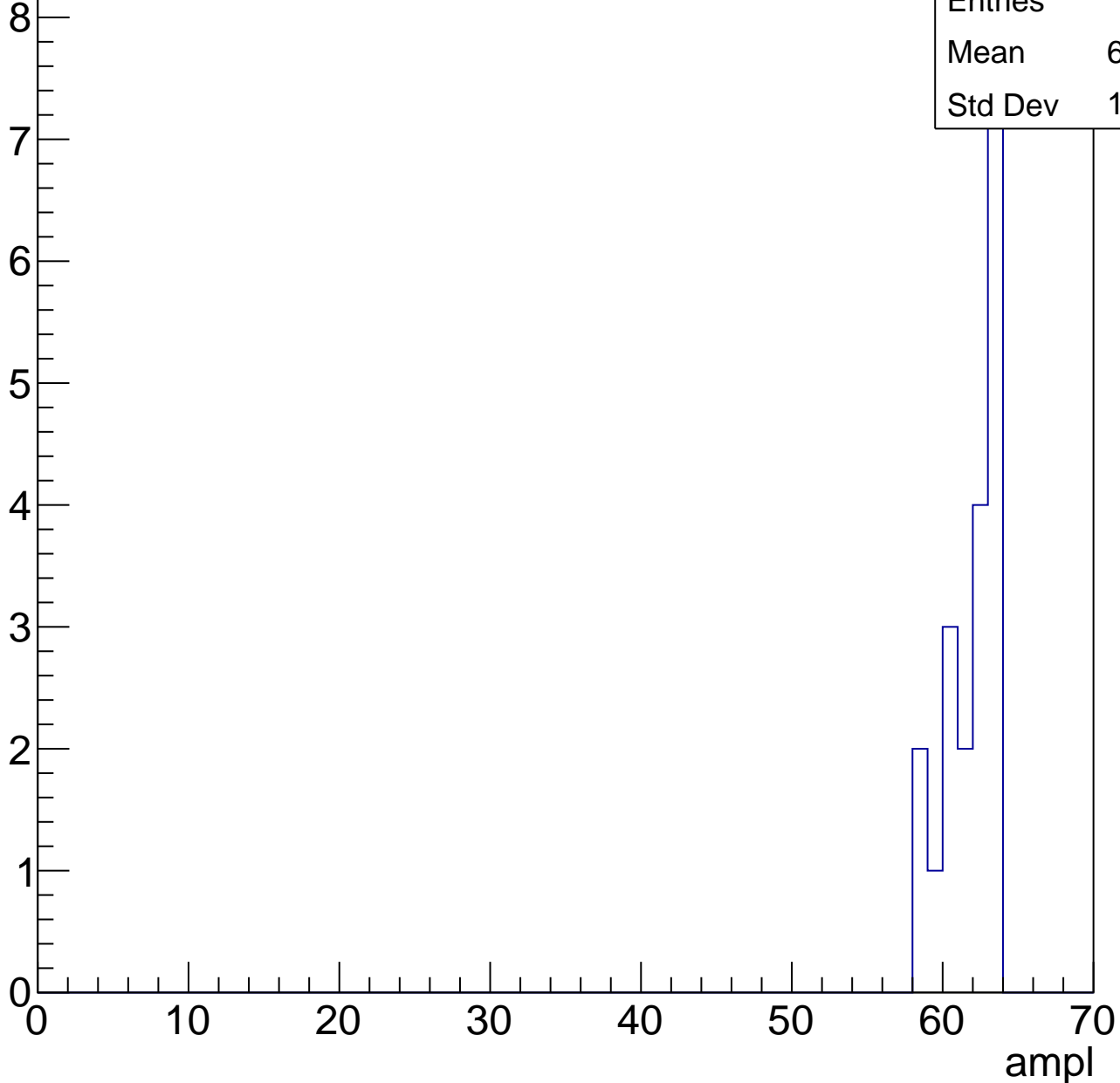


B1L100S, U1-ch36, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	61.45
Std Dev	1.687



B1L100S, U1-ch36, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch37, adc0

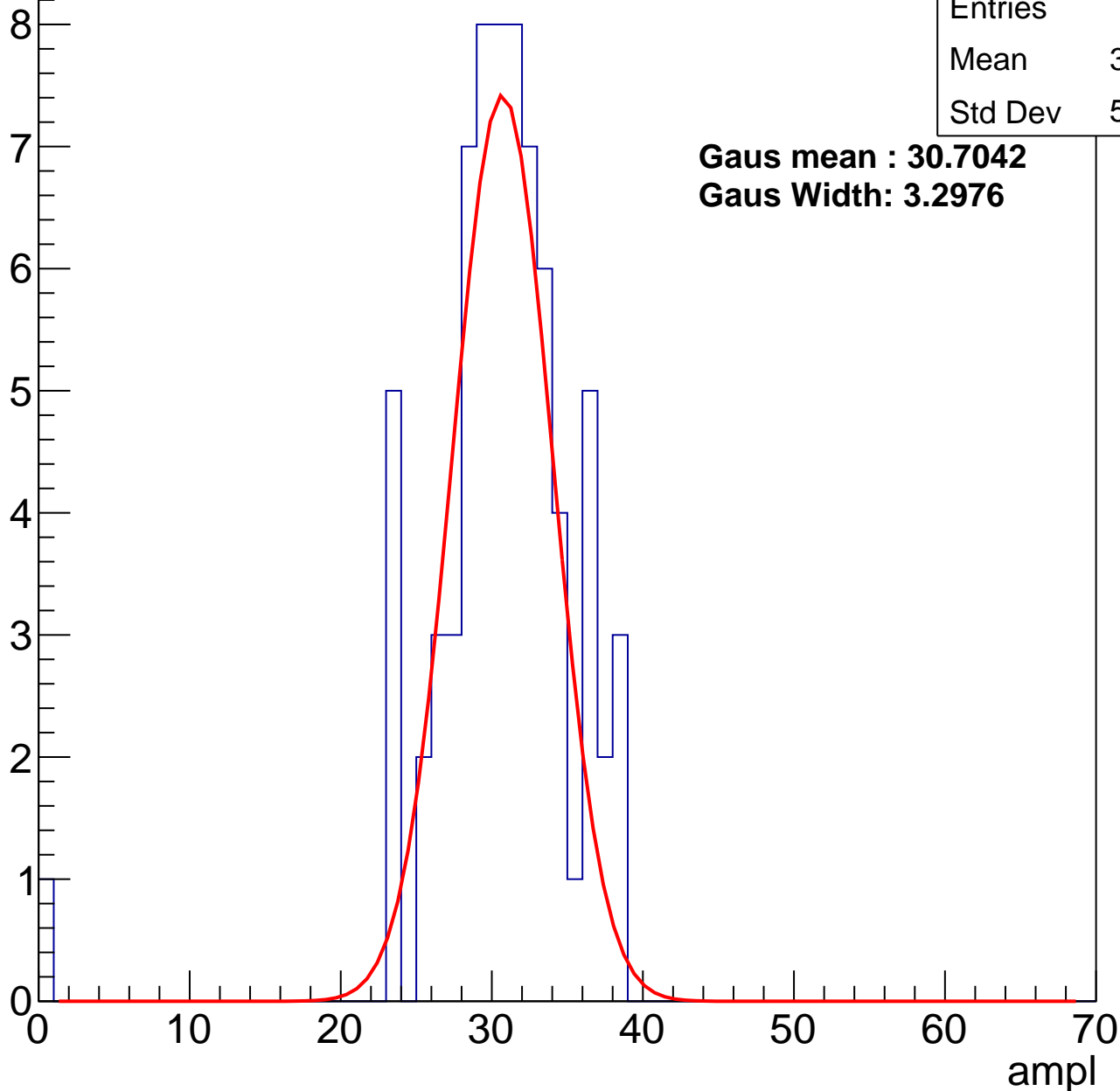
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	30.15
Std Dev	5.173

Gaus mean : 30.7042

Gaus Width: 3.2976



B1L100S, U1-ch37, adc1

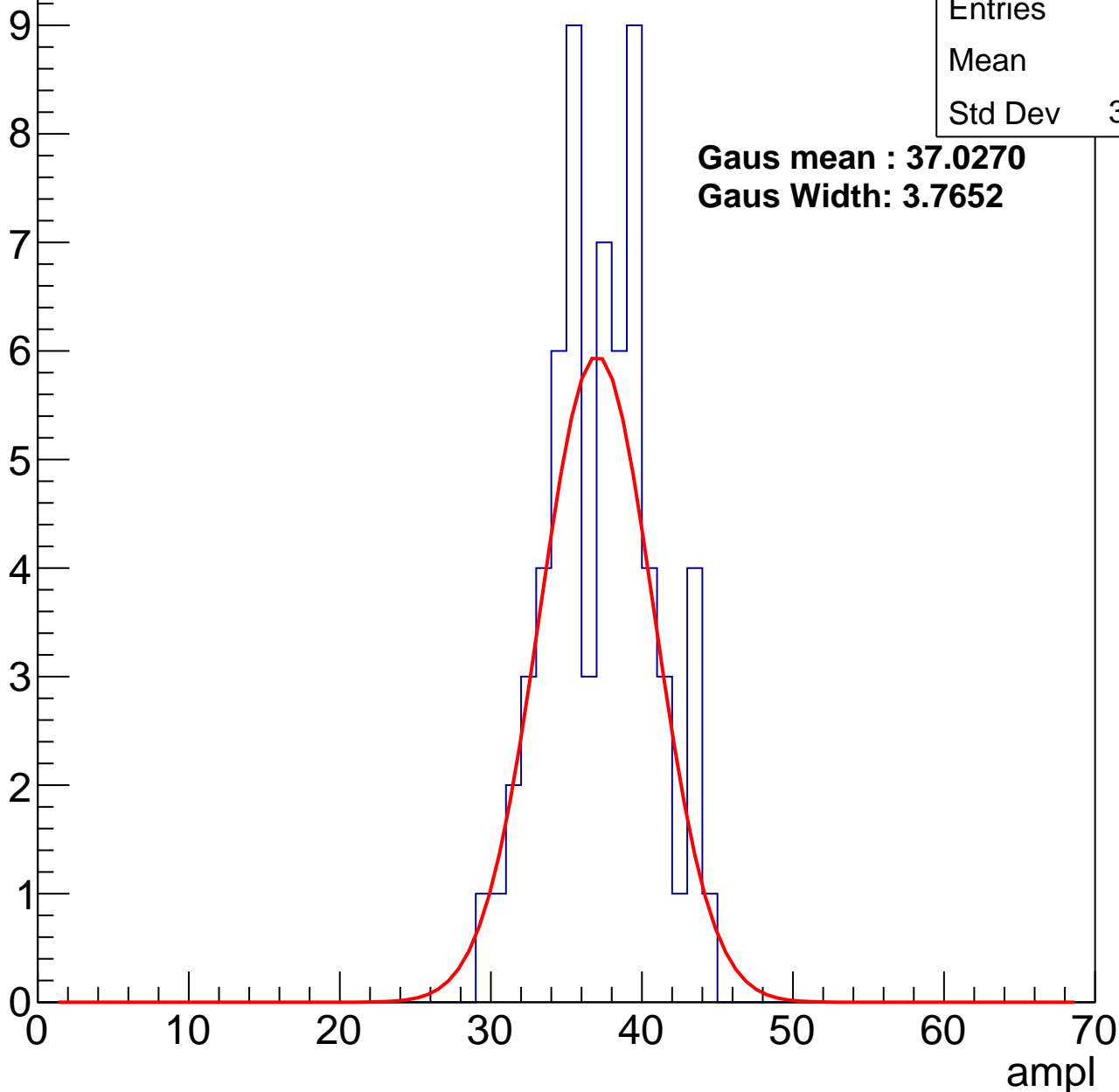
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.8
Std Dev	3.442

Gaus mean : 37.0270

Gaus Width: 3.7652



B1L100S, U1-ch37, adc2

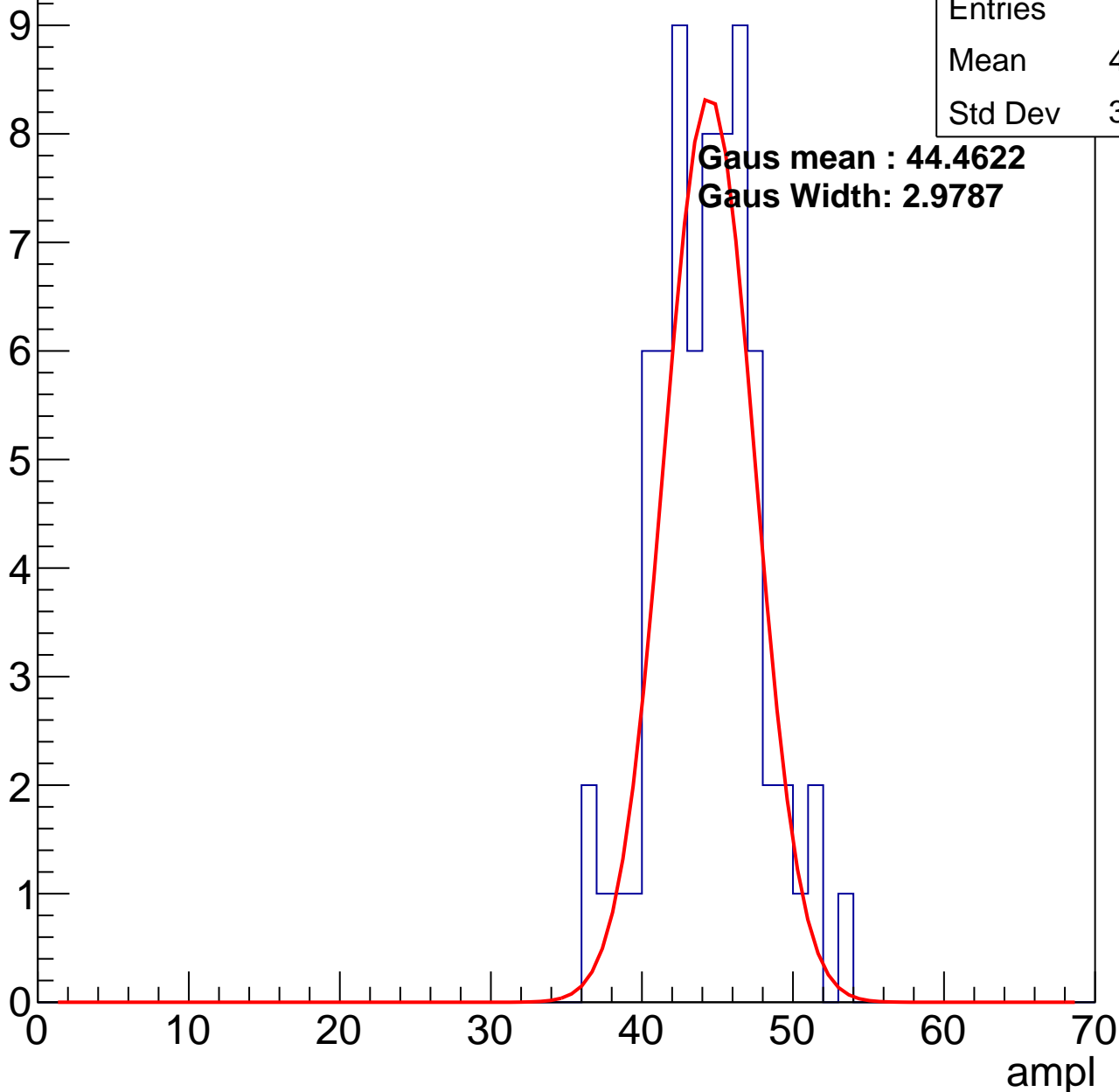
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	43.87
Std Dev	3.406

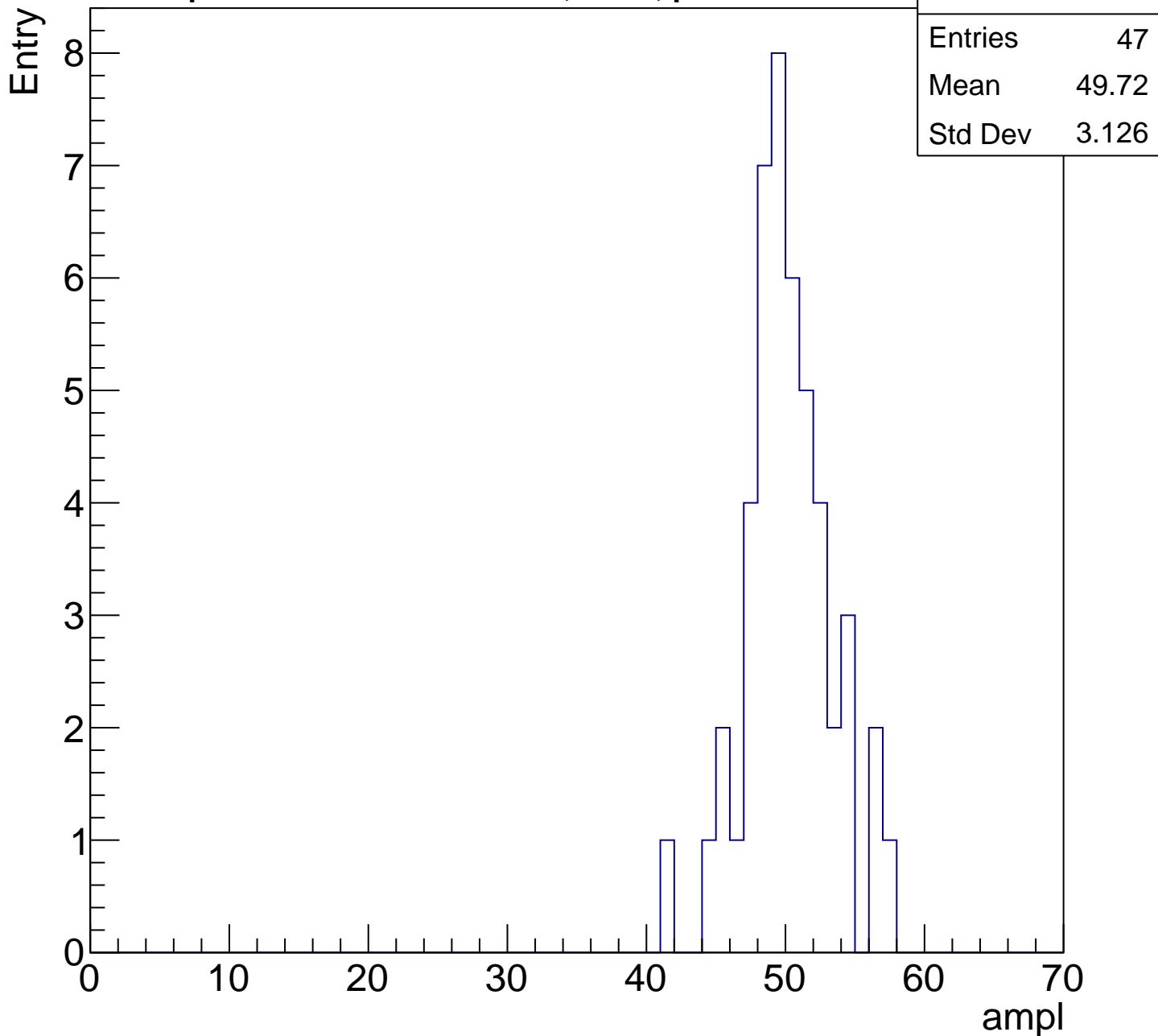
Gaus mean : 44.4622

Gaus Width: 2.9787



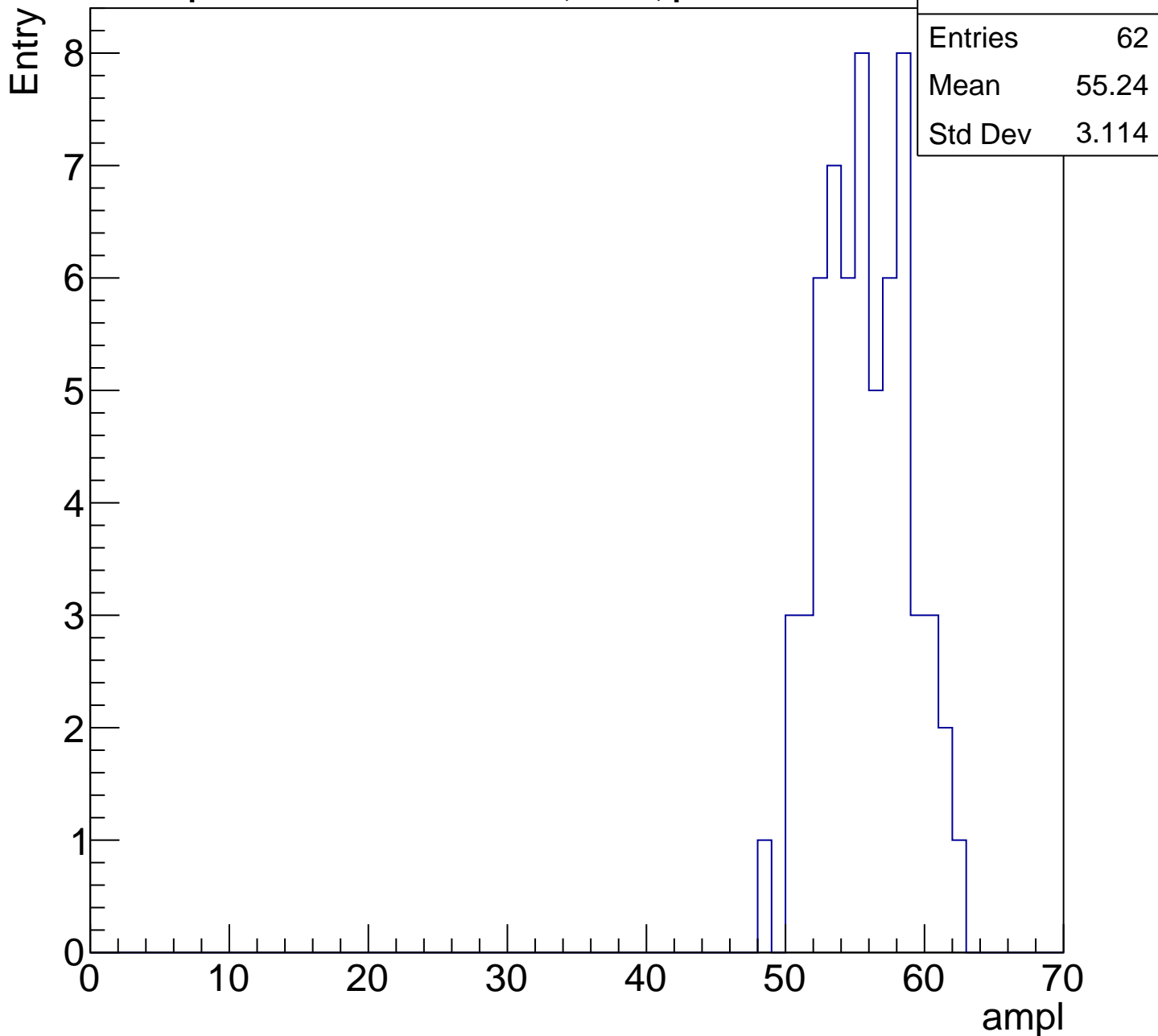
B1L100S, U1-ch37, adc3

calib_packv5_042523_0143.root, FC#4, port A2



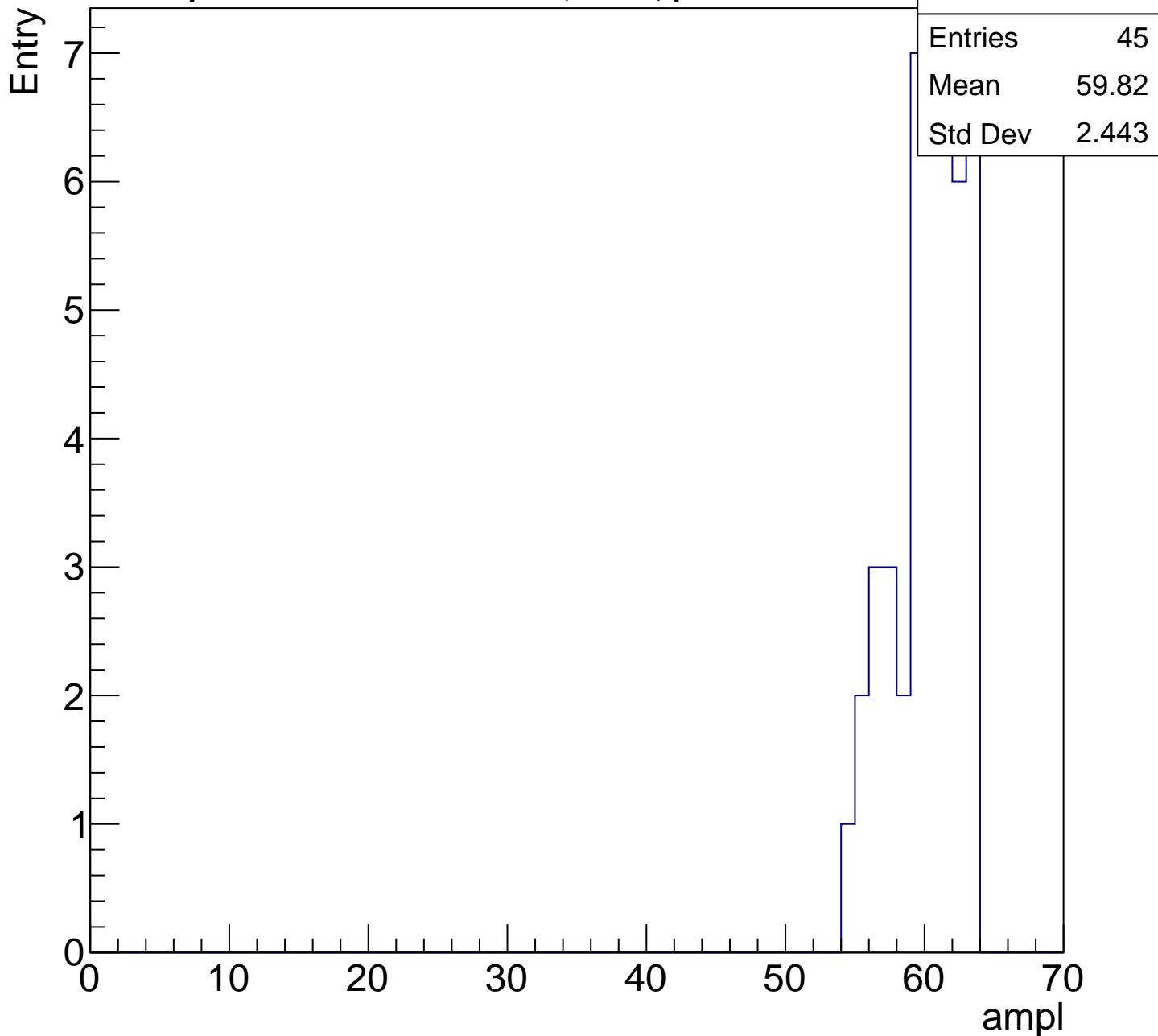
B1L100S, U1-ch37, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch37, adc5

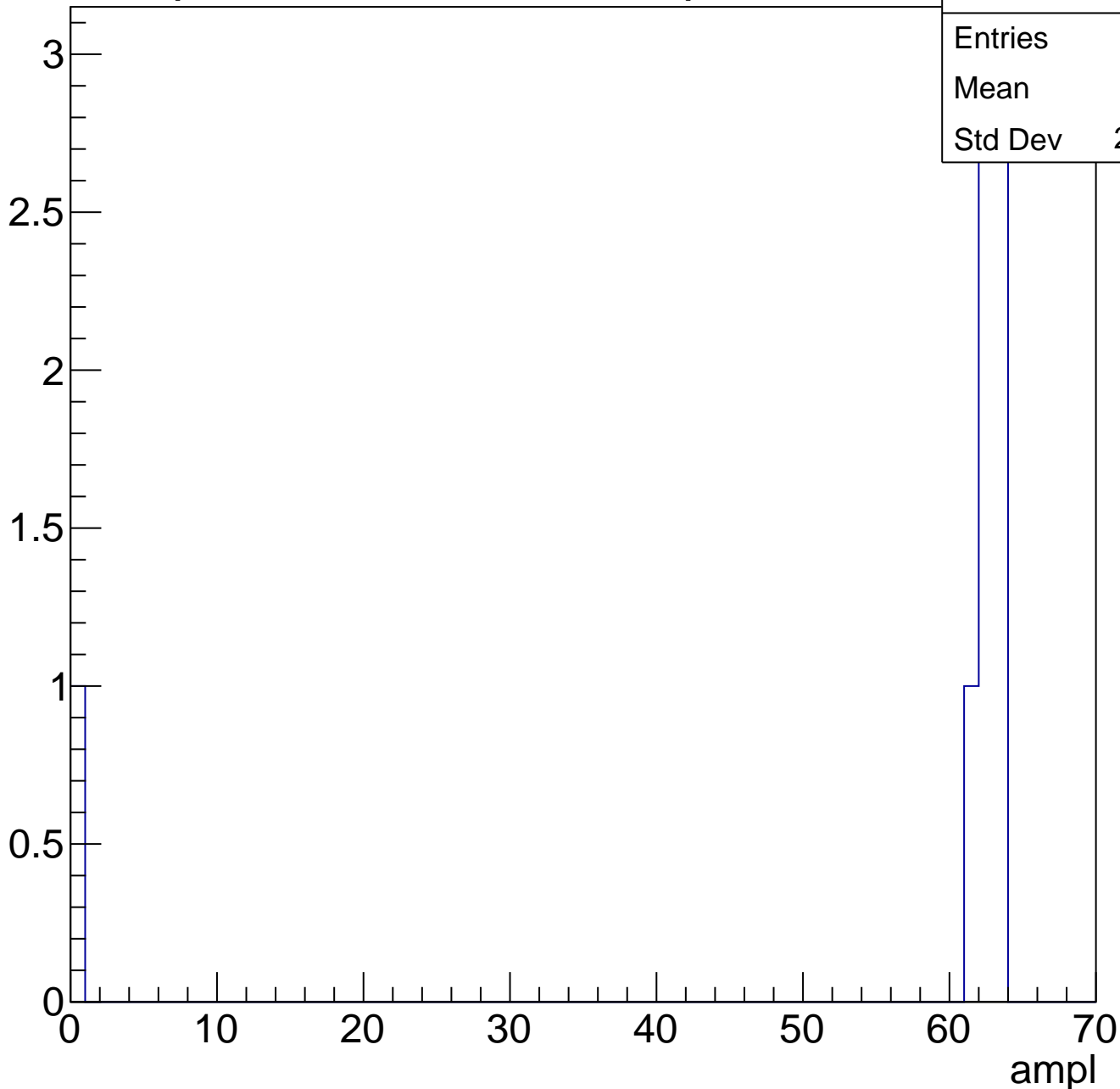
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch37, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	8
Mean	54.5
Std Dev	20.61

B1L100S, U1-ch37, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch38, adc0

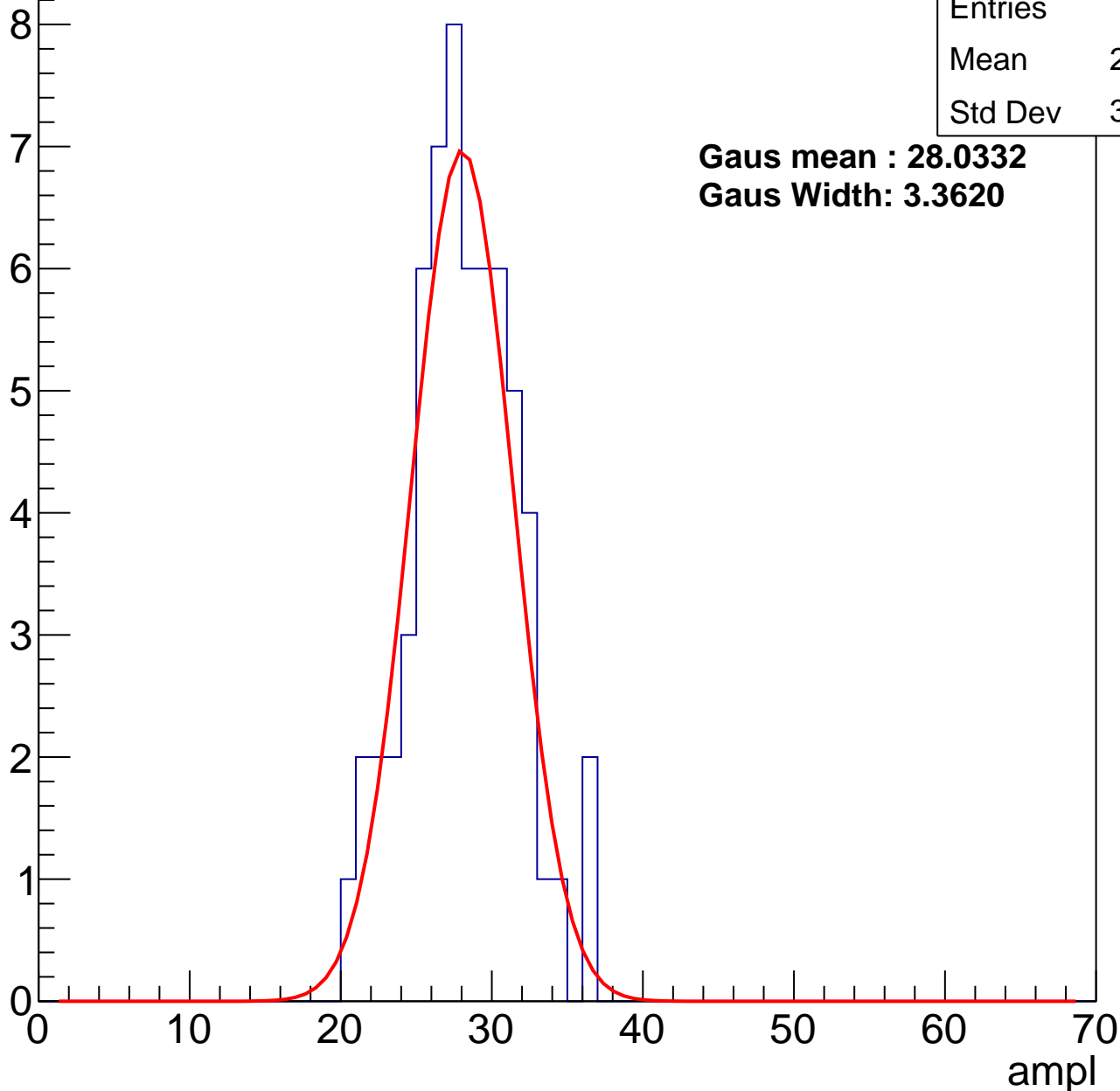
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.68
Std Dev	3.463

Gaus mean : 28.0332

Gaus Width: 3.3620



B1L100S, U1-ch38, adc1

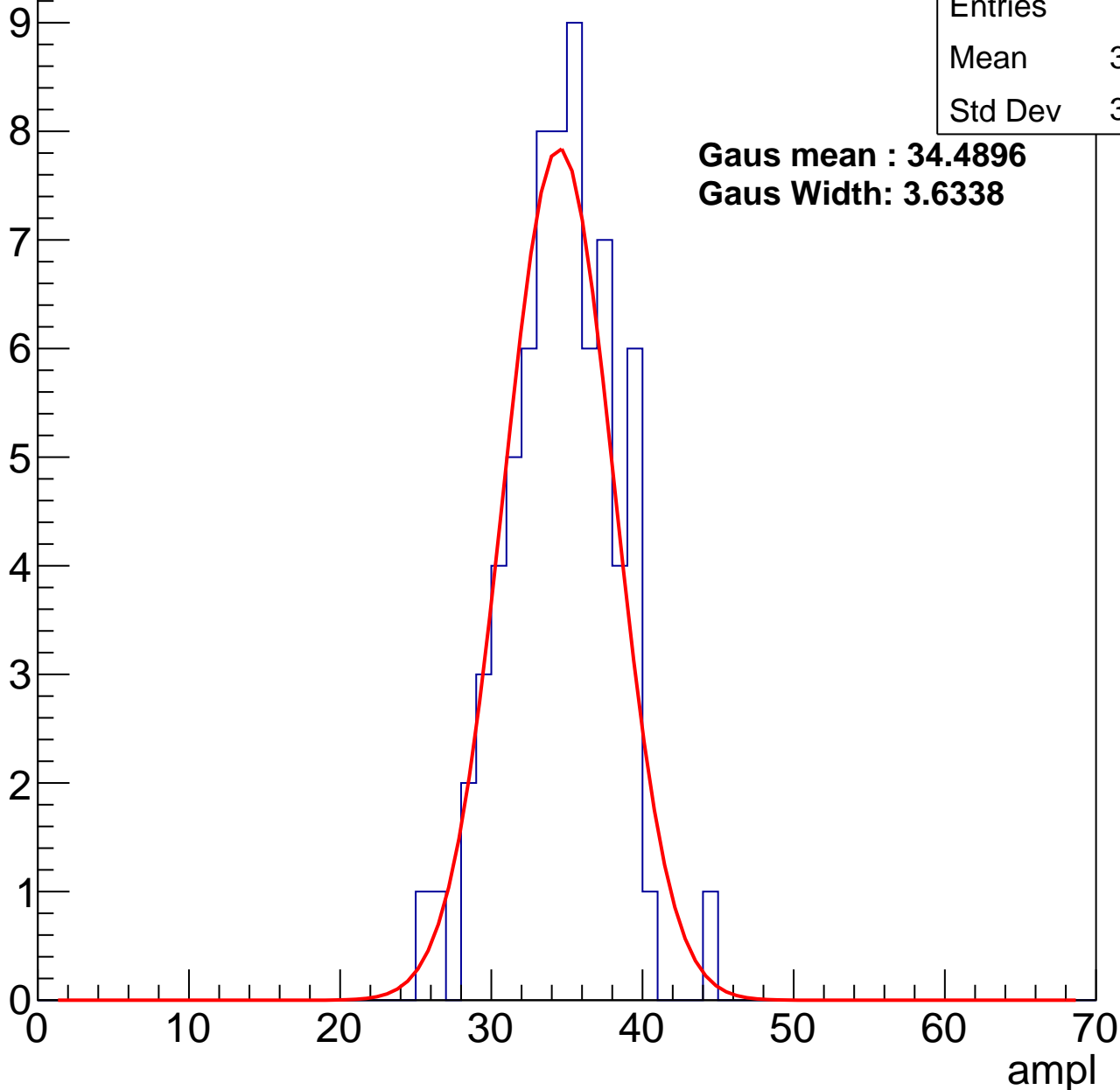
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	34.12
Std Dev	3.492

Gaus mean : 34.4896

Gaus Width: 3.6338



B1L100S, U1-ch38, adc2

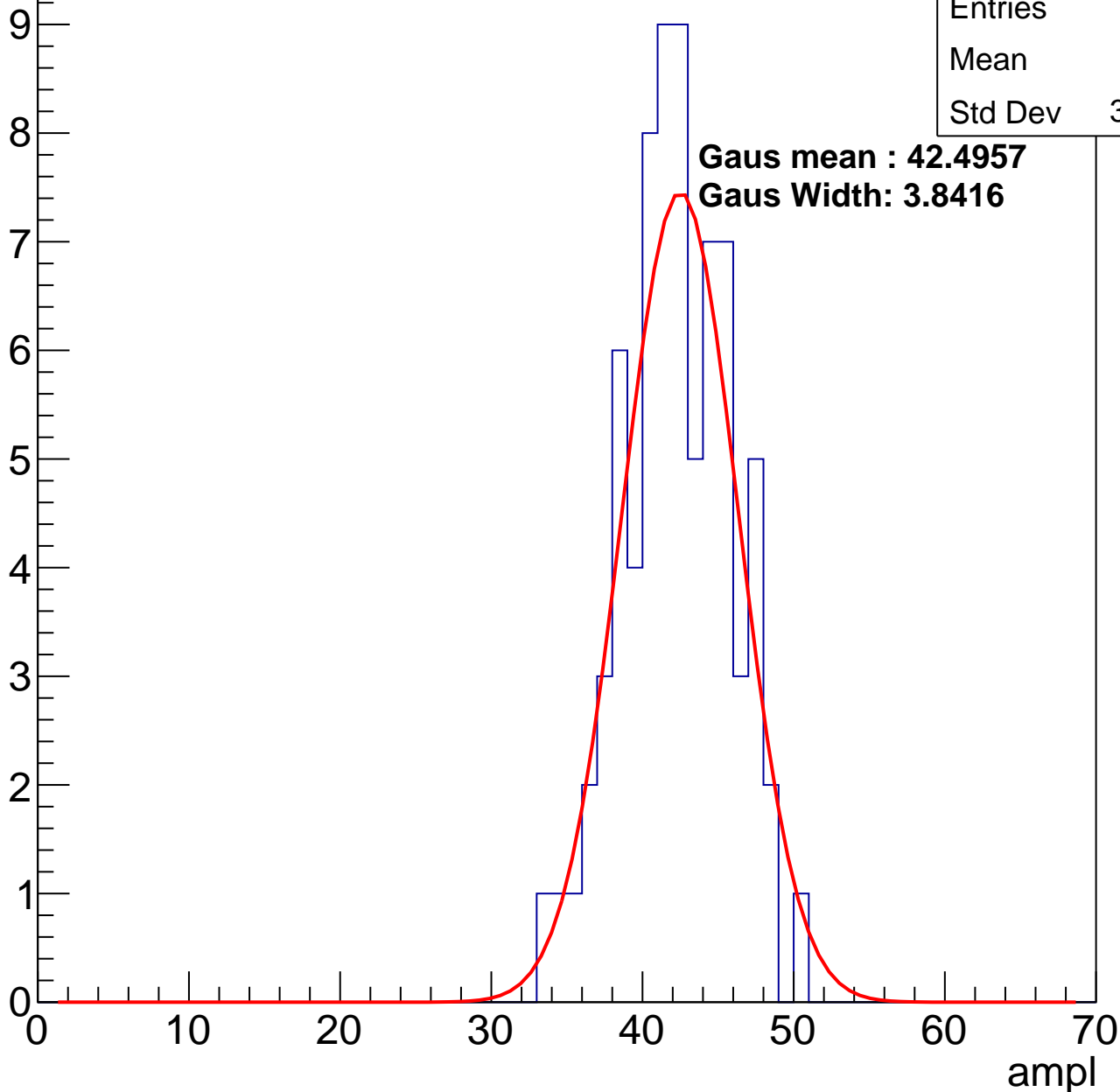
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	41.8
Std Dev	3.518

Gaus mean : 42.4957

Gaus Width: 3.8416

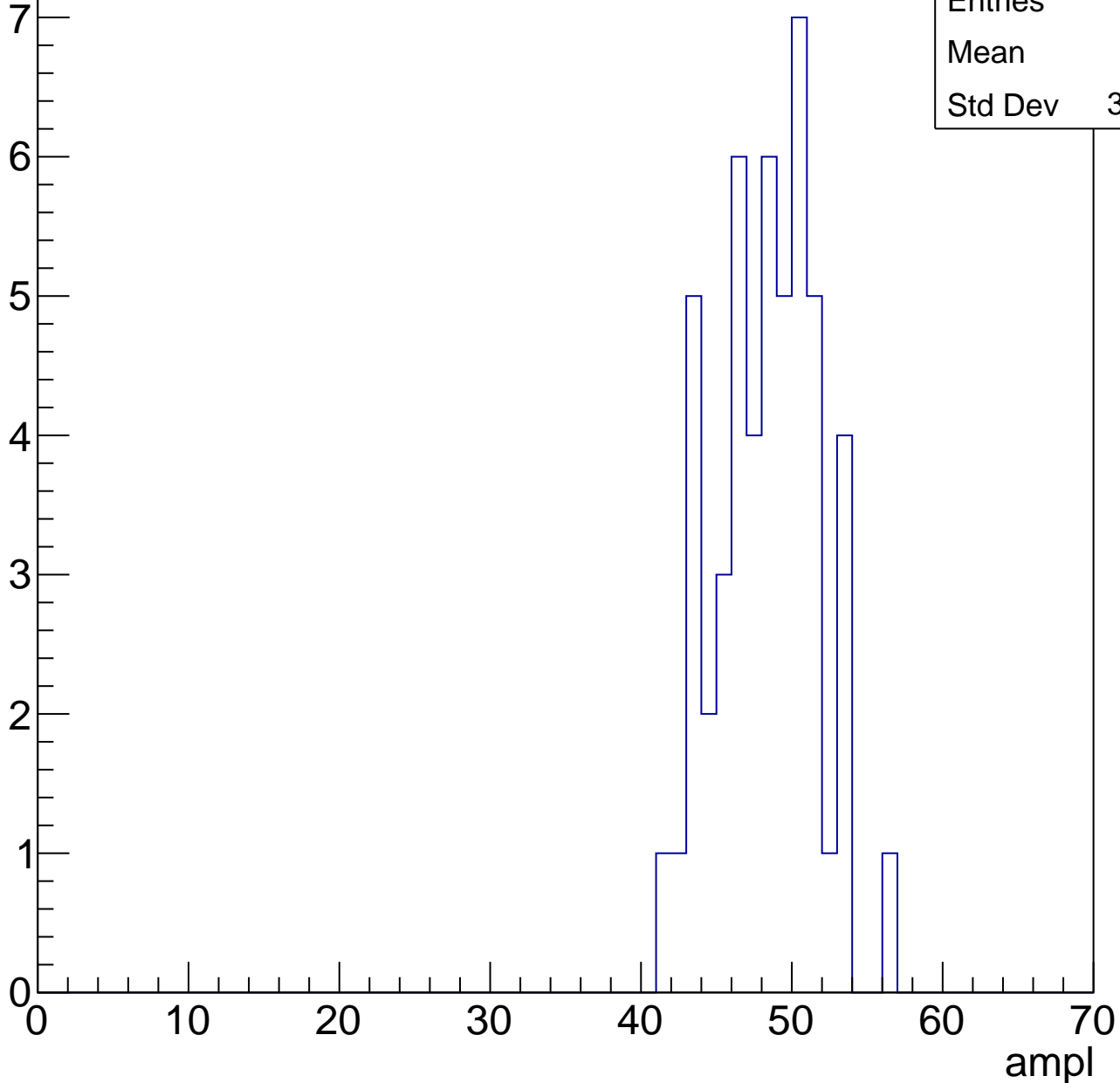


B1L100S, U1-ch38, adc3

calib_packv5_042523_0143.root, FC#4, port A2

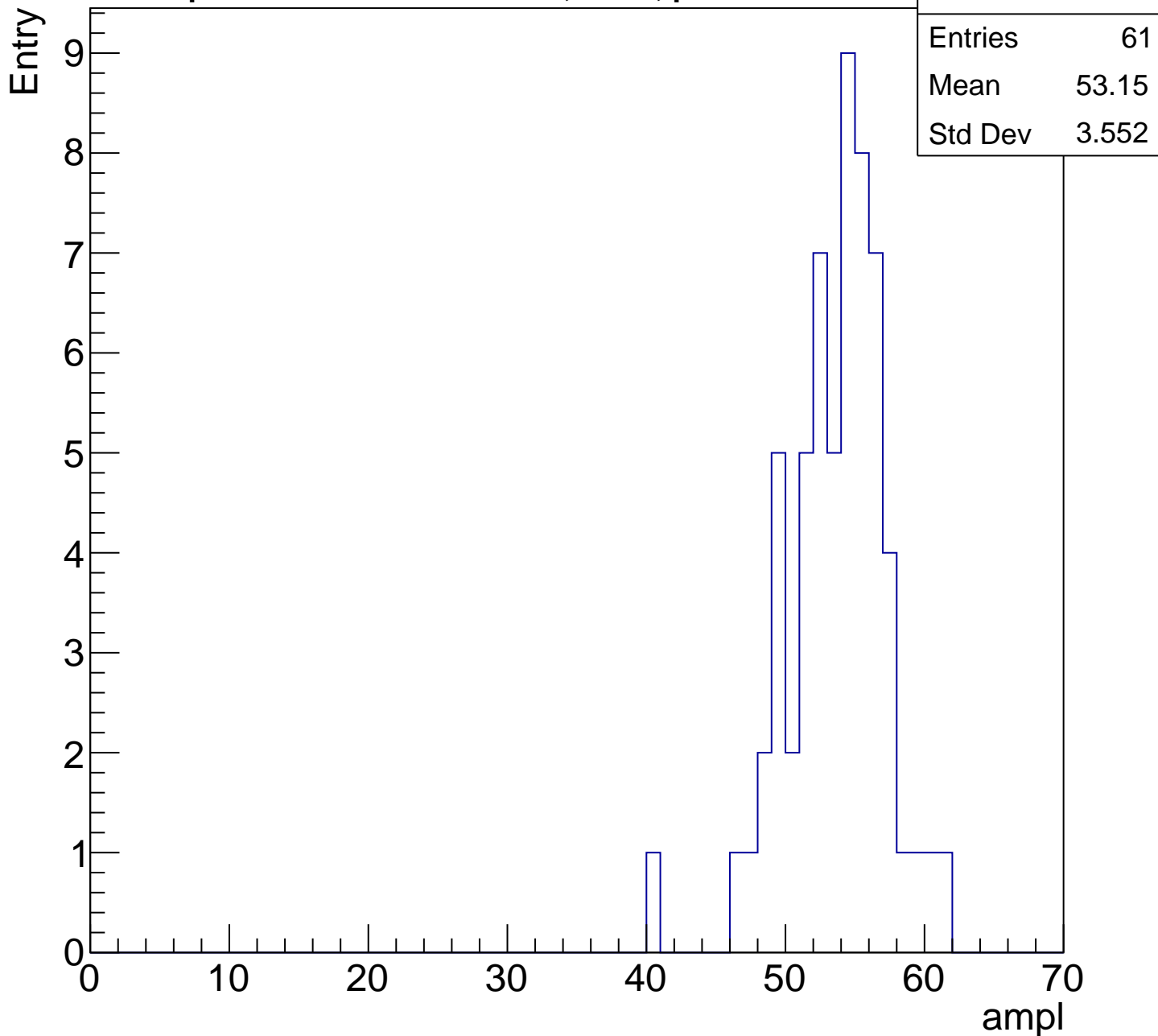
Entry

Entries	51
Mean	47.9
Std Dev	3.297



B1L100S, U1-ch38, adc4

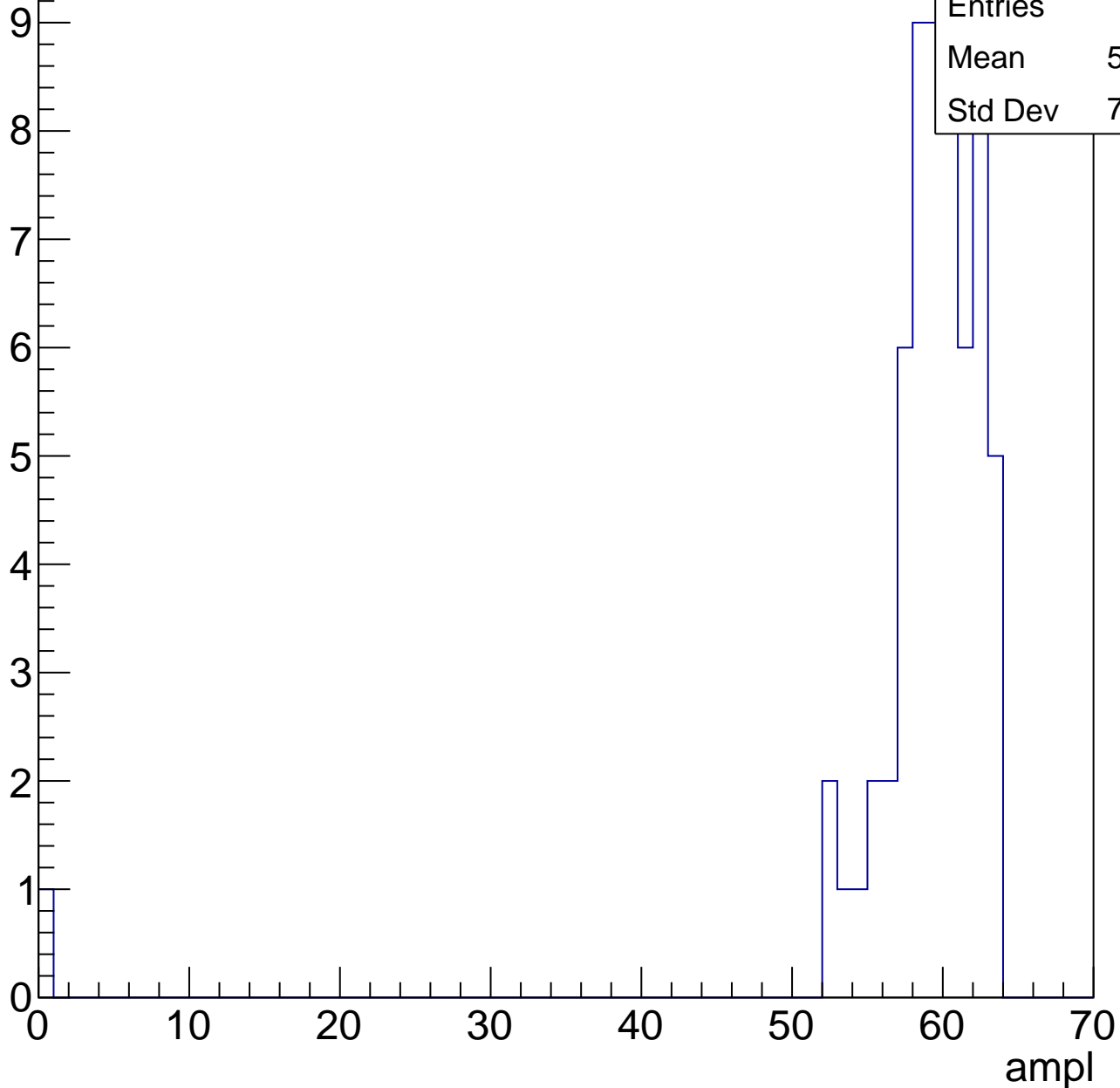
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch38, adc5

calib_packv5_042523_0143.root, FC#4, port A2

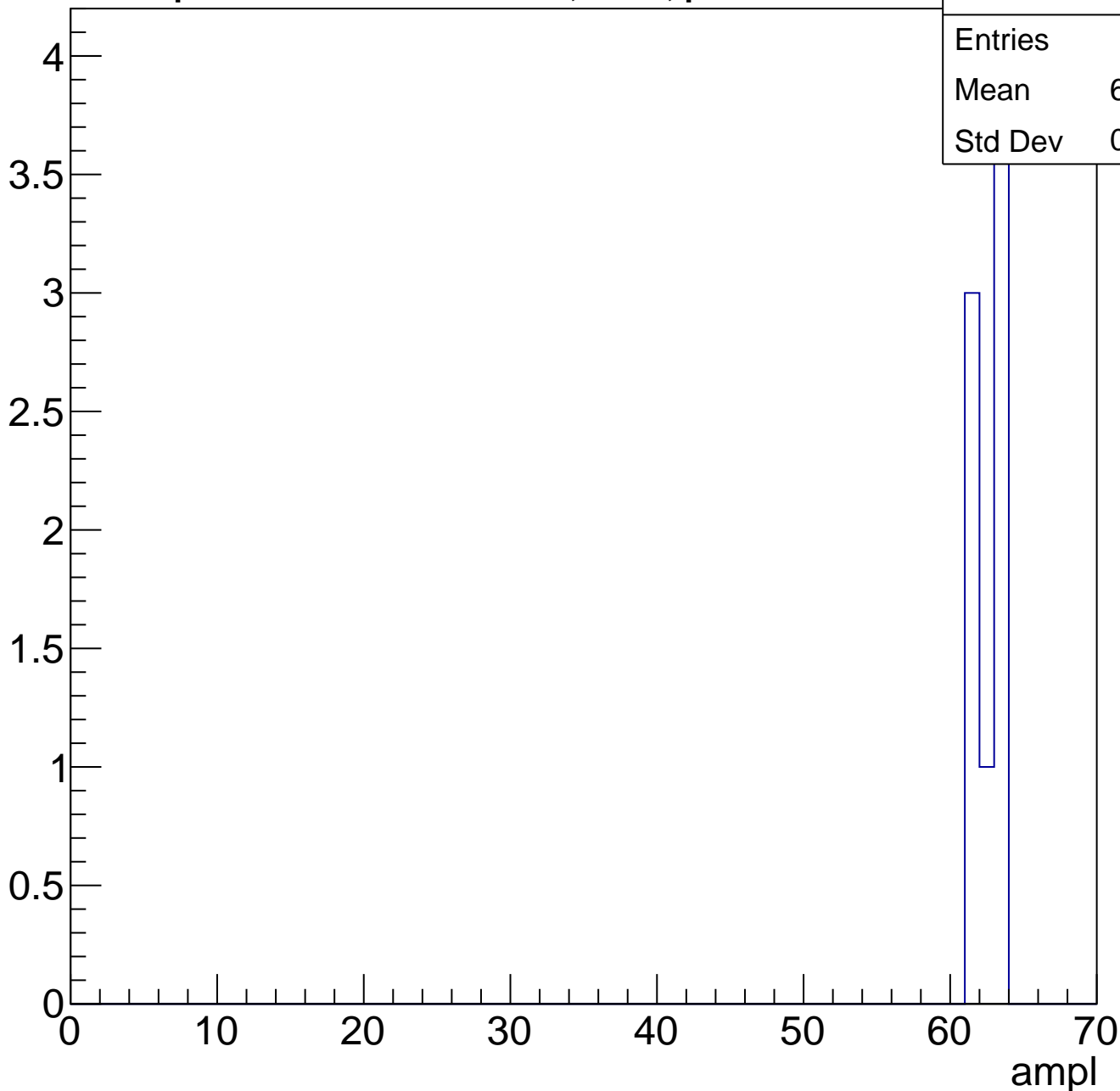
Entry



B1L100S, U1-ch38, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch38, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch39, adc0

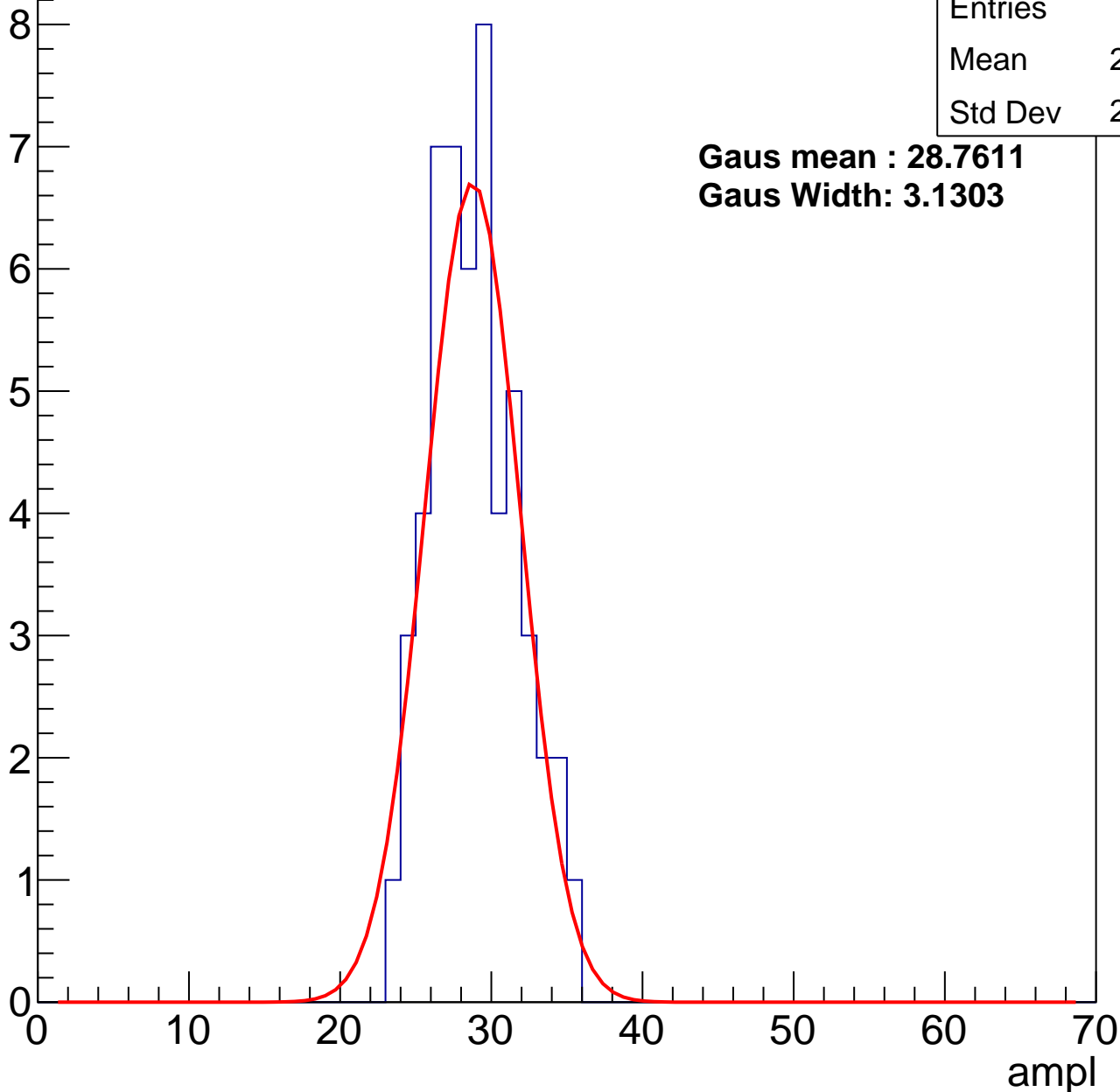
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	28.42
Std Dev	2.825

Gaus mean : 28.7611

Gaus Width: 3.1303



B1L100S, U1-ch39, adc1

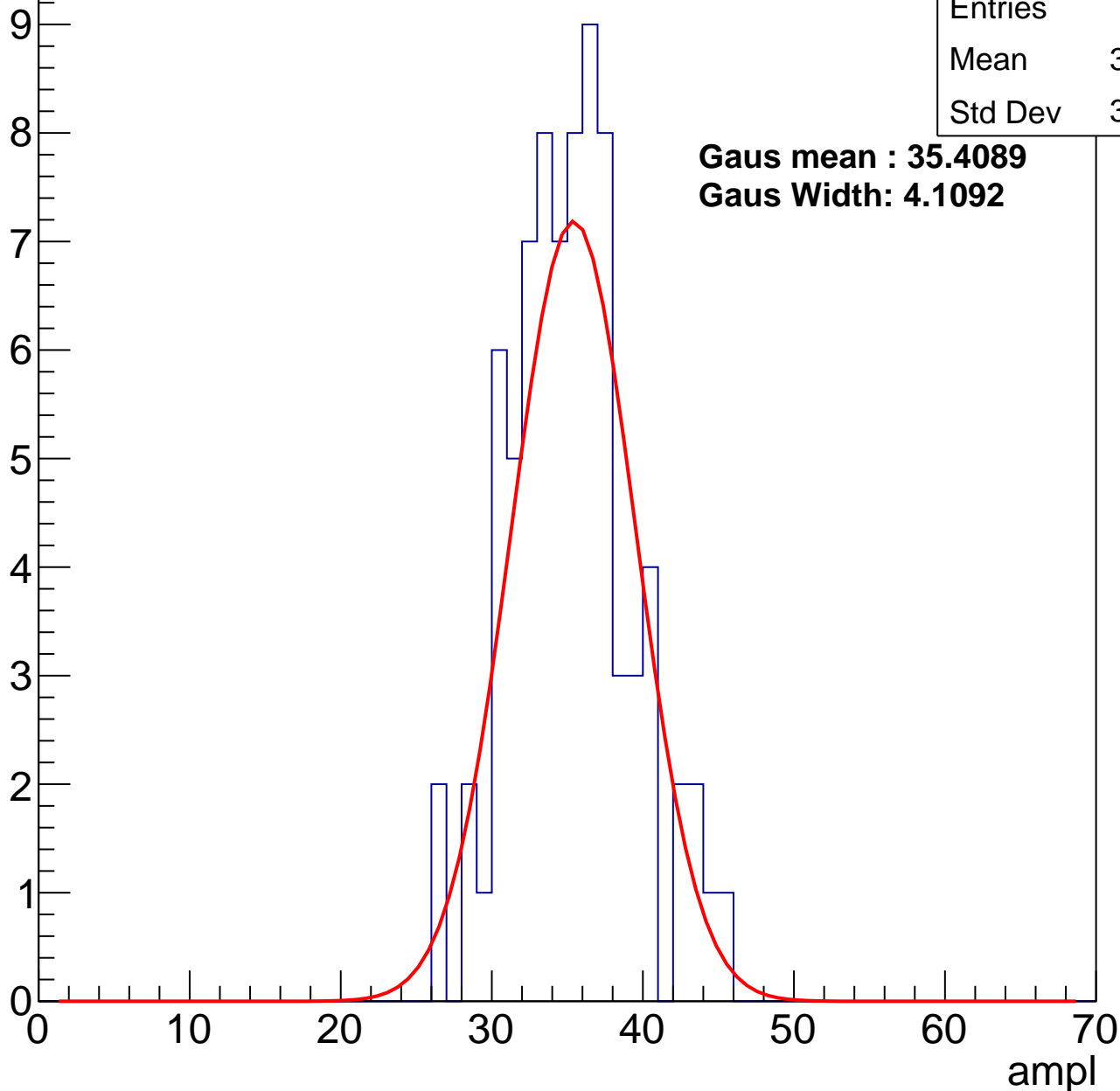
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	34.78
Std Dev	3.974

Gaus mean : 35.4089

Gaus Width: 4.1092



B1L100S, U1-ch39, adc2

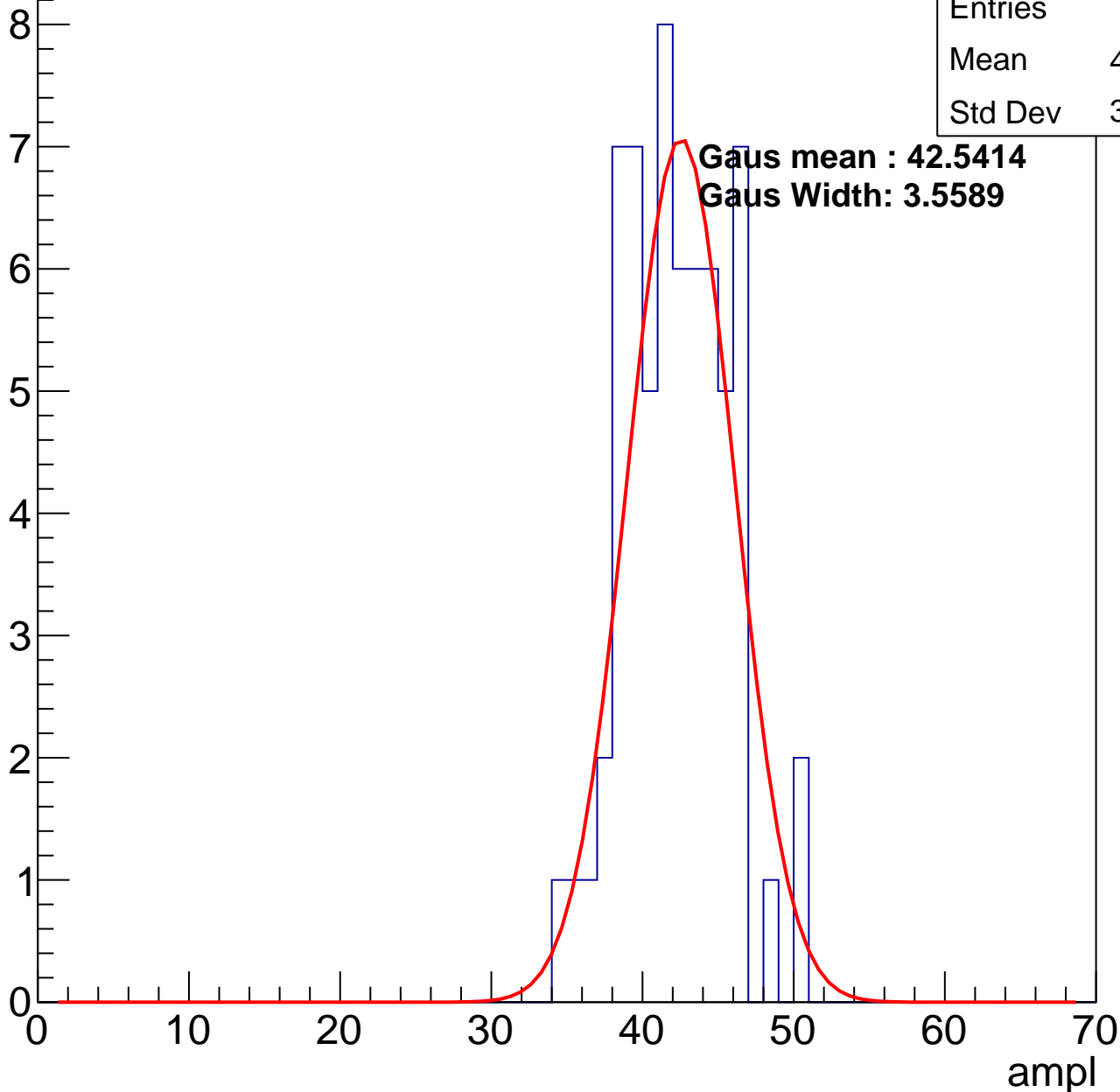
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.77
Std Dev	3.396

Gaus mean : 42.5414

Gaus Width: 3.5589



B1L100S, U1-ch39, adc3

calib_packv5_042523_0143.root, FC#4, port A2

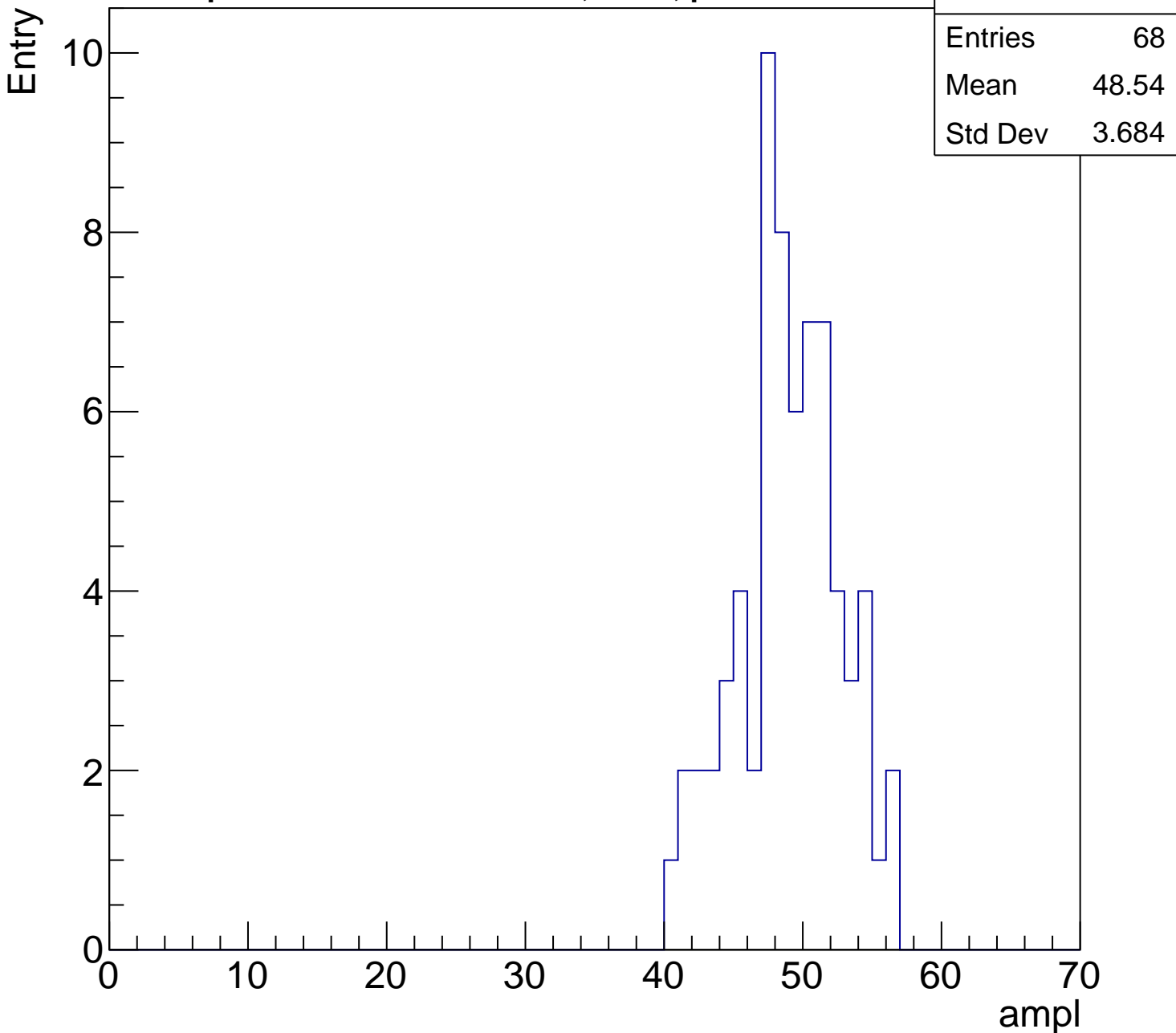
Entries	68
Mean	48.54
Std Dev	3.684

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

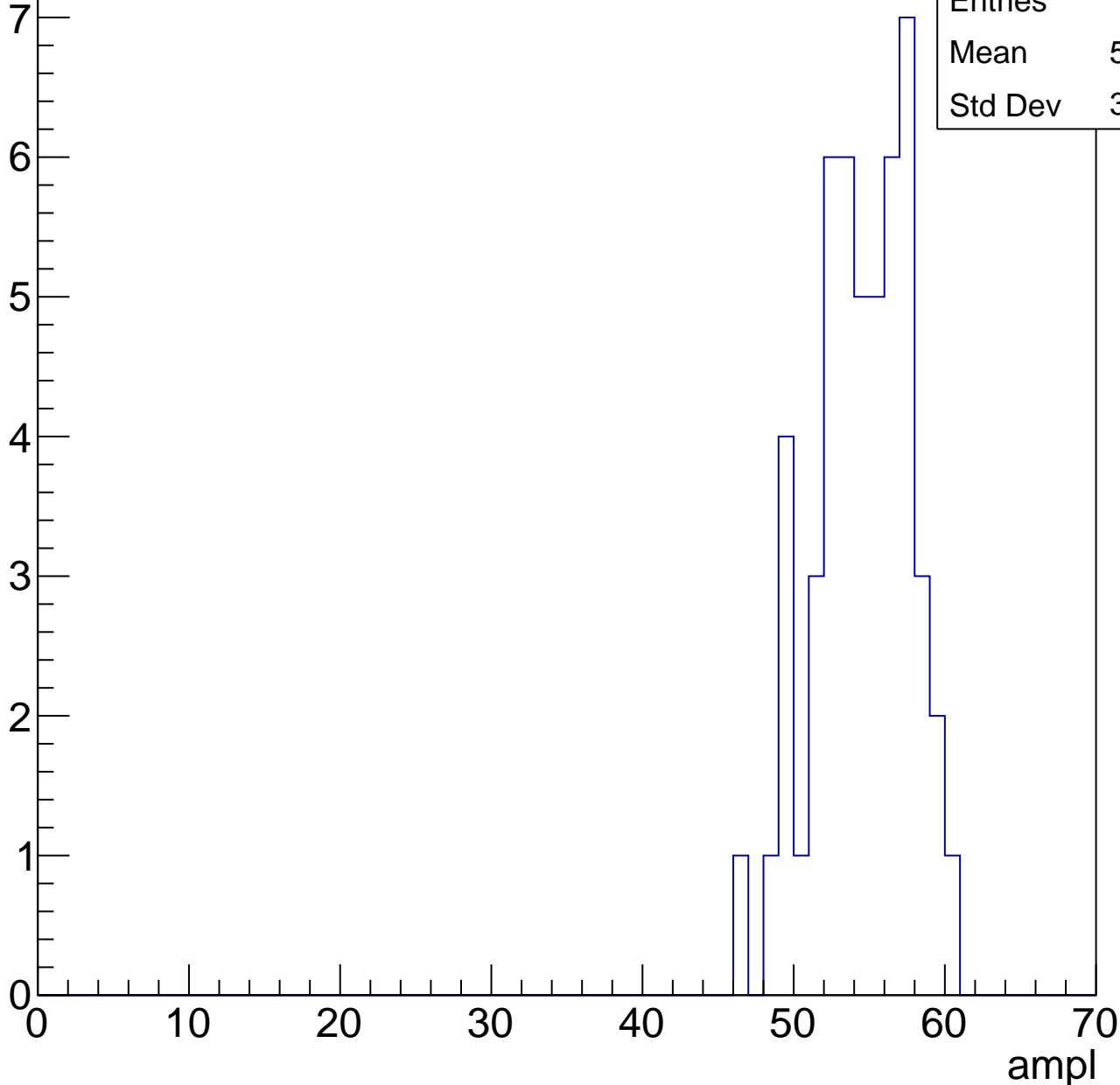


B1L100S, U1-ch39, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

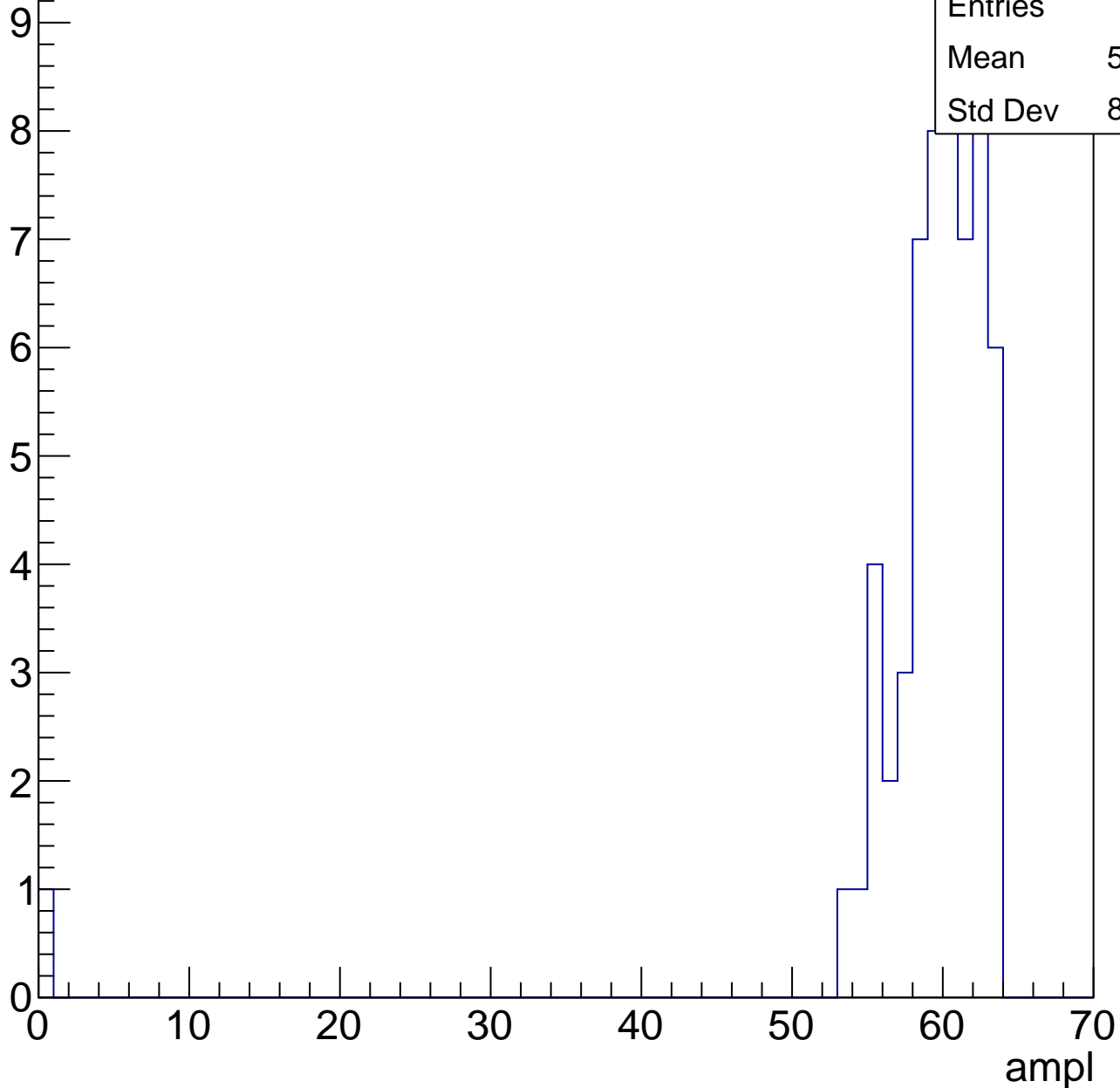
Entries	51
Mean	54.02
Std Dev	3.128



B1L100S, U1-ch39, adc5

calib_packv5_042523_0143.root, FC#4, port A2

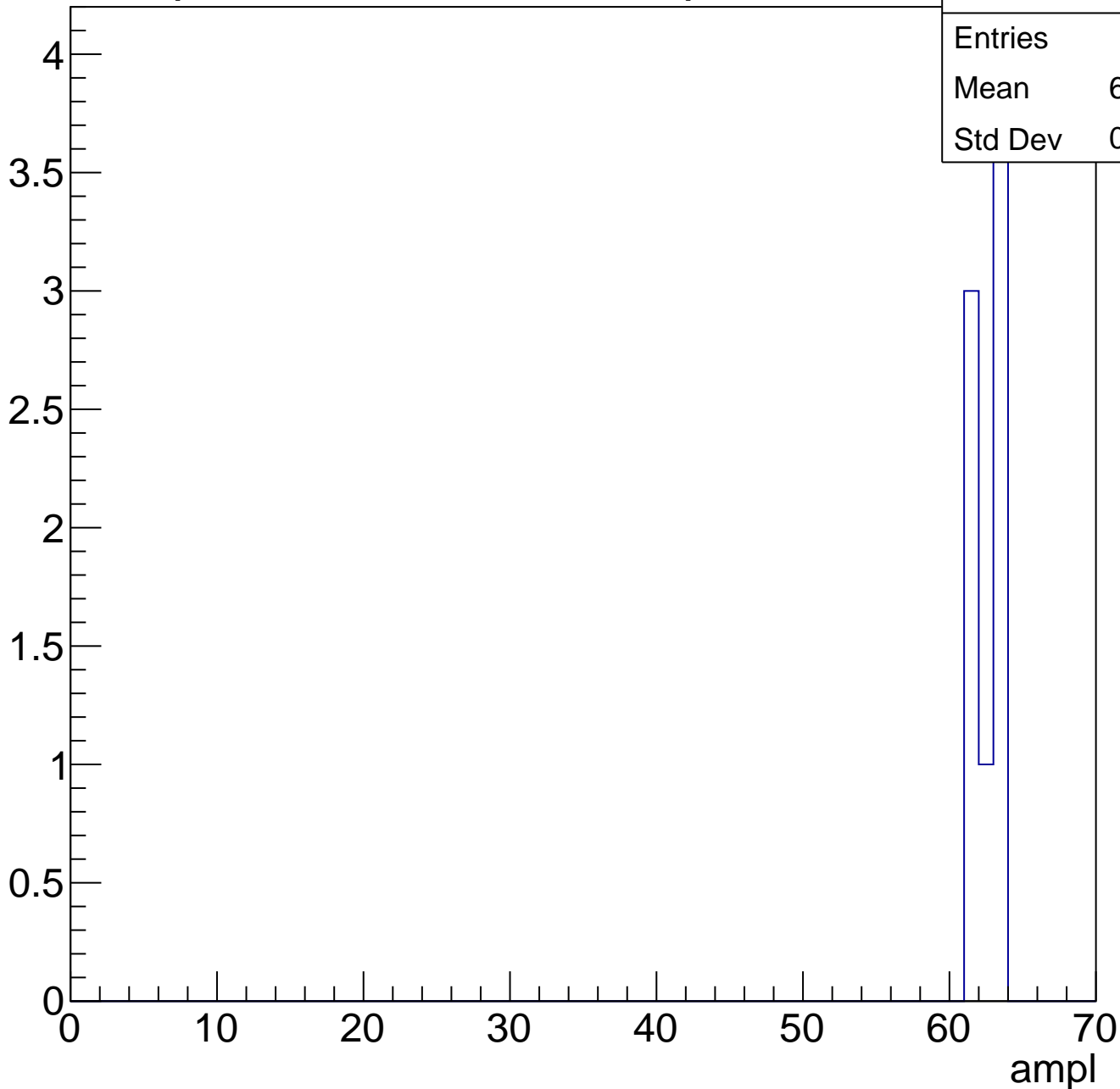
Entry



B1L100S, U1-ch39, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch39, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch40, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	27.36
Std Dev	6.407

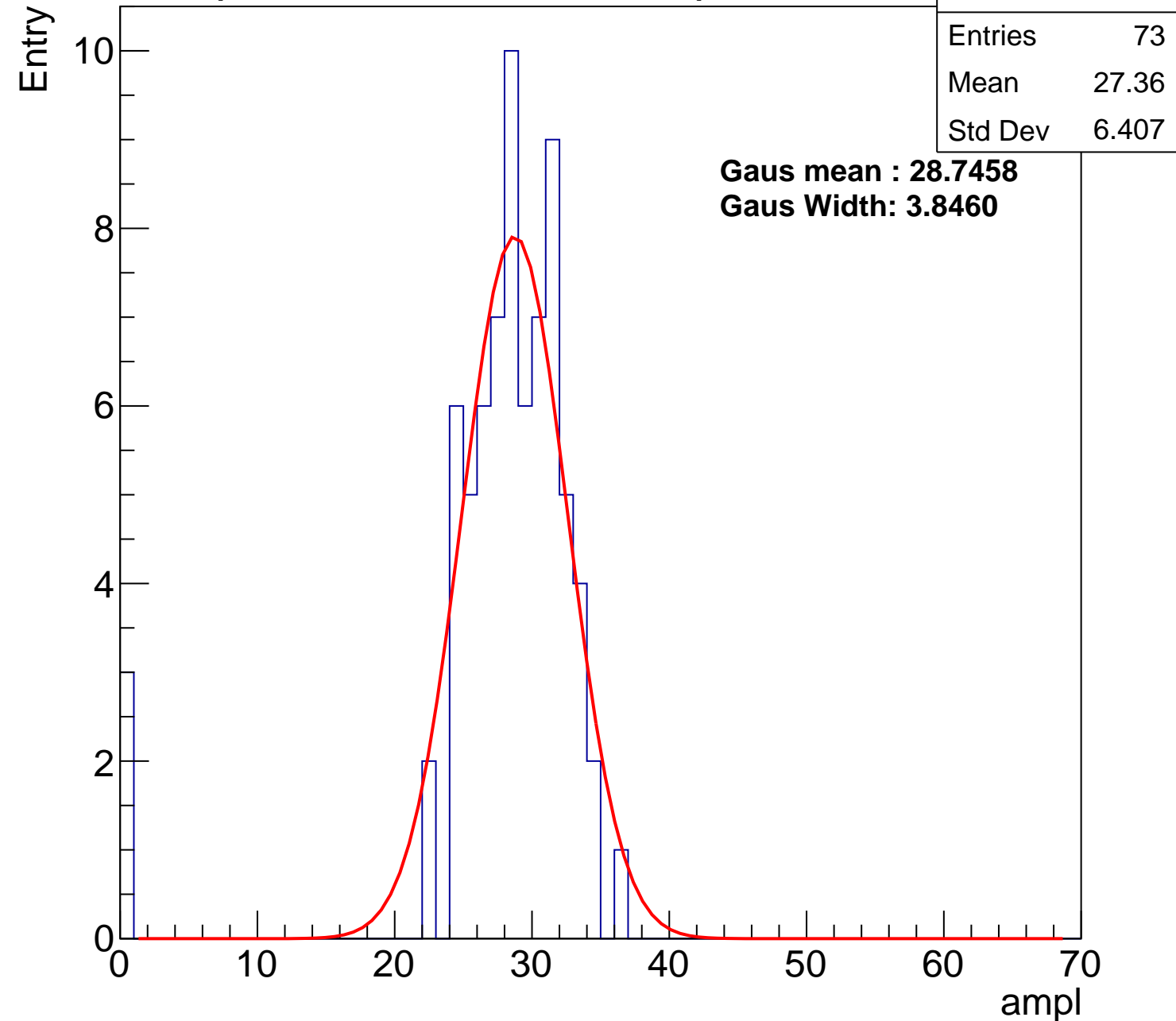
Gaus mean : 28.7458

Gaus Width: 3.8460

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



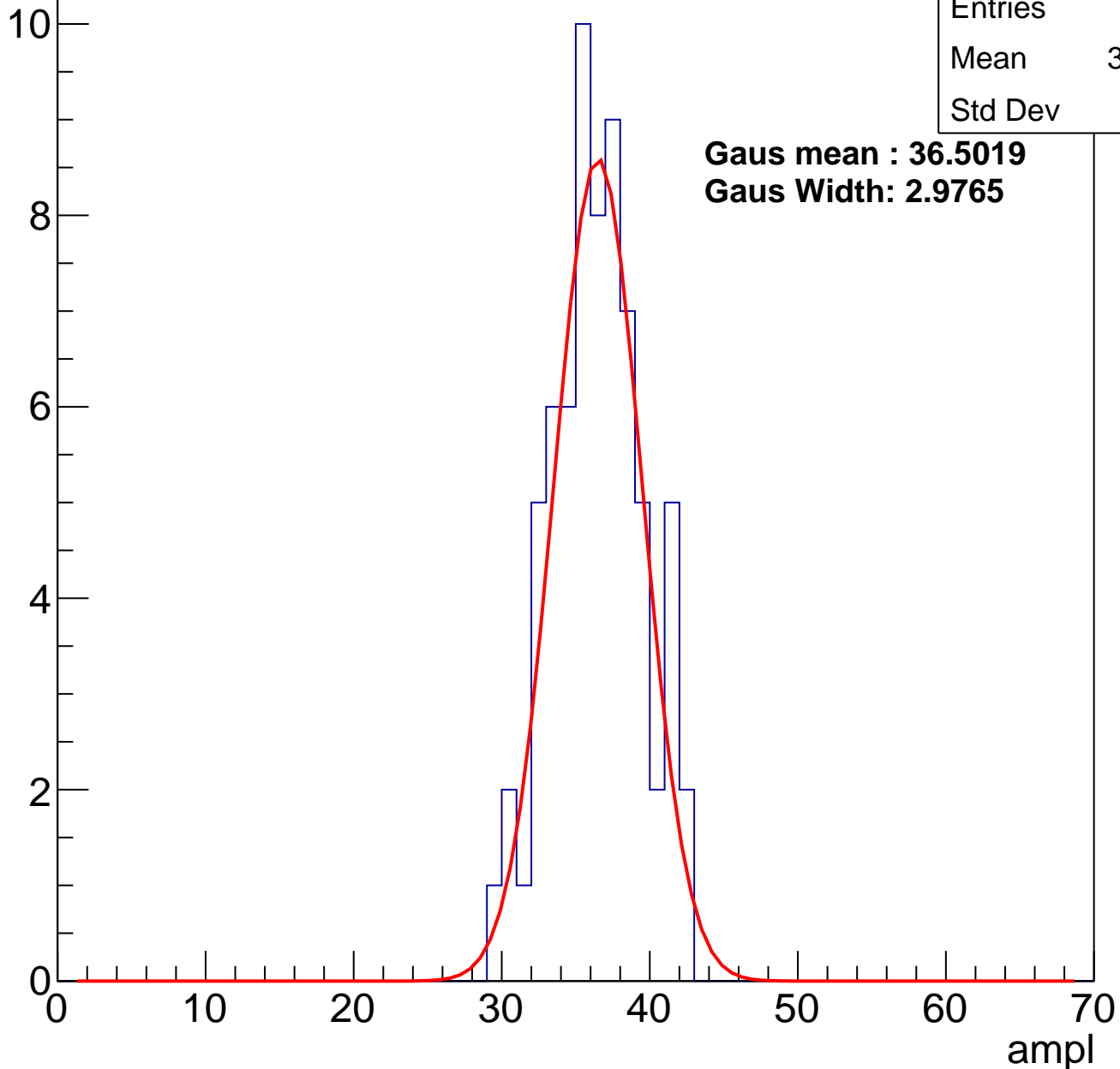
B1L100S, U1-ch40, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	35.99
Std Dev	3.01

Gaus mean : 36.5019
Gaus Width: 2.9765

Entry



B1L100S, U1-ch40, adc2

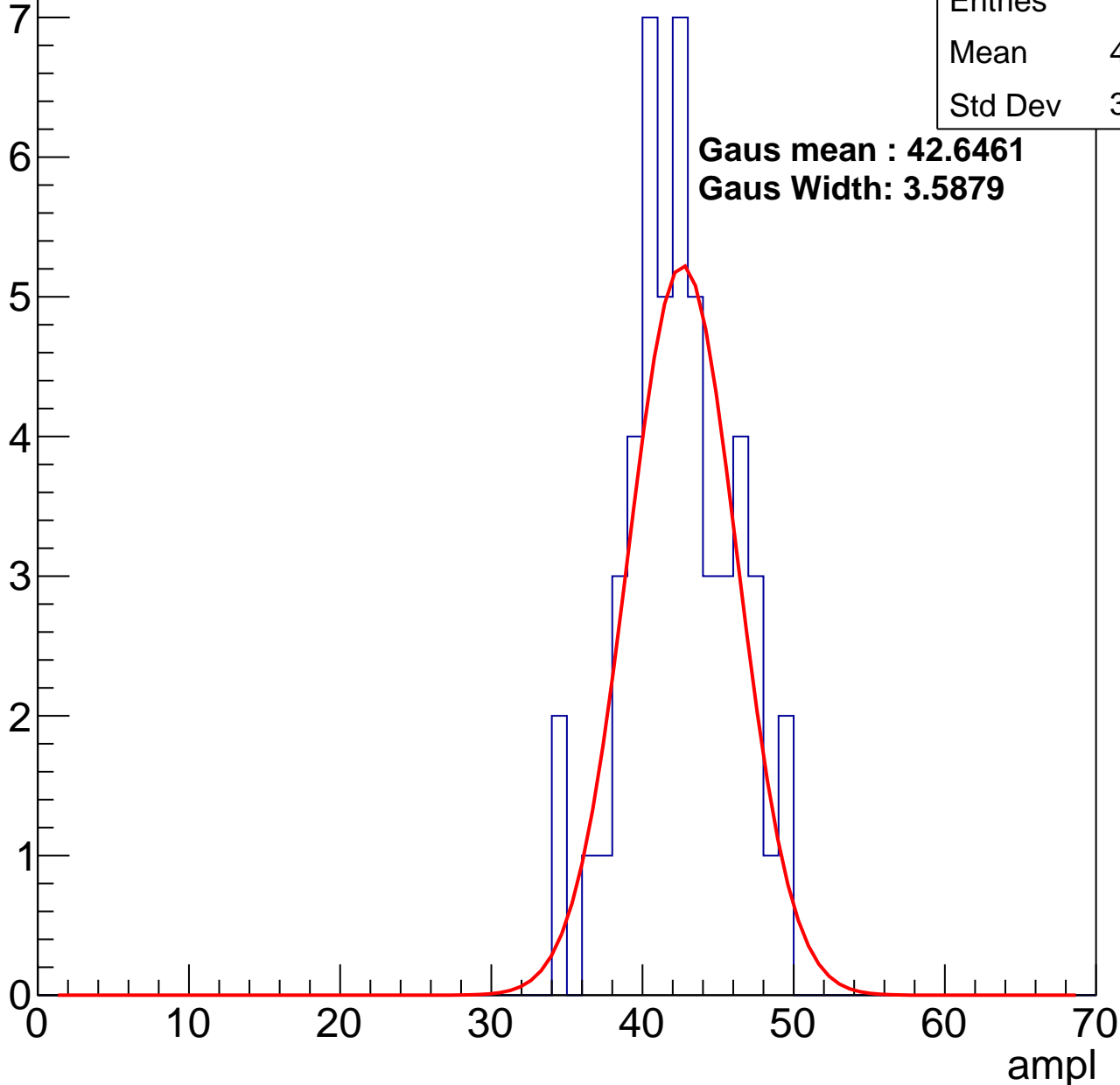
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.02
Std Dev	3.495

Gaus mean : 42.6461

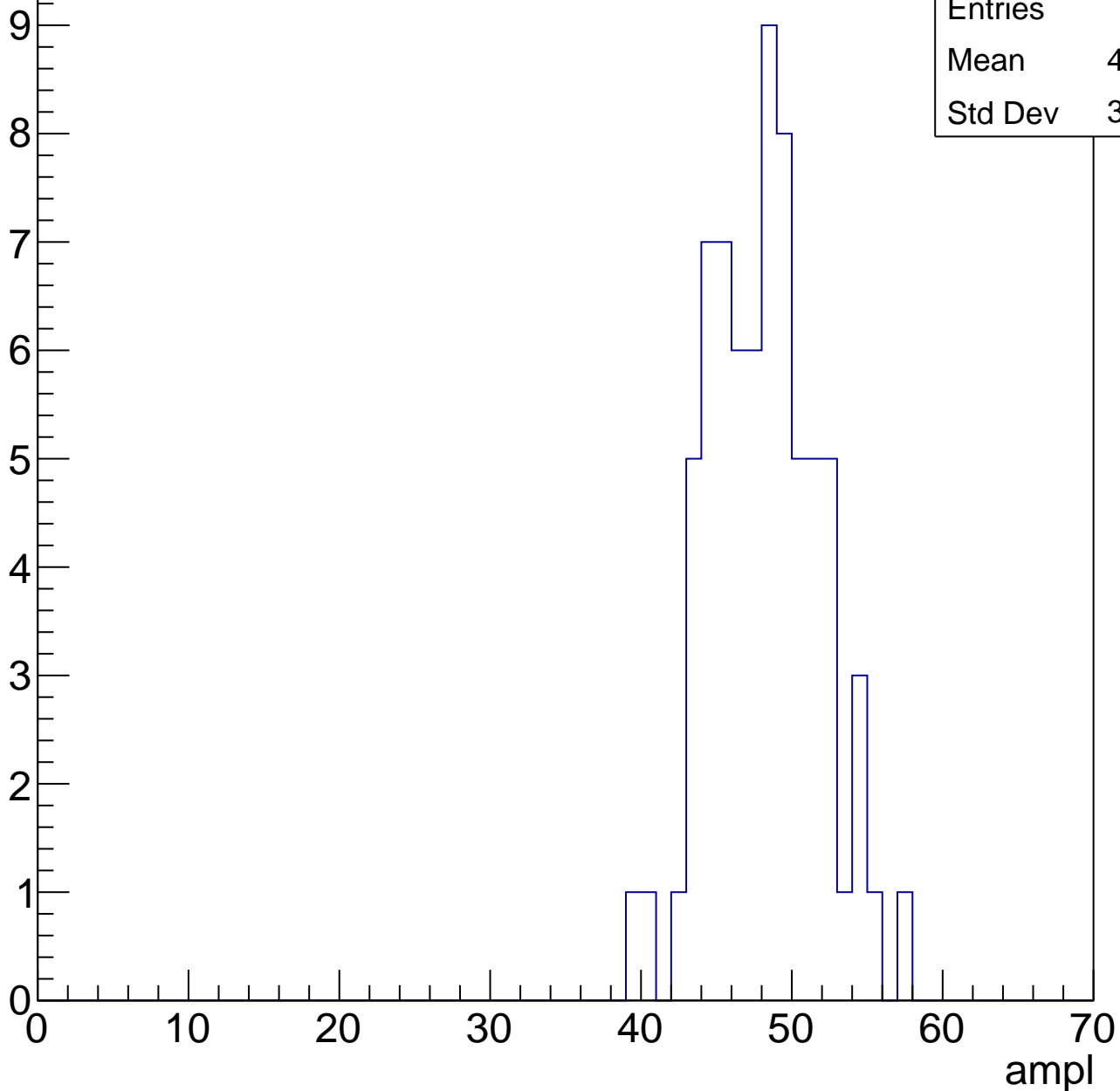
Gaus Width: 3.5879



B1L100S, U1-ch40, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

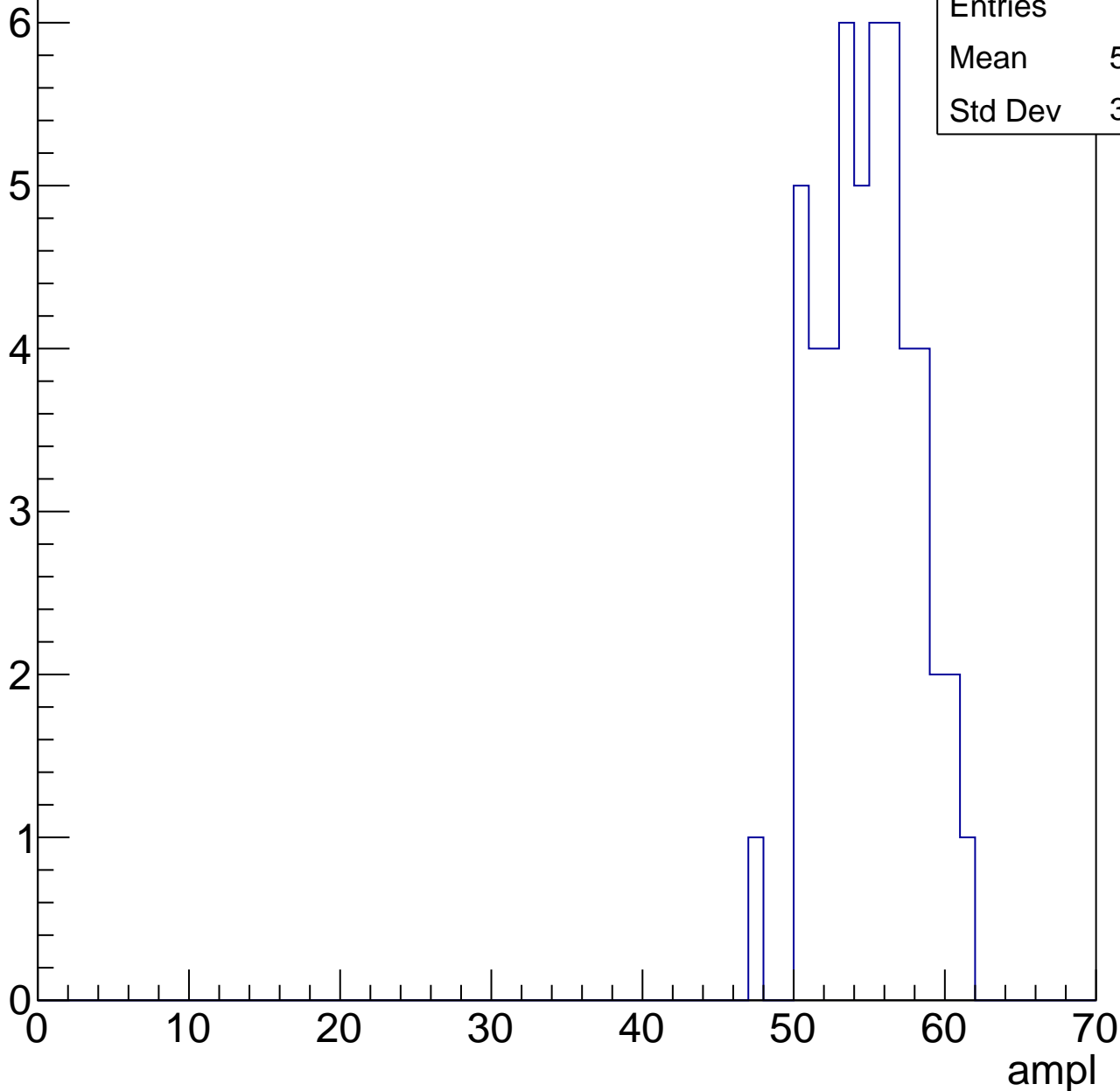


B1L100S, U1-ch40, adc4

calib_packv5_042523_0143.root, FC#4, port A2

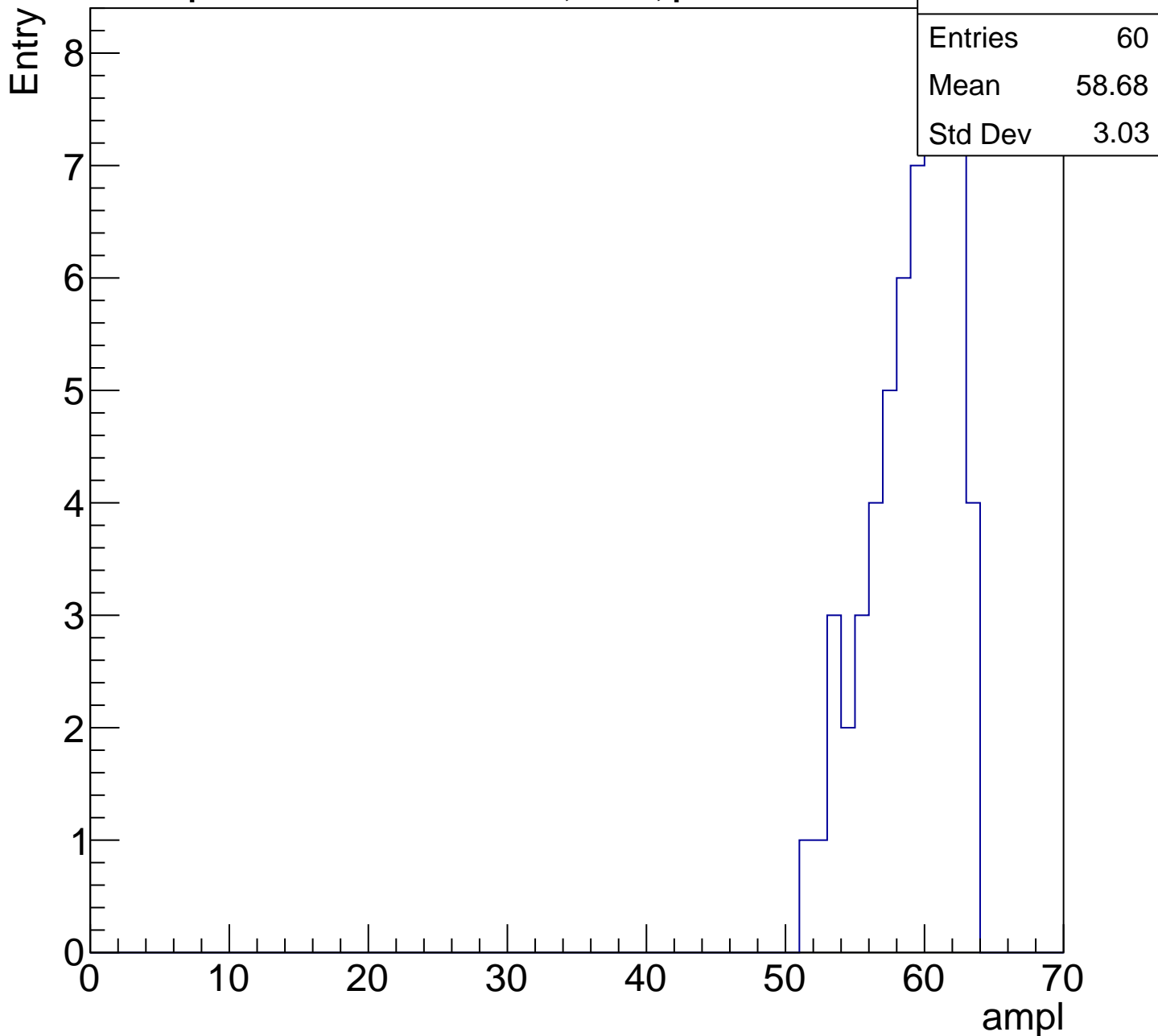
Entry

Entries	50
Mean	54.44
Std Dev	3.093



B1L100S, U1-ch40, adc5

calib_packv5_042523_0143.root, FC#4, port A2

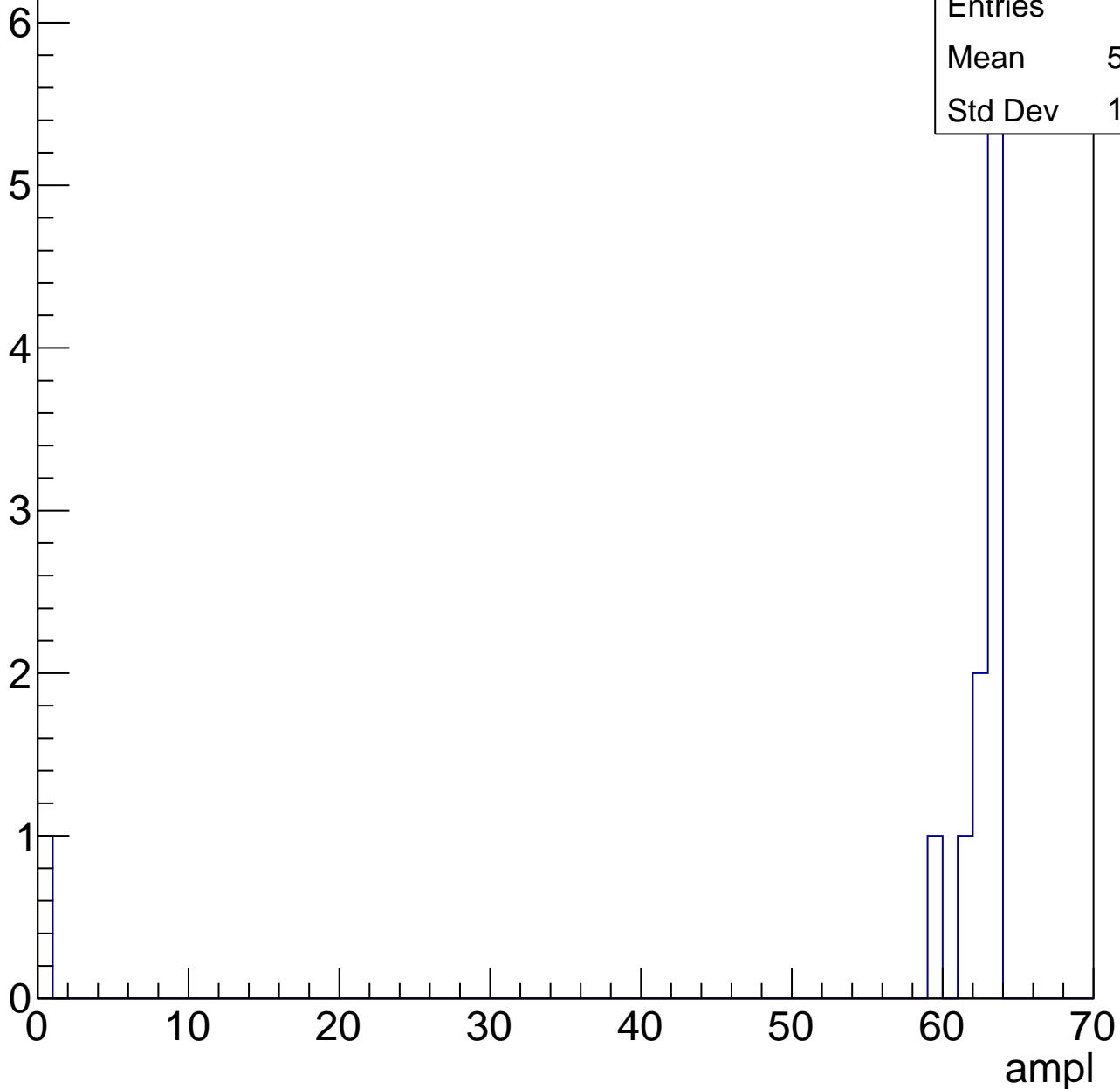


B1L100S, U1-ch40, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	56.55
Std Dev	17.92



B1L100S, U1-ch40, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch41, adc0

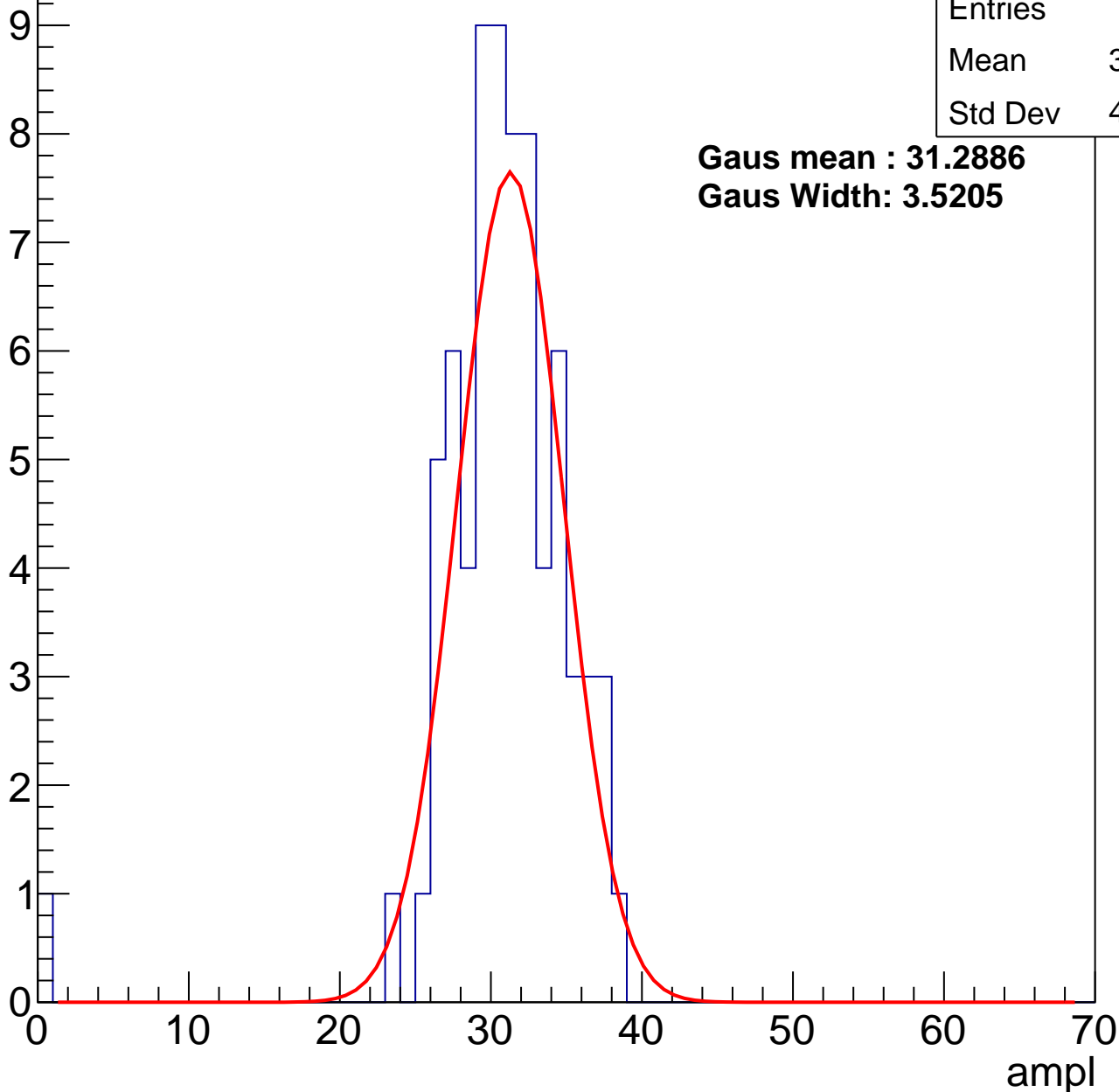
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.35
Std Dev	4.845

Gaus mean : 31.2886

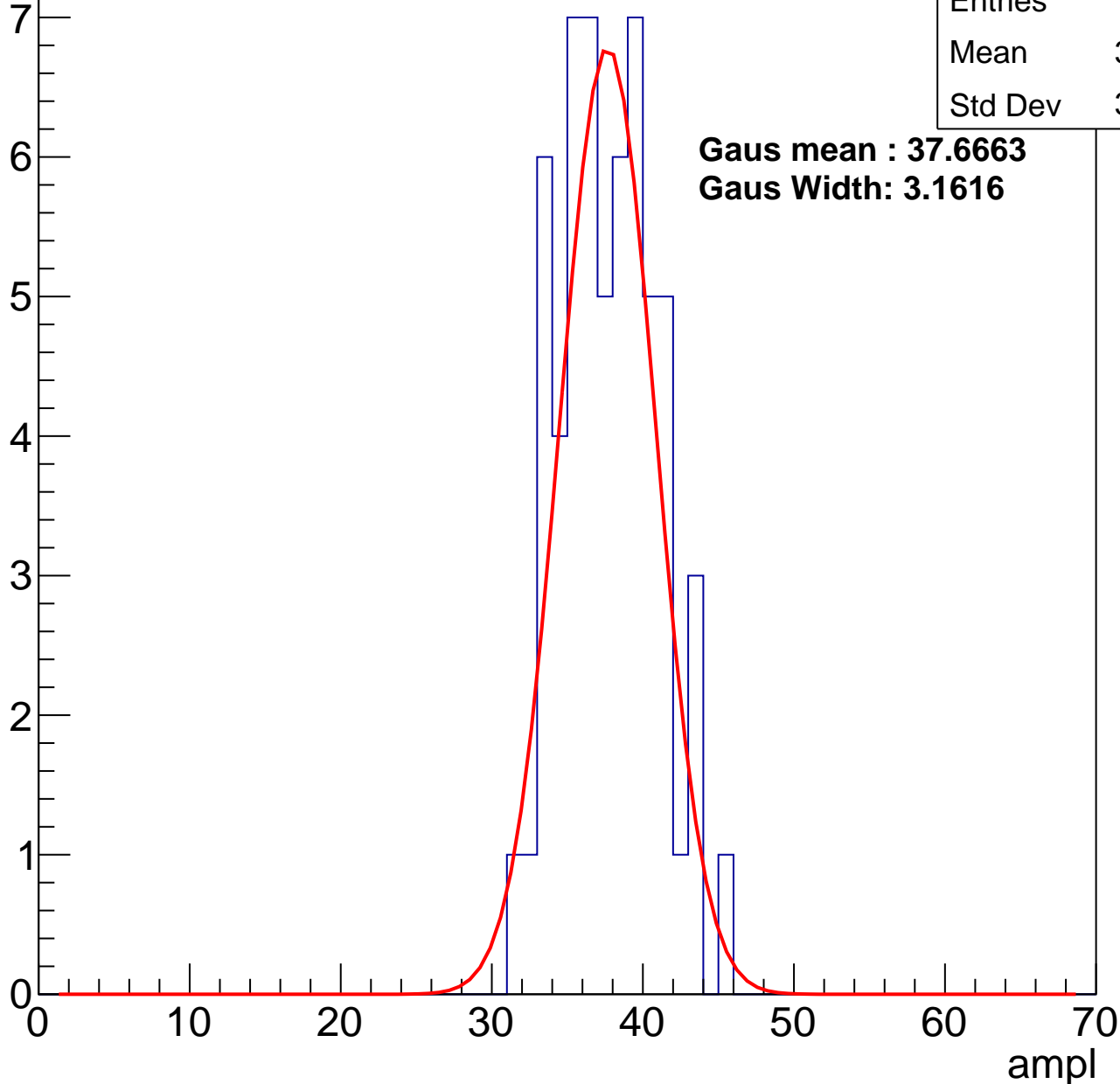
Gaus Width: 3.5205



B1L100S, U1-ch41, adc1

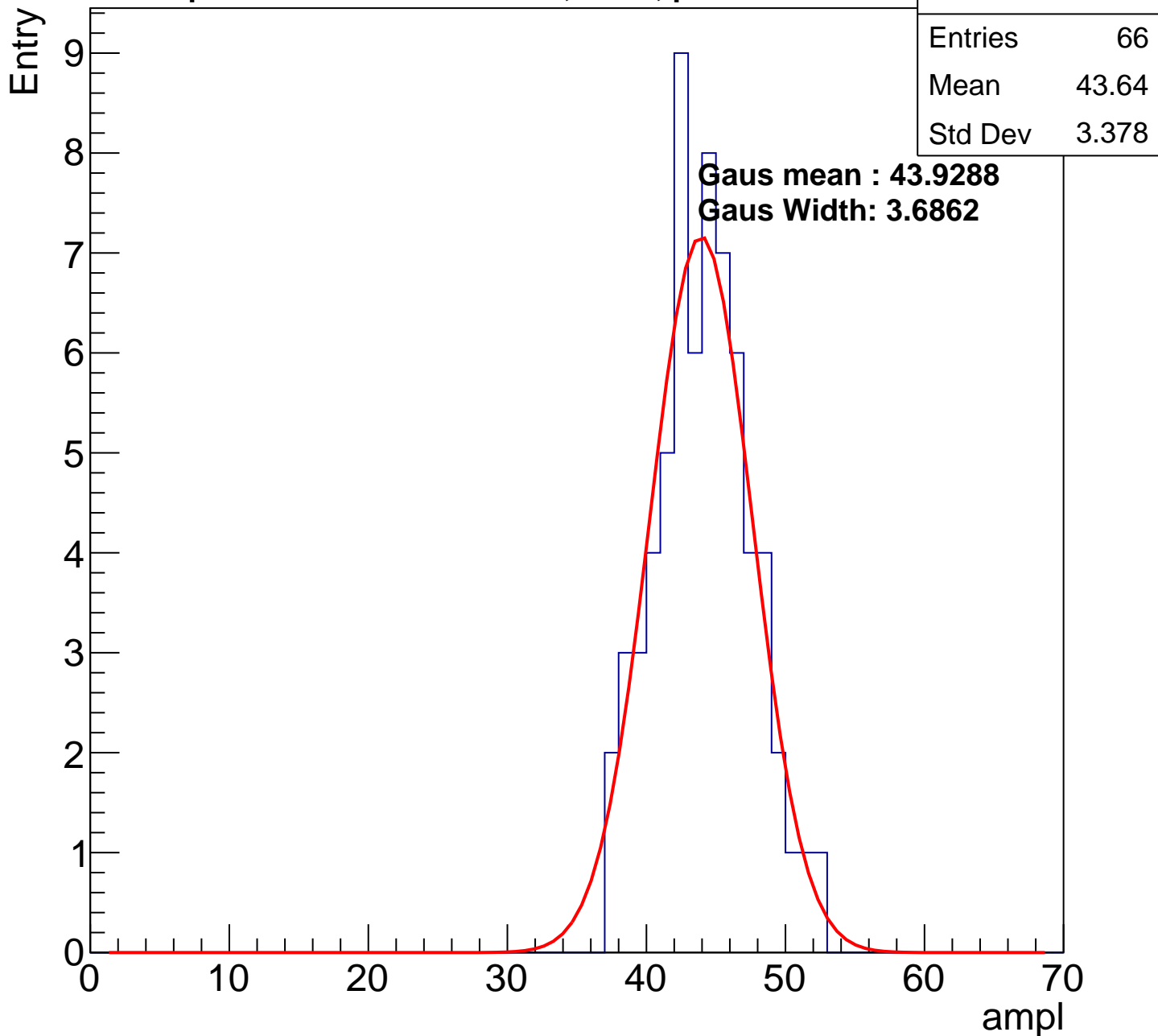
calib_packv5_042523_0143.root, FC#4, port A2

Entry



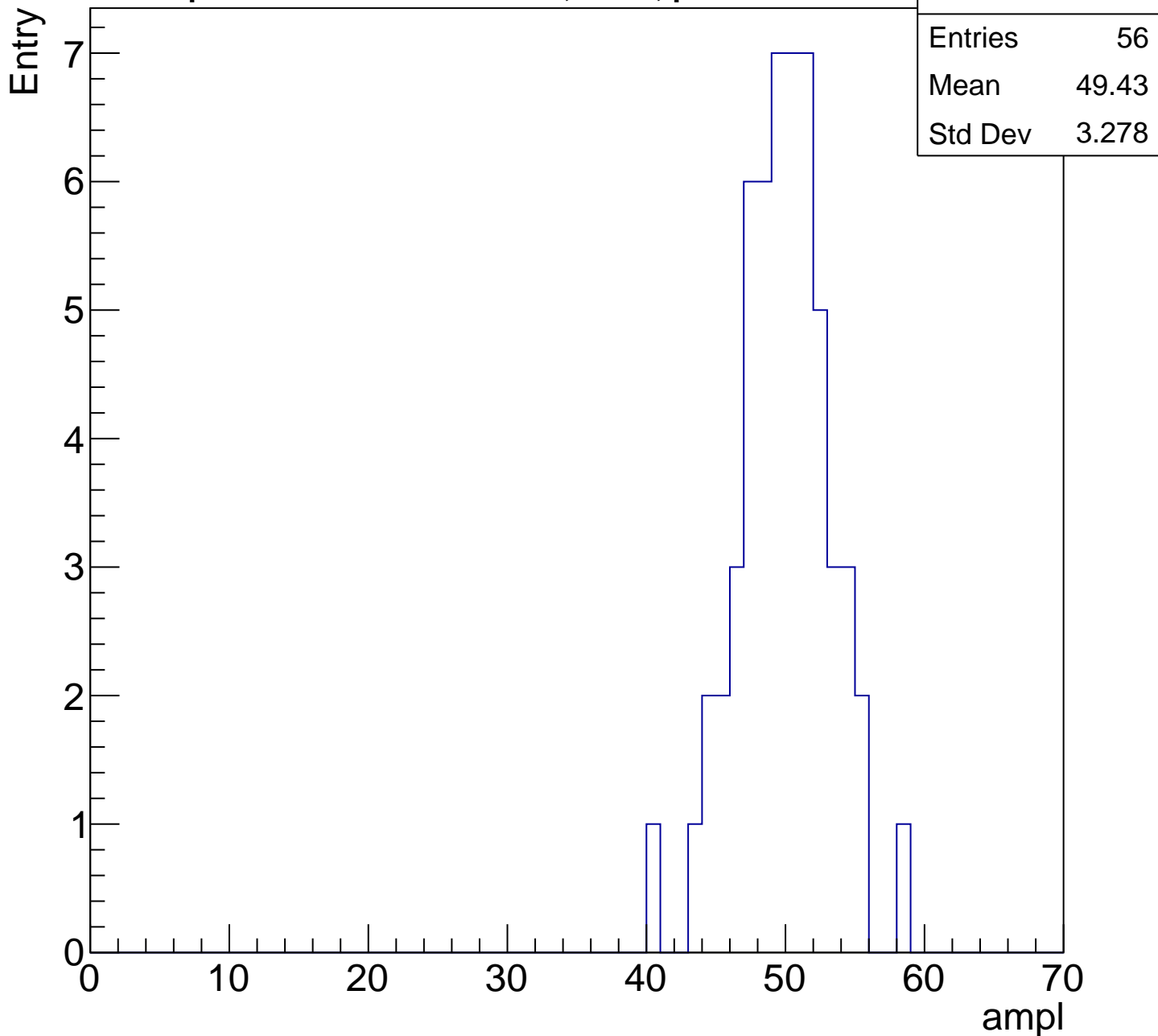
B1L100S, U1-ch41, adc2

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch41, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch41, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	55.66
Std Dev	3.267

Entry

10

8

6

4

2

0

0

10

20

30

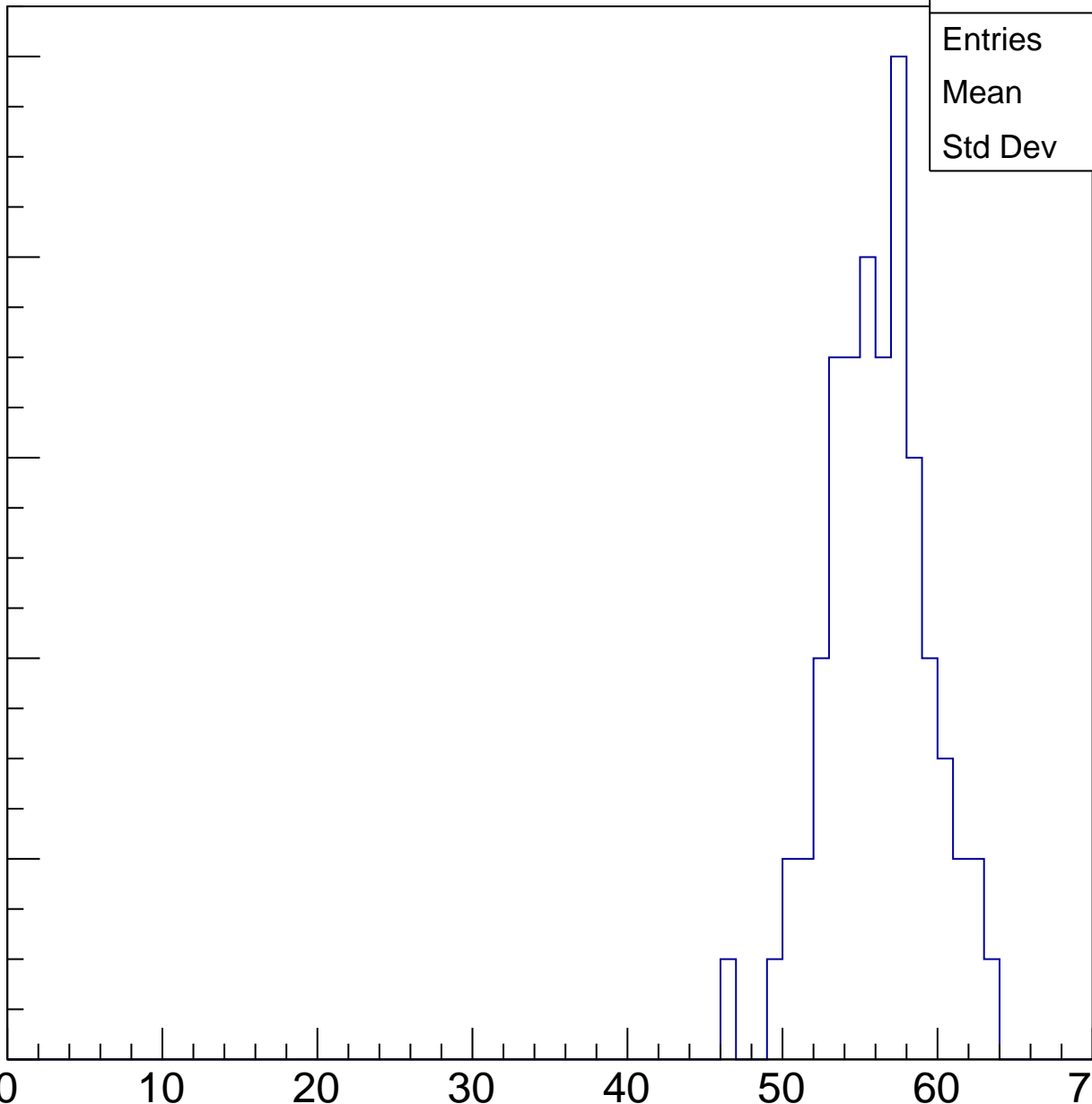
40

50

60

70

ampl

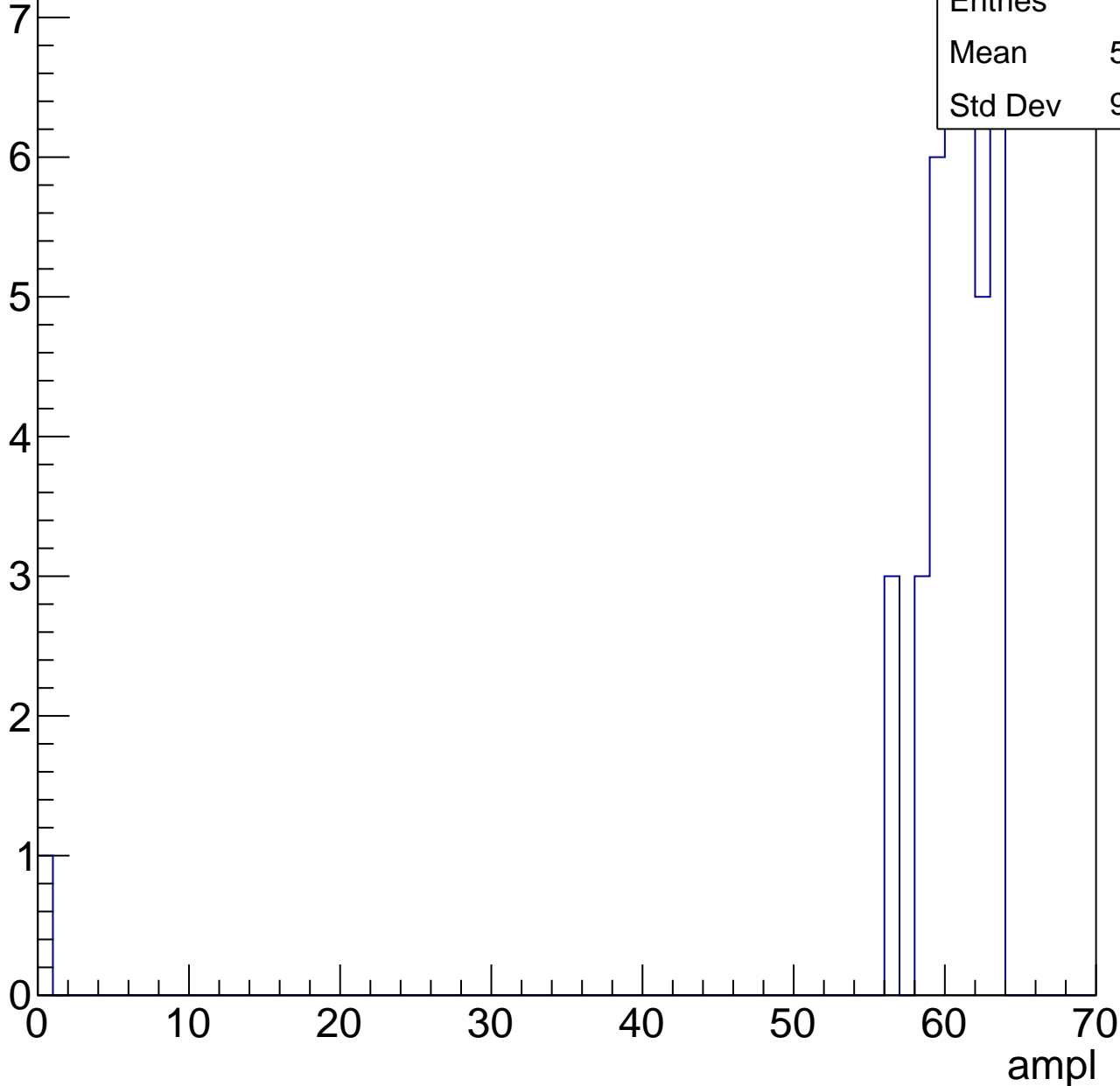


B1L100S, U1-ch41, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

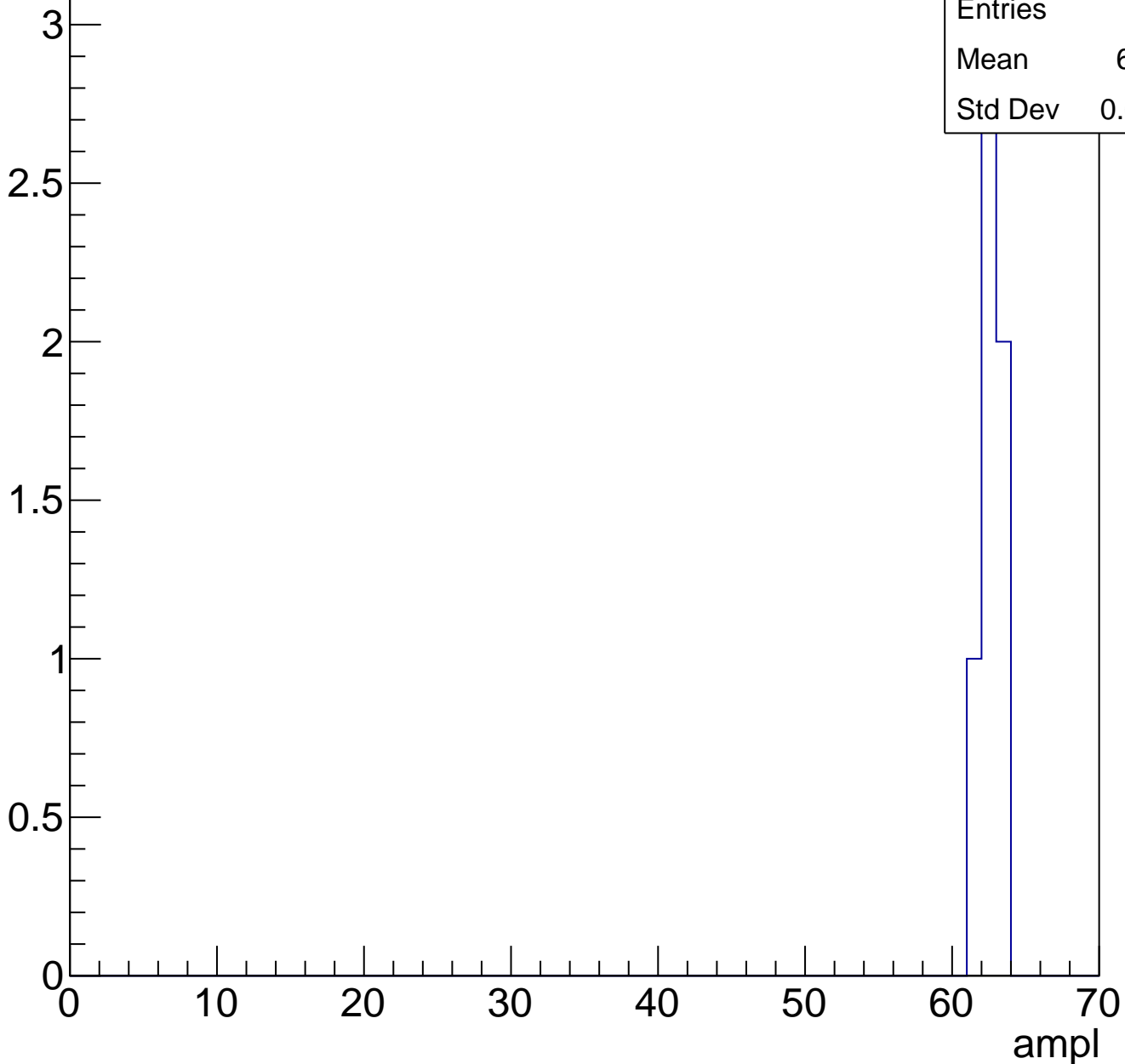
Entries	39
Mean	58.82
Std Dev	9.743



B1L100S, U1-ch41, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch41, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	7.667
Std Dev	10.84

B1L100S, U1-ch42, adc0

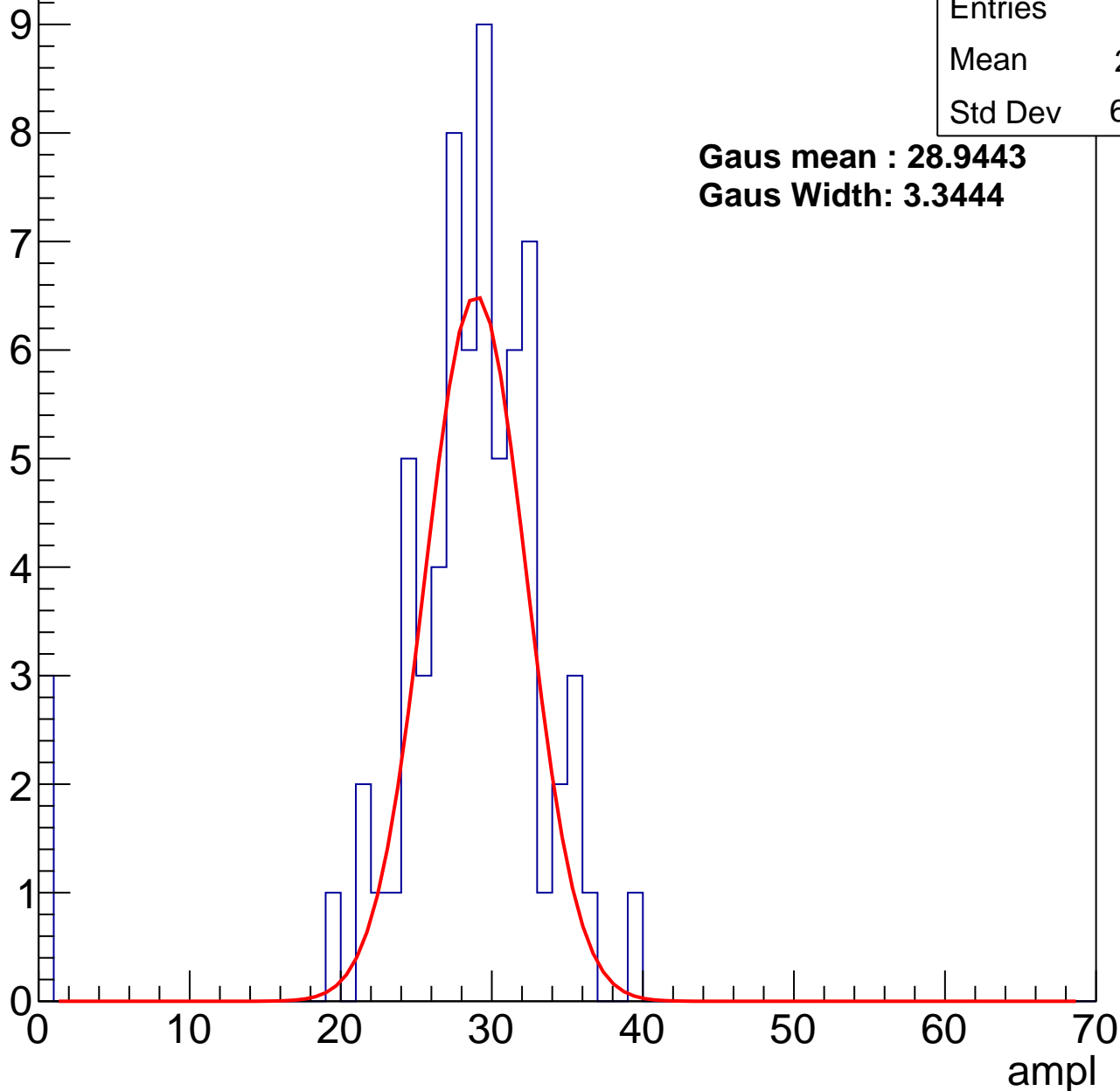
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	27.41
Std Dev	6.939

Gaus mean : 28.9443

Gaus Width: 3.3444



B1L100S, U1-ch42, adc1

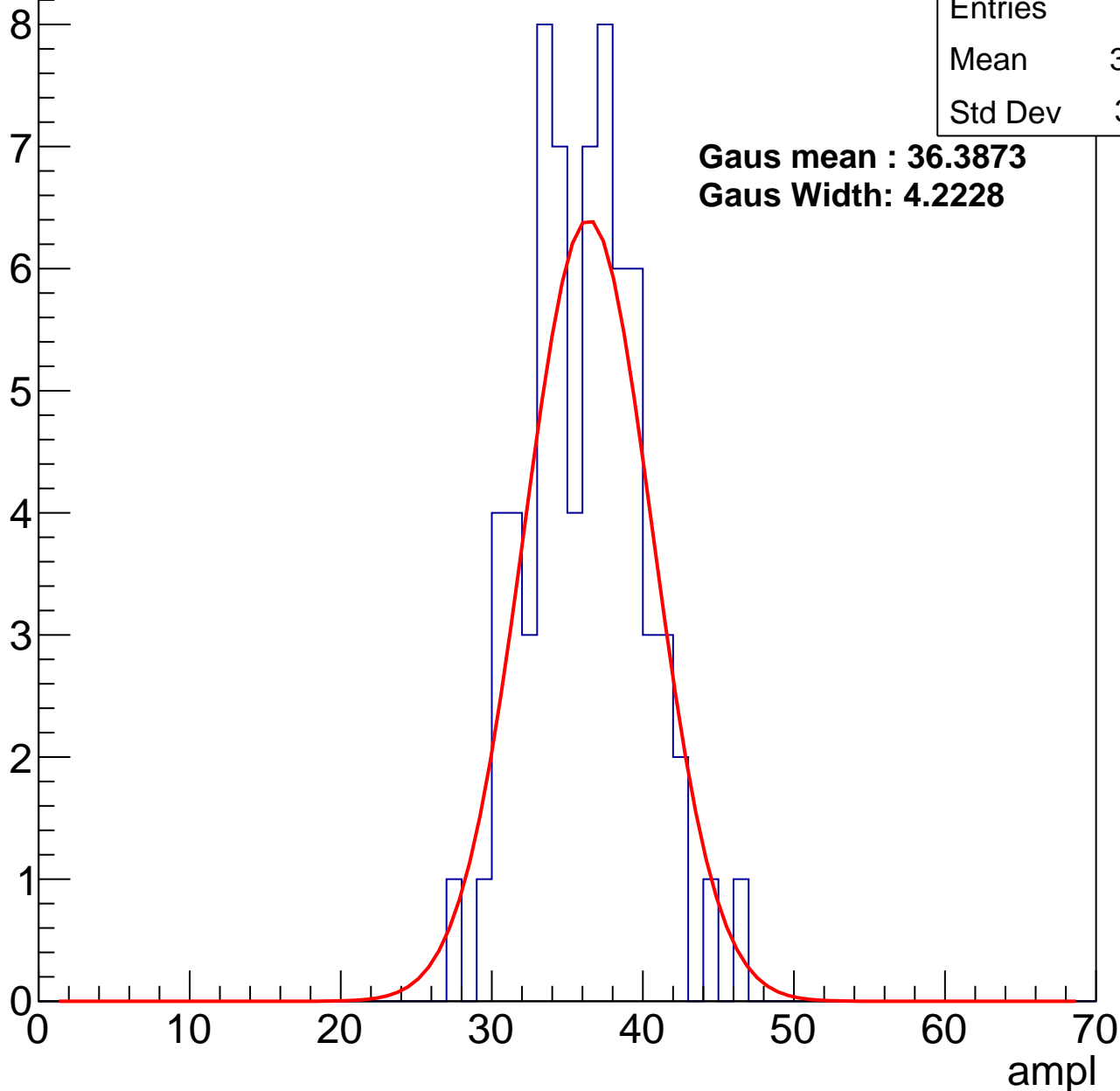
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	35.72
Std Dev	3.741

Gaus mean : 36.3873

Gaus Width: 4.2228



B1L100S, U1-ch42, adc2

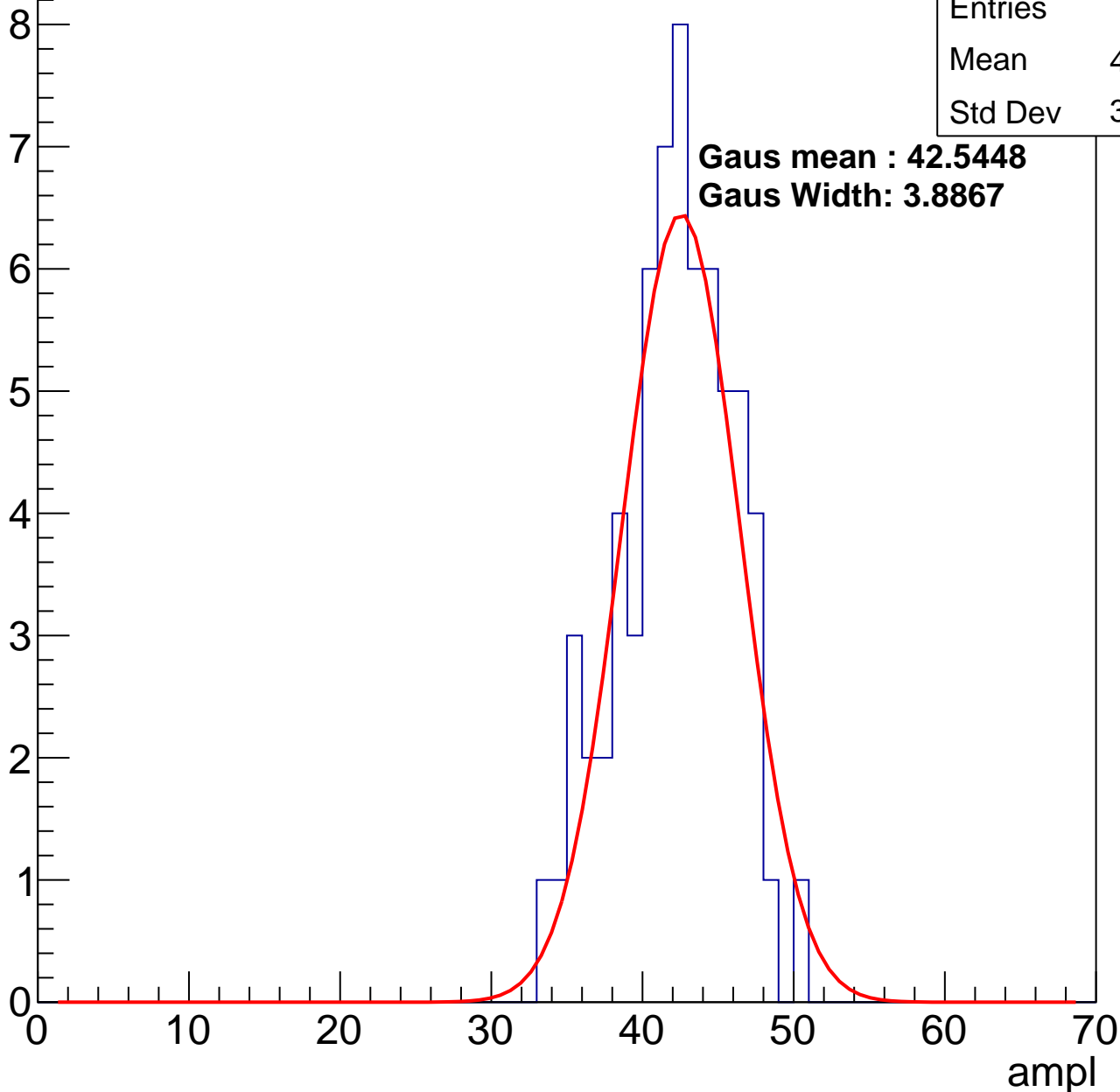
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.74
Std Dev	3.693

Gaus mean : 42.5448

Gaus Width: 3.8867

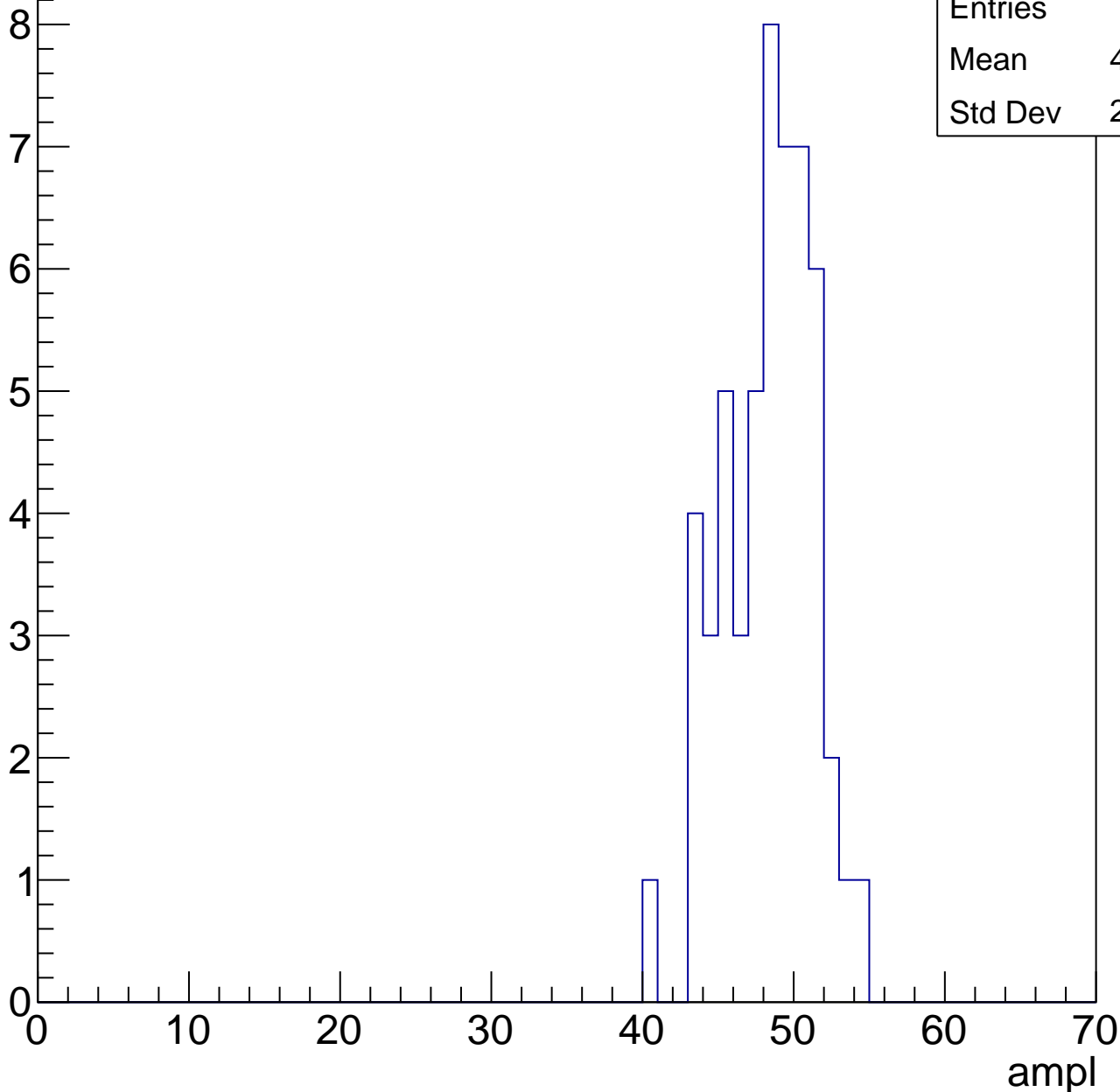


B1L100S, U1-ch42, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

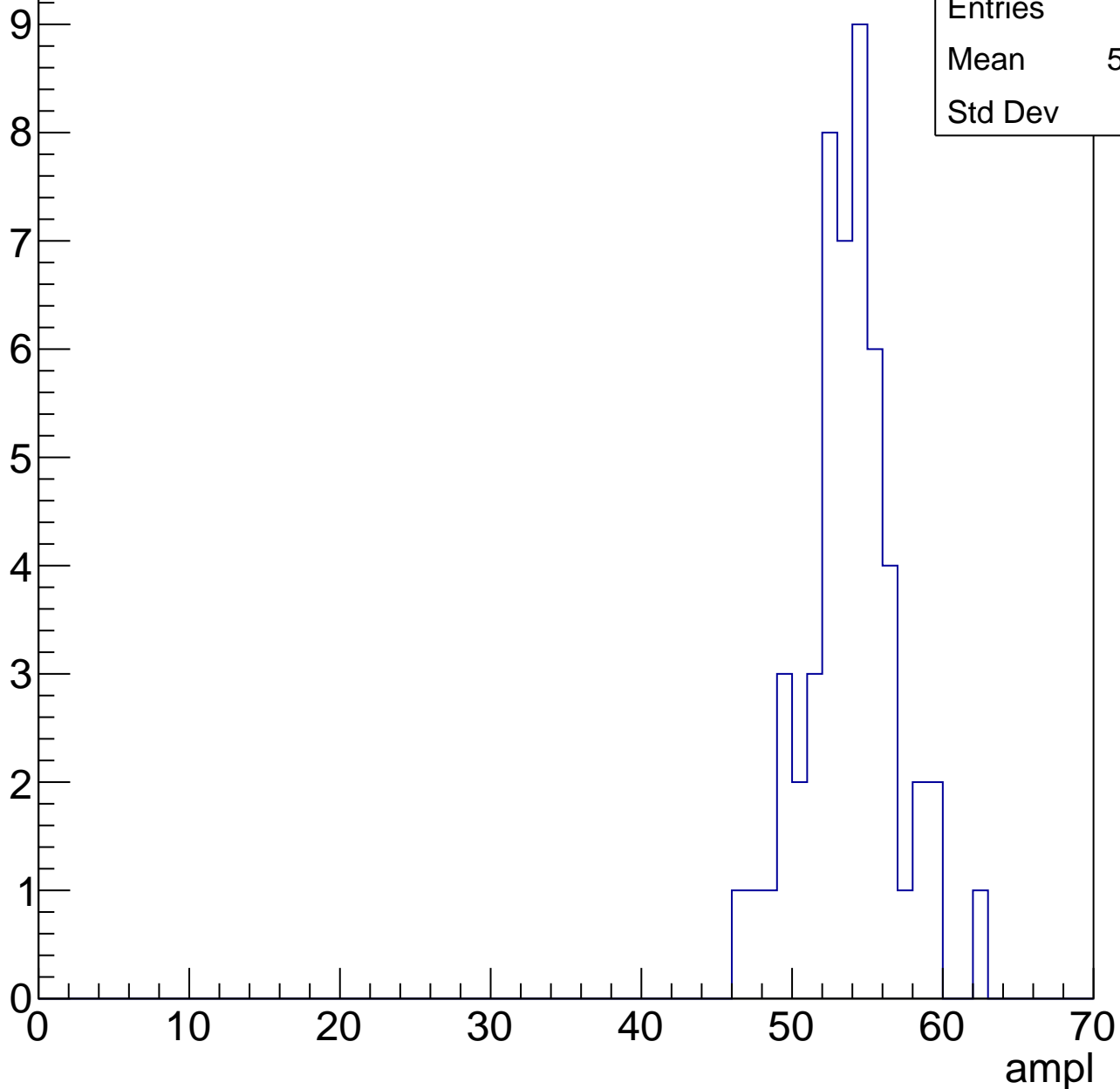
Entries	53
Mean	47.85
Std Dev	2.929



B1L100S, U1-ch42, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

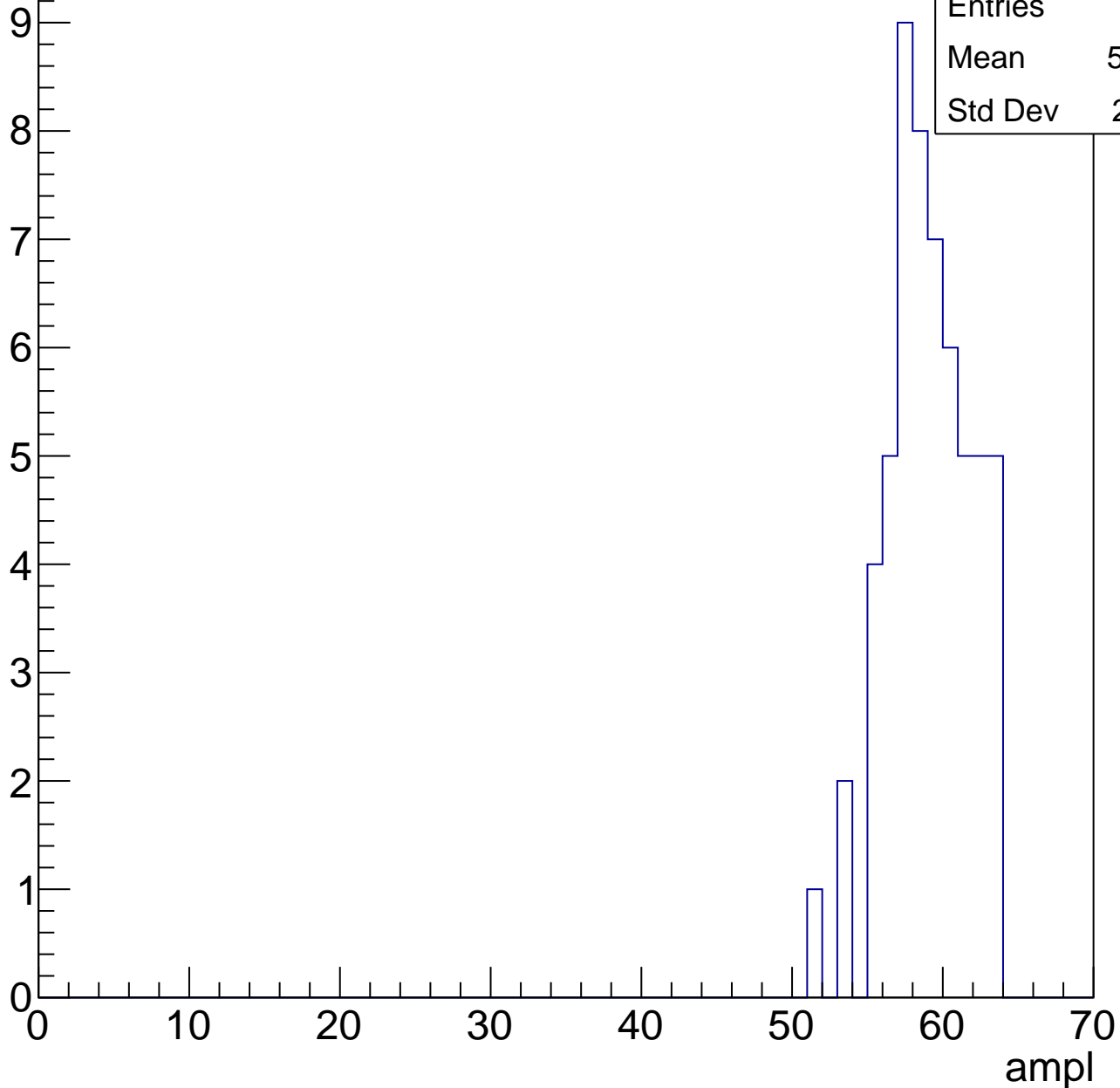


Entries	51
Mean	53.35
Std Dev	3.06

B1L100S, U1-ch42, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



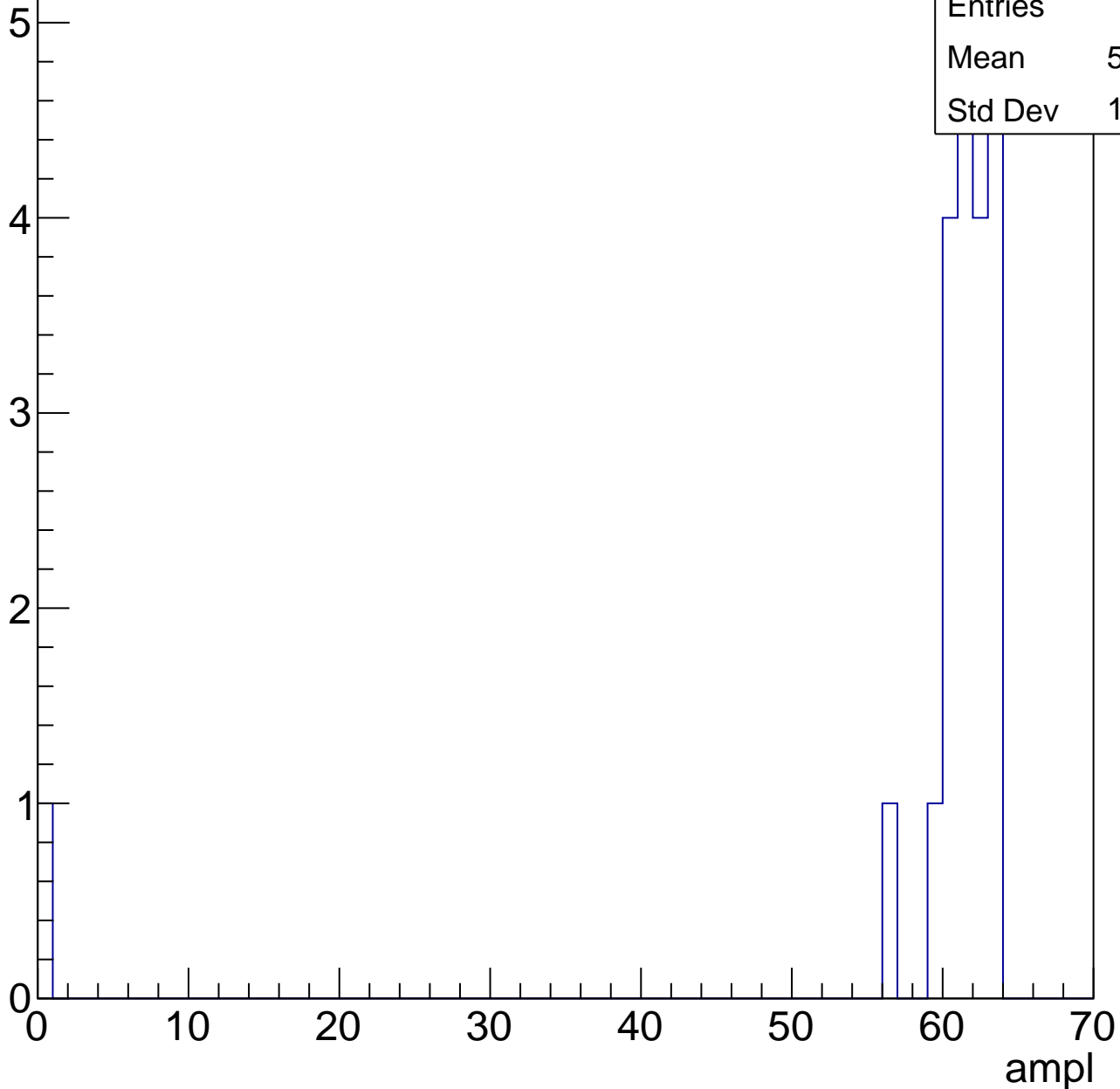
Entries	57
Mean	58.54
Std Dev	2.741

B1L100S, U1-ch42, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.24
Std Dev	13.13



B1L100S, U1-ch42, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch43, adc0

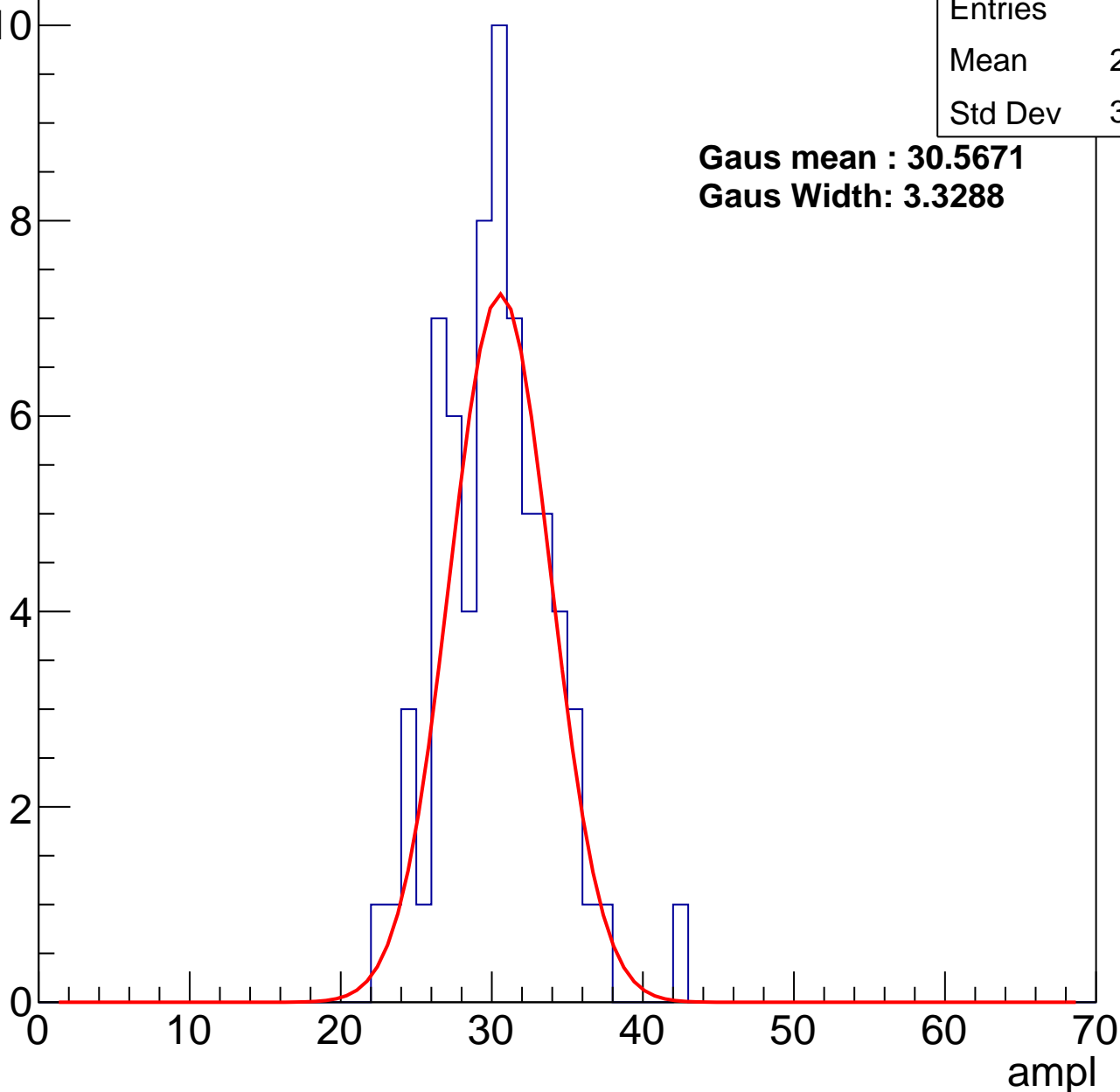
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	29.82
Std Dev	3.585

Gaus mean : 30.5671

Gaus Width: 3.3288



B1L100S, U1-ch43, adc1

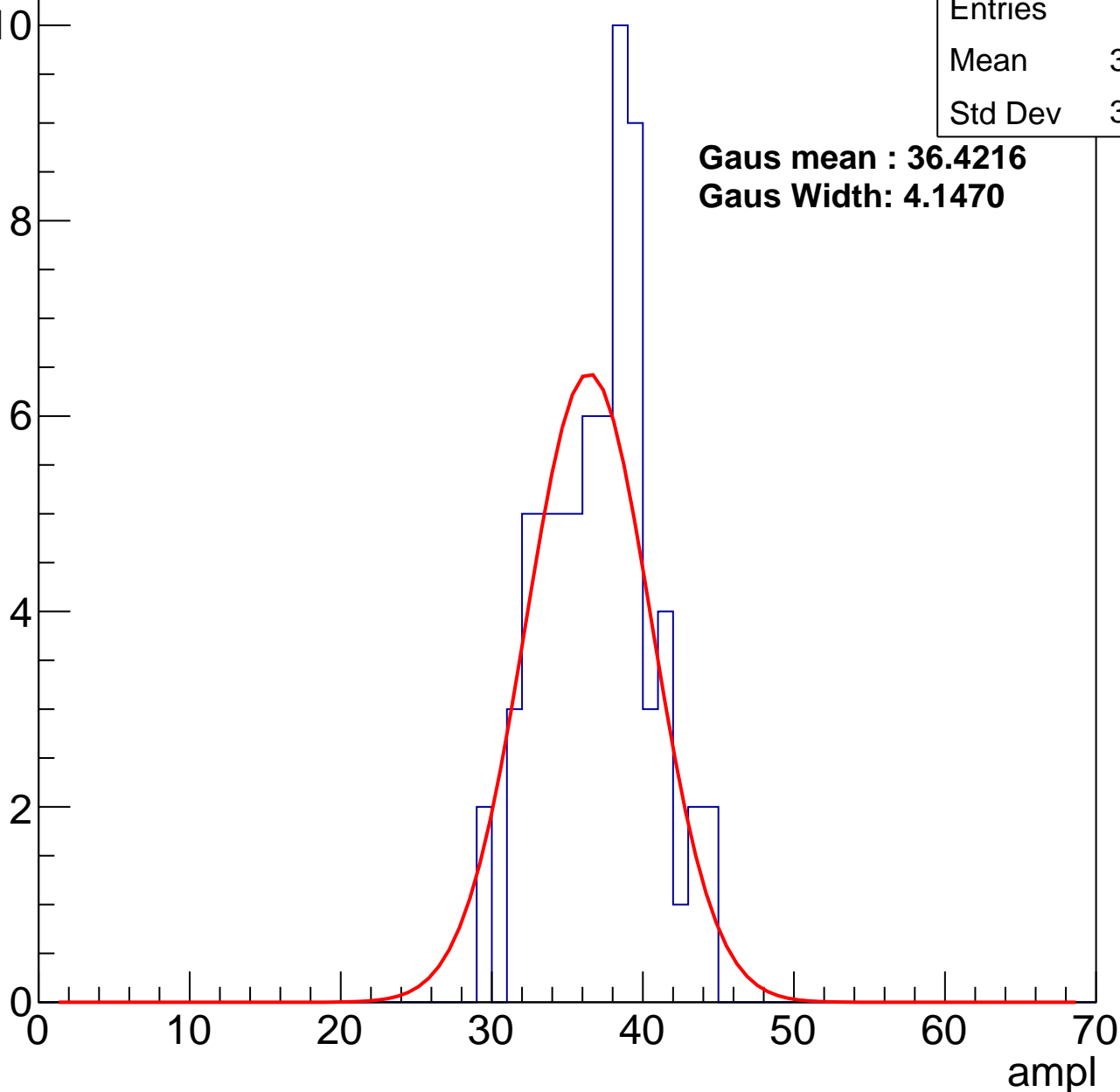
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	36.62
Std Dev	3.502

Gaus mean : 36.4216

Gaus Width: 4.1470



B1L100S, U1-ch43, adc2

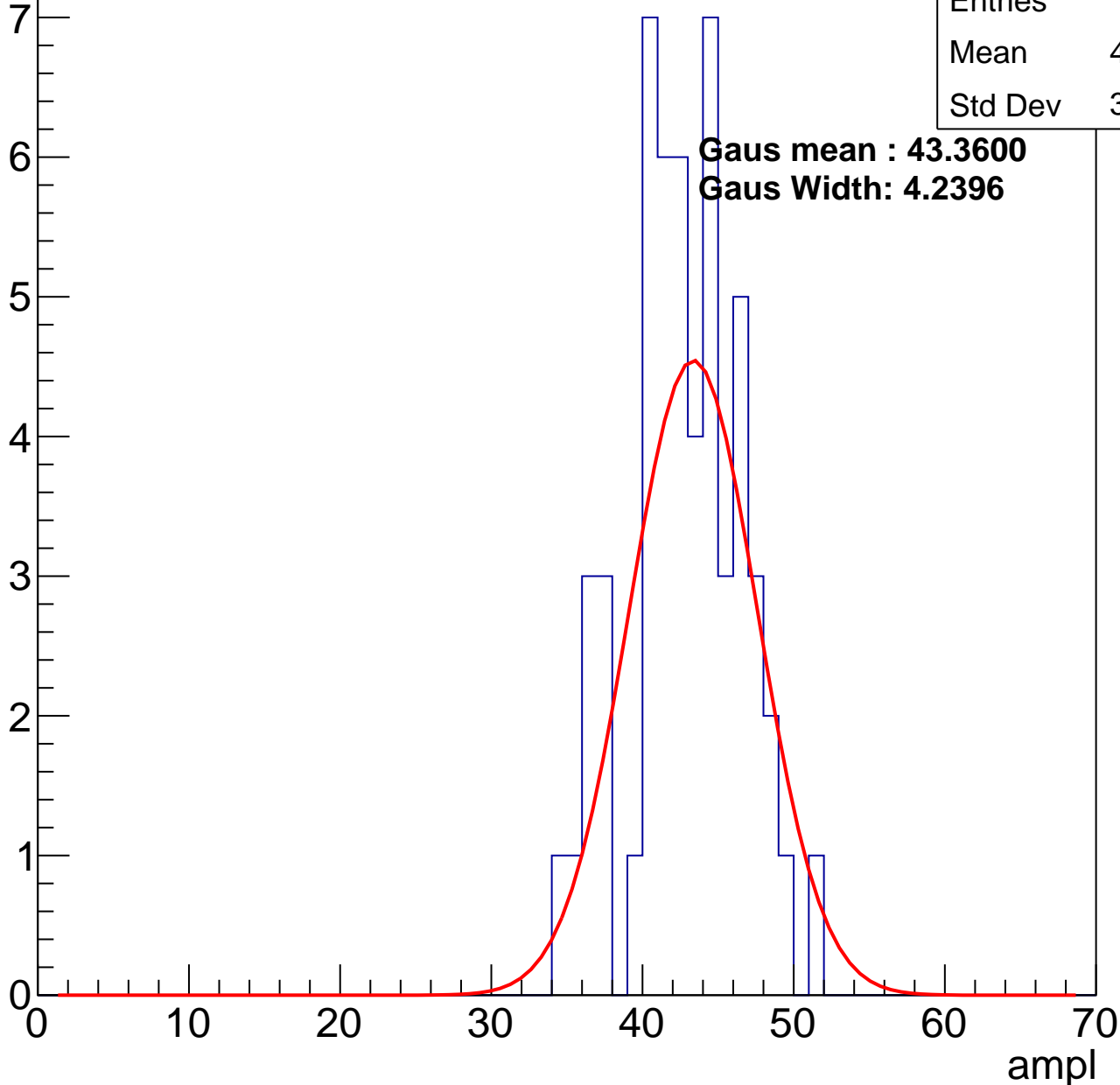
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	42.35
Std Dev	3.723

Gaus mean : 43.3600

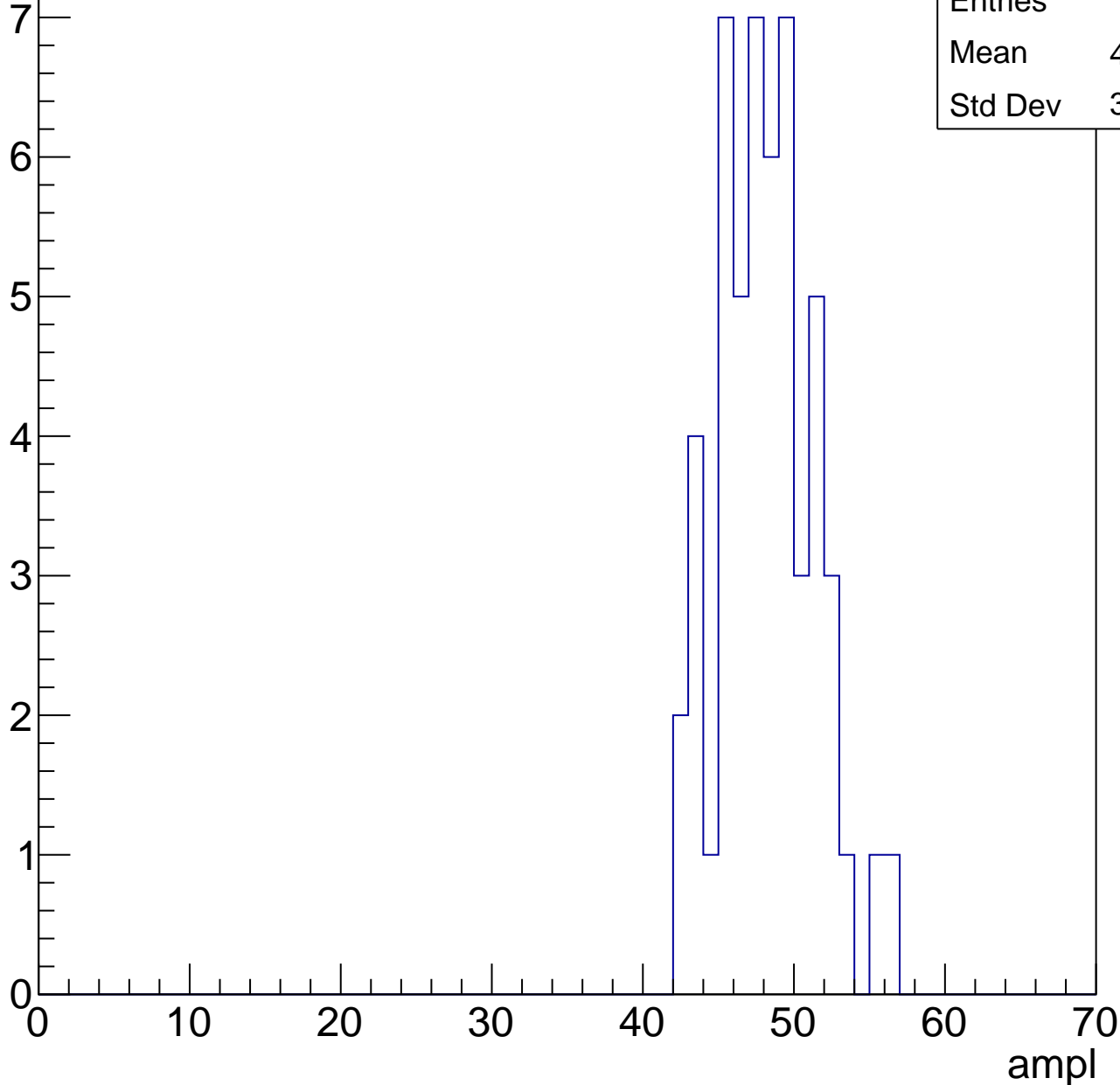
Gaus Width: 4.2396



B1L100S, U1-ch43, adc3

calib_packv5_042523_0143.root, FC#4, port A2

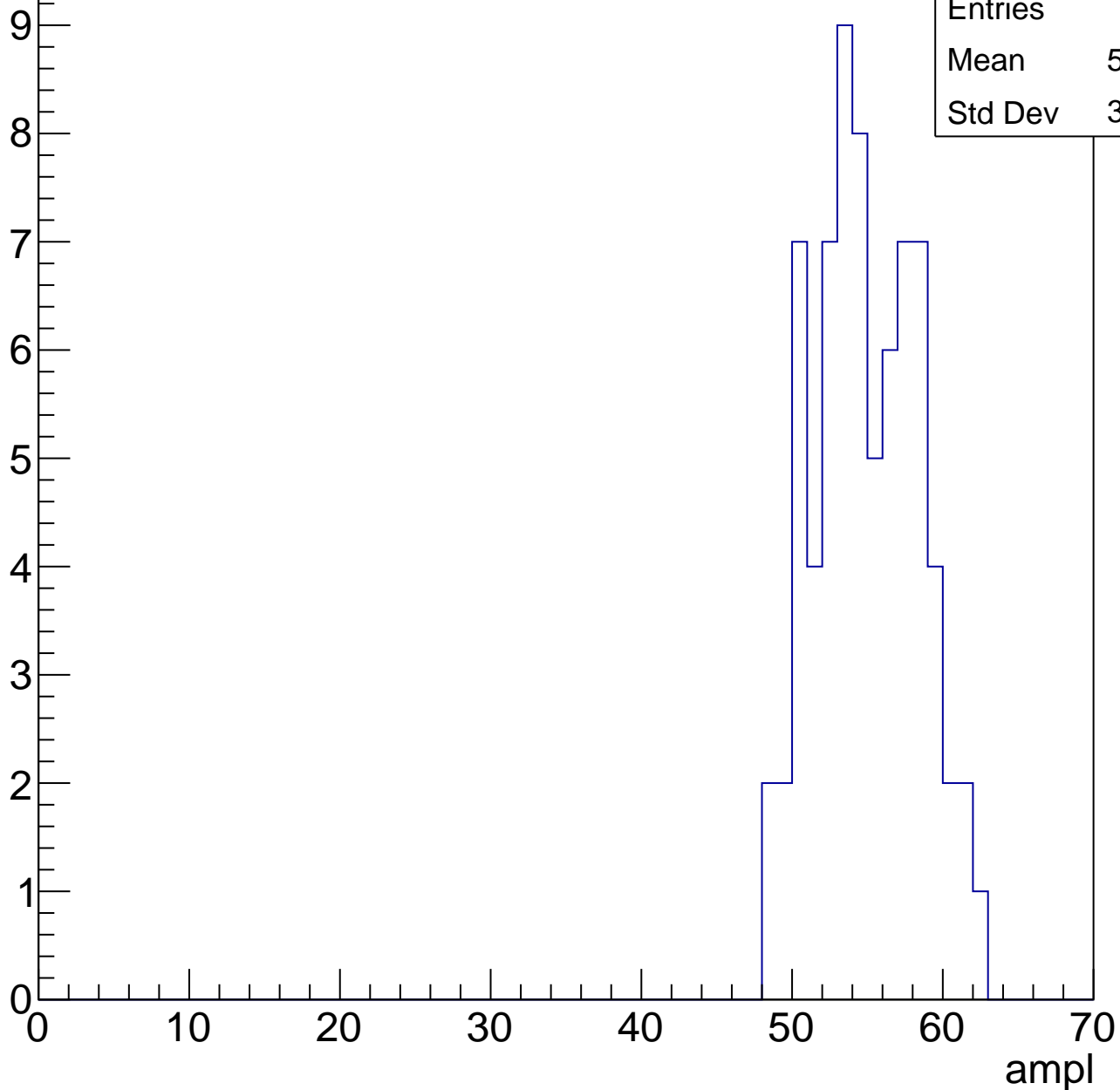
Entry



B1L100S, U1-ch43, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	73
Mean	54.48
Std Dev	3.368

B1L100S, U1-ch43, adc5

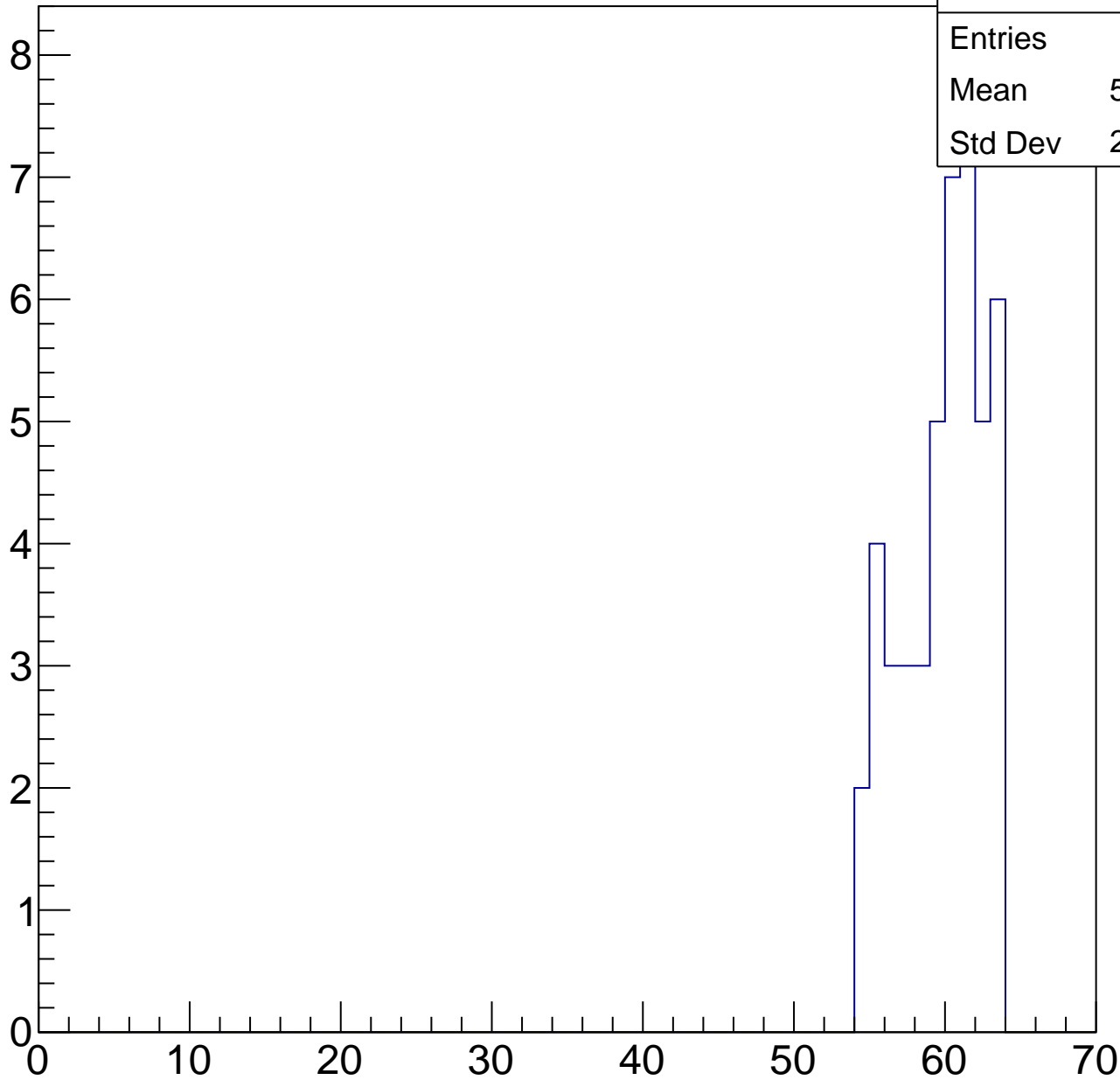
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	46
Mean	59.39
Std Dev	2.674

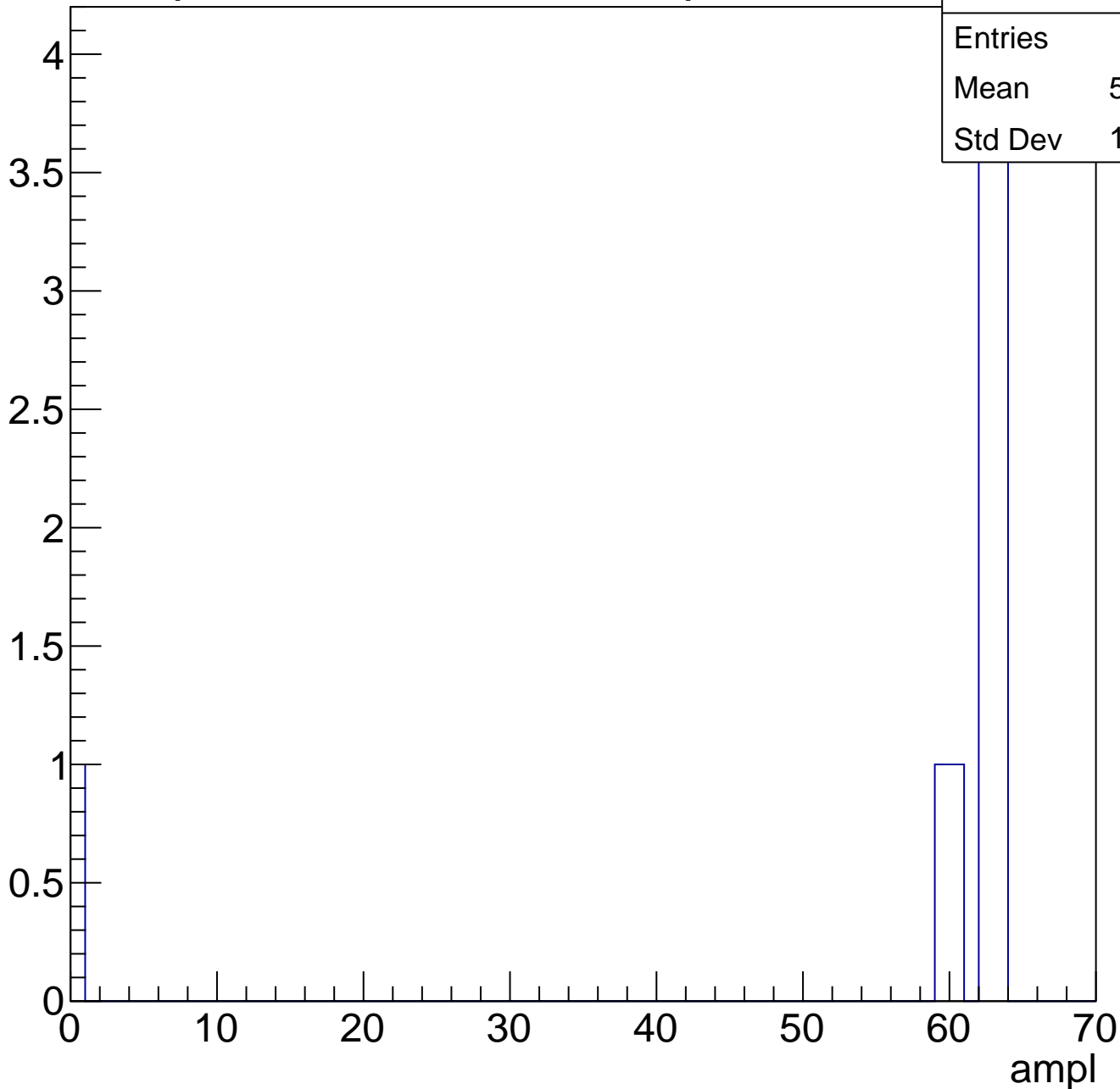
ampl



B1L100S, U1-ch43, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	11
Mean	56.27
Std Dev	17.84

B1L100S, U1-ch43, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch44, adc0

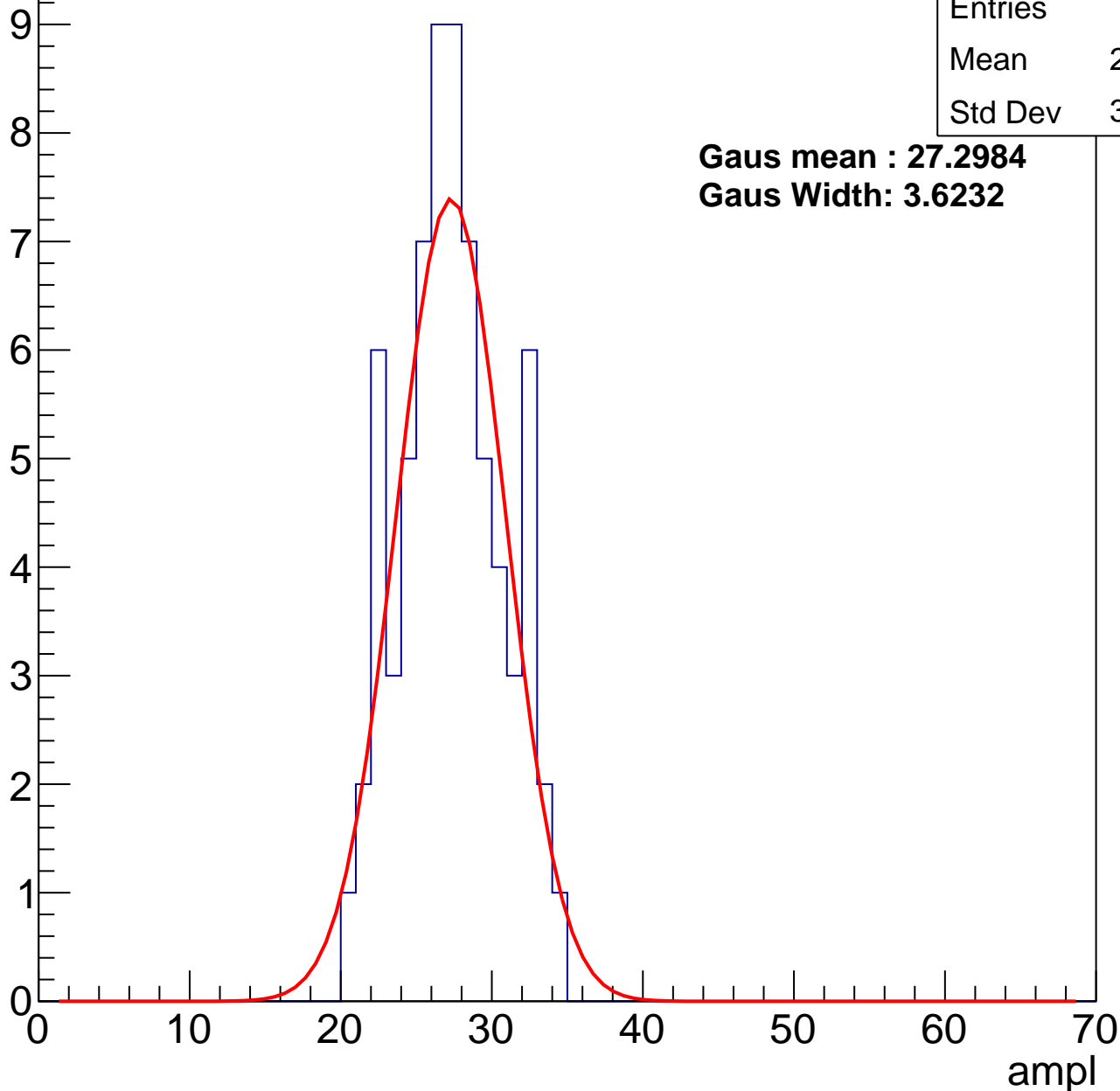
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	26.87
Std Dev	3.342

Gaus mean : 27.2984

Gaus Width: 3.6232



B1L100S, U1-ch44, adc1

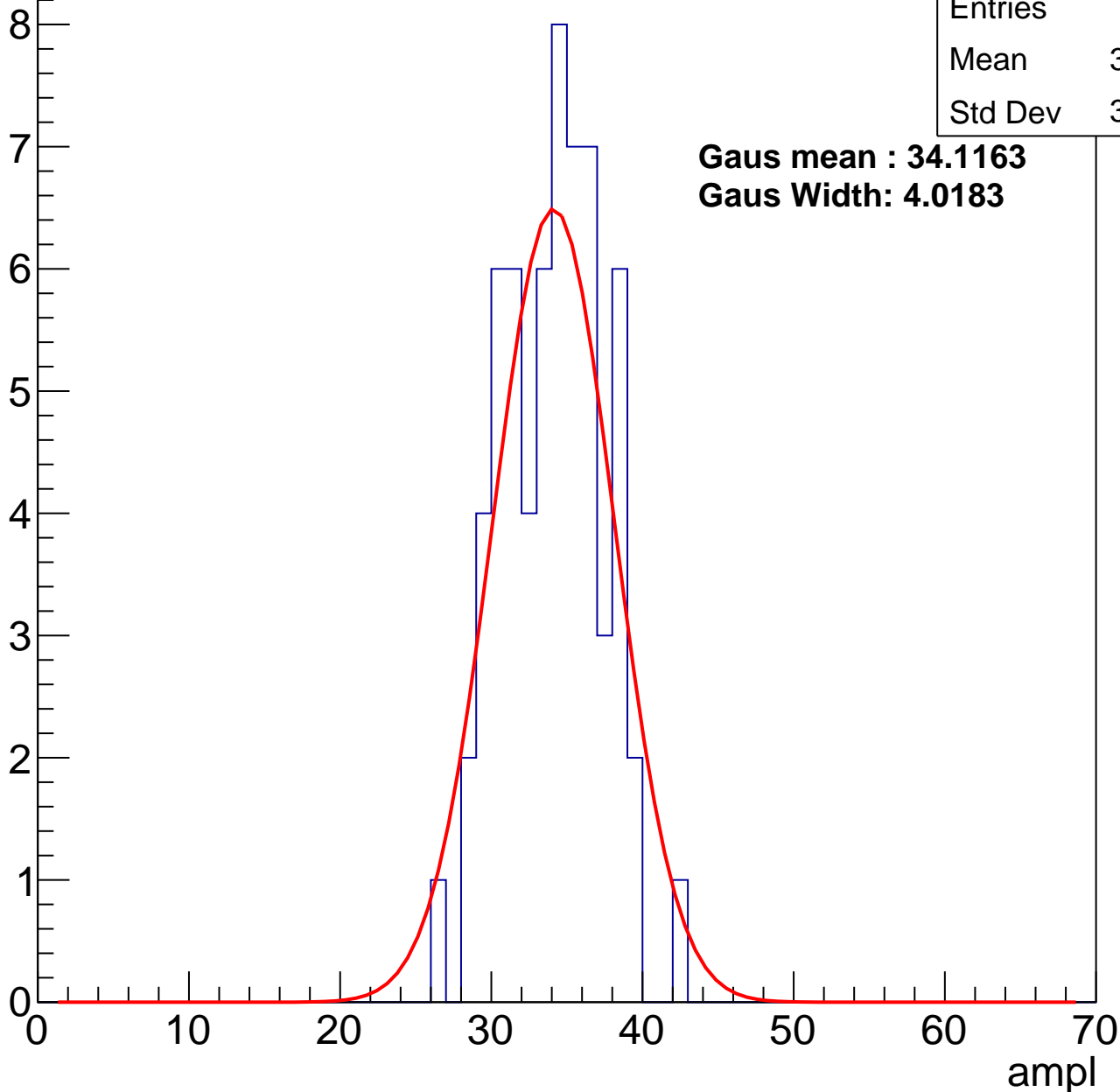
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	33.62
Std Dev	3.263

Gaus mean : 34.1163

Gaus Width: 4.0183



B1L100S, U1-ch44, adc2

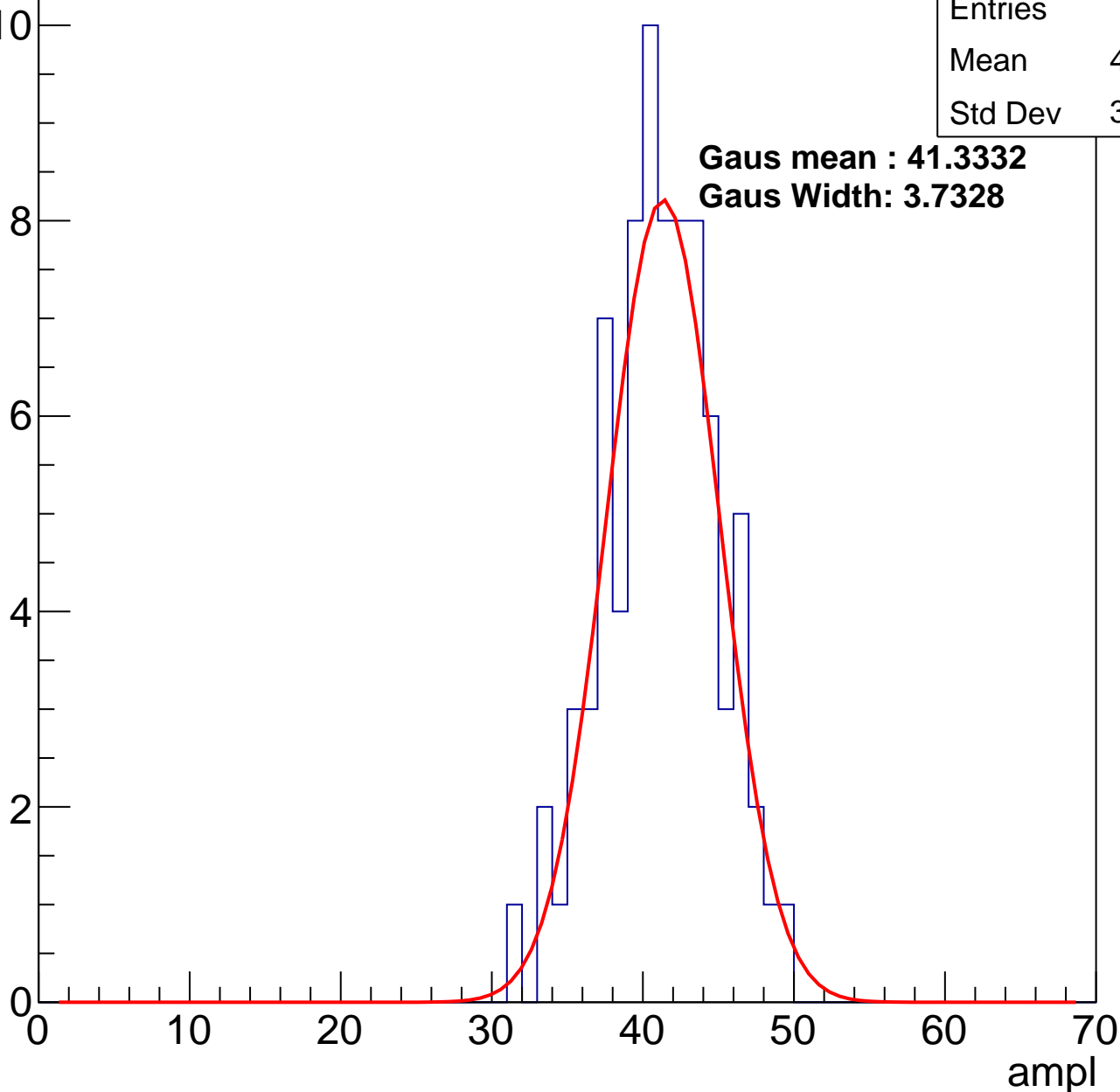
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	40.68
Std Dev	3.674

Gaus mean : 41.3332

Gaus Width: 3.7328

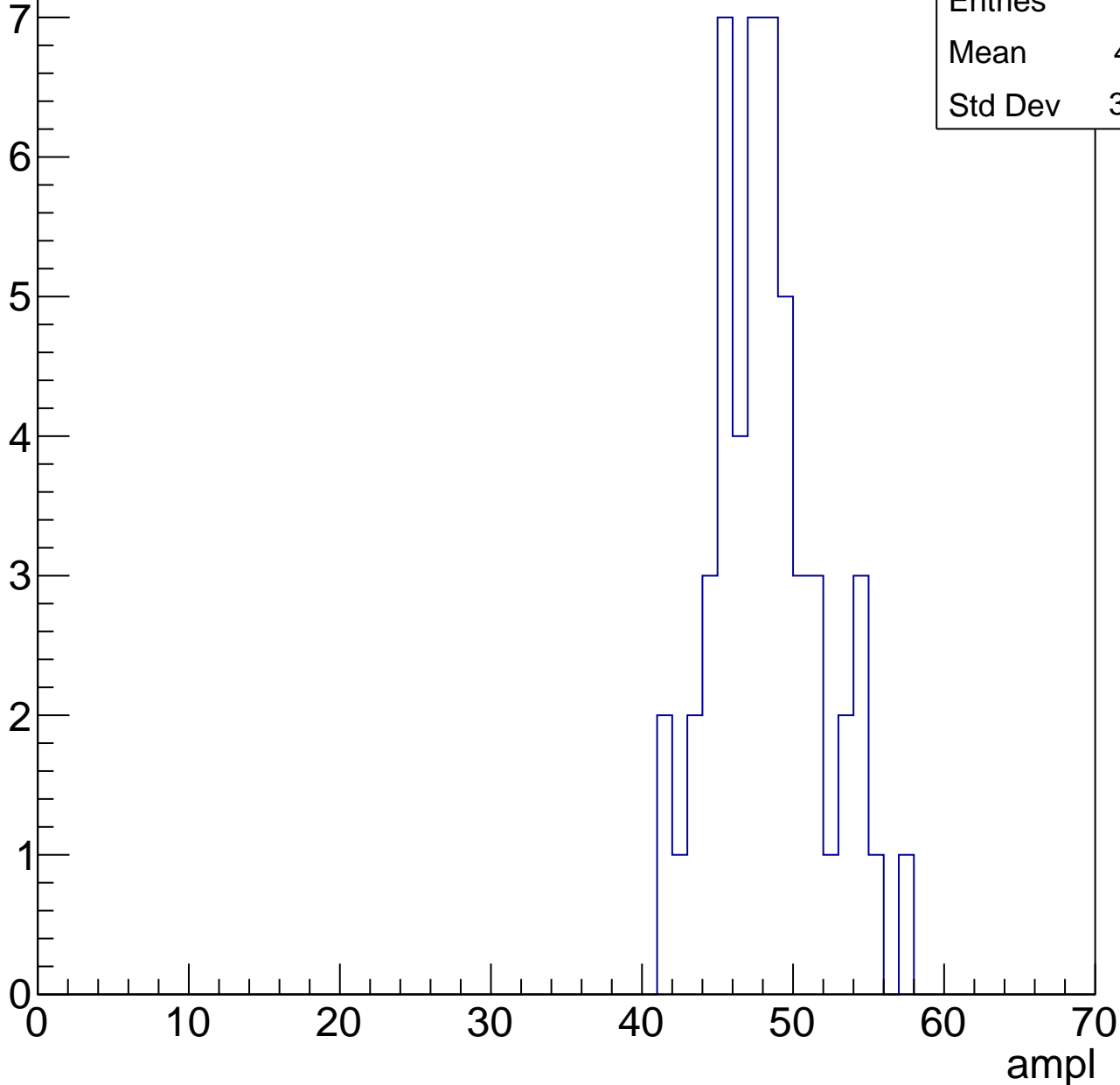


B1L100S, U1-ch44, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	47.81
Std Dev	3.574

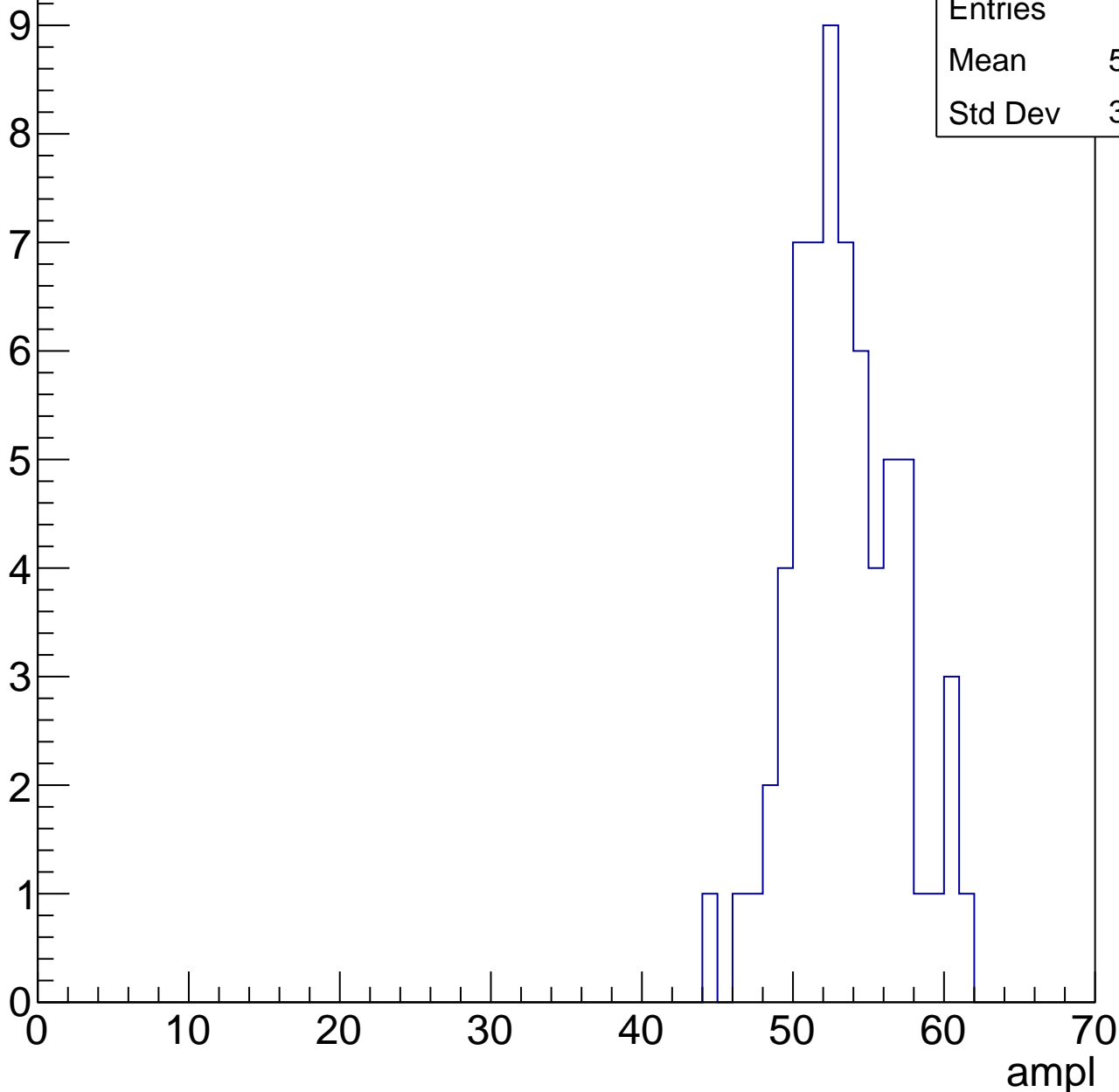


B1L100S, U1-ch44, adc4

calib_packv5_042523_0143.root, FC#4, port A2

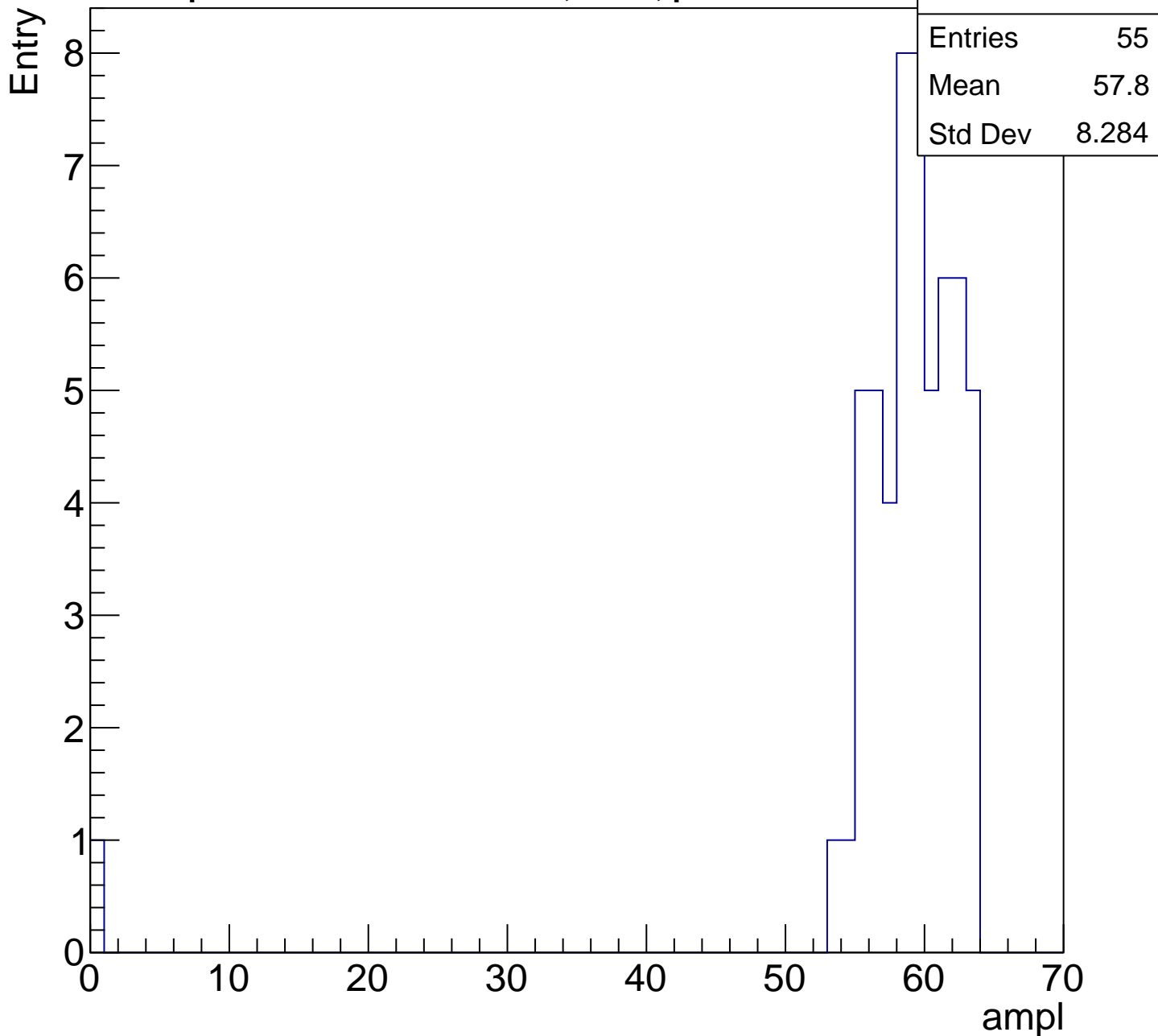
Entry

Entries	65
Mean	52.95
Std Dev	3.506



B1L100S, U1-ch44, adc5

calib_packv5_042523_0143.root, FC#4, port A2

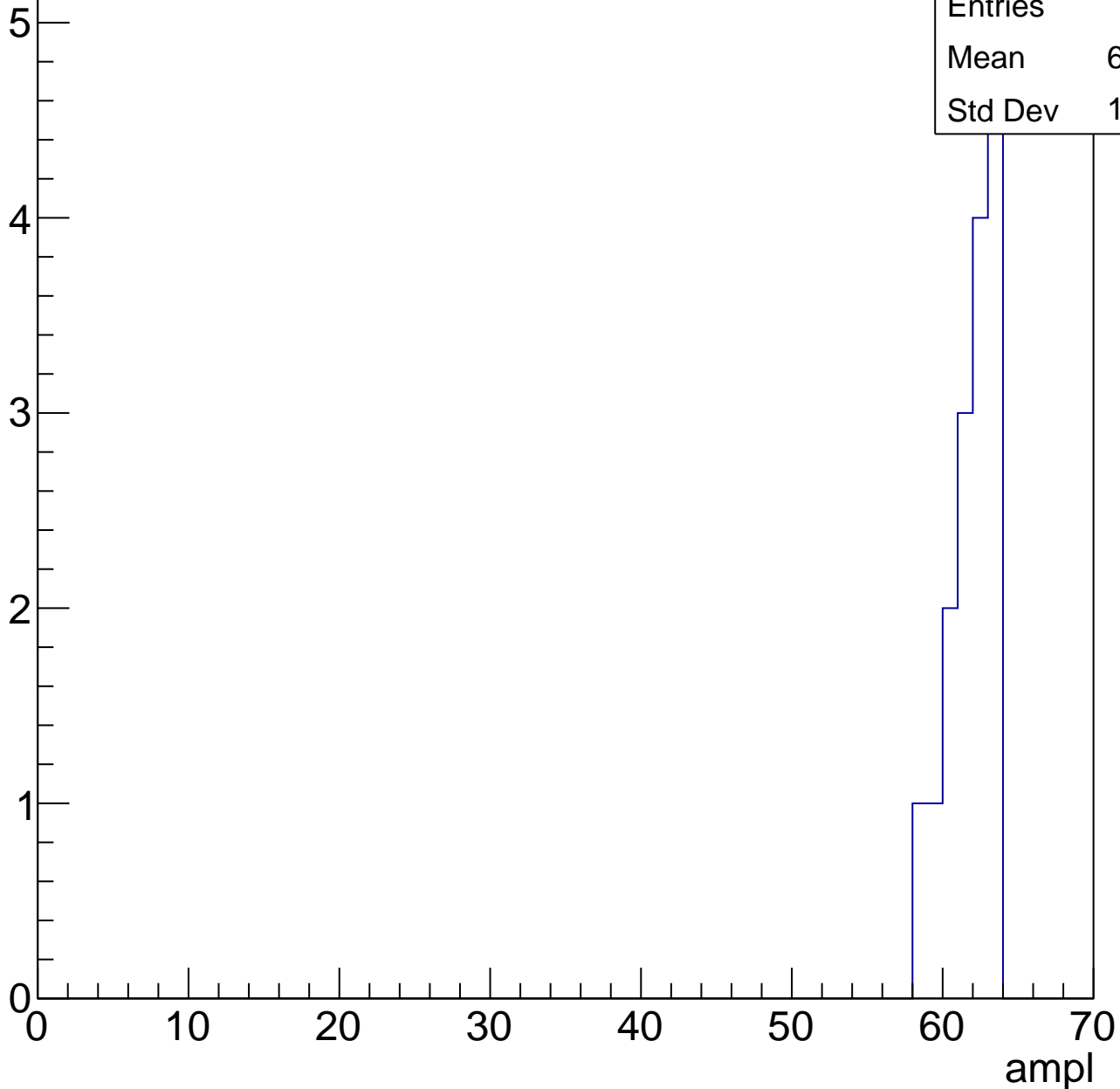


B1L100S, U1-ch44, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	61.44
Std Dev	1.499



B1L100S, U1-ch44, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch45, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	96
Mean	28.32
Std Dev	4.902

Gaus mean : 29.2406

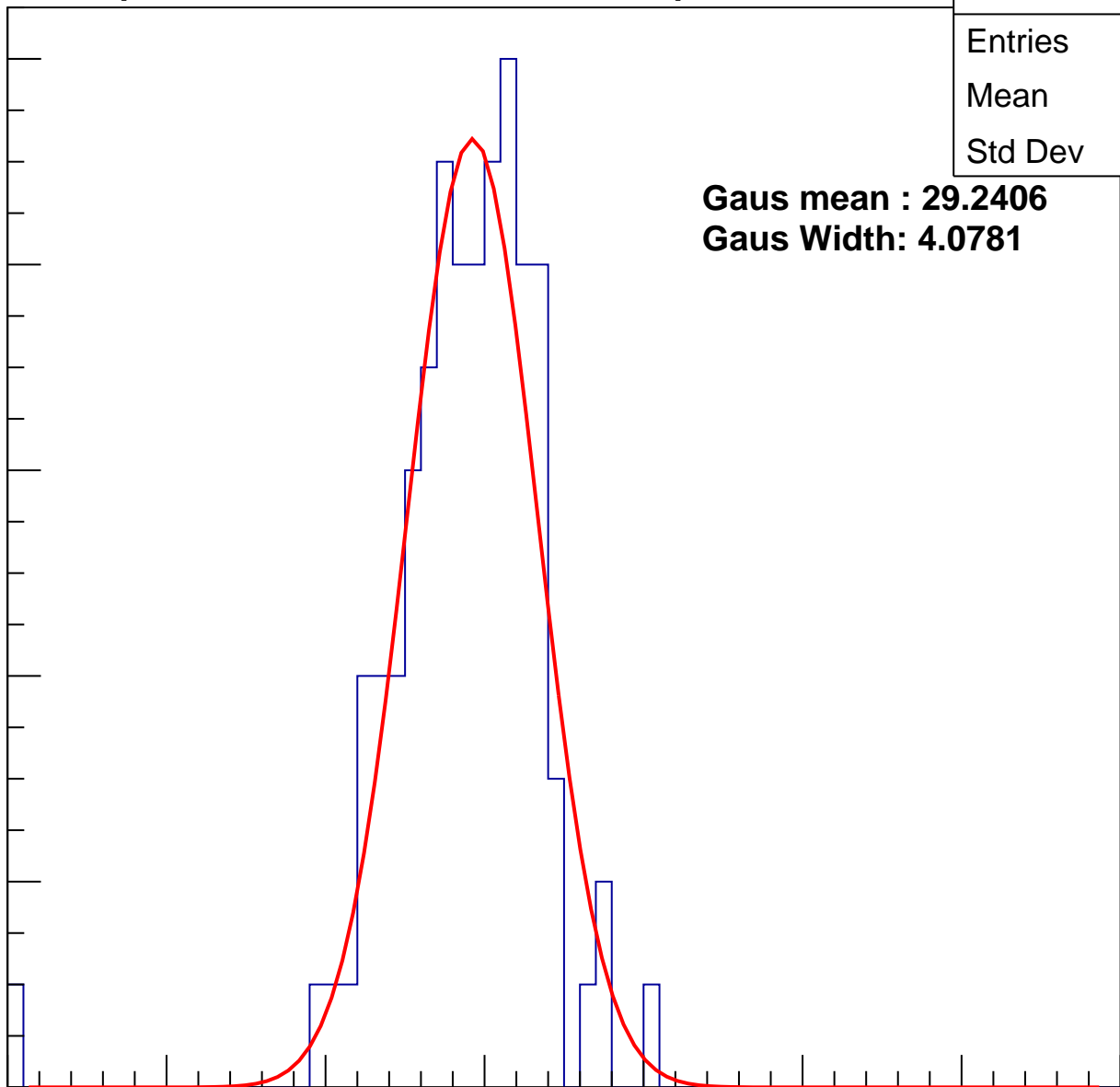
Gaus Width: 4.0781

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch45, adc1

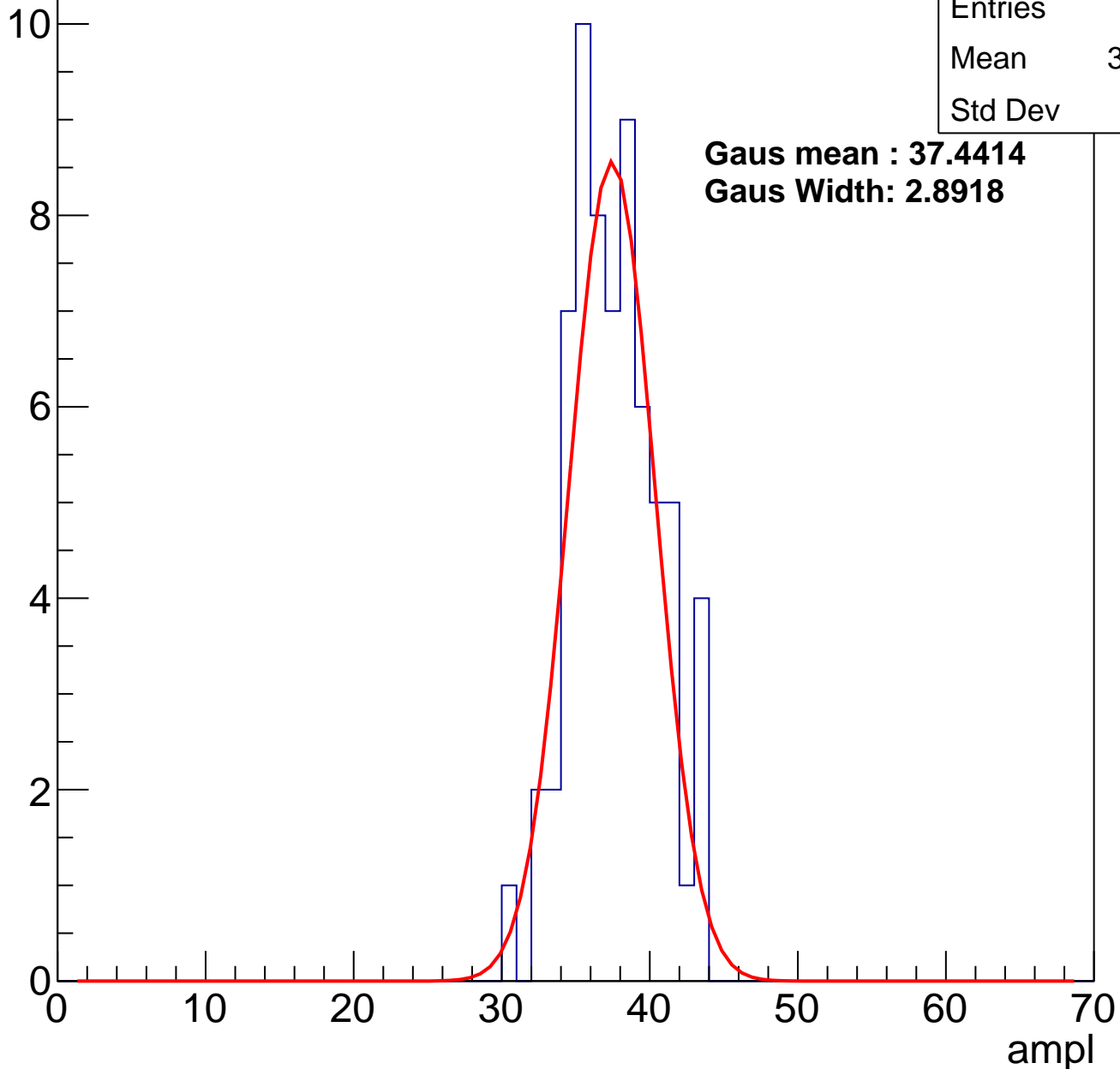
calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	37.16
Std Dev	2.91

Gaus mean : 37.4414

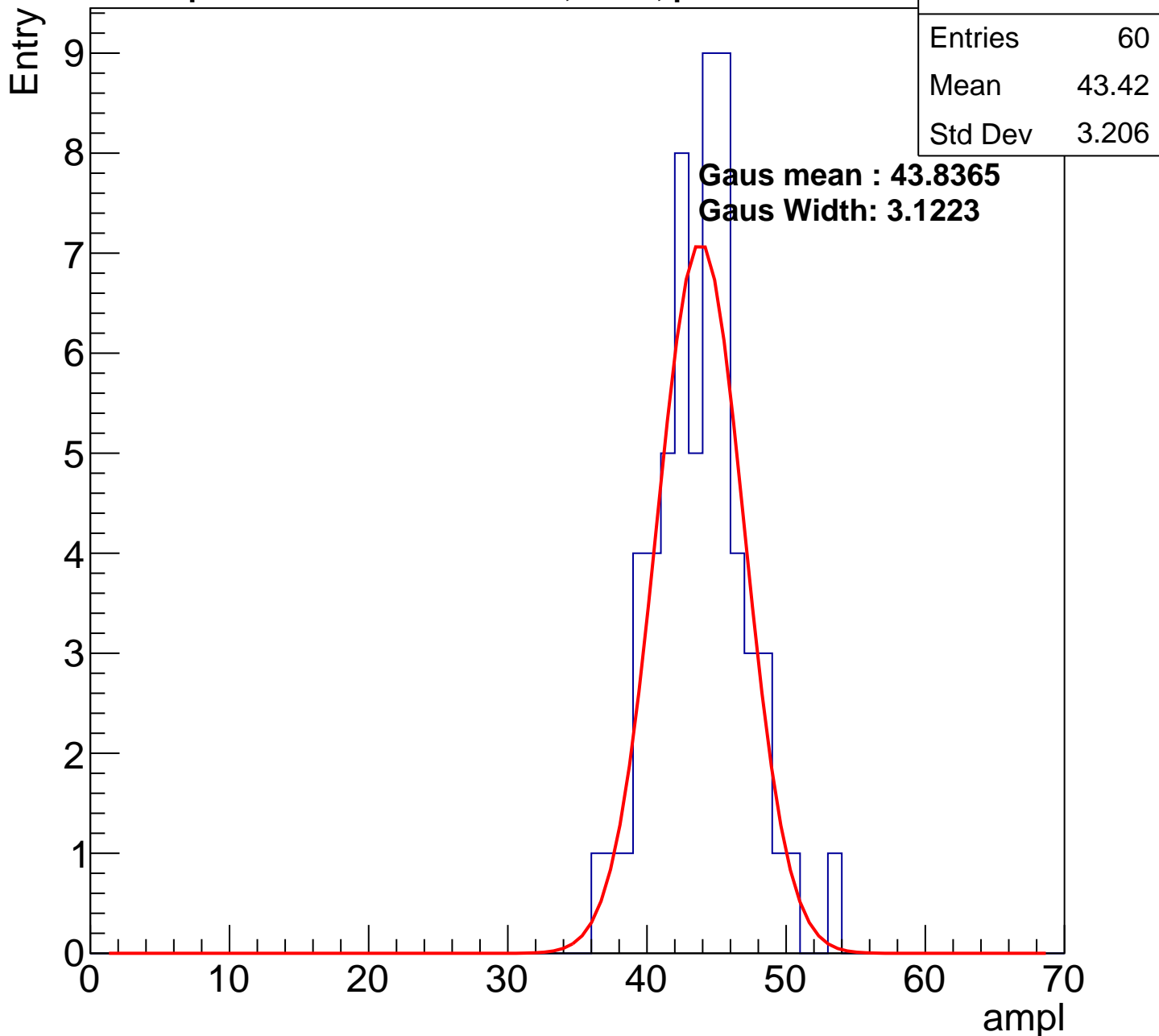
Gaus Width: 2.8918

Entry



B1L100S, U1-ch45, adc2

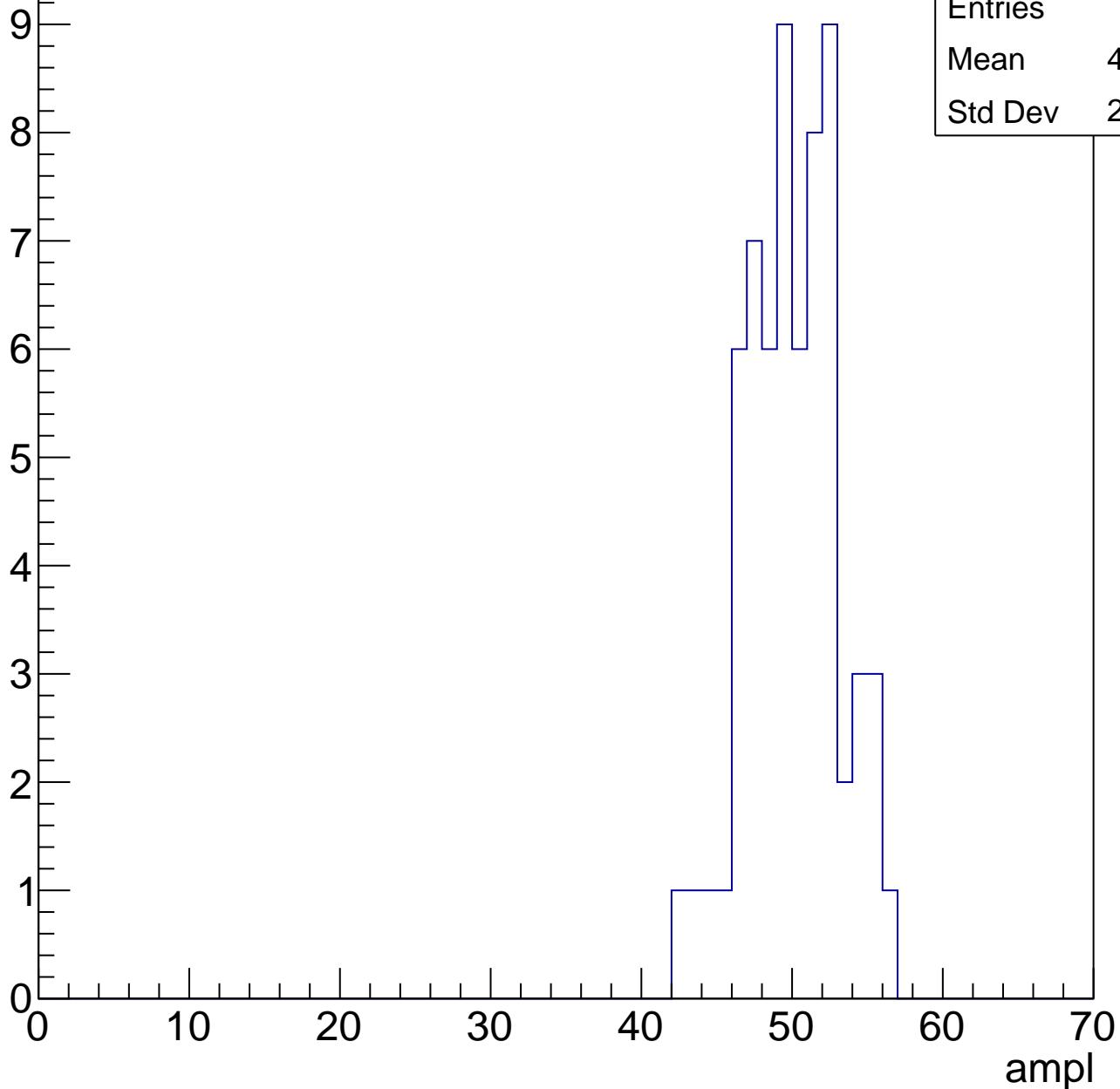
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch45, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

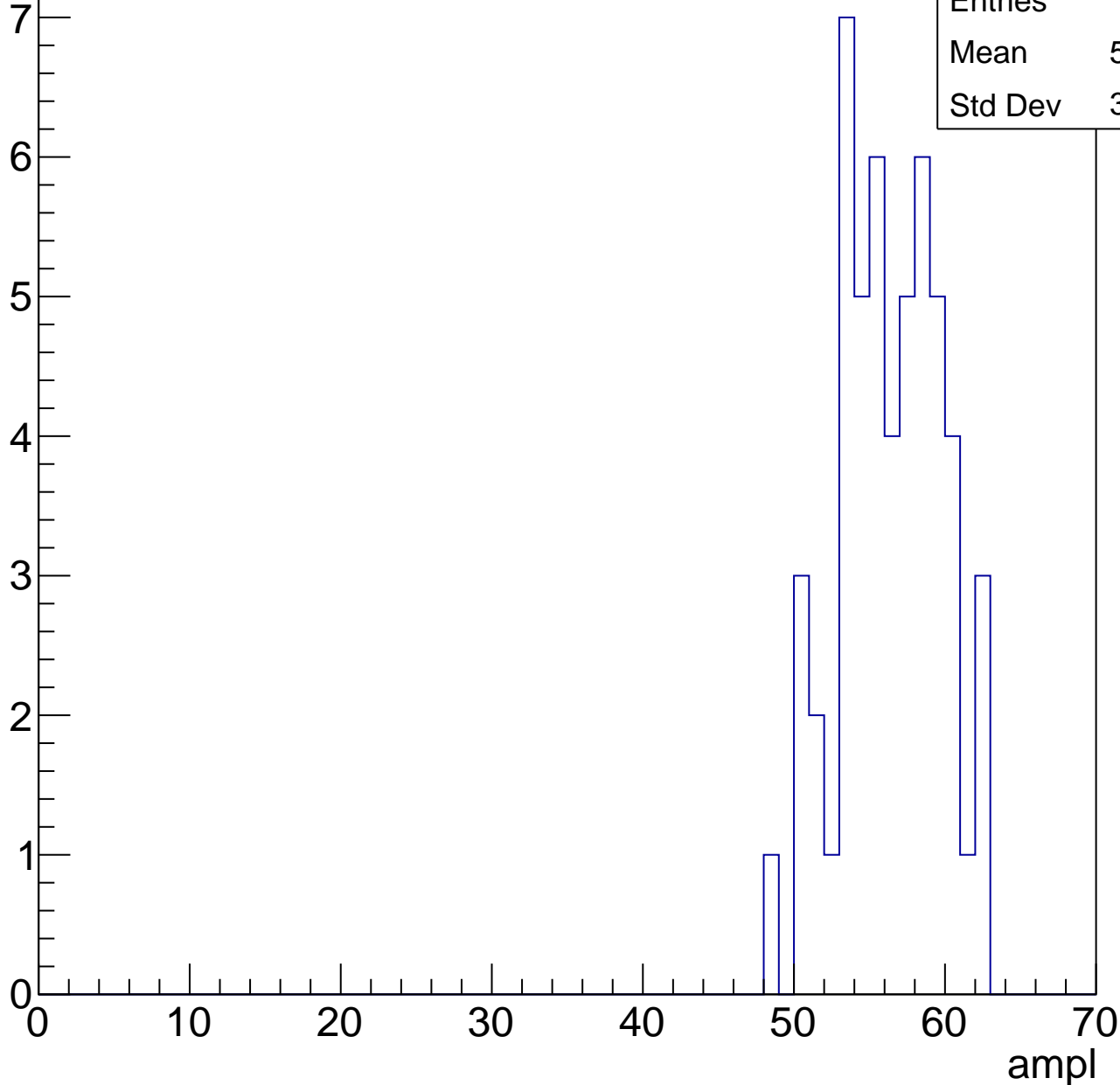


B1L100S, U1-ch45, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	55.89
Std Dev	3.357

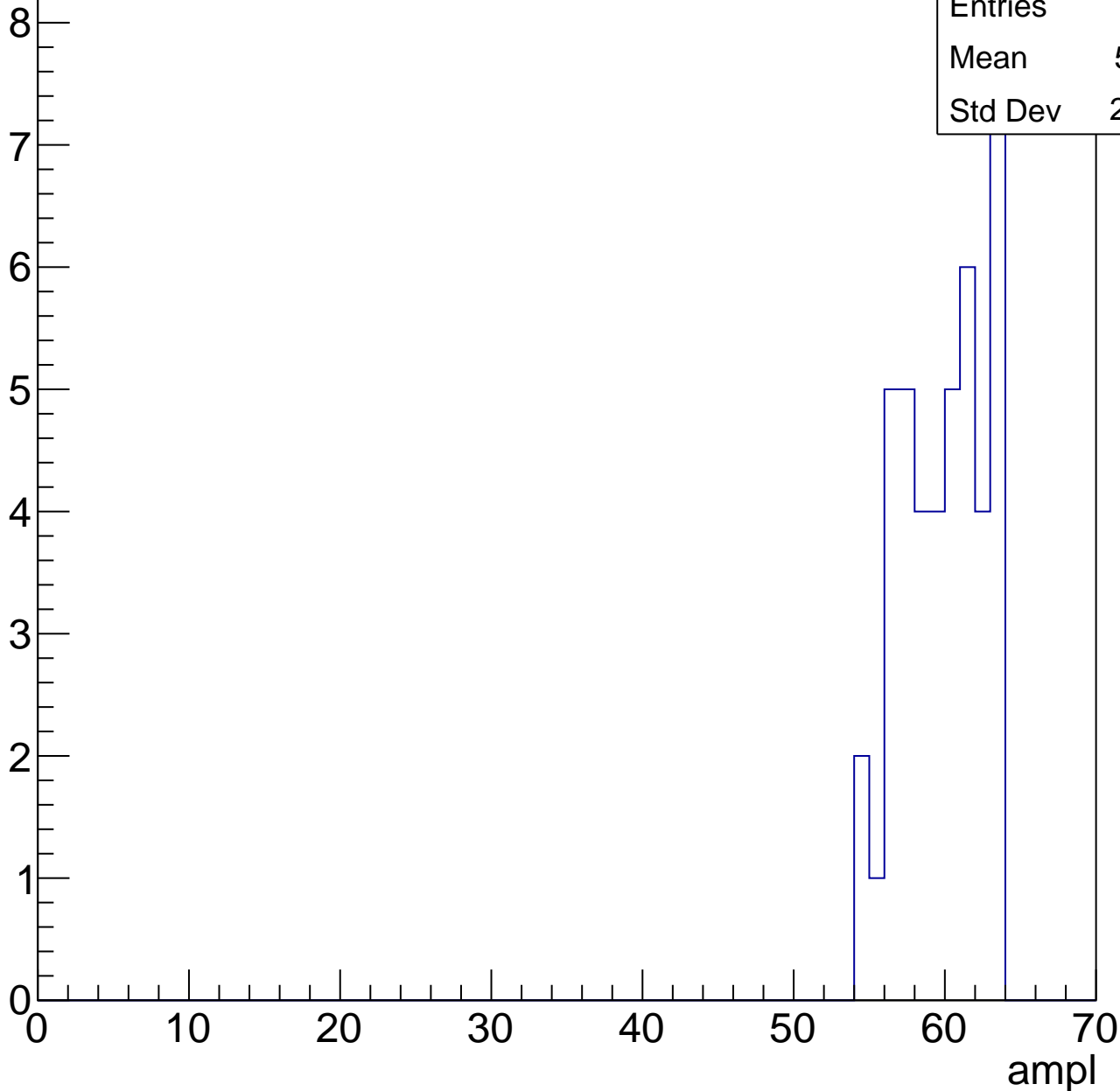


B1L100S, U1-ch45, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

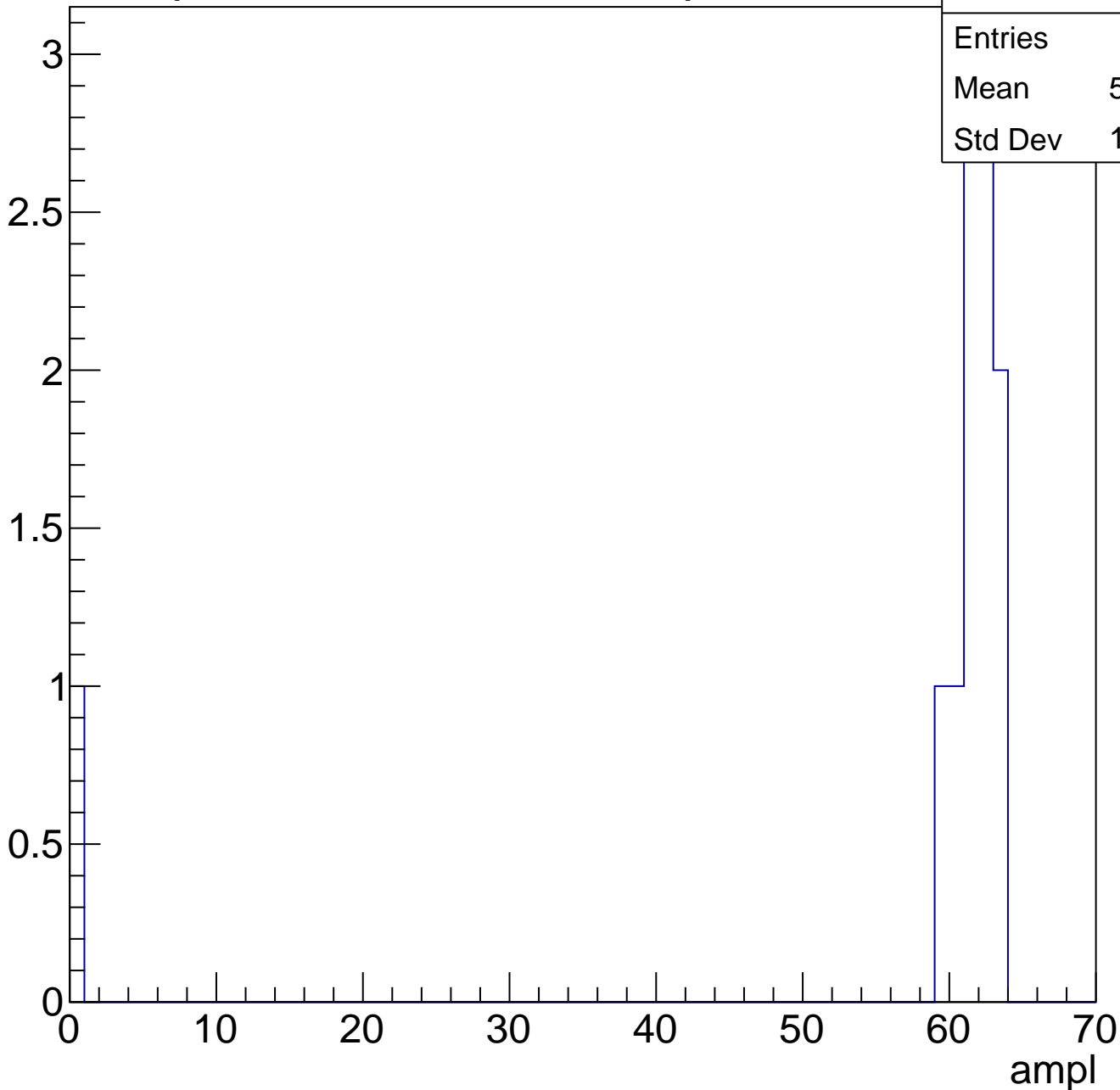
Entries	44
Mean	59.41
Std Dev	2.699



B1L100S, U1-ch45, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch45, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch46, adc0

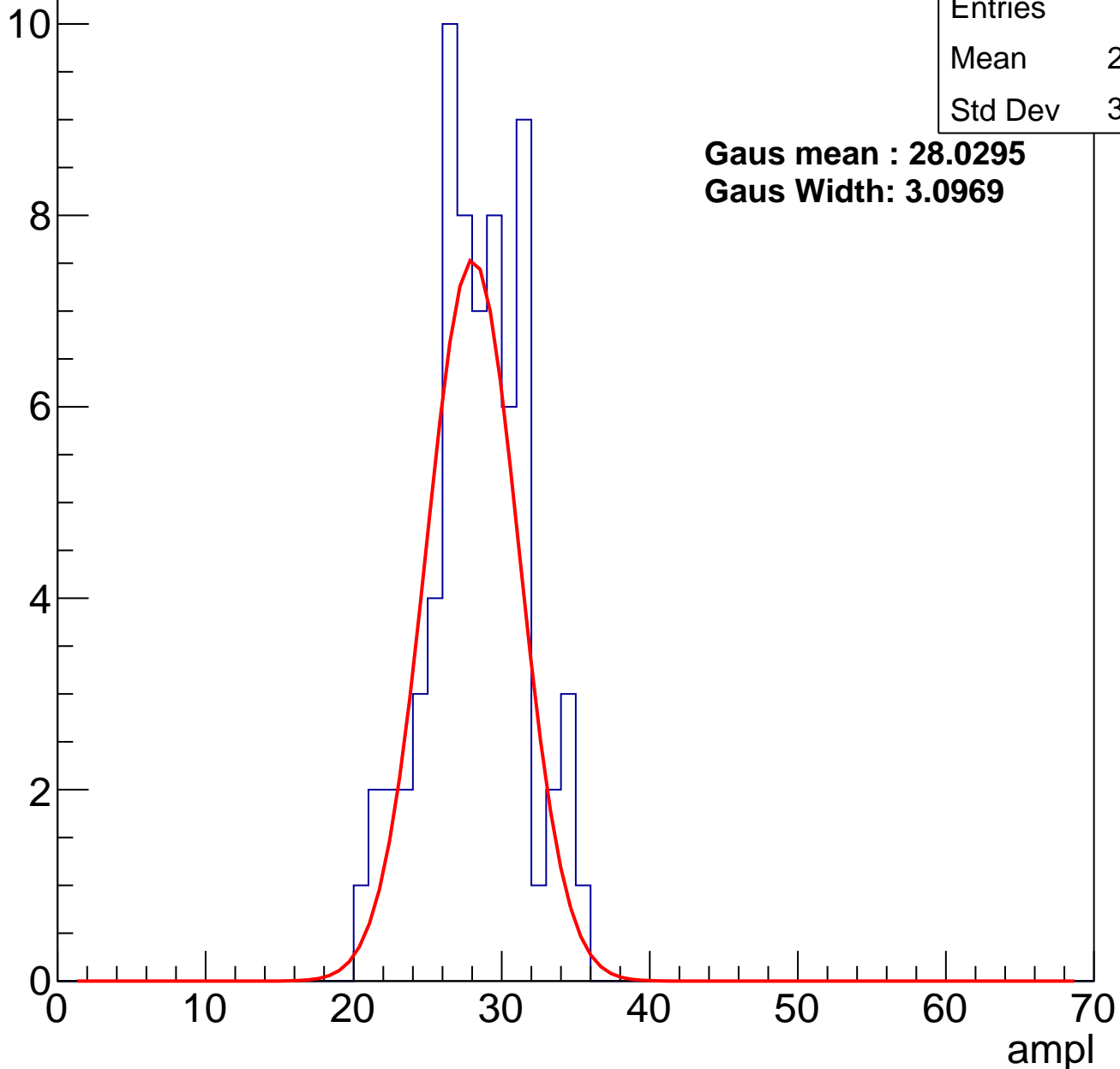
calib_packv5_042523_0143.root, FC#4, port A2

Entries	69
Mean	27.86
Std Dev	3.298

Gaus mean : 28.0295

Gaus Width: 3.0969

Entry



B1L100S, U1-ch46, adc1

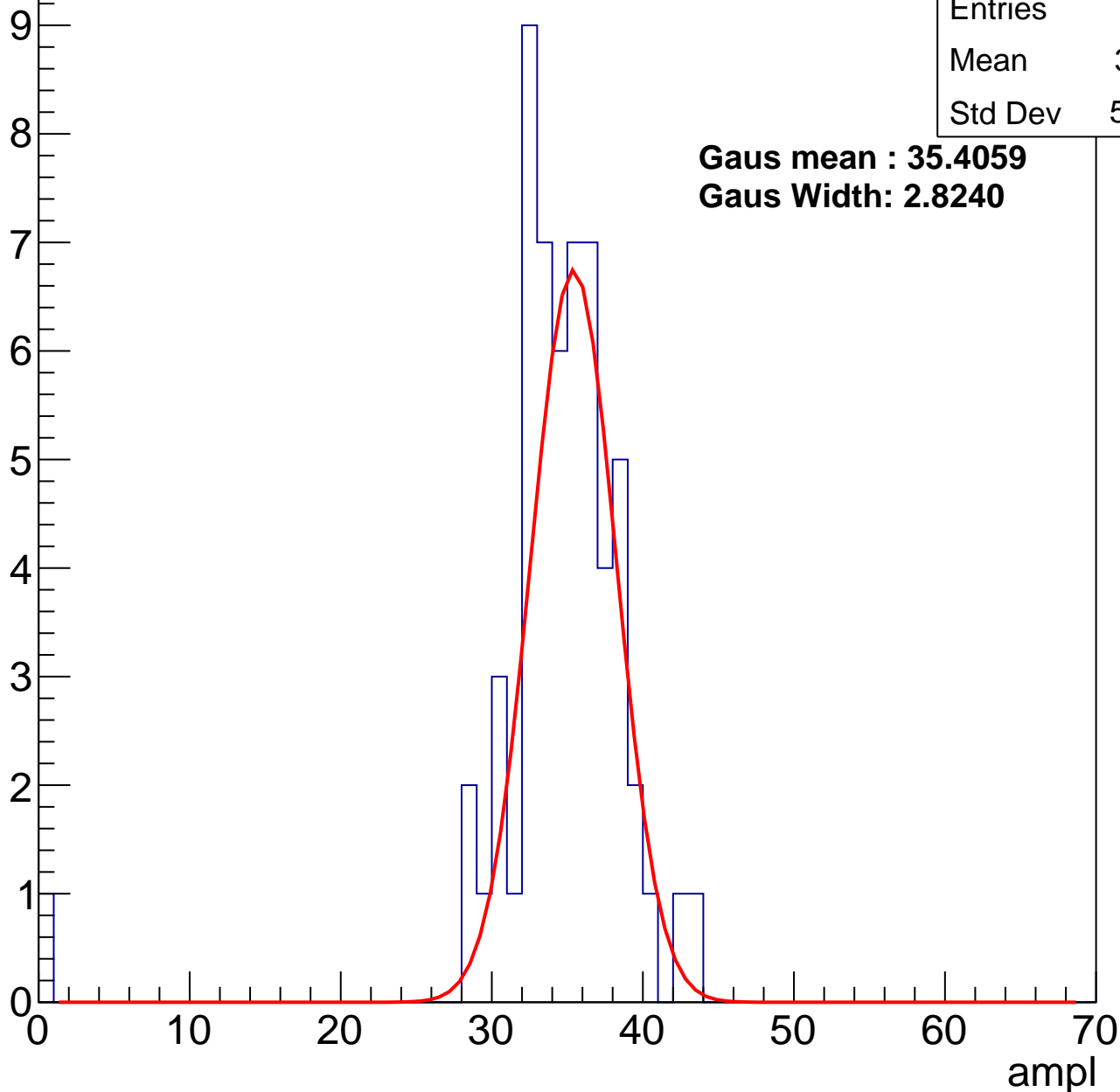
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	33.91
Std Dev	5.469

Gaus mean : 35.4059

Gaus Width: 2.8240



B1L100S, U1-ch46, adc2

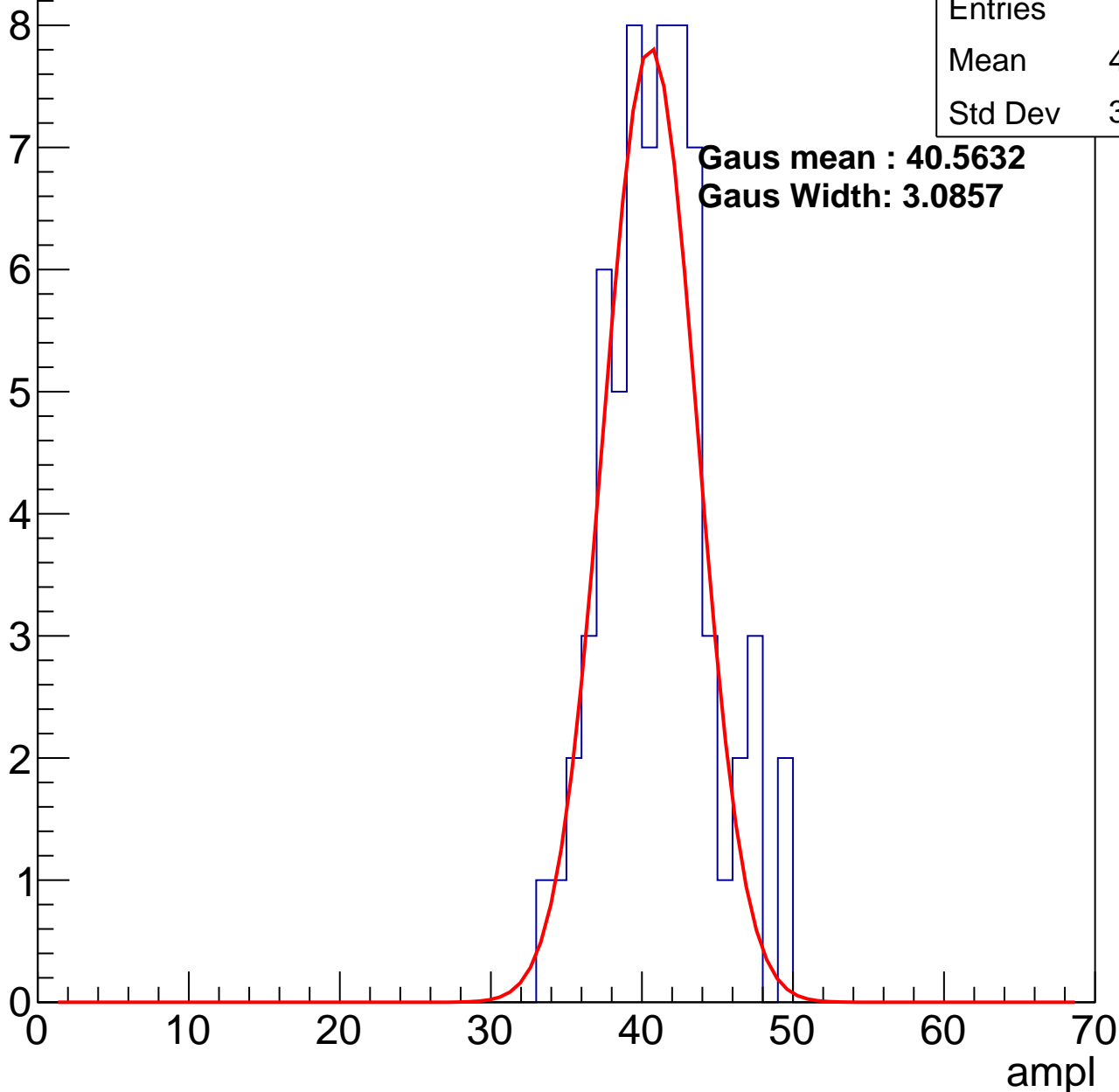
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	40.63
Std Dev	3.446

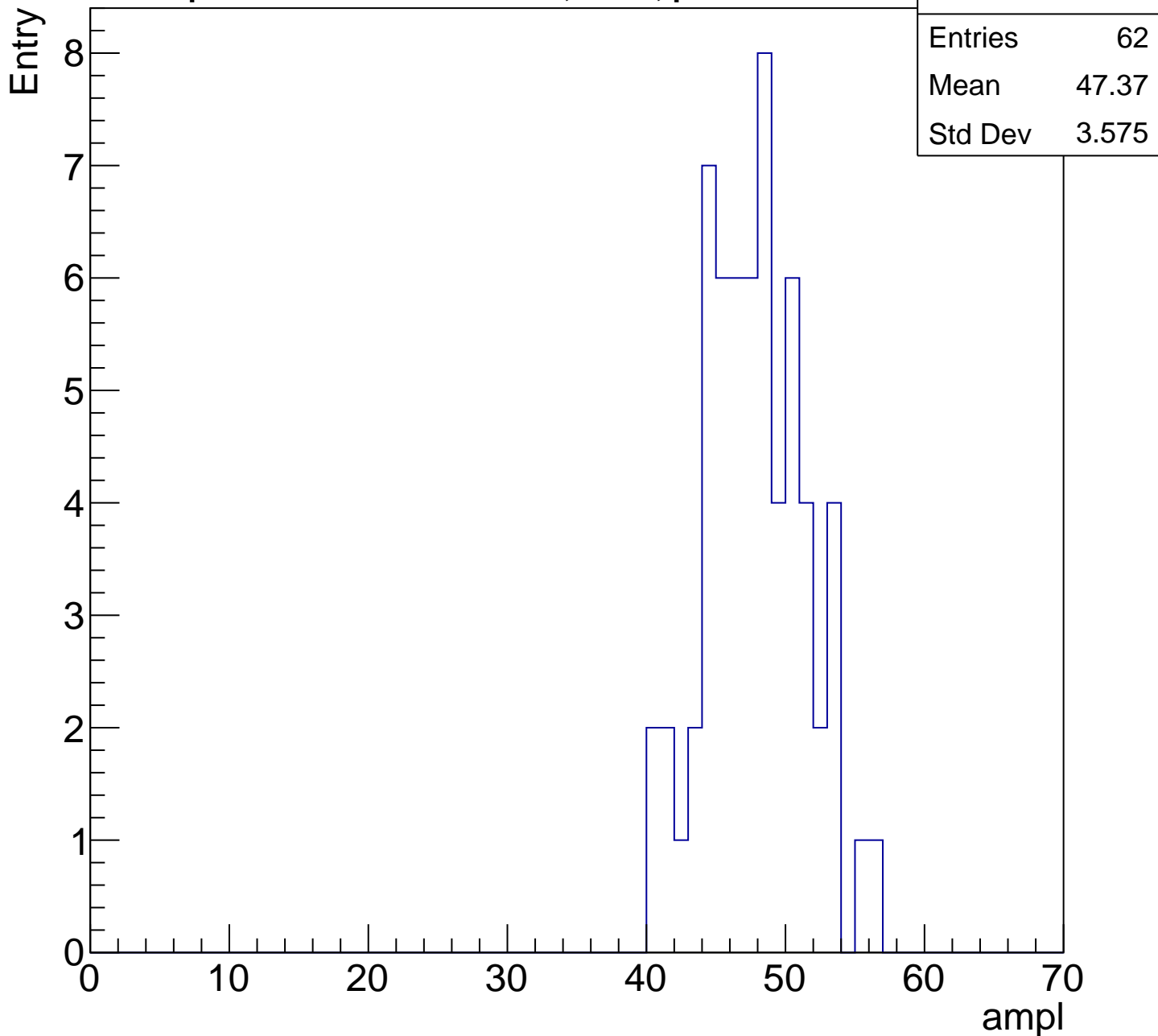
Gaus mean : 40.5632

Gaus Width: 3.0857



B1L100S, U1-ch46, adc3

calib_packv5_042523_0143.root, FC#4, port A2

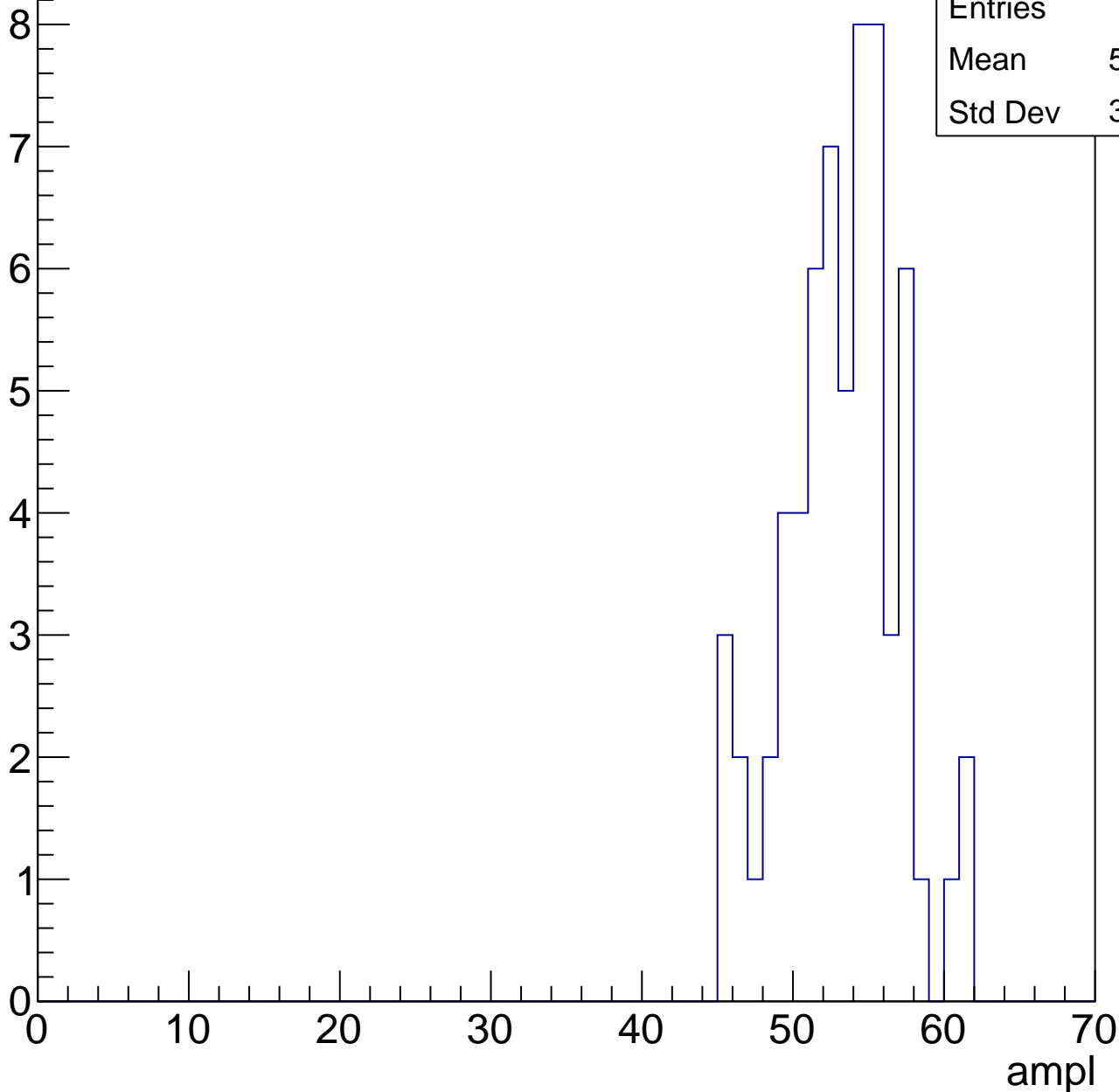


B1L100S, U1-ch46, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	52.75
Std Dev	3.712

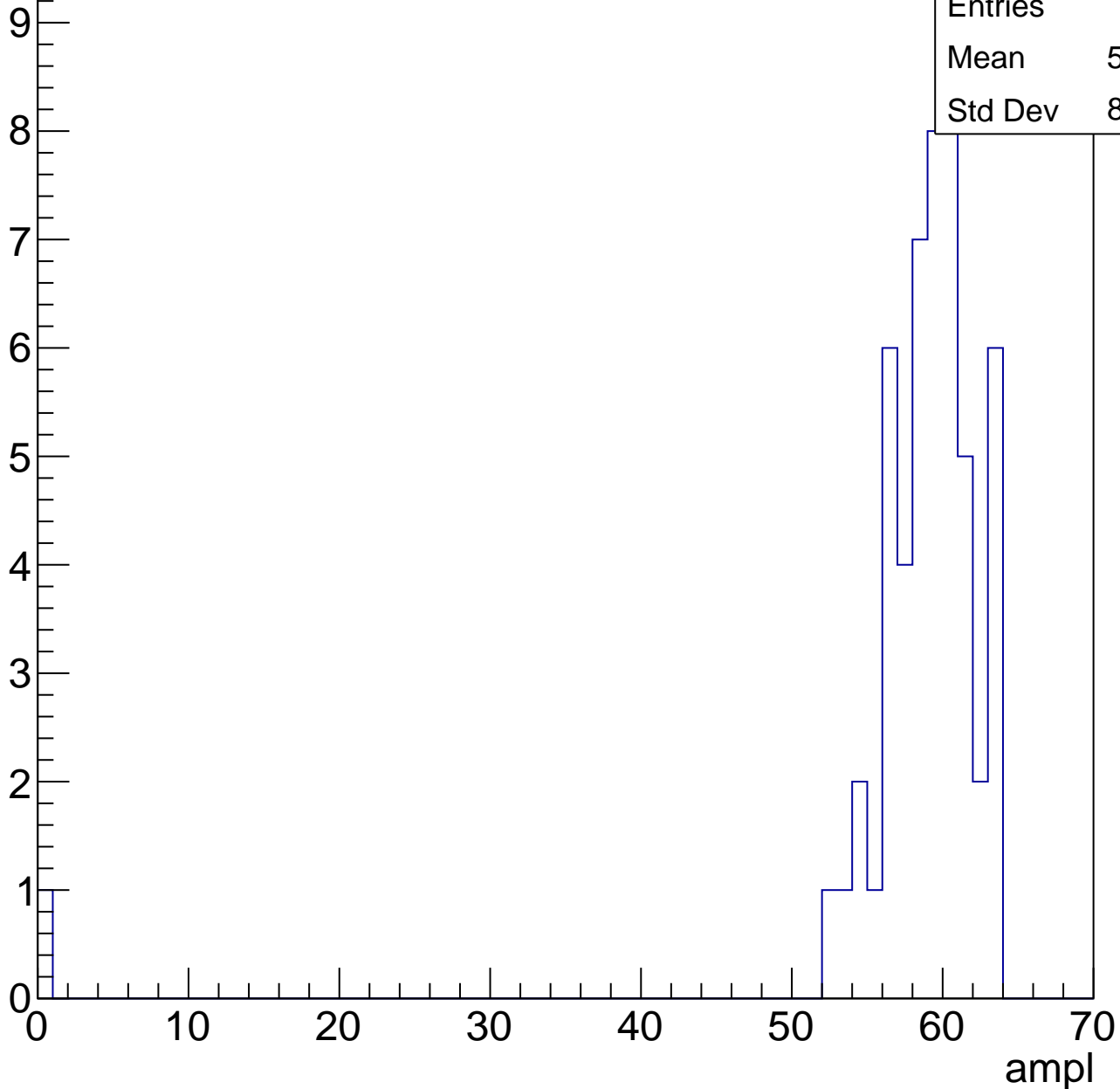


B1L100S, U1-ch46, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	57.68
Std Dev	8.422

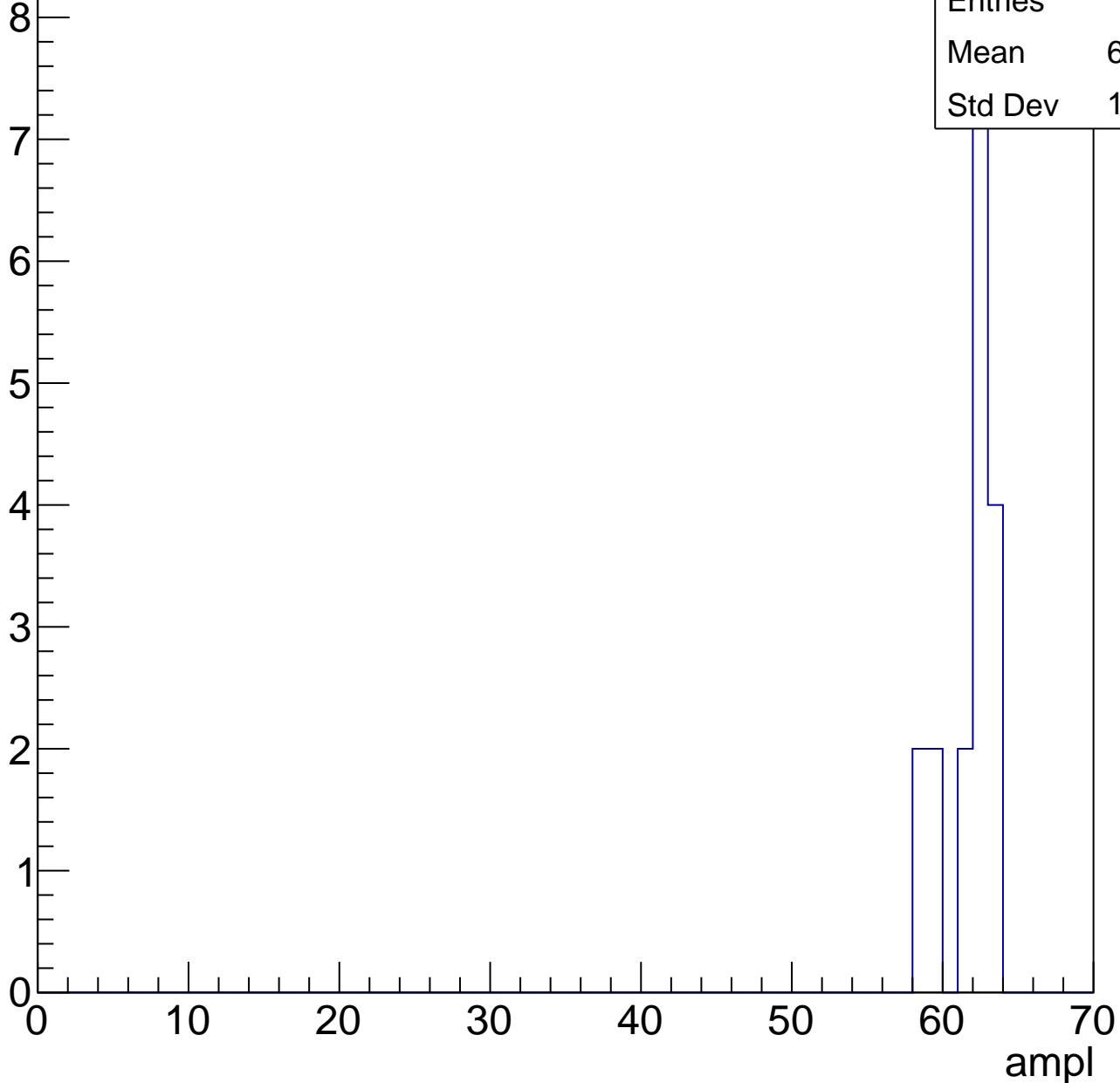


B1L100S, U1-ch46, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

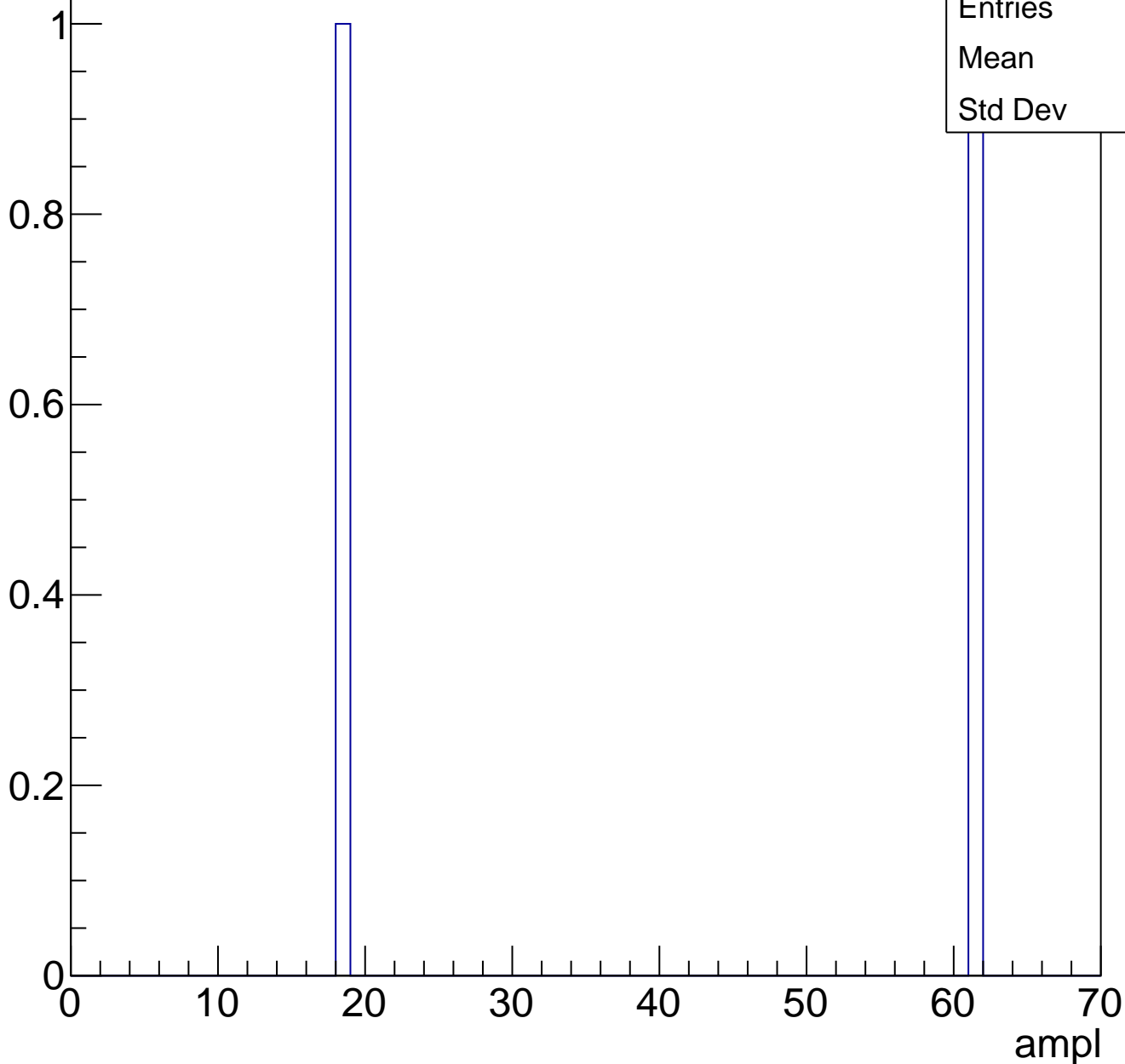
Entries	18
Mean	61.33
Std Dev	1.633



B1L100S, U1-ch46, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch47, adc0

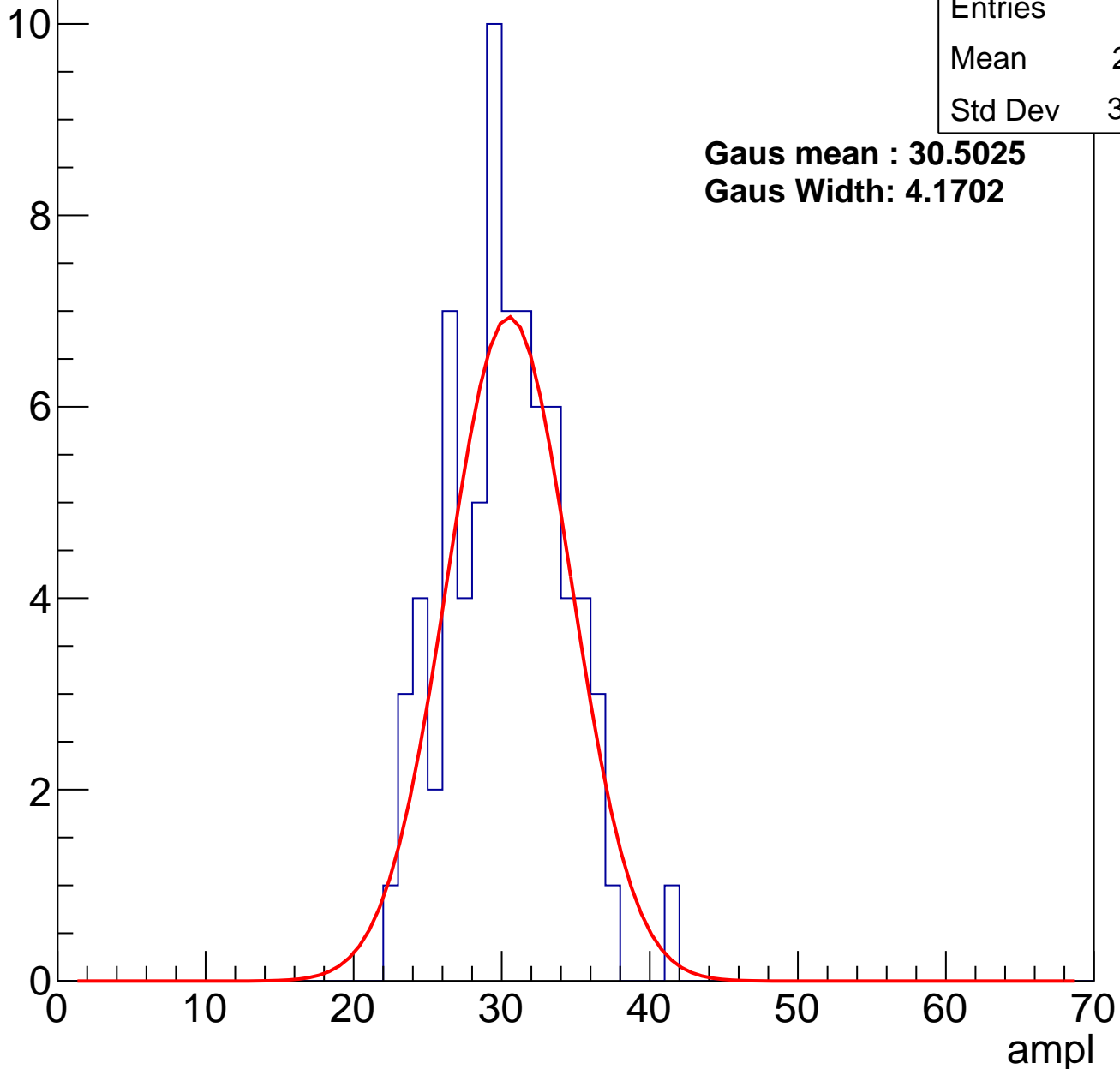
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	29.81
Std Dev	3.843

Gaus mean : 30.5025

Gaus Width: 4.1702

Entry



B1L100S, U1-ch47, adc1

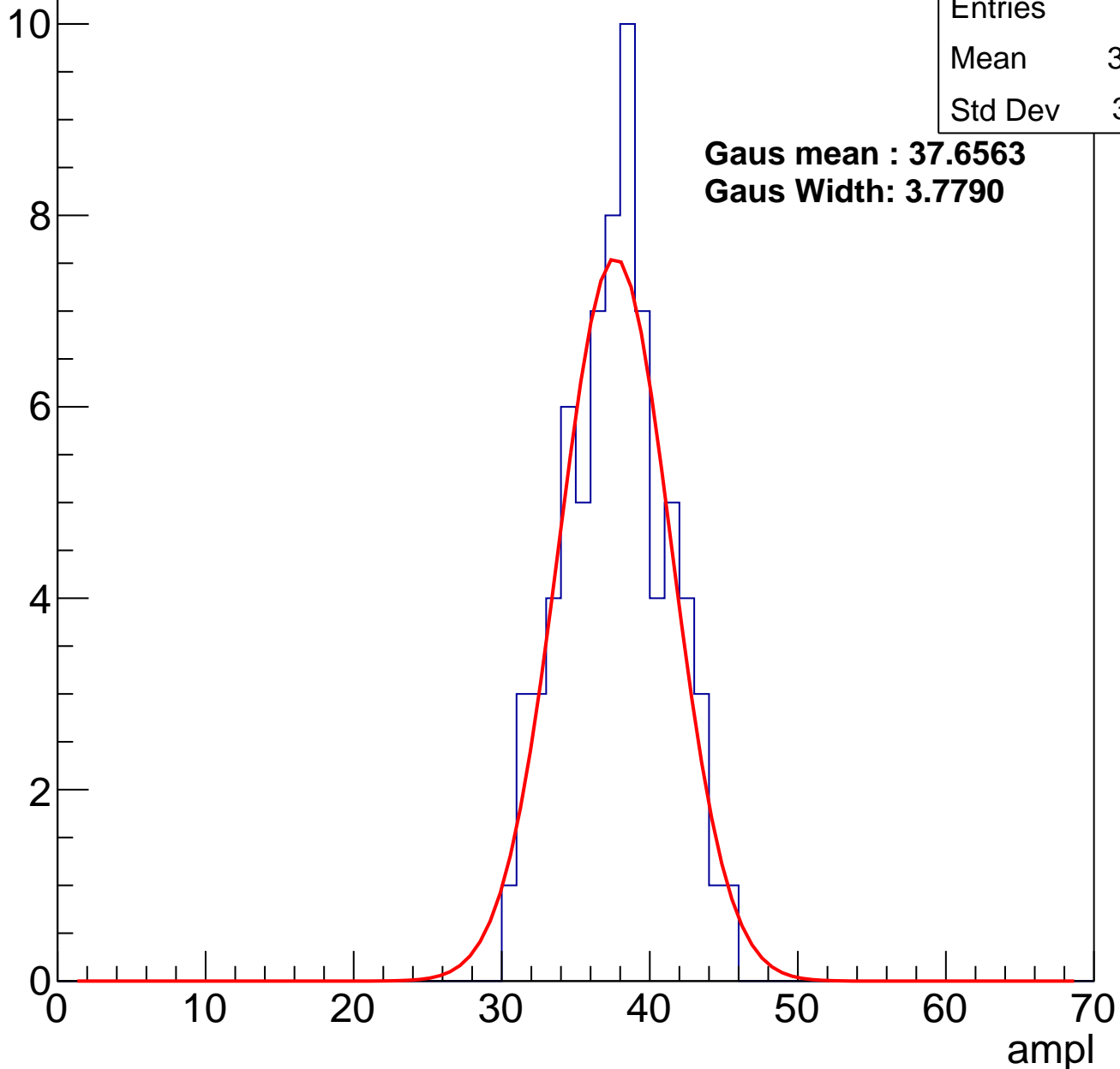
calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	37.25
Std Dev	3.431

Gaus mean : 37.6563

Gaus Width: 3.7790

Entry



B1L100S, U1-ch47, adc2

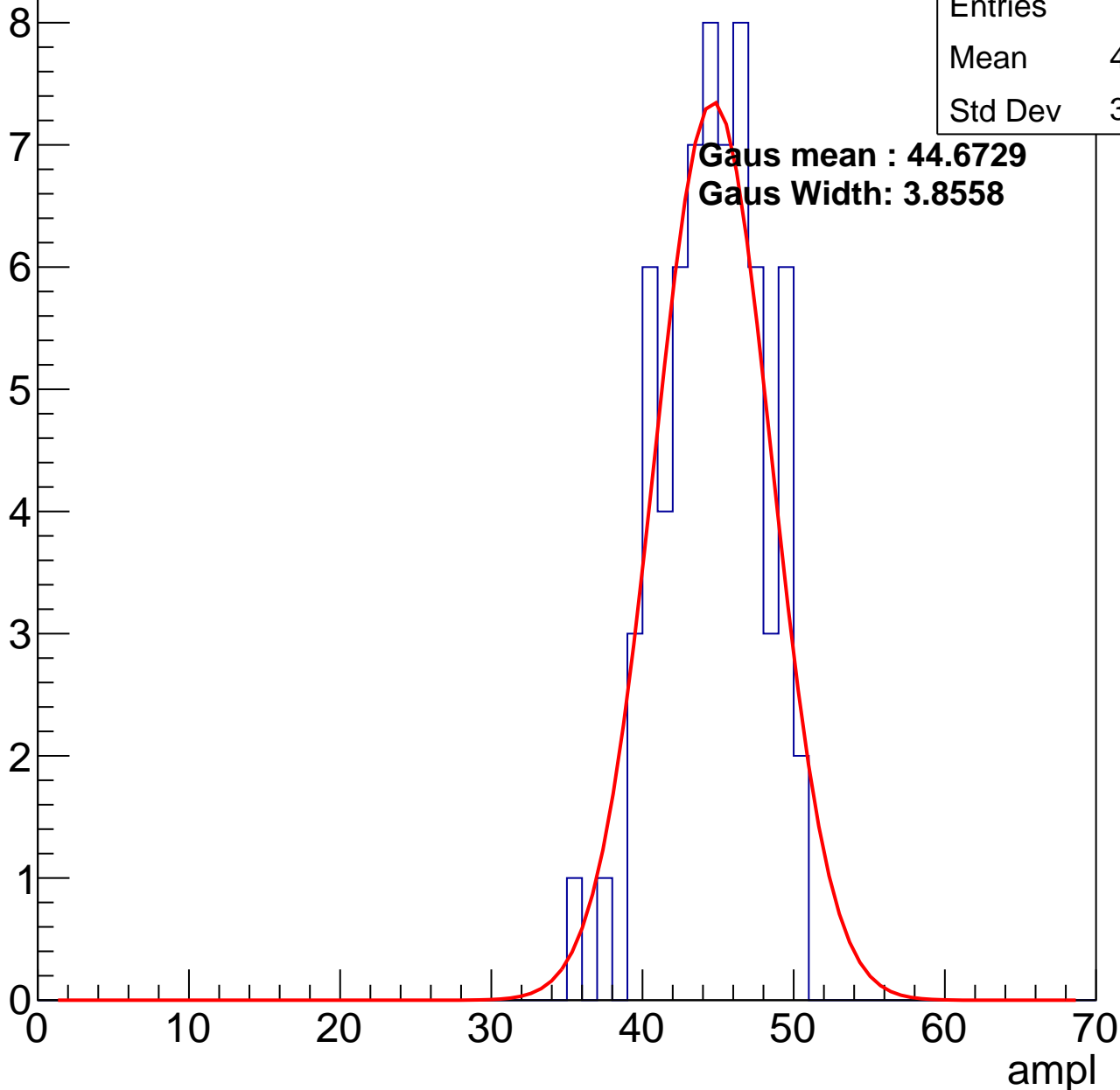
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	44.13
Std Dev	3.276

Gaus mean : 44.6729

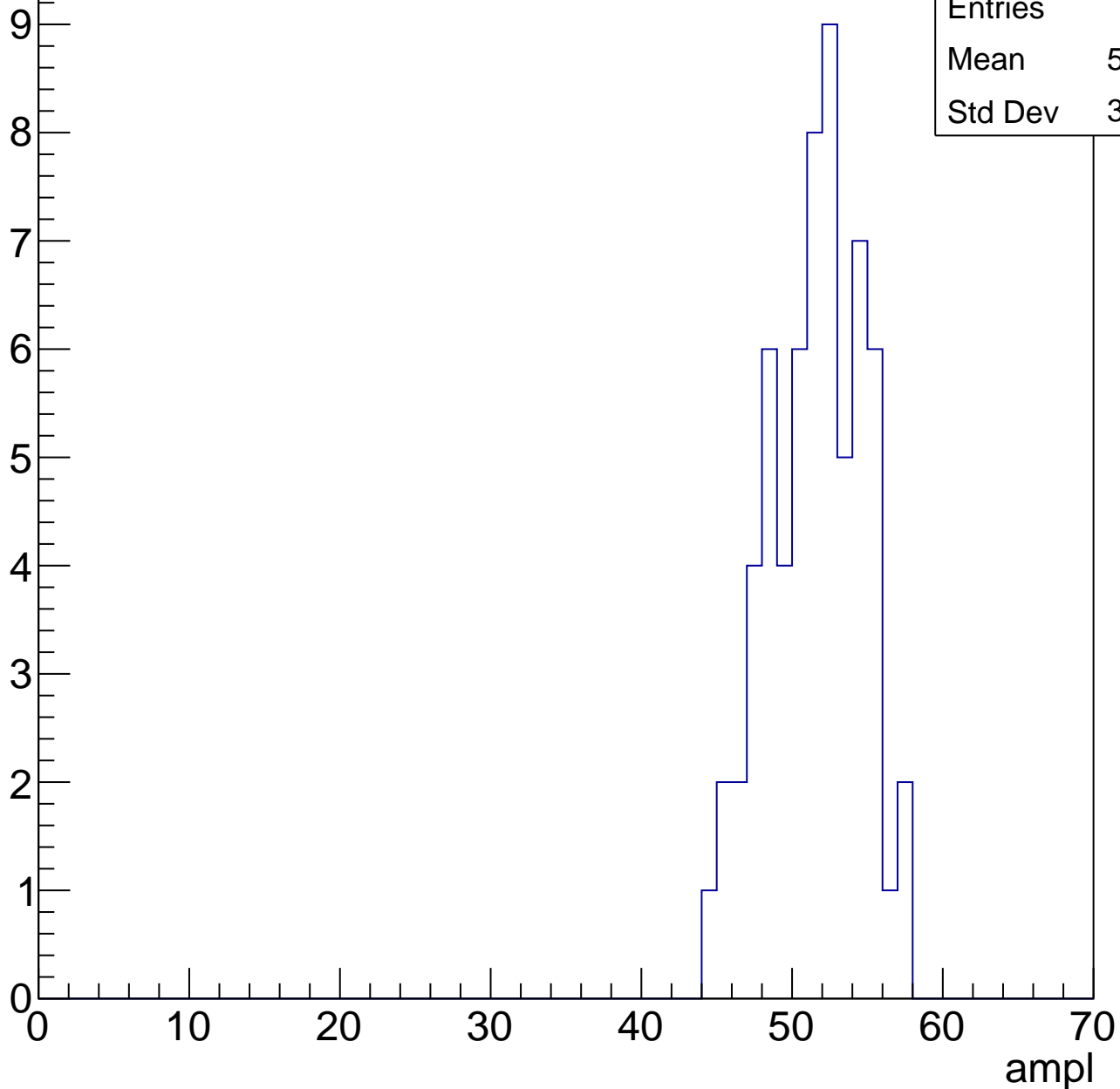
Gaus Width: 3.8558



B1L100S, U1-ch47, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



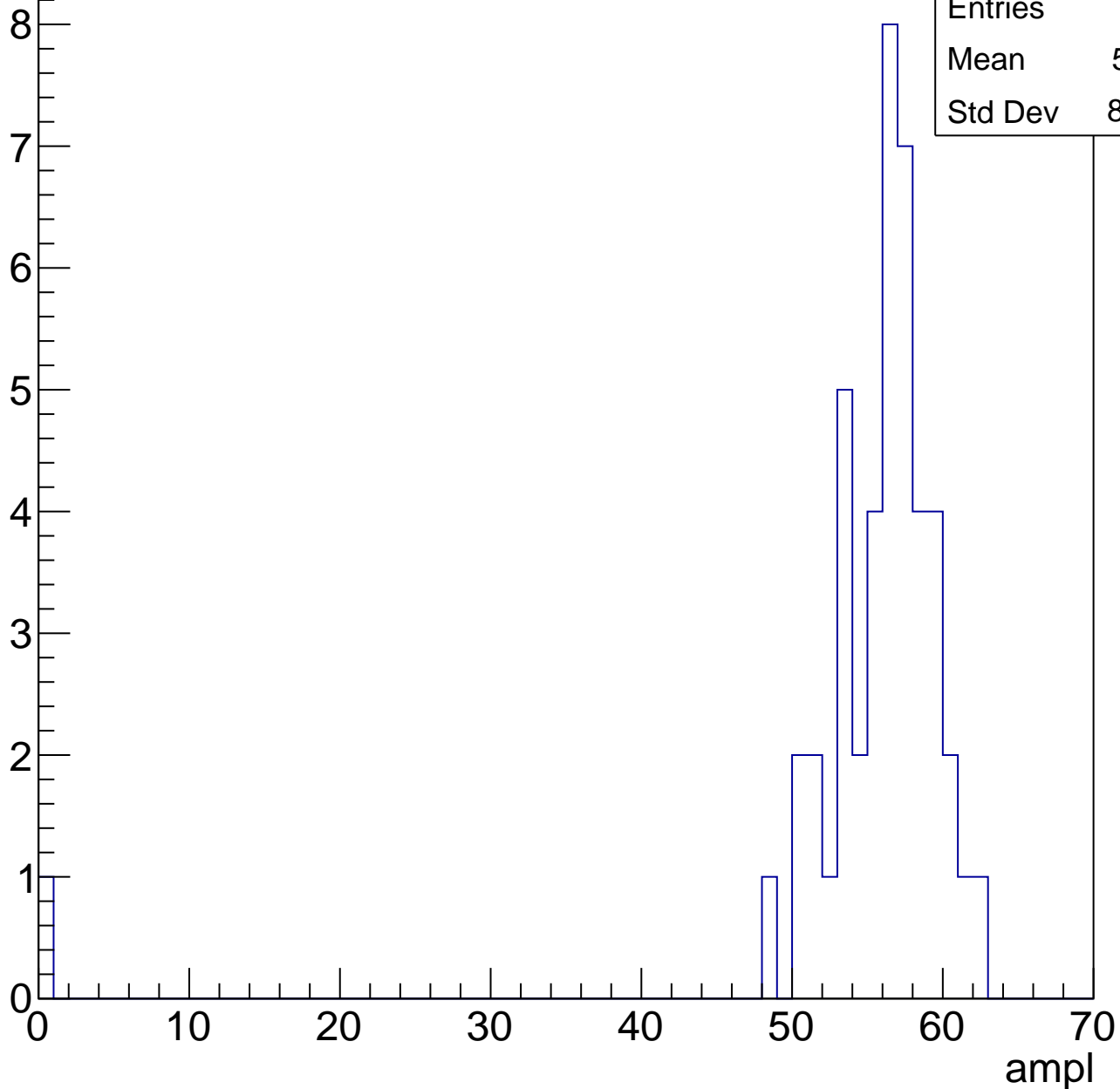
Entries	63
Mean	51.06
Std Dev	3.075

B1L100S, U1-ch47, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	54.51
Std Dev	8.752



B1L100S, U1-ch47, adc5

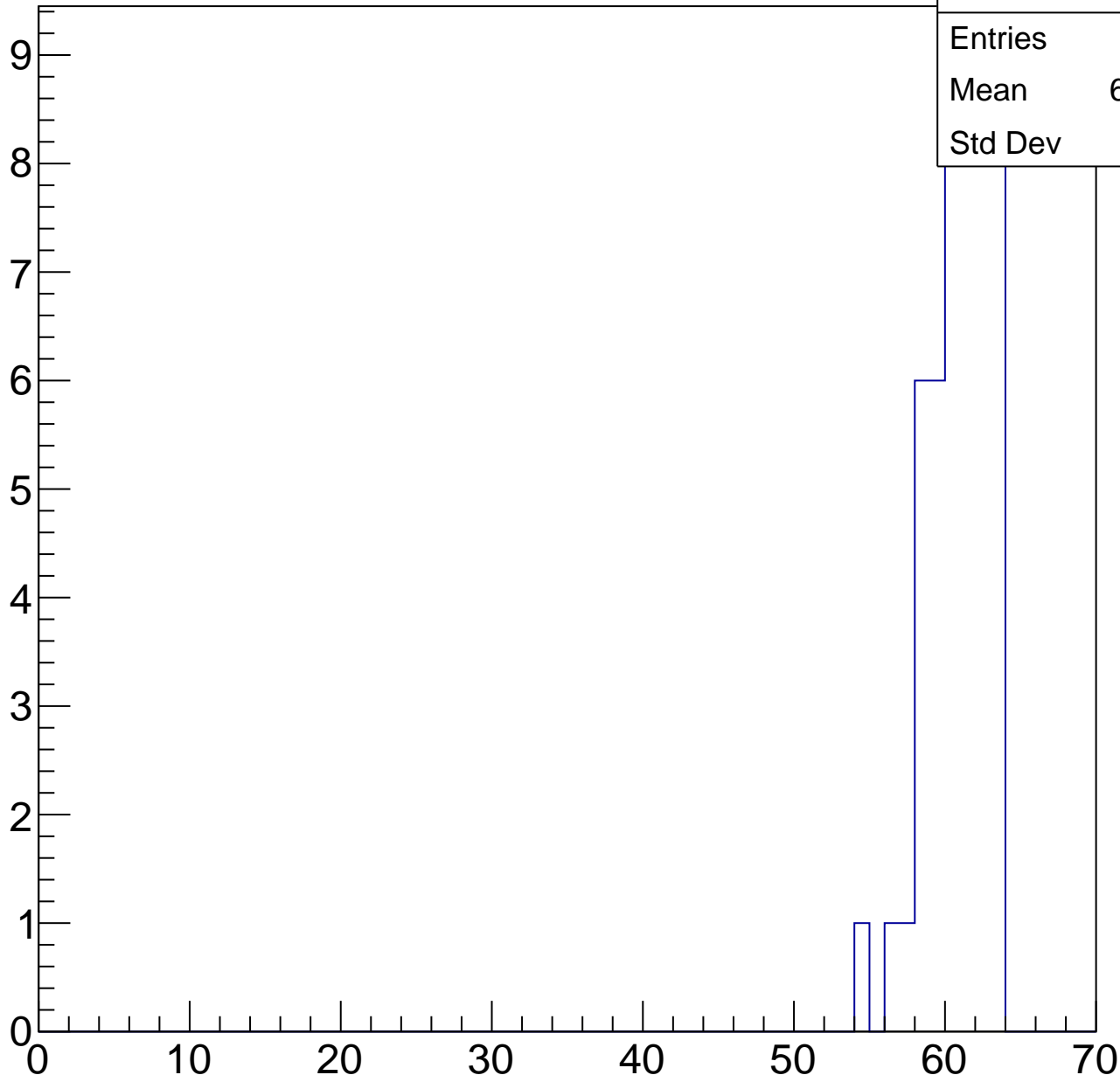
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.43
Std Dev	2.04

ampl



B1L100S, U1-ch47, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch47, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch48, adc0

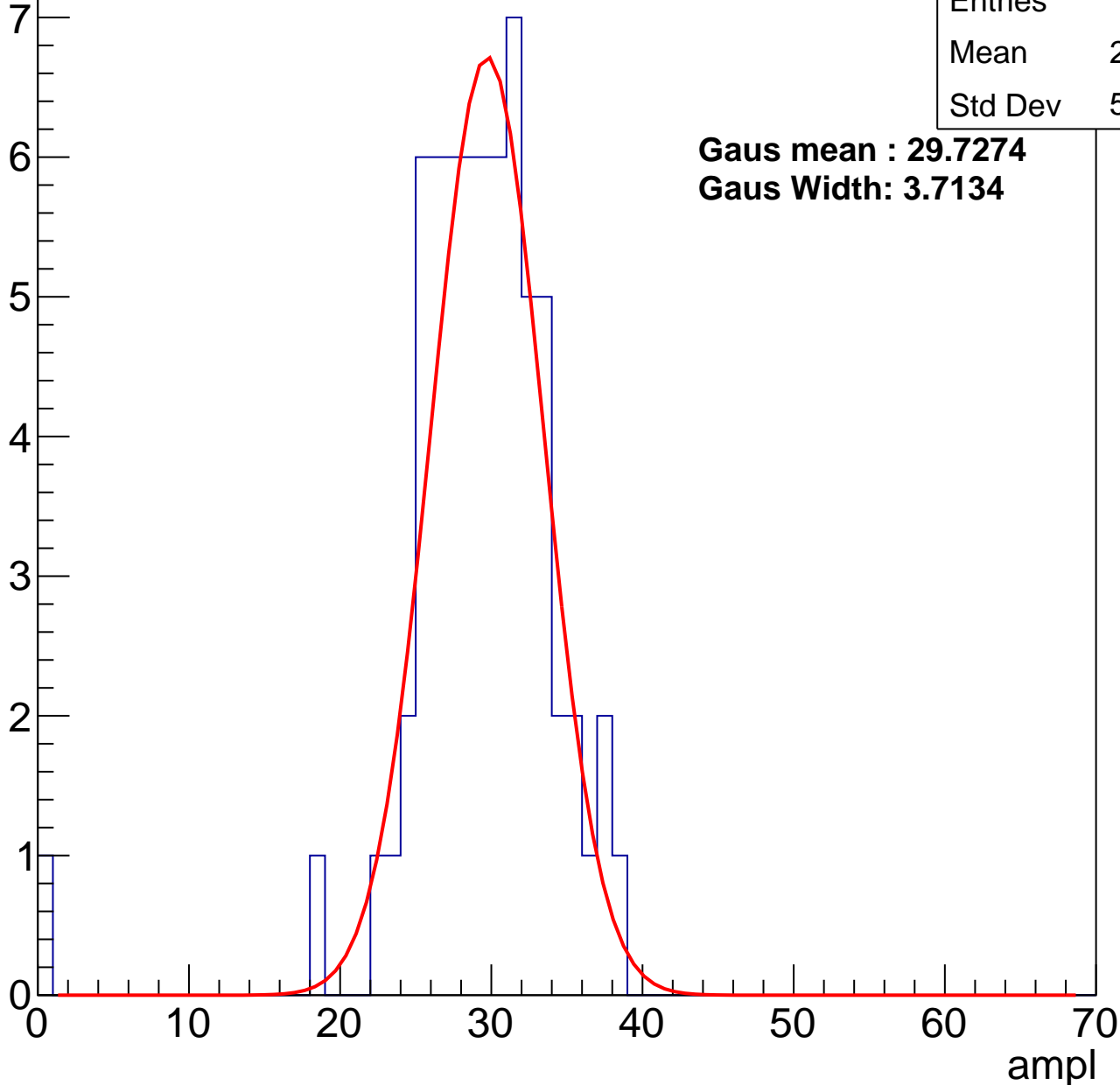
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.79
Std Dev	5.202

Gaus mean : 29.7274

Gaus Width: 3.7134



B1L100S, U1-ch48, adc1

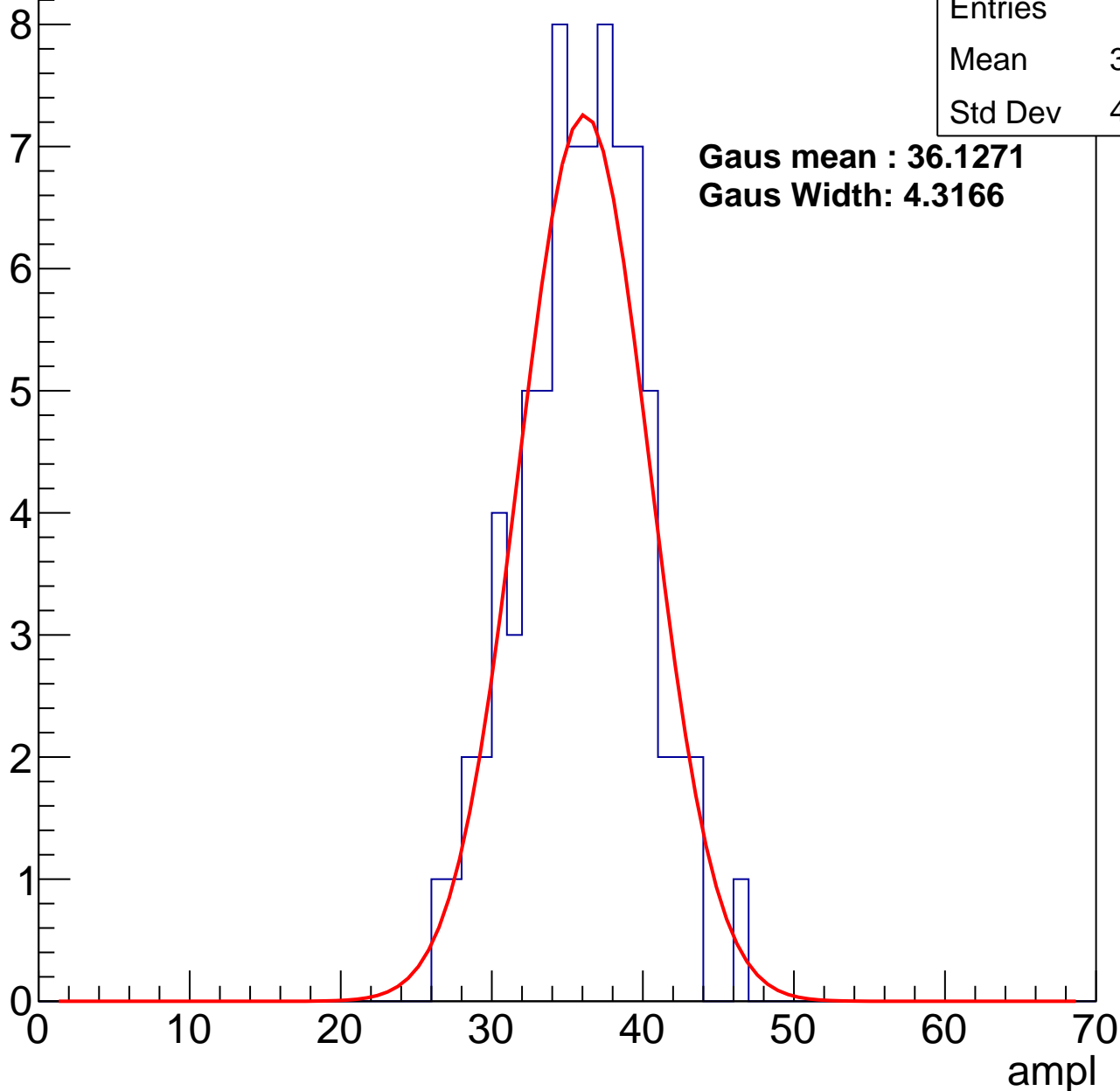
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.53
Std Dev	4.012

Gaus mean : 36.1271

Gaus Width: 4.3166



B1L100S, U1-ch48, adc2

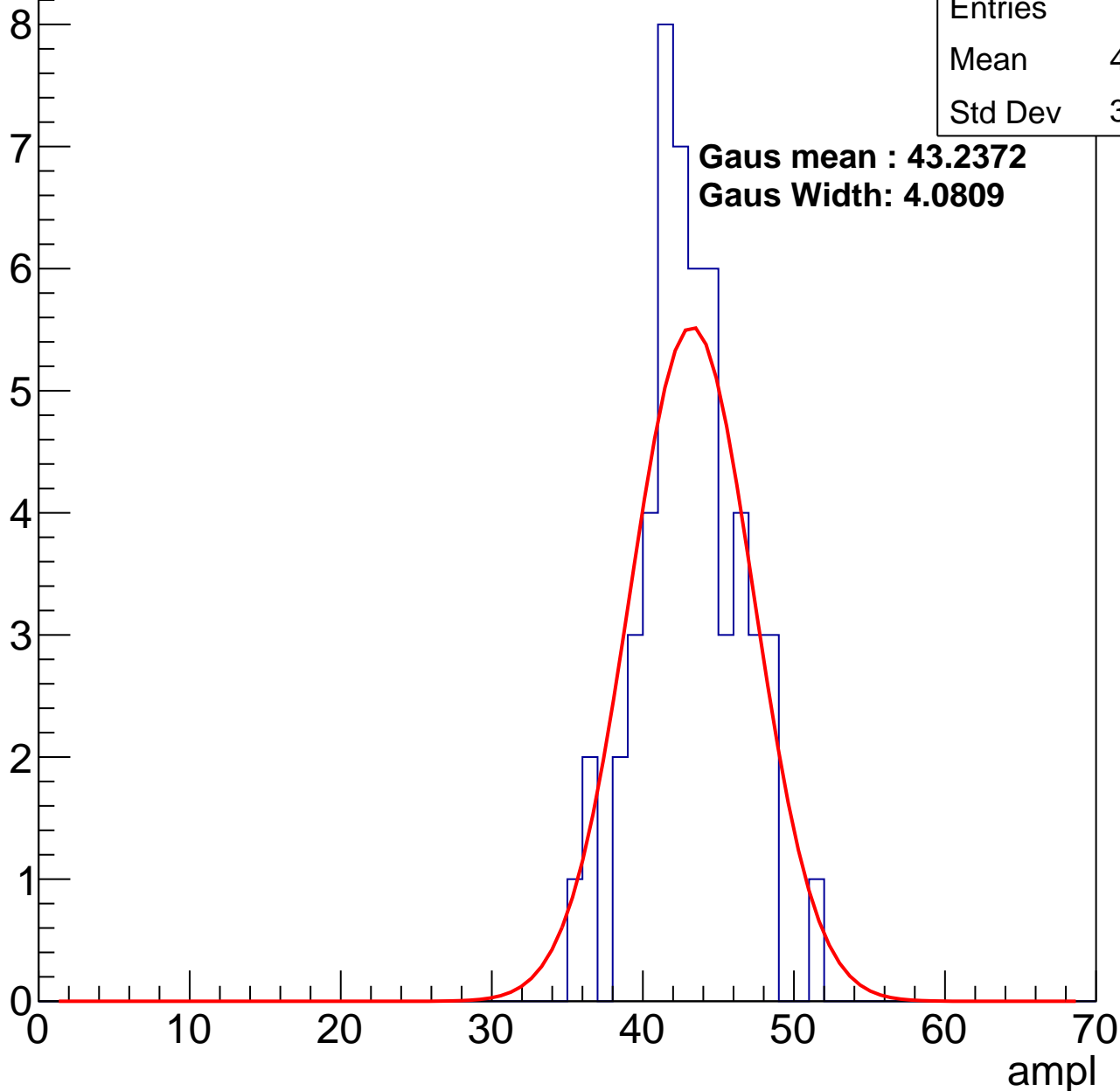
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	42.62
Std Dev	3.275

Gaus mean : 43.2372

Gaus Width: 4.0809

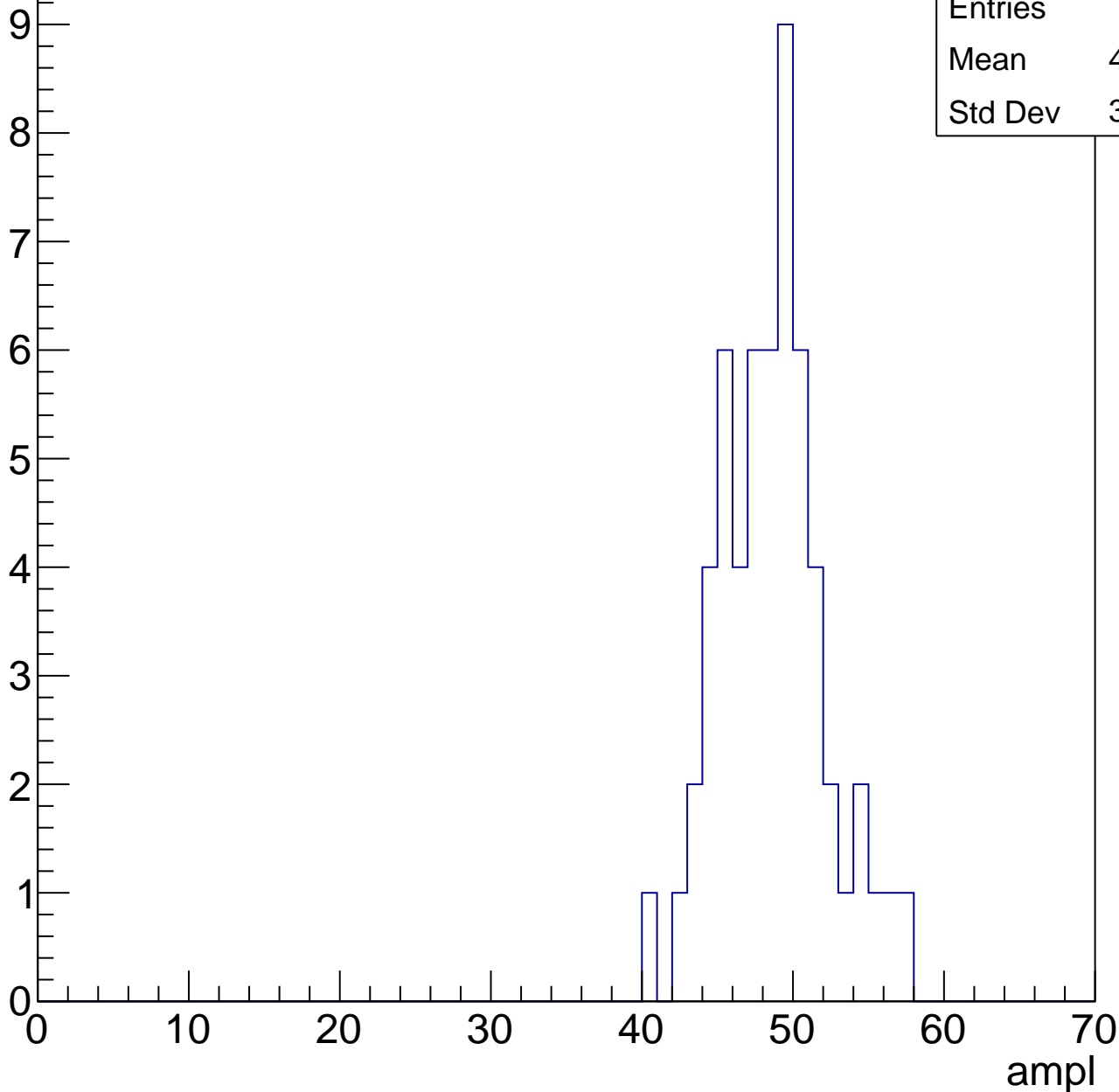


B1L100S, U1-ch48, adc3

calib_packv5_042523_0143.root, FC#4, port A2

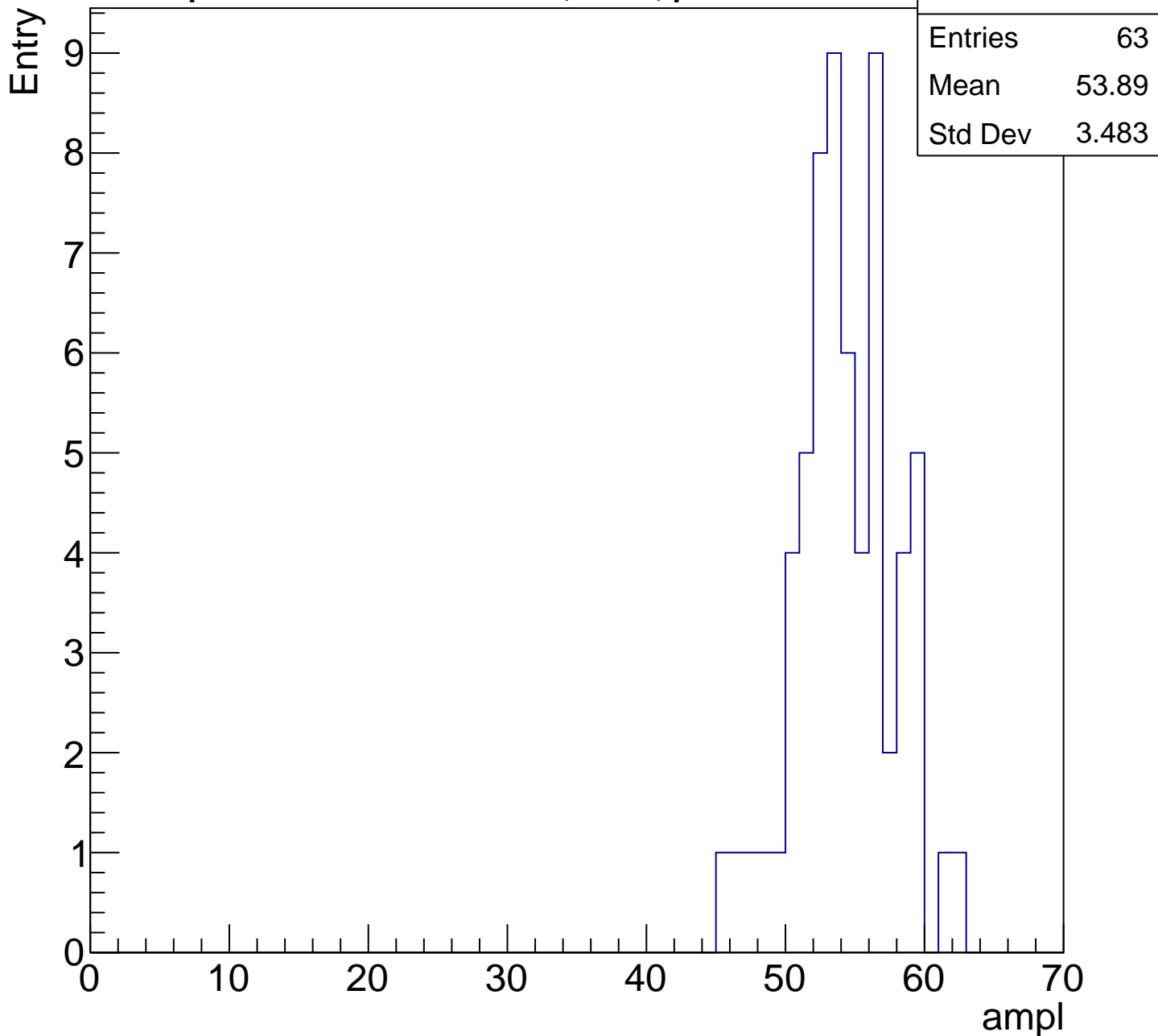
Entry

Entries	57
Mean	48.18
Std Dev	3.455



B1L100S, U1-ch48, adc4

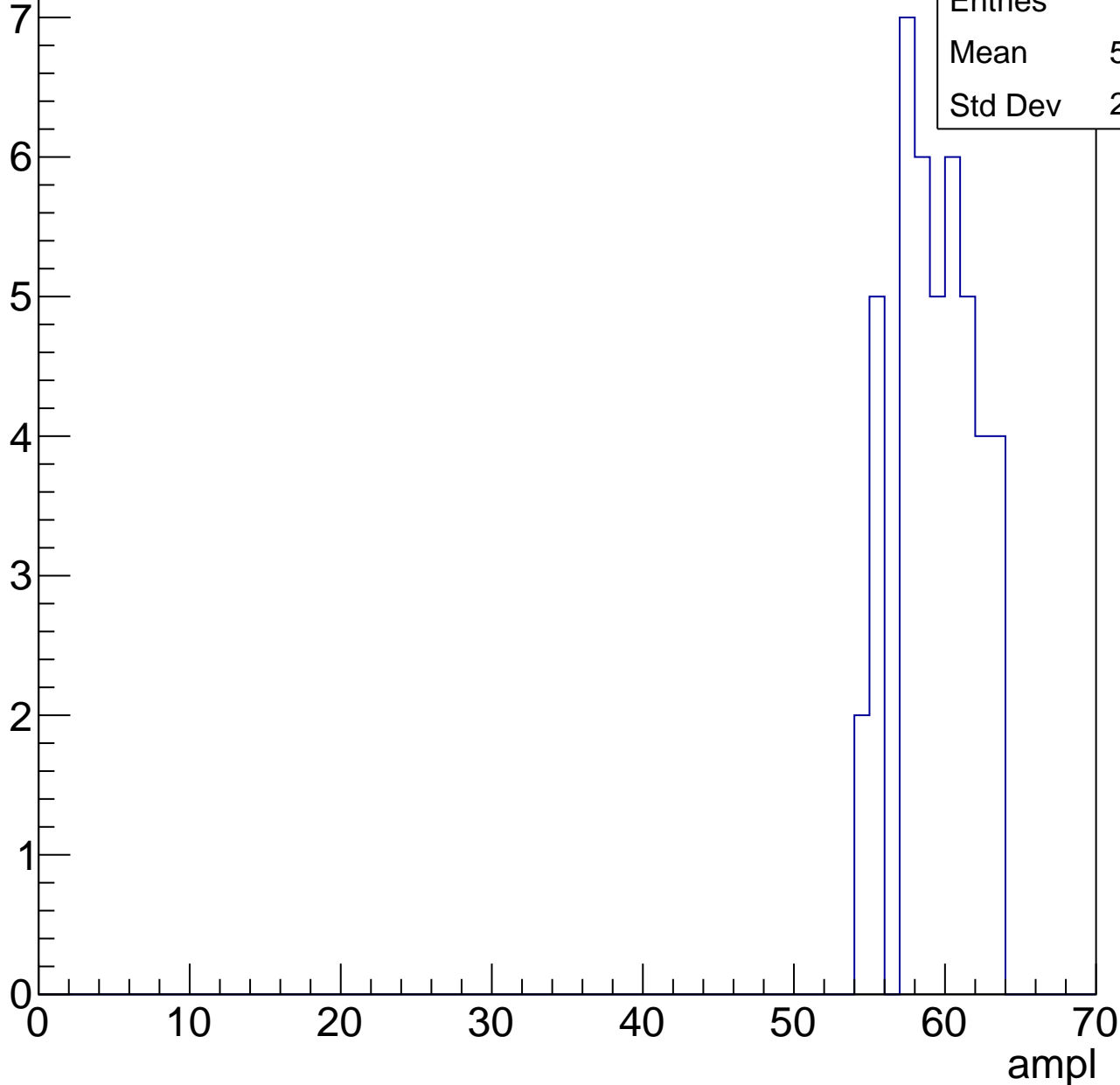
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch48, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



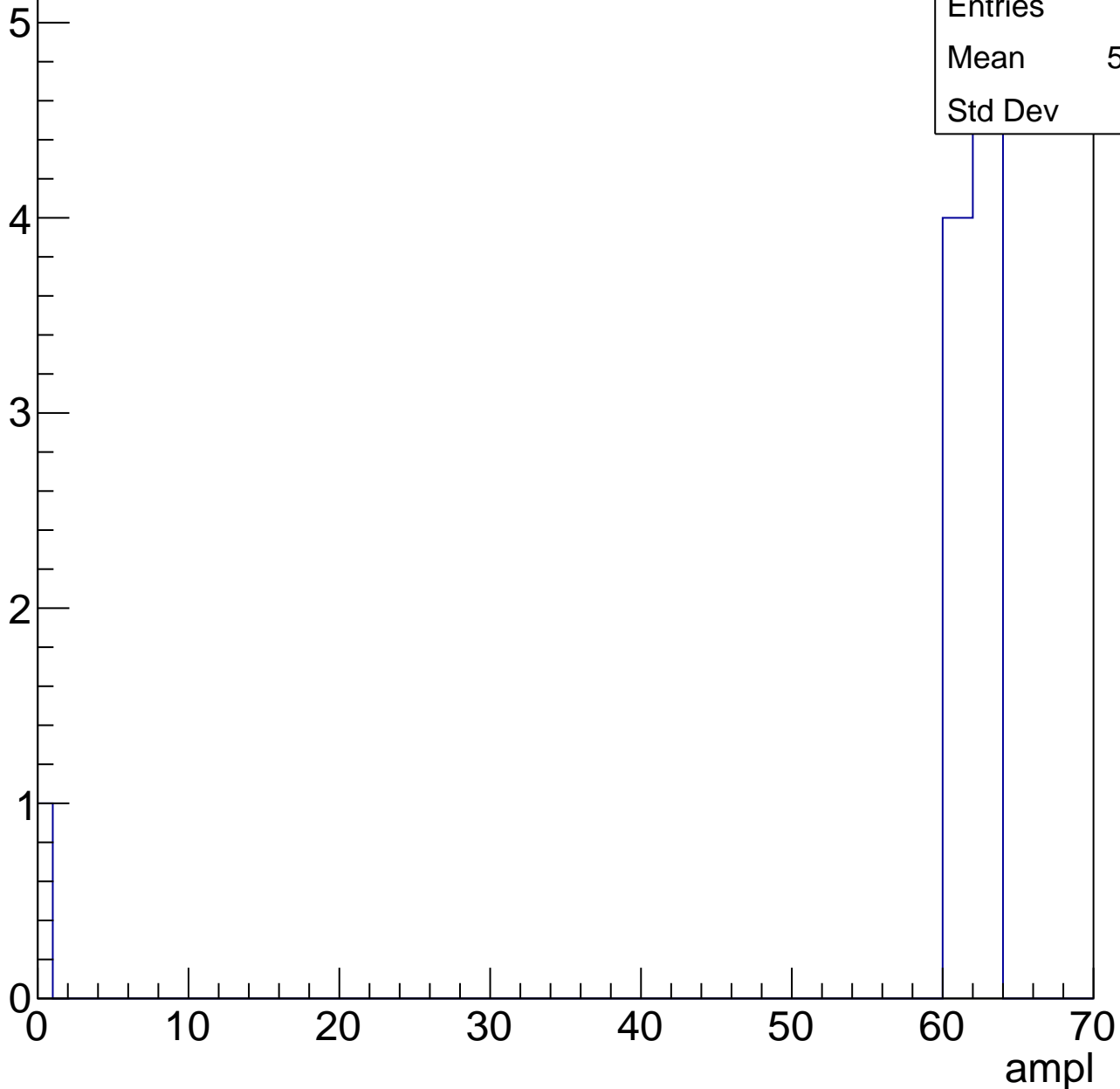
Entries	44
Mean	58.86
Std Dev	2.564

B1L100S, U1-ch48, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	58.37
Std Dev	13.8



B1L100S, U1-ch48, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch49, adc0

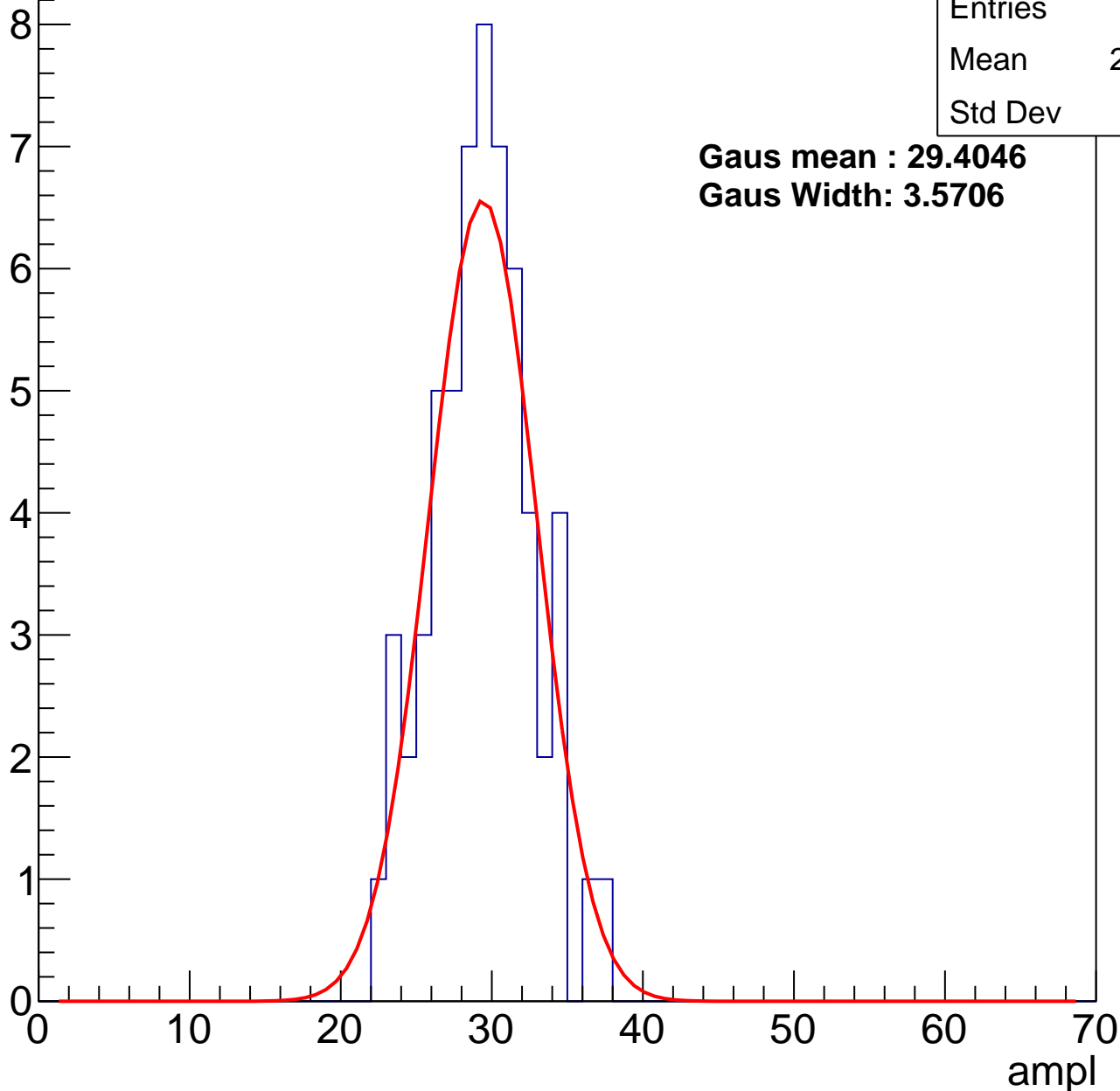
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	28.92
Std Dev	3.29

Gaus mean : 29.4046

Gaus Width: 3.5706



B1L100S, U1-ch49, adc1

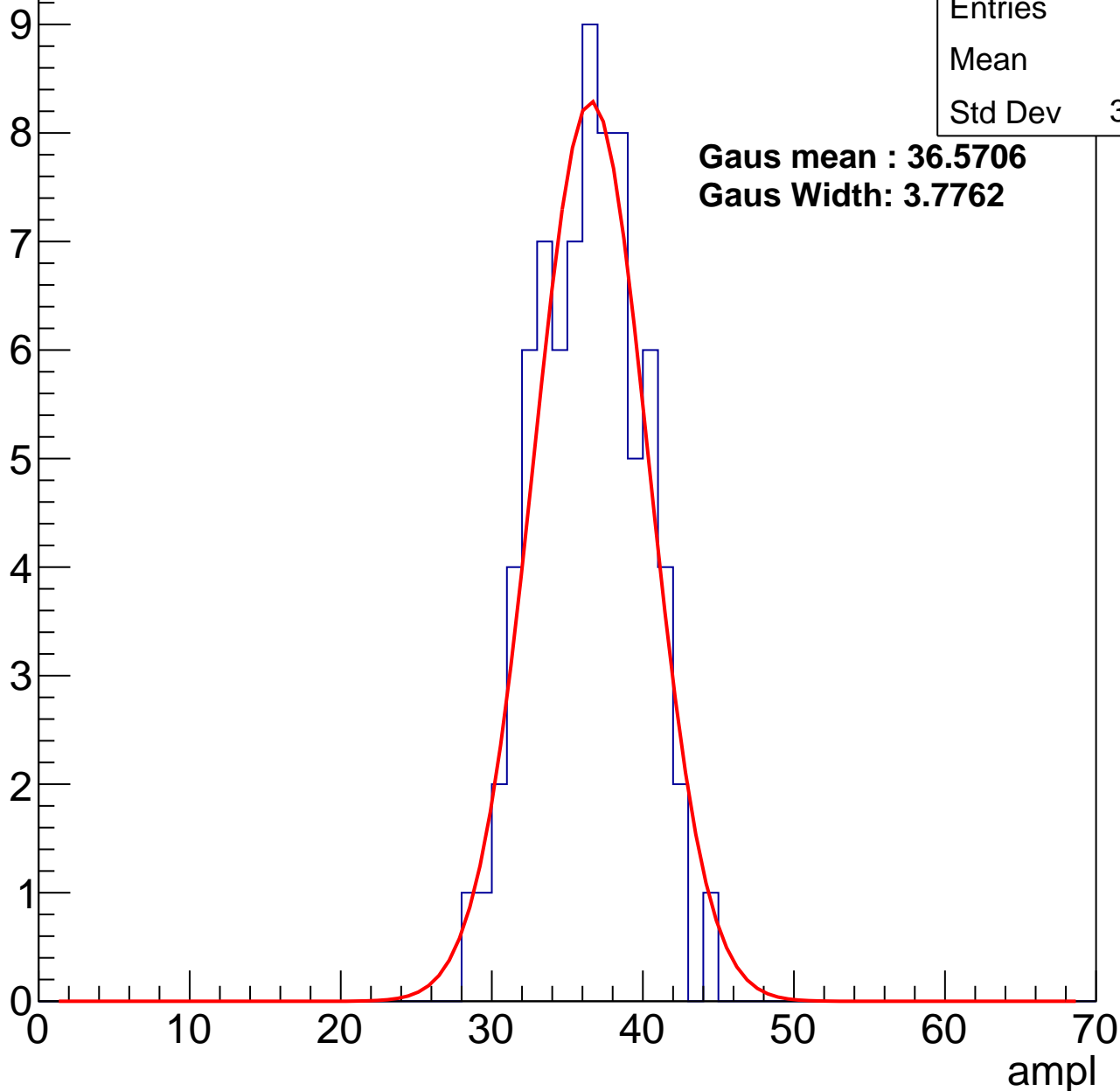
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	35.9
Std Dev	3.402

Gaus mean : 36.5706

Gaus Width: 3.7762



B1L100S, U1-ch49, adc2

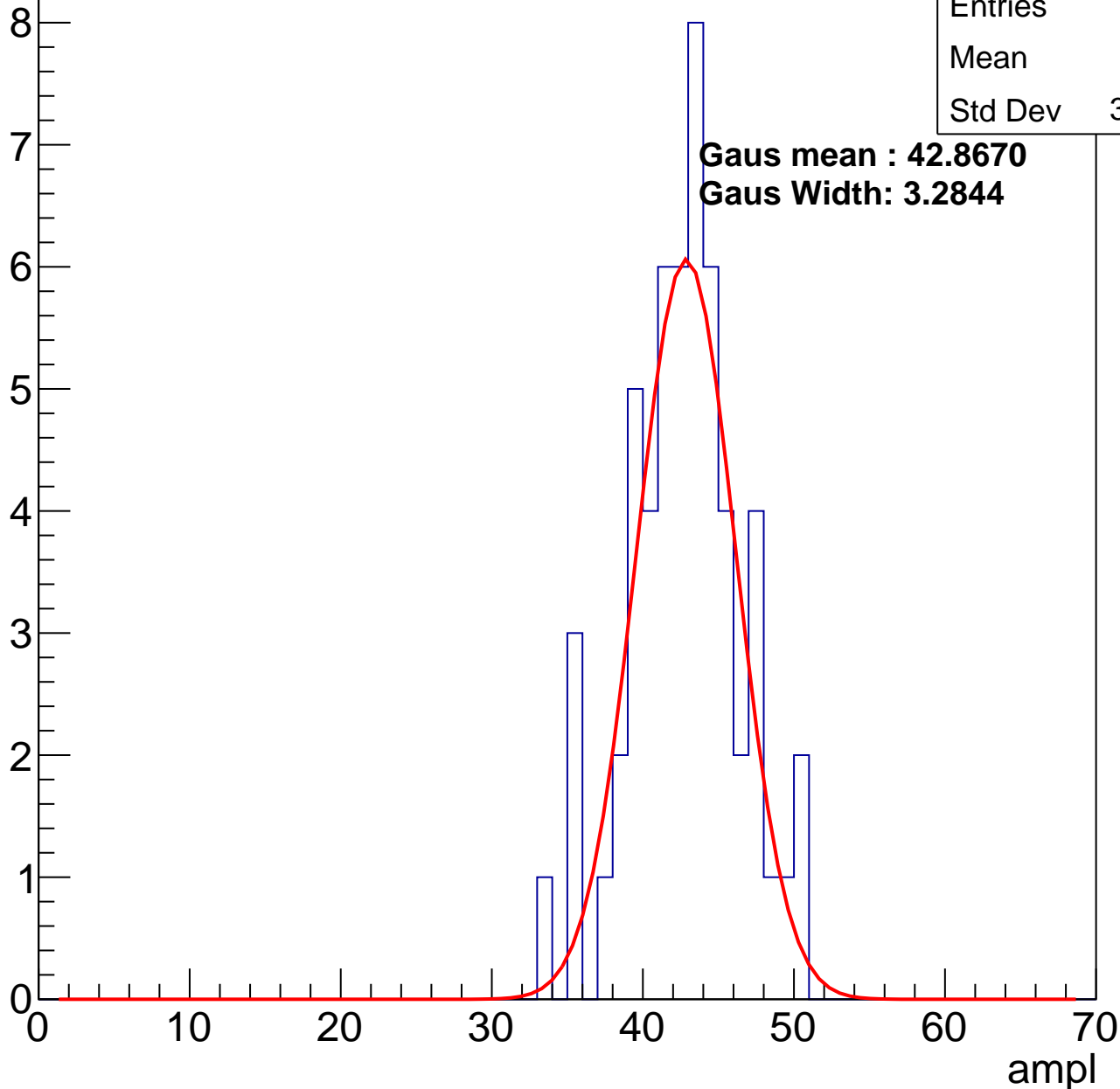
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	42.3
Std Dev	3.684

Gaus mean : 42.8670

Gaus Width: 3.2844

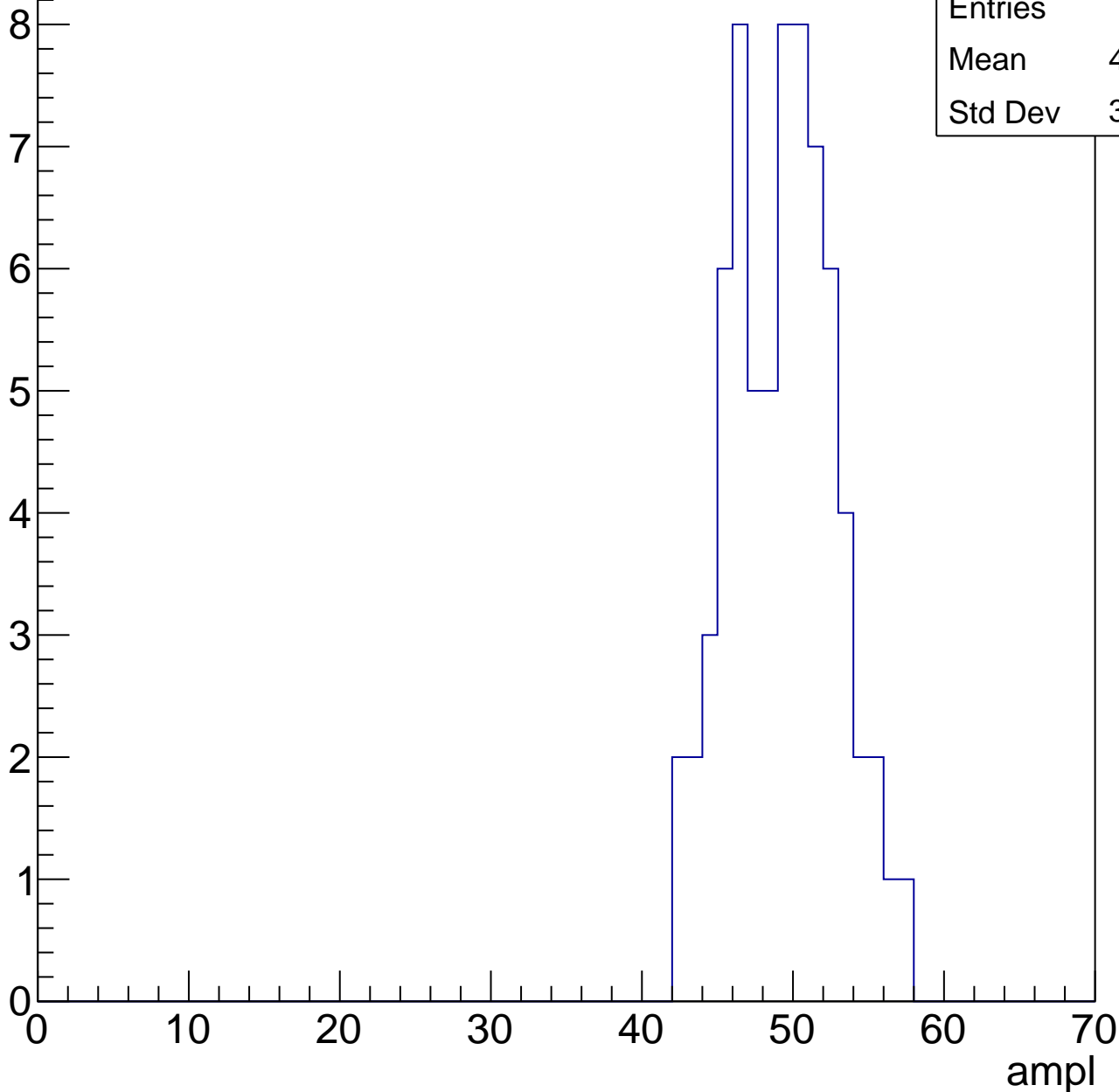


B1L100S, U1-ch49, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	48.84
Std Dev	3.434

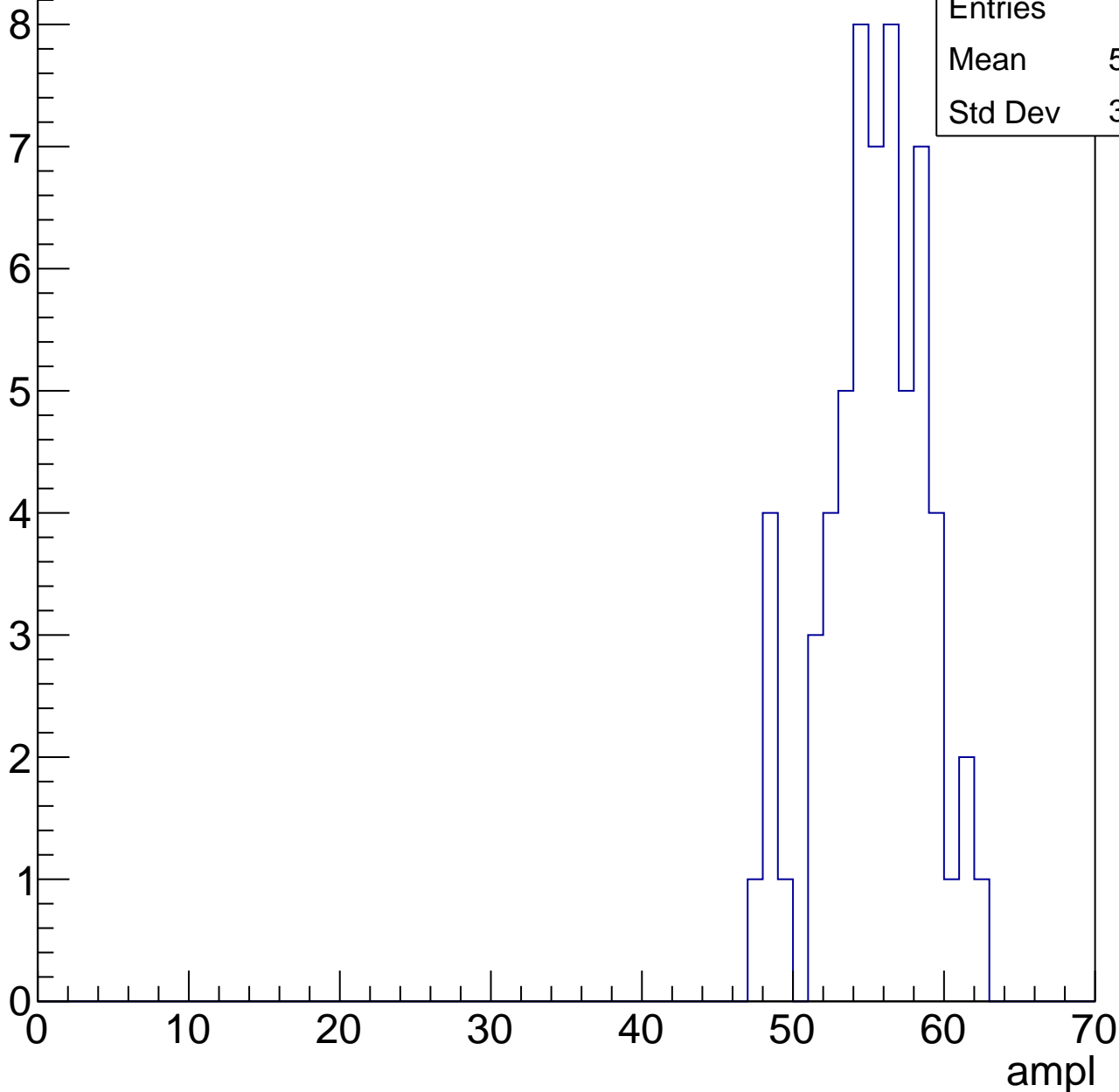


B1L100S, U1-ch49, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	54.92
Std Dev	3.408



B1L100S, U1-ch49, adc5

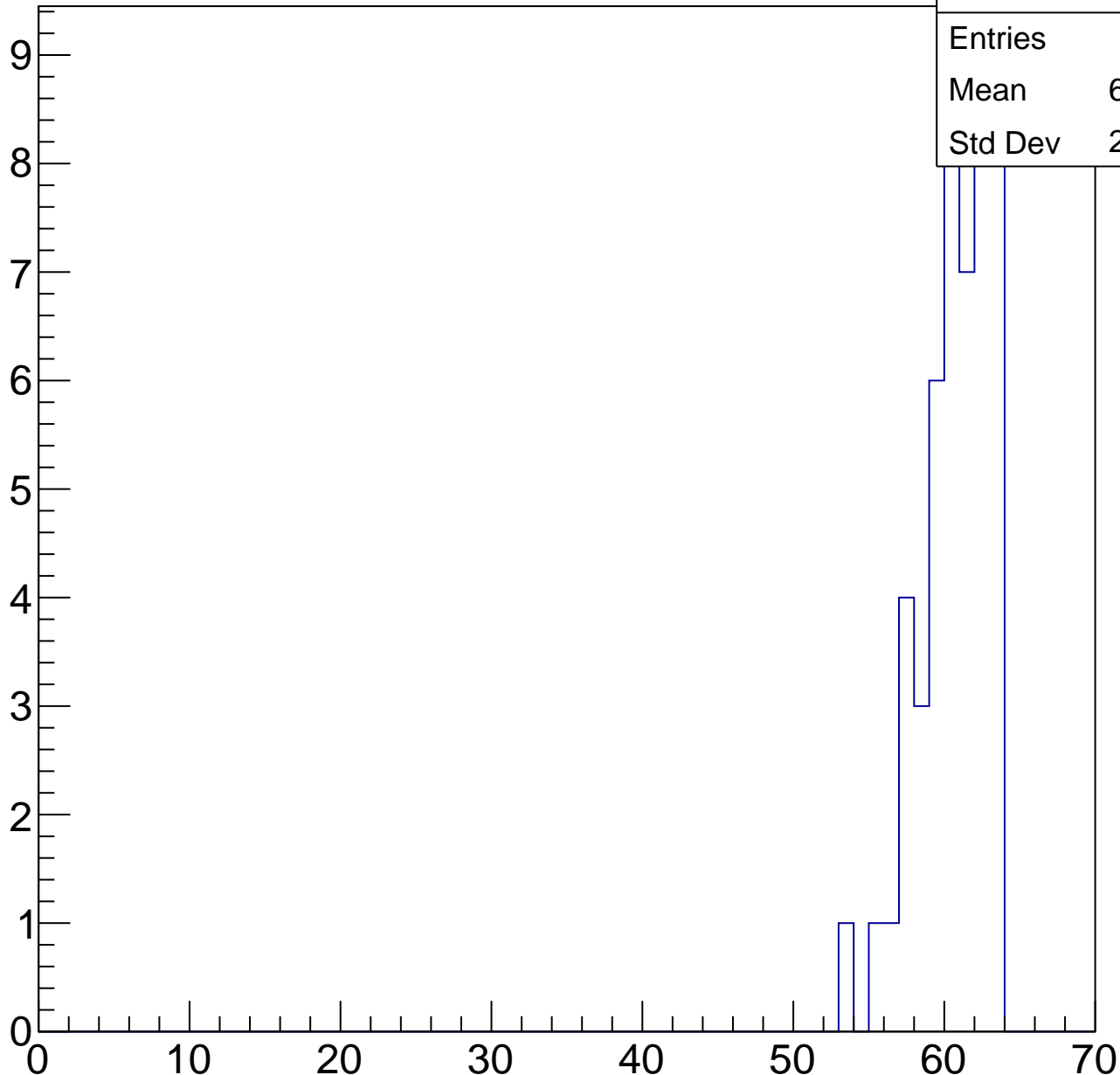
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	60.18
Std Dev	2.292

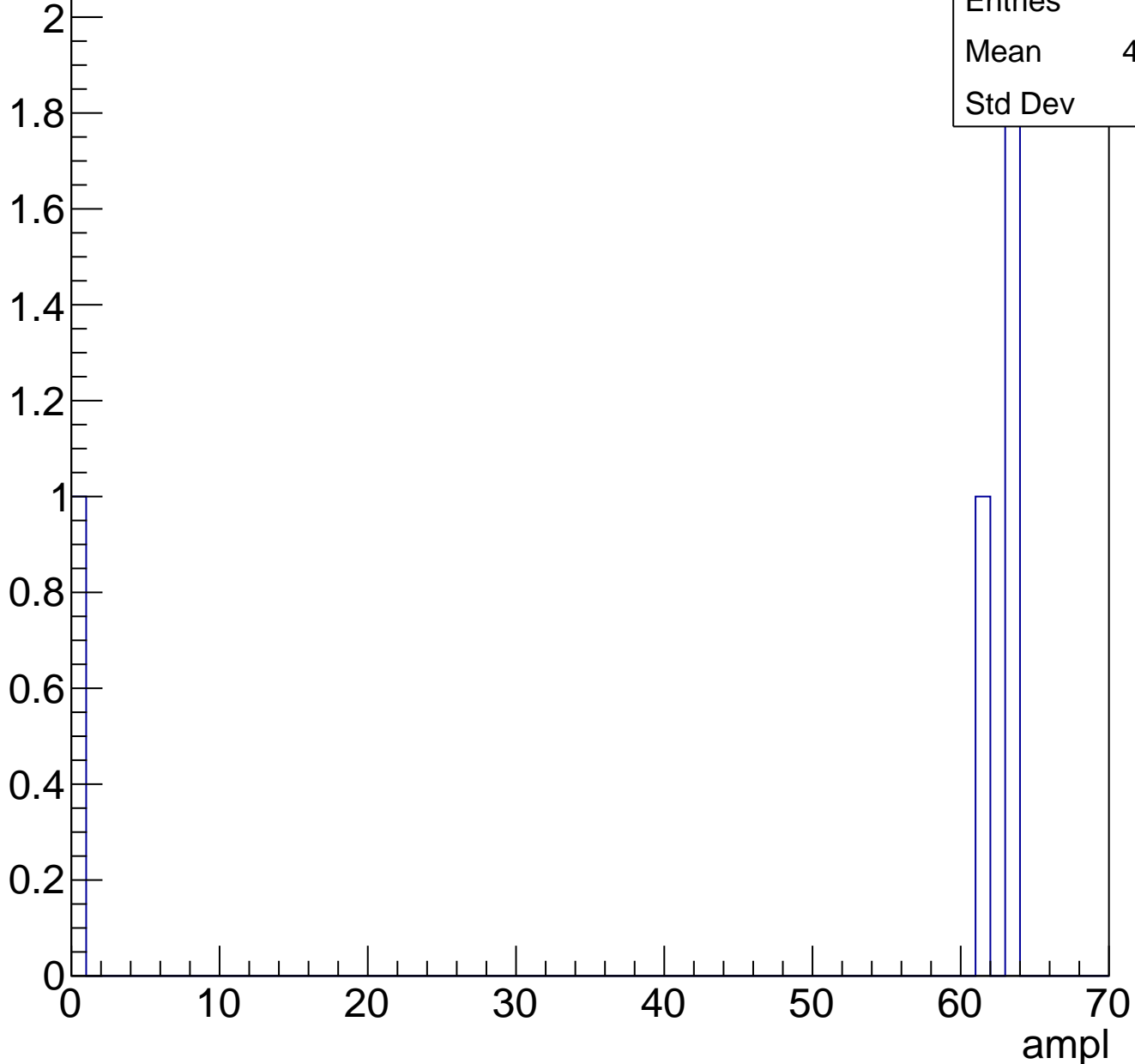
ampl



B1L100S, U1-ch49, adc6

calib_packv5_042523_0143.root, FC#4, port A2

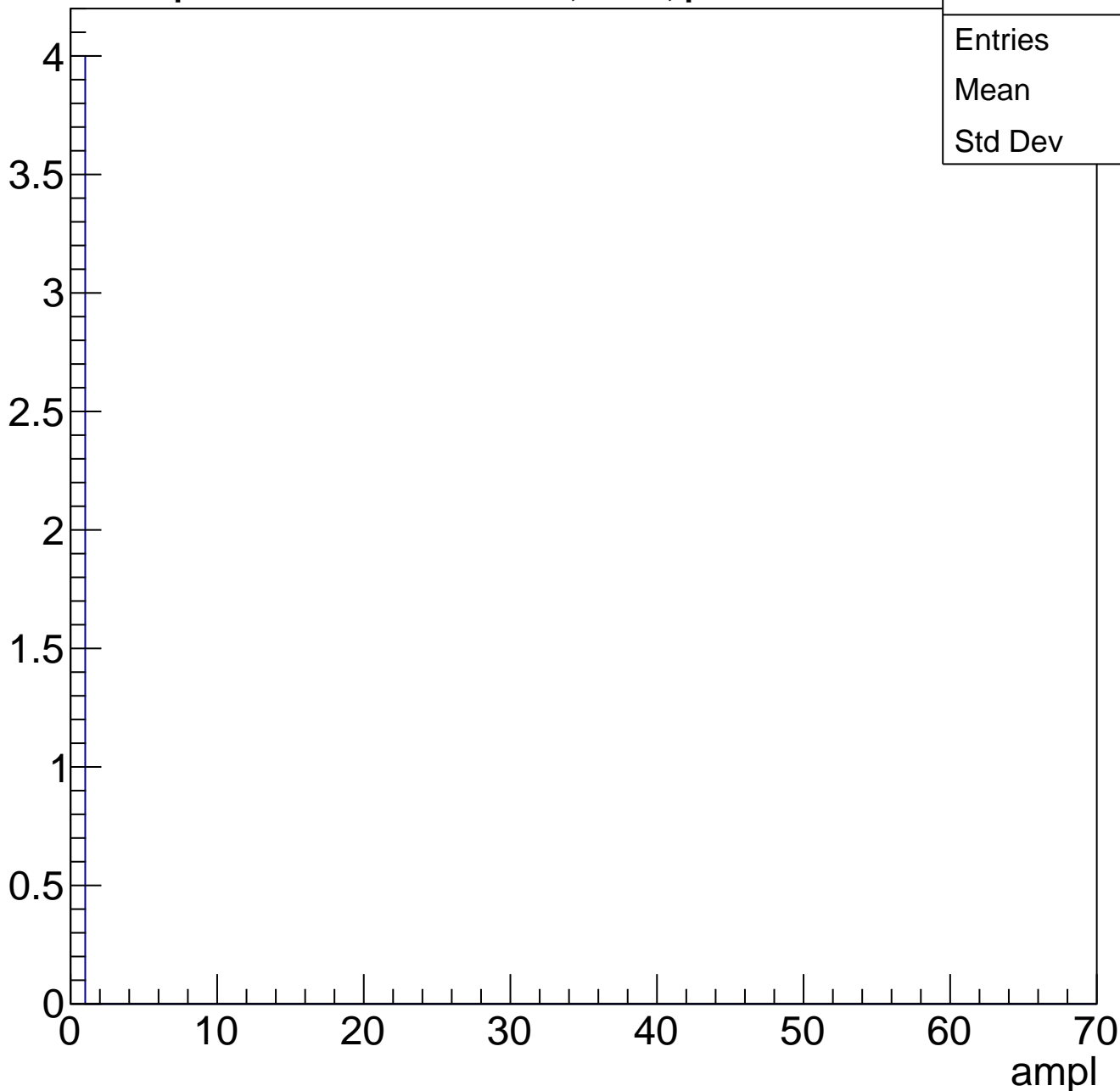
Entry



B1L100S, U1-ch49, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	0
Std Dev	0

B1L100S, U1-ch50, adc0

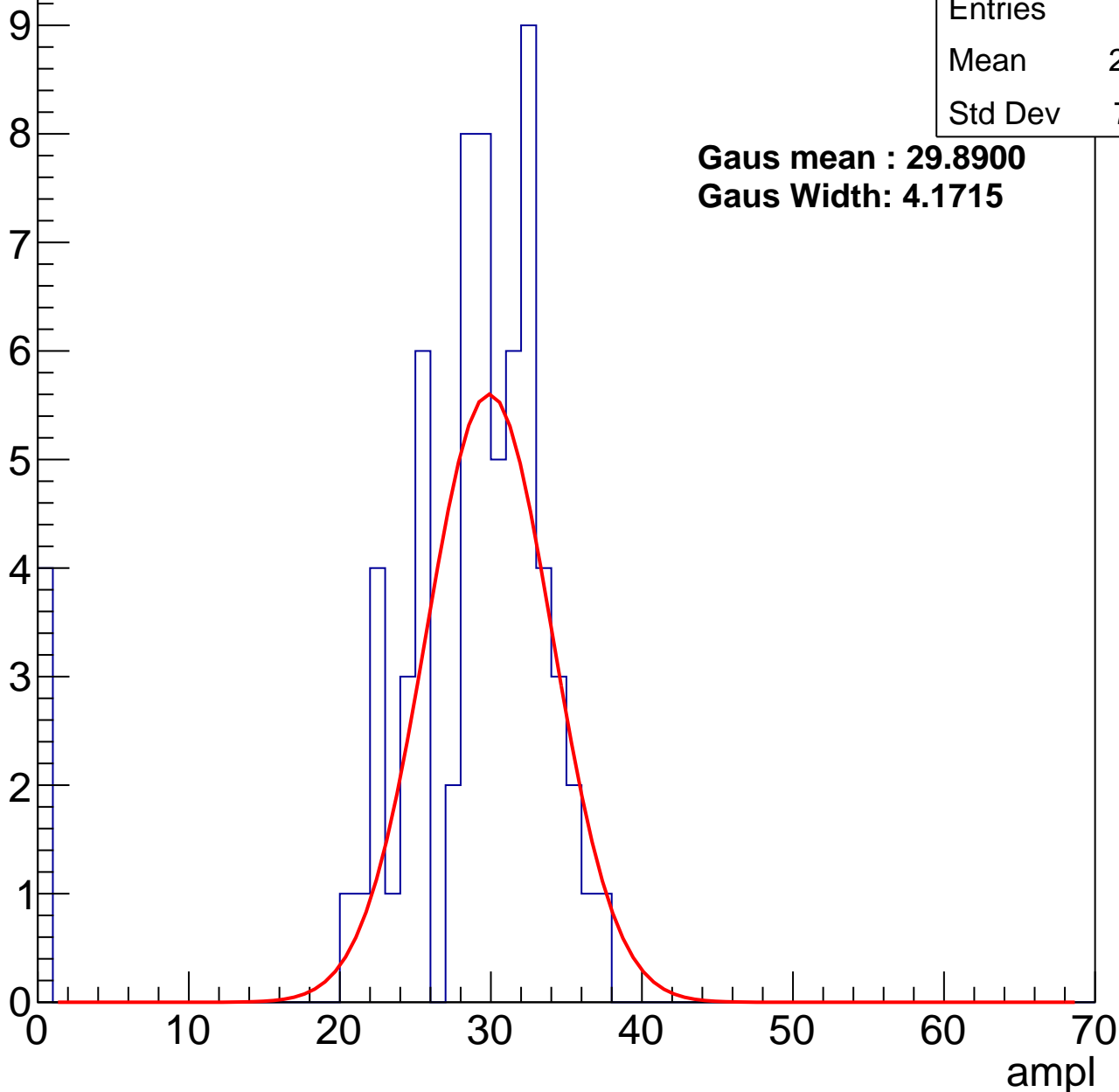
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	27.32
Std Dev	7.771

Gaus mean : 29.8900

Gaus Width: 4.1715



B1L100S, U1-ch50, adc1

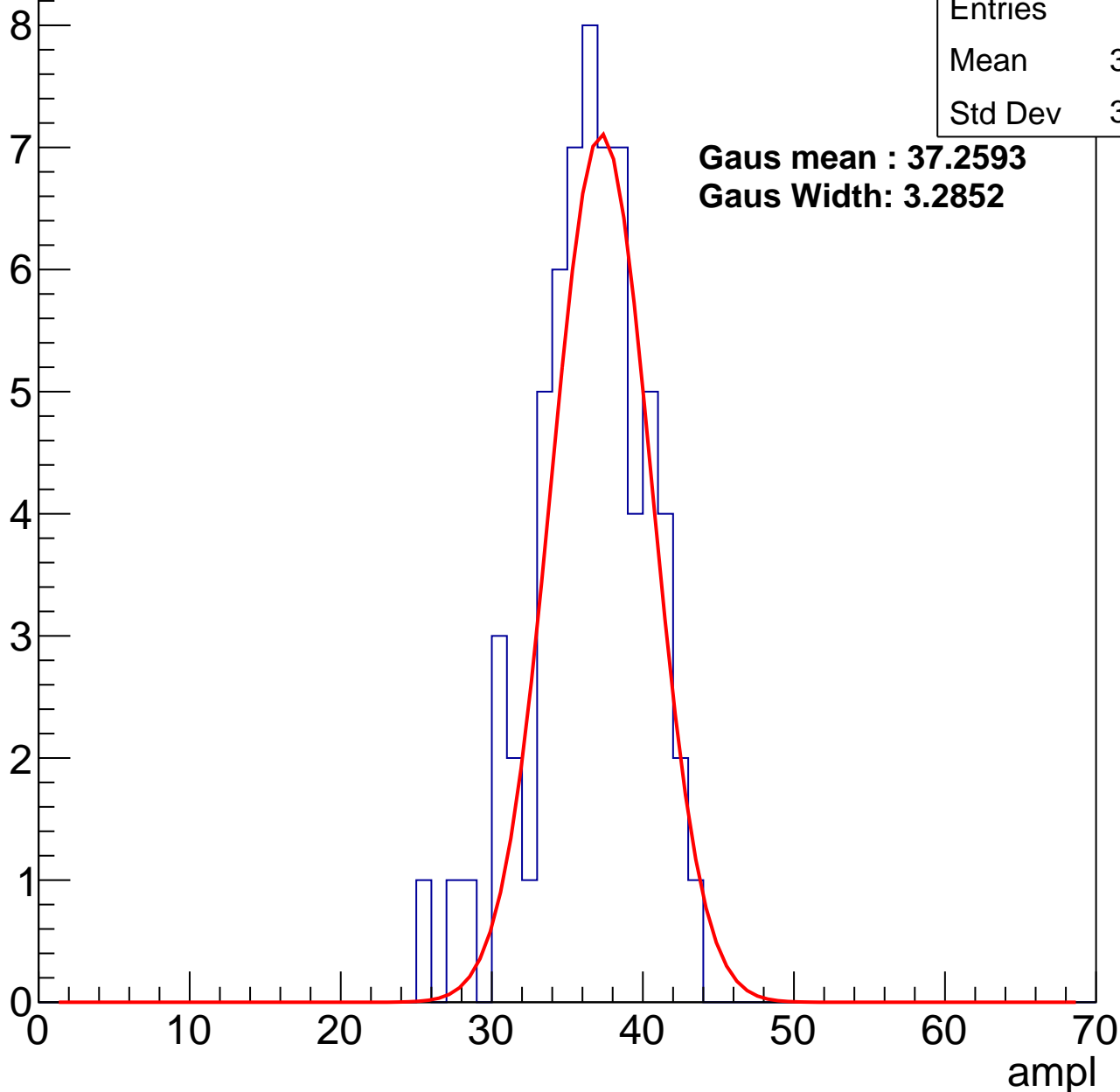
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.97
Std Dev	3.696

Gaus mean : 37.2593

Gaus Width: 3.2852



B1L100S, U1-ch50, adc2

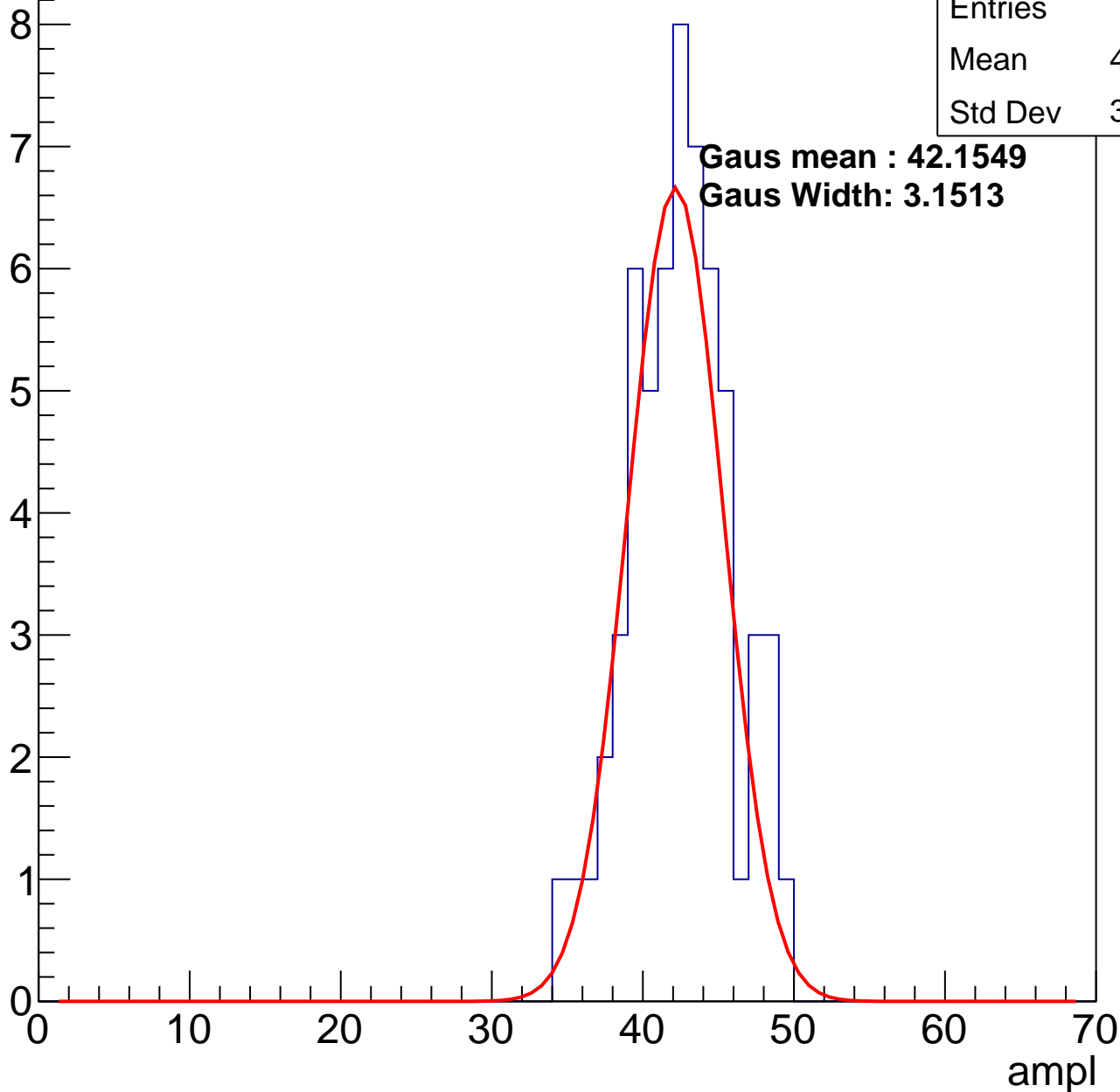
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	42.02
Std Dev	3.322

Gaus mean : 42.1549

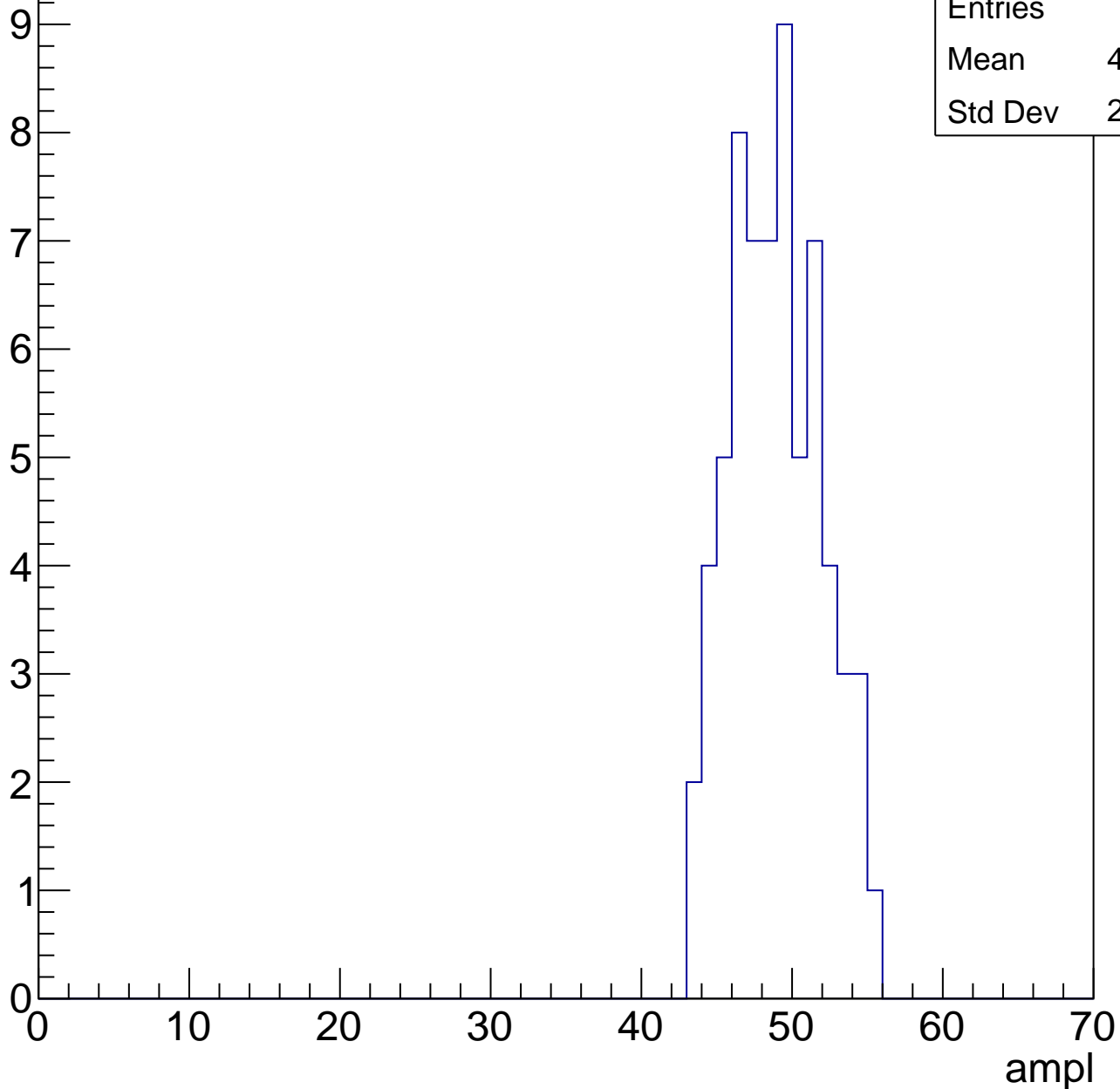
Gaus Width: 3.1513



B1L100S, U1-ch50, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

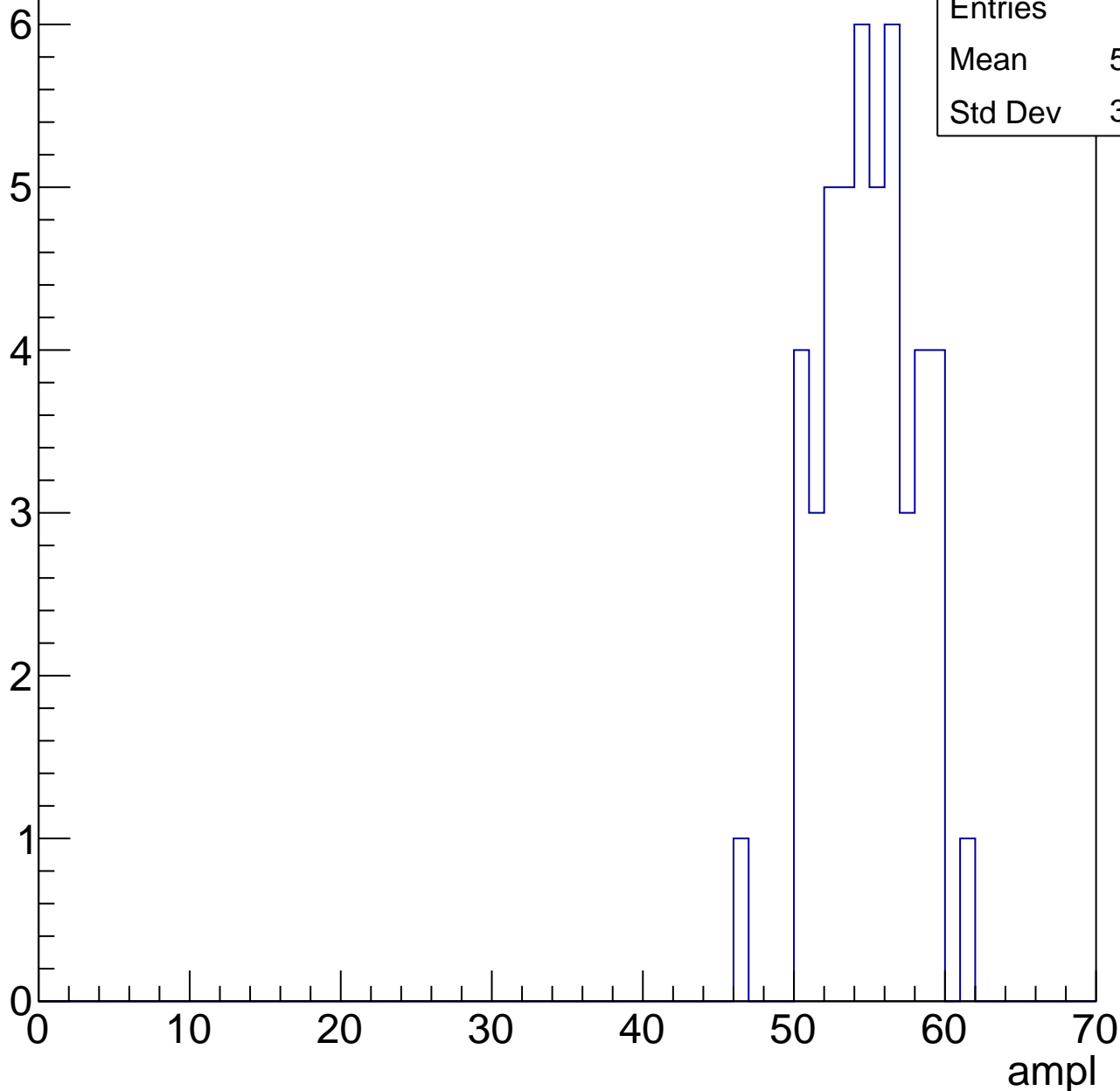


B1L100S, U1-ch50, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	54.45
Std Dev	3.058

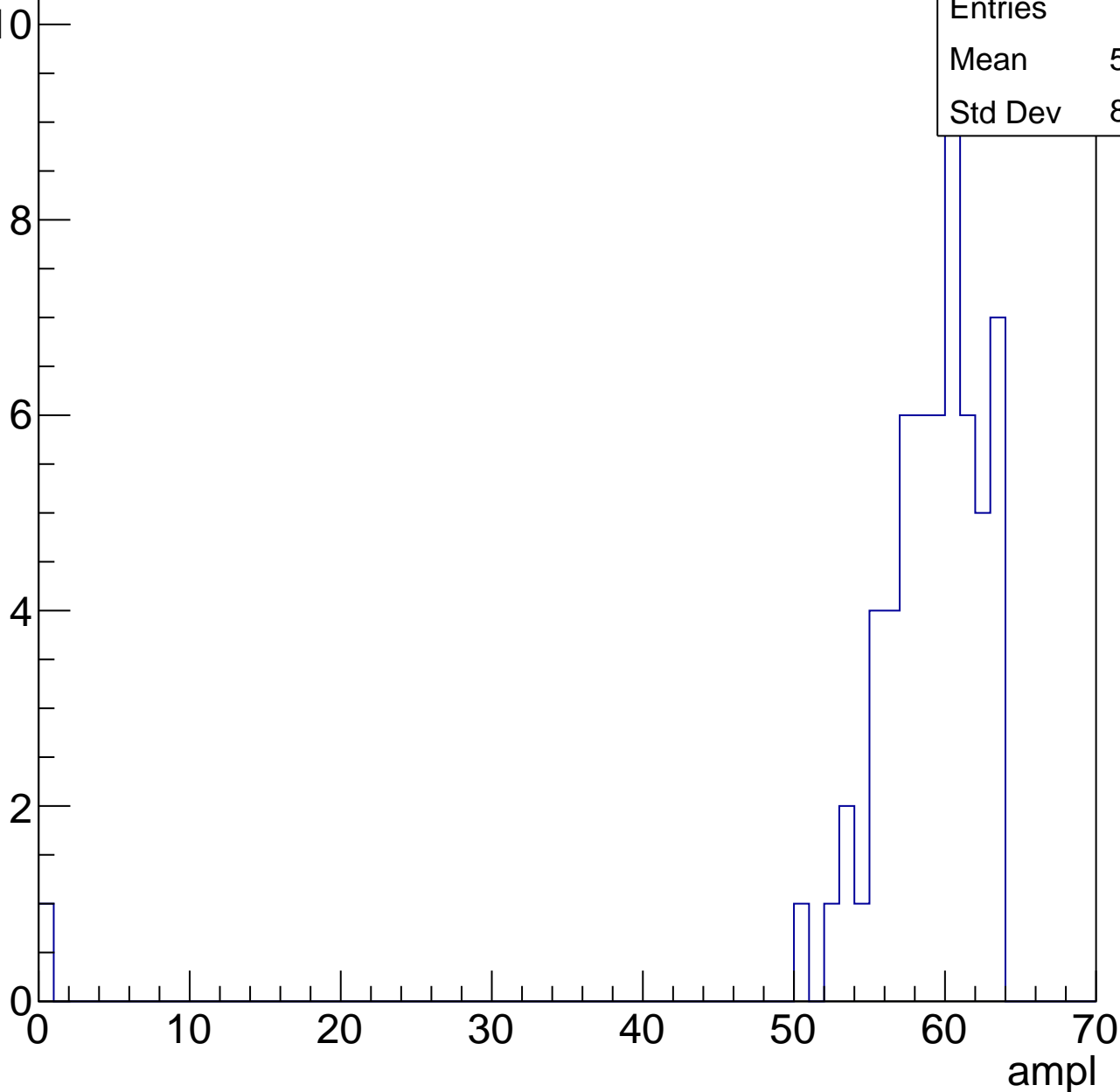


B1L100S, U1-ch50, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

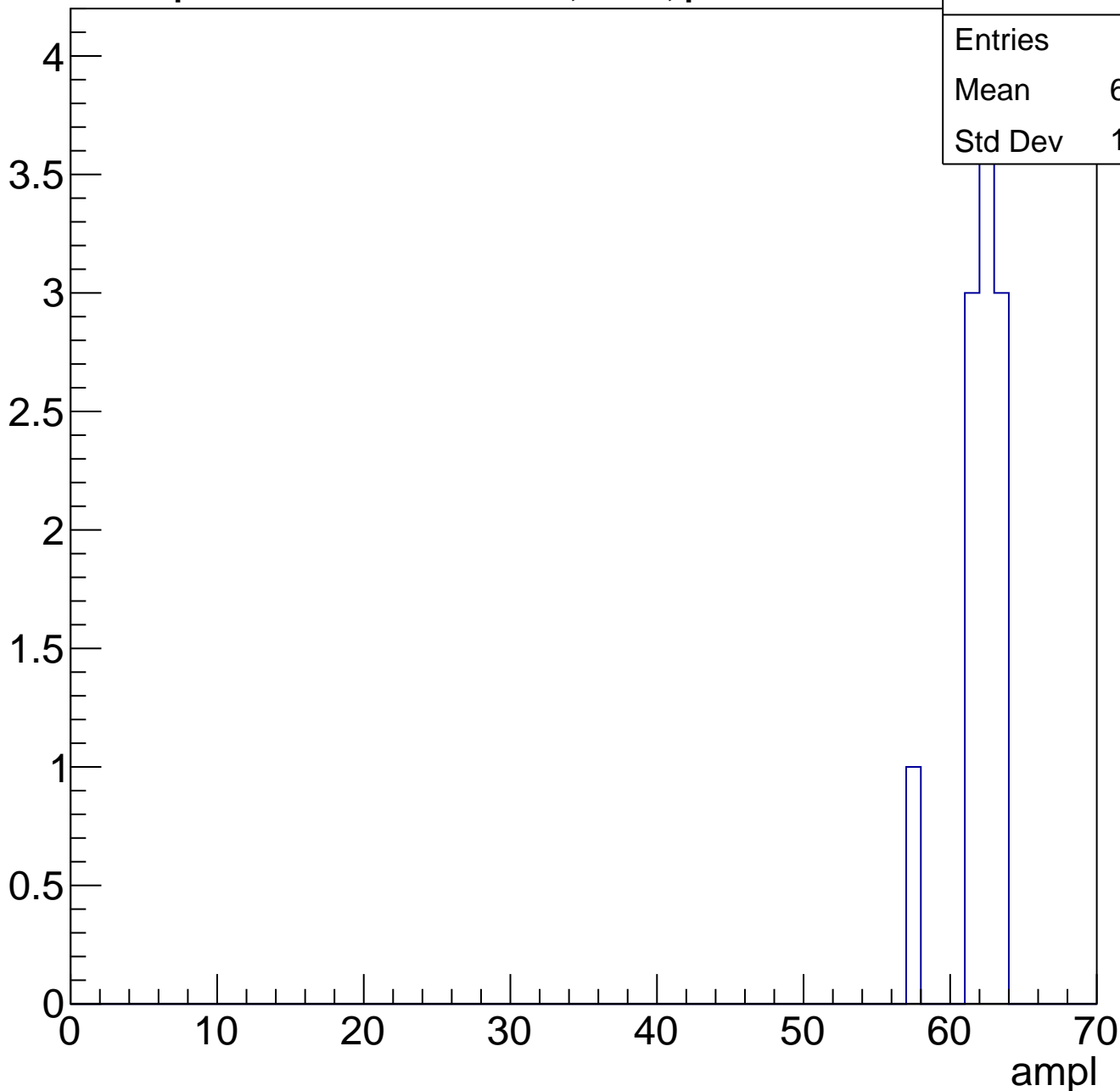
Entries	60
Mean	57.78
Std Dev	8.104



B1L100S, U1-ch50, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch50, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1

0.8

0.6

0.4

0.2

0

0

10

20

30

40

50

60

70

ampl

Entries

1

Mean

62

Std Dev

0

B1L100S, U1-ch51, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	77
Mean	28.48
Std Dev	5.106

Gaus mean : 29.0658

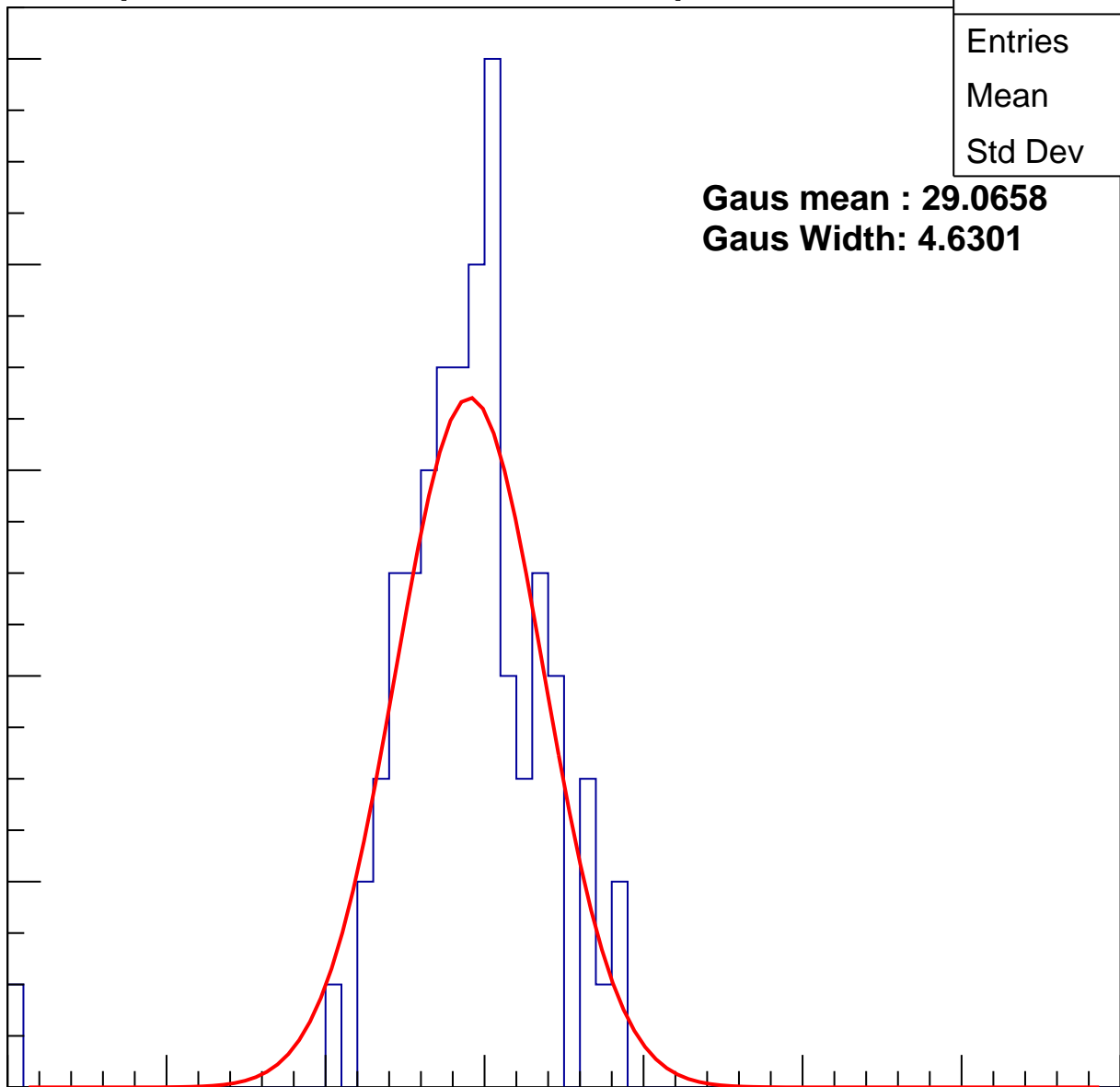
Gaus Width: 4.6301

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch51, adc1

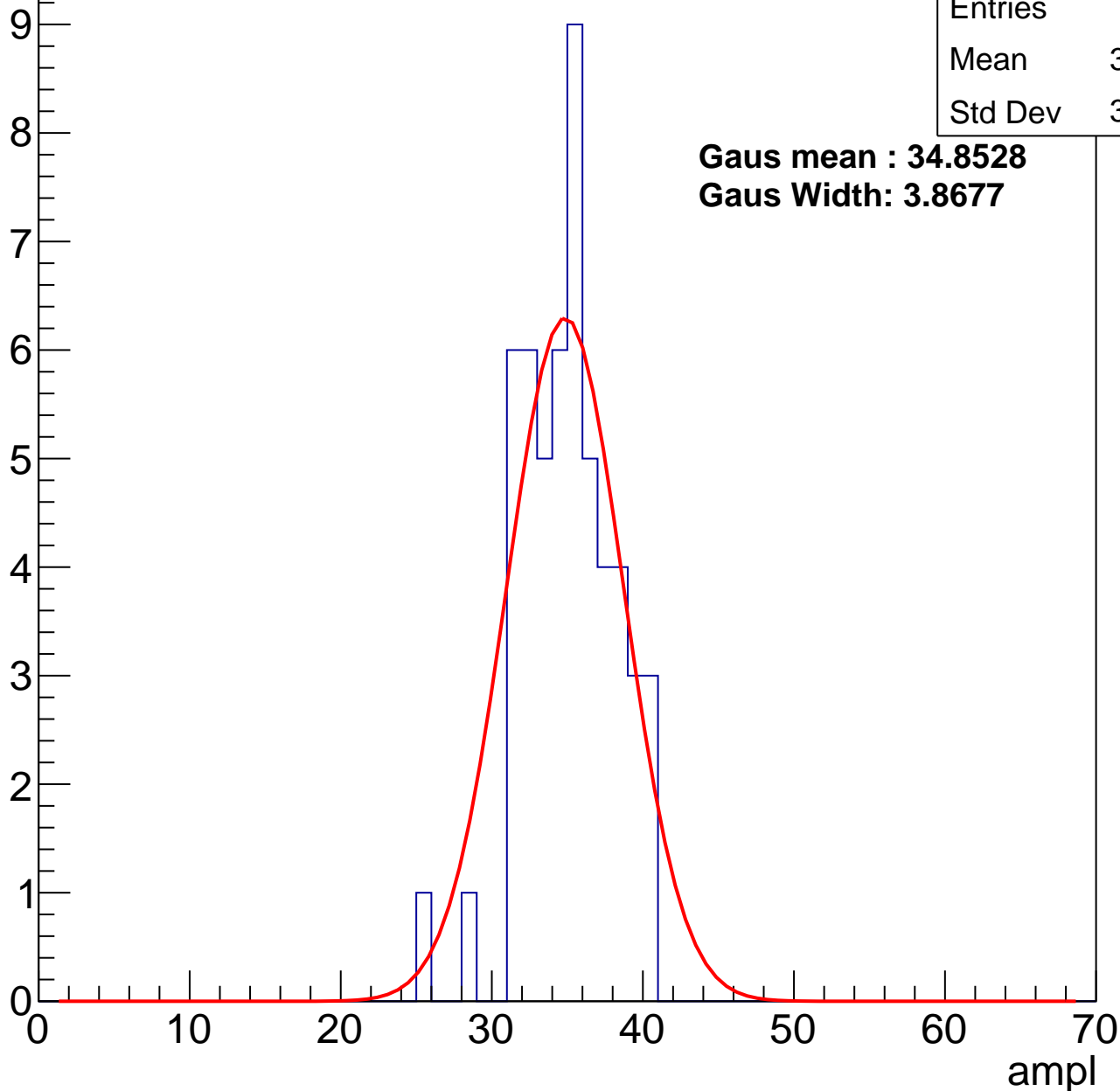
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	34.57
Std Dev	3.056

Gaus mean : 34.8528

Gaus Width: 3.8677



B1L100S, U1-ch51, adc2

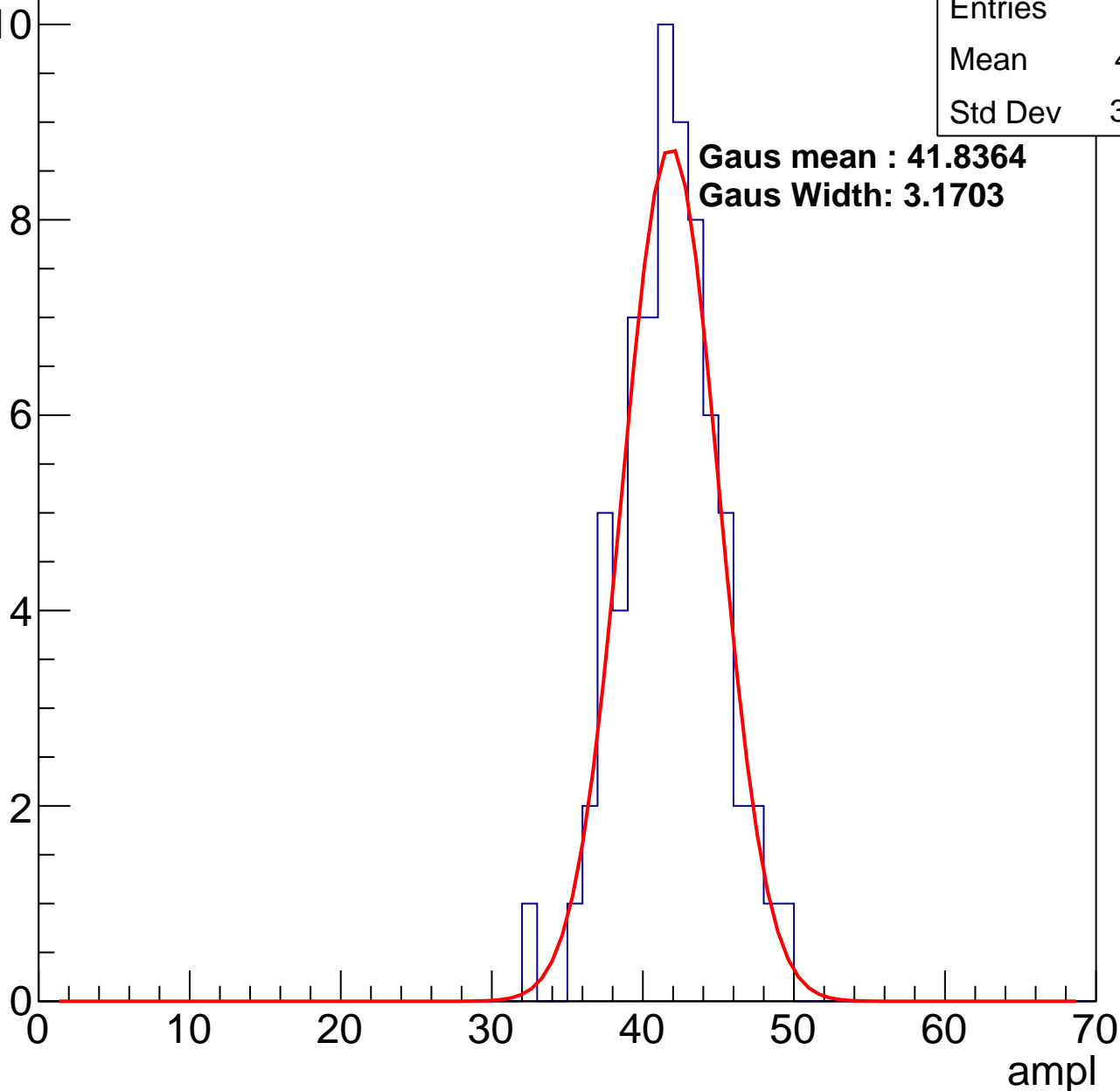
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	41.31
Std Dev	3.187

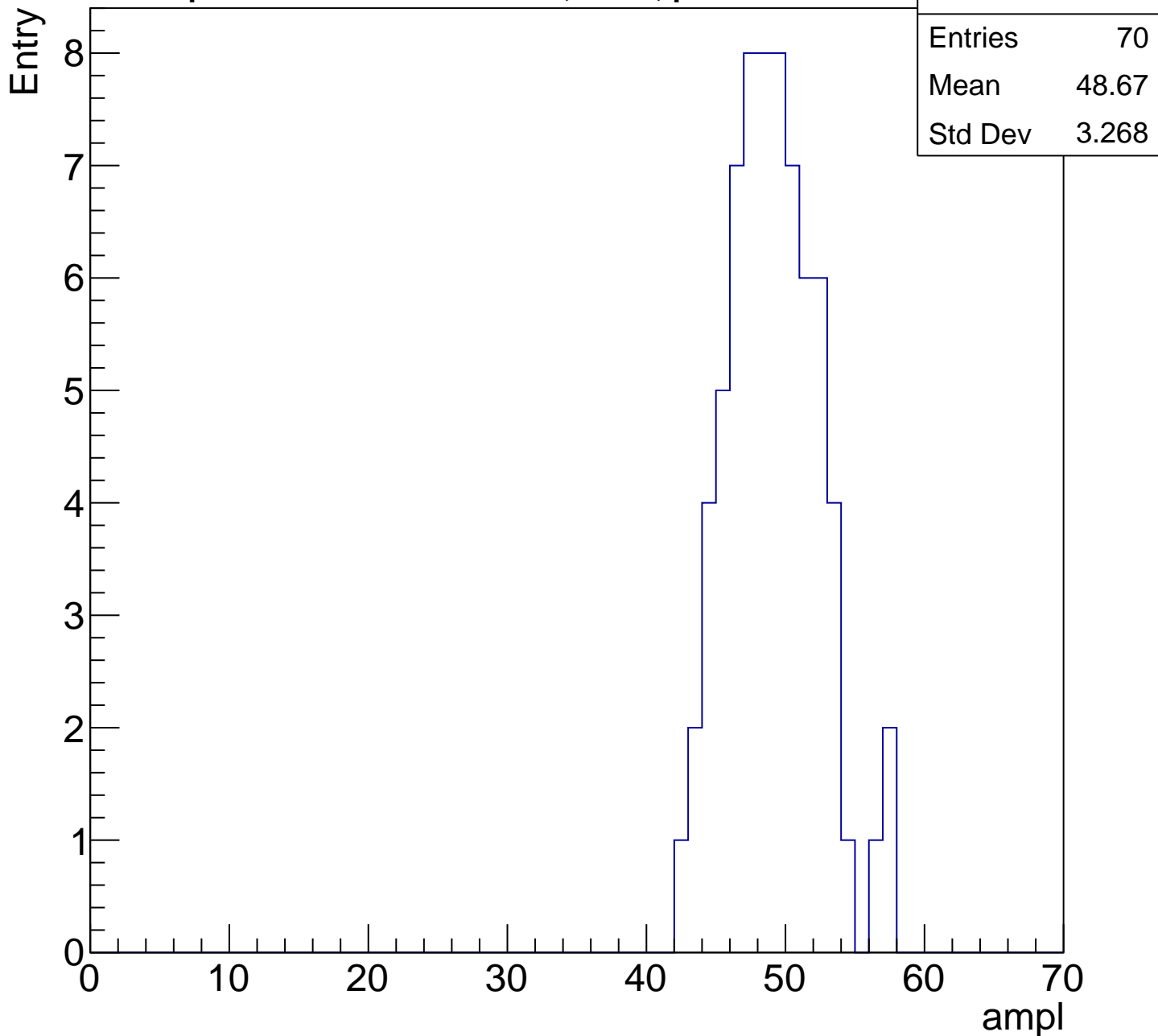
Gaus mean : 41.8364

Gaus Width: 3.1703



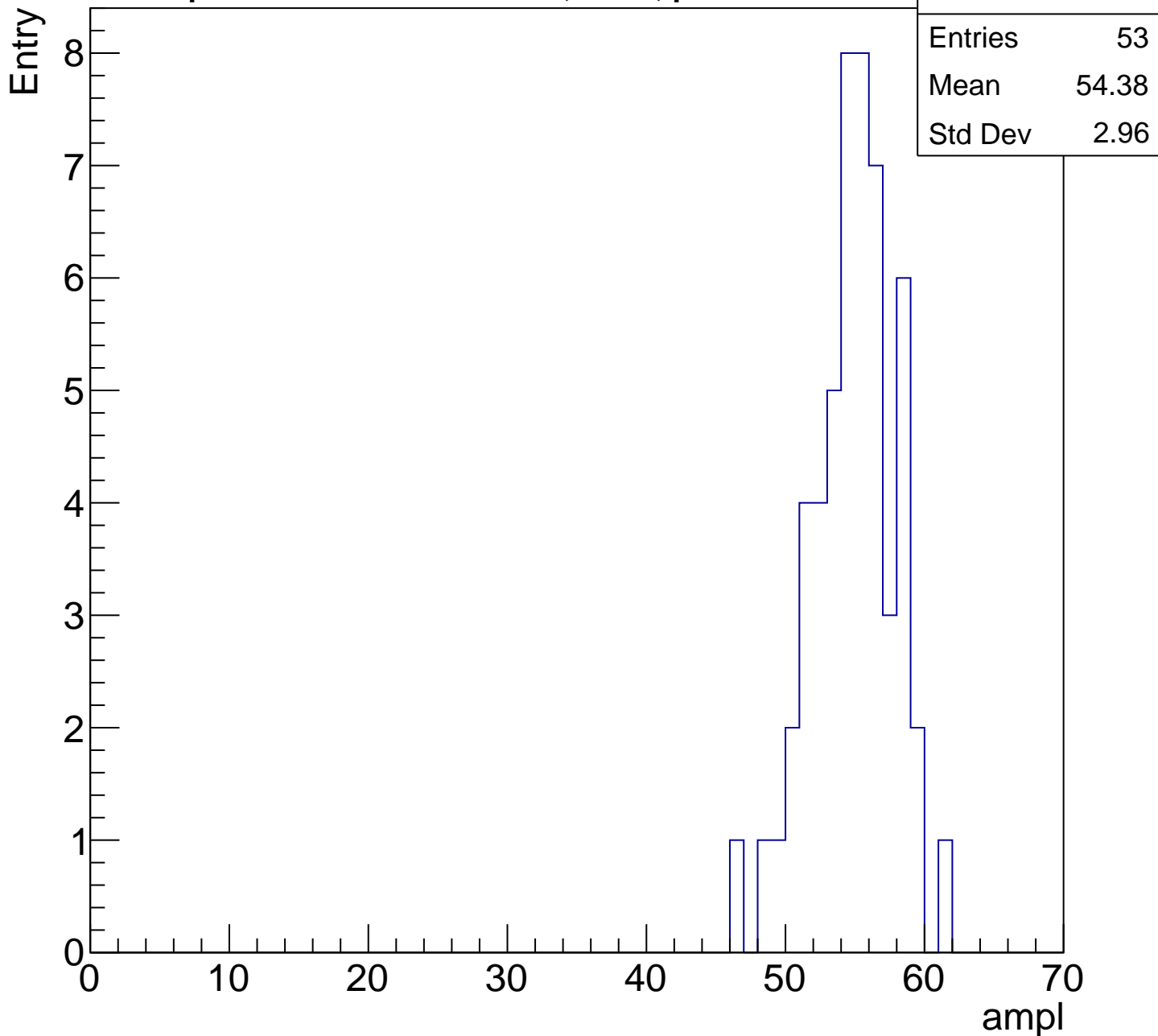
B1L100S, U1-ch51, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch51, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch51, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

8

6

4

2

0

0

10

20

30

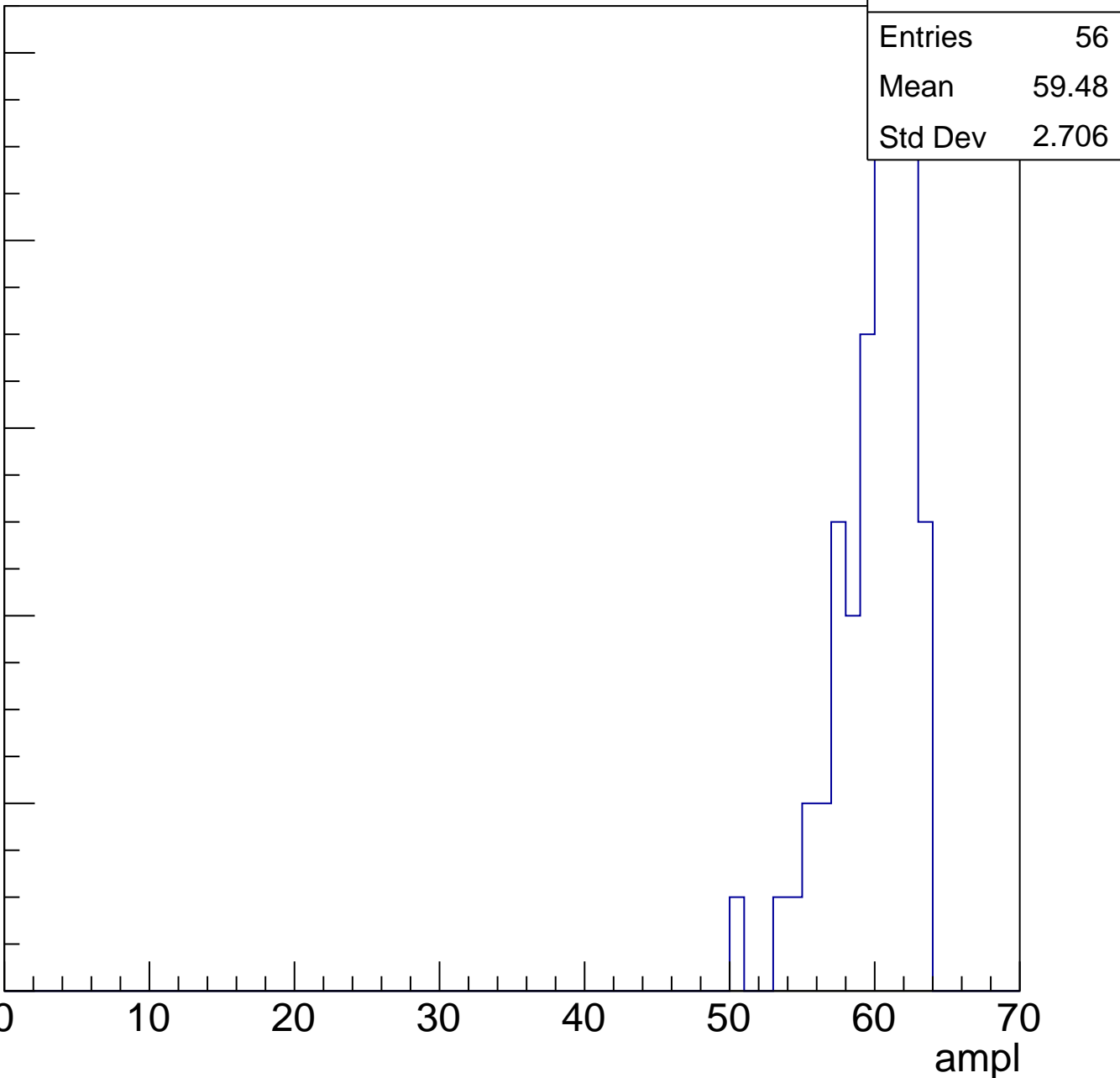
40

50

60

ampl

Entries	56
Mean	59.48
Std Dev	2.706

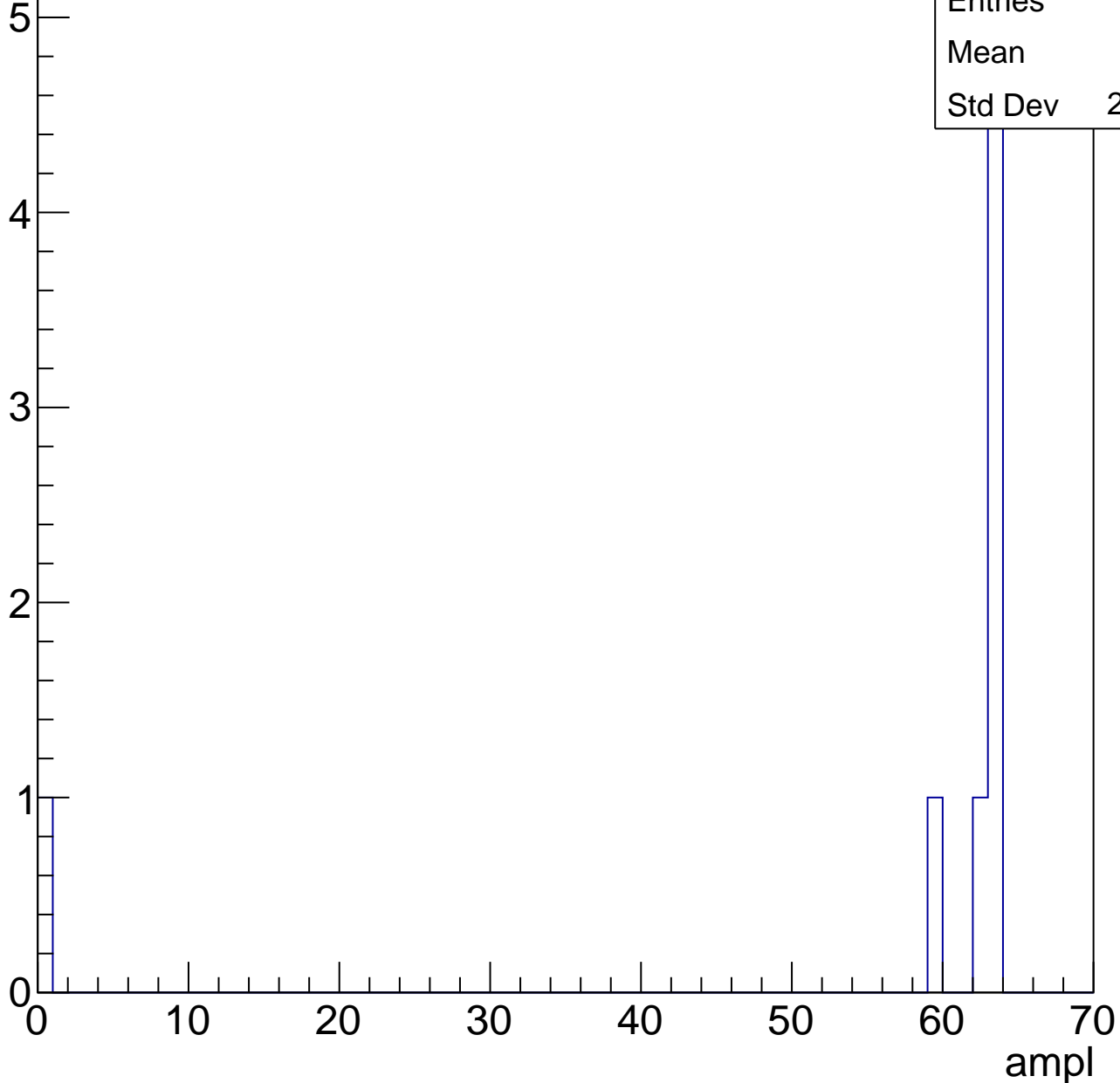


B1L100S, U1-ch51, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	8
Mean	54.5
Std Dev	20.64



B1L100S, U1-ch51, adc7

calib_packv5_042523_0143.root, FC#4, port A2

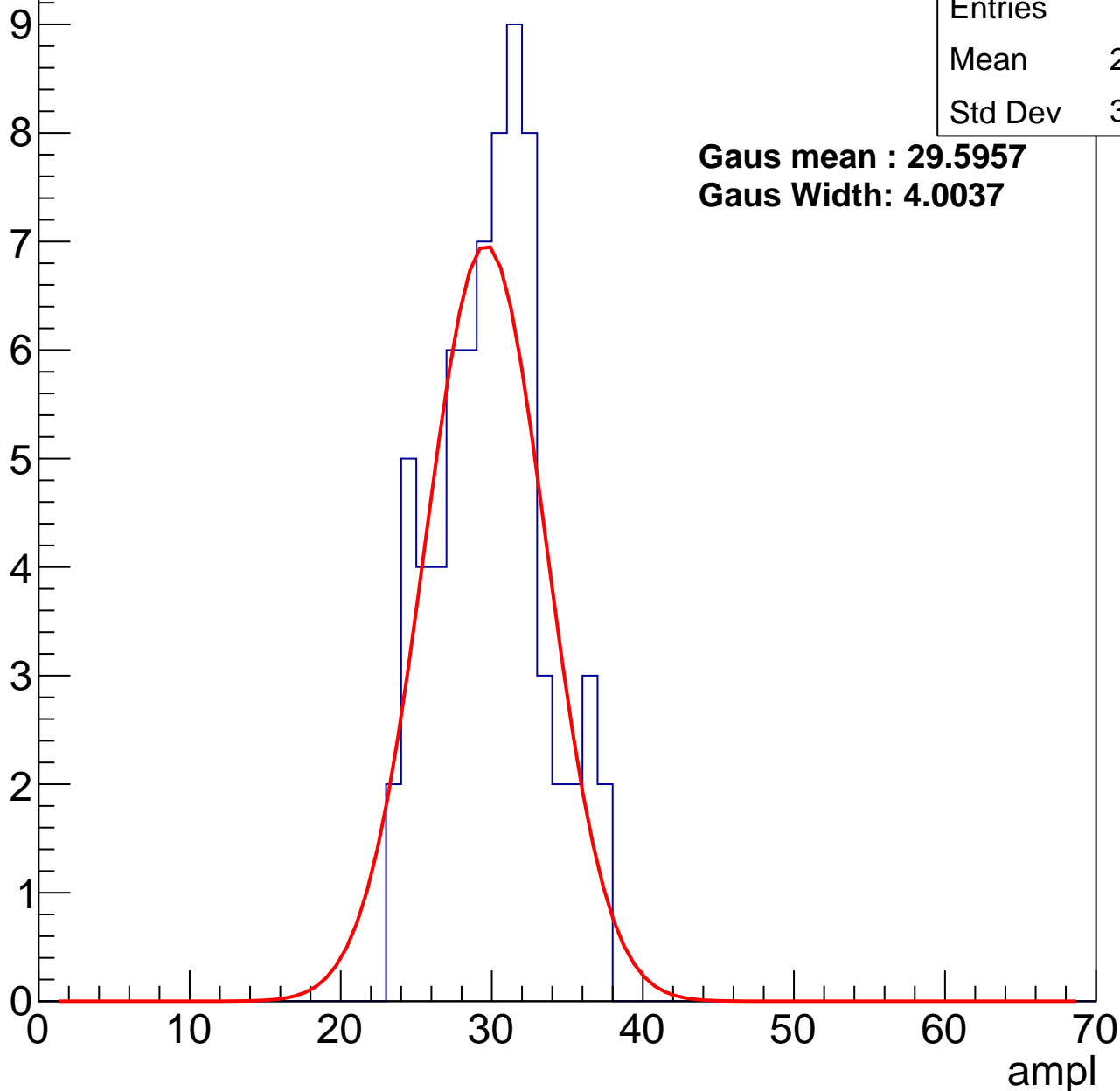
Entry



B1L100S, U1-ch52, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch52, adc1

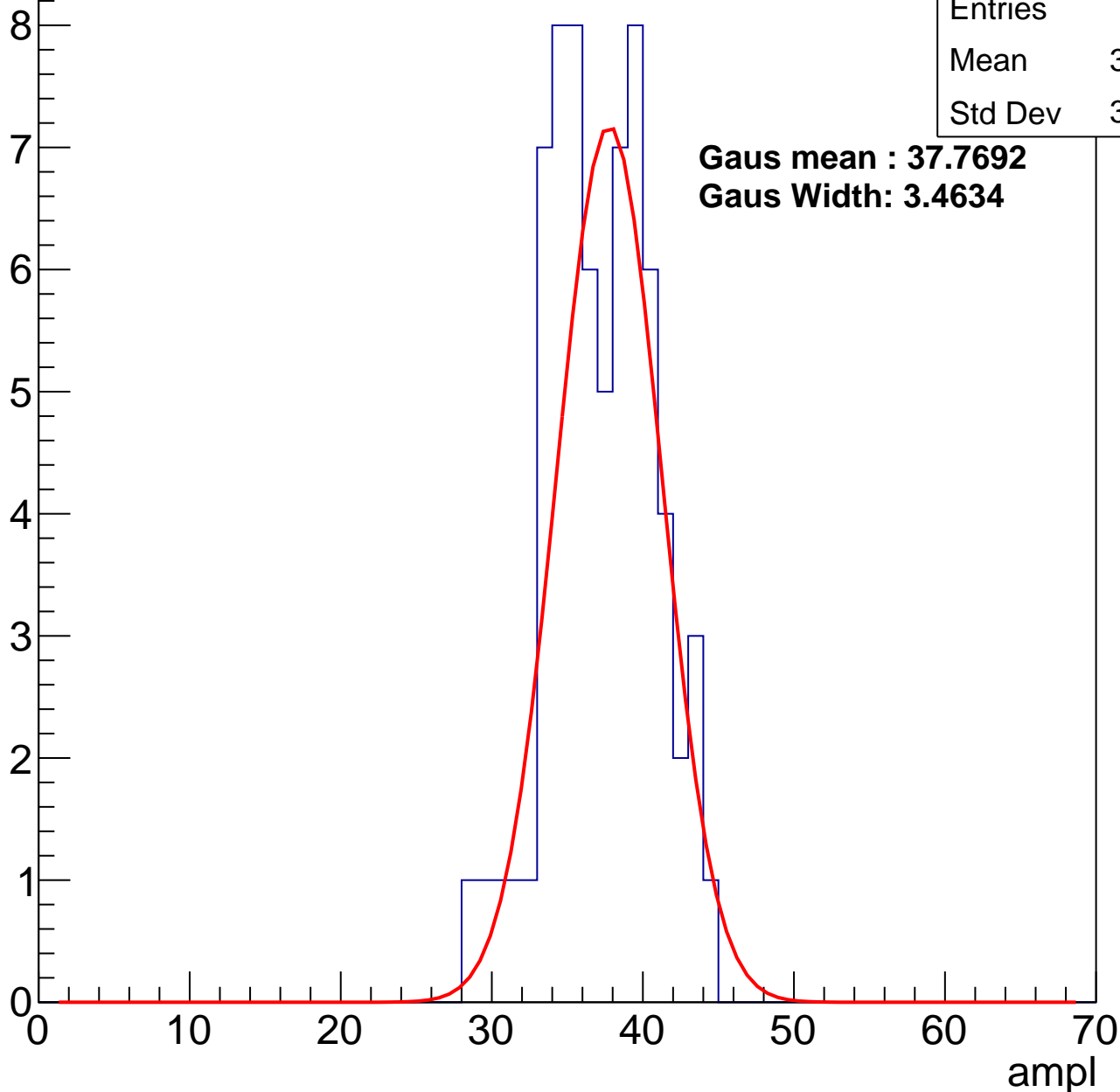
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	36.76
Std Dev	3.458

Gaus mean : 37.7692

Gaus Width: 3.4634

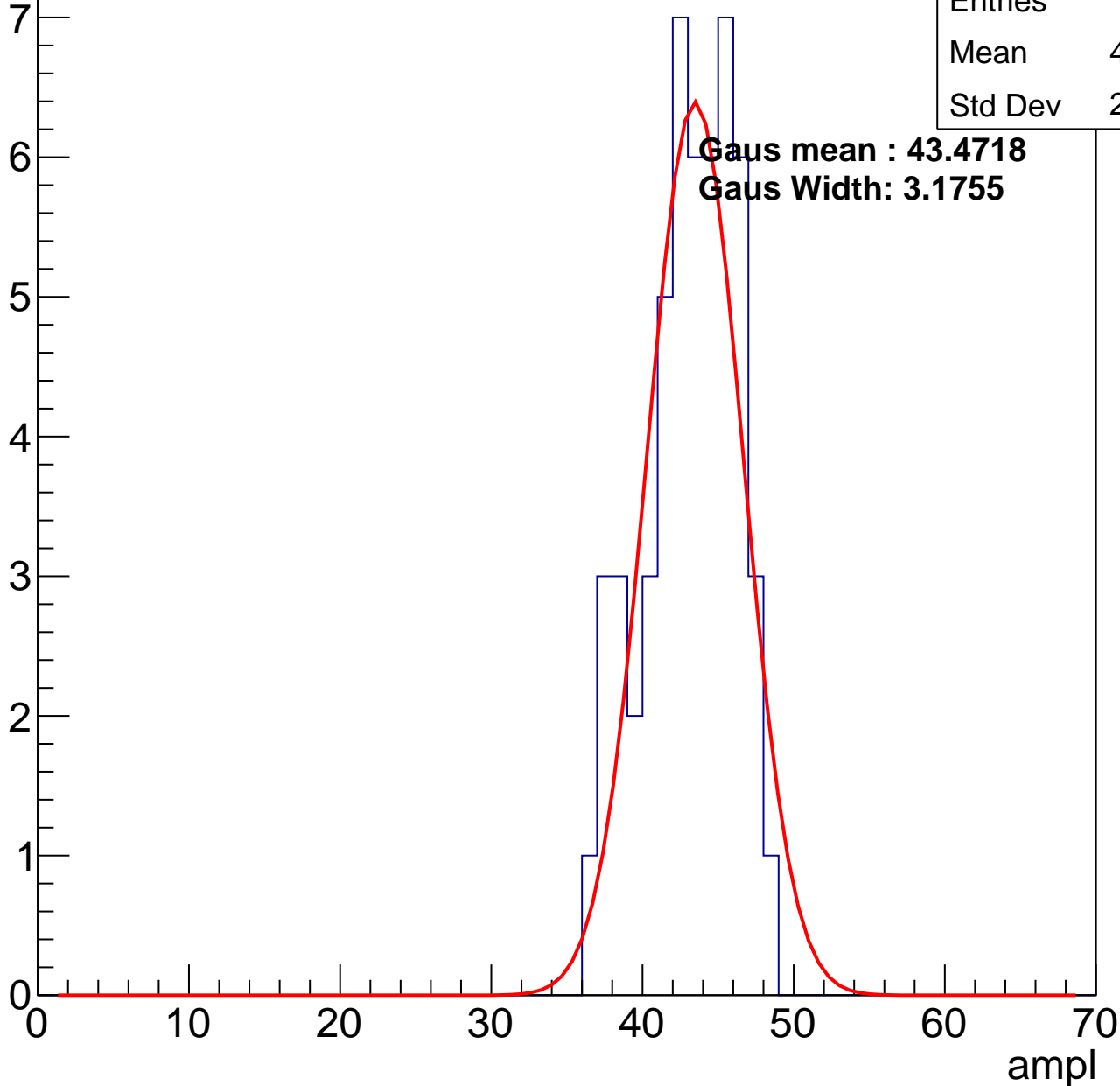


B1L100S, U1-ch52, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

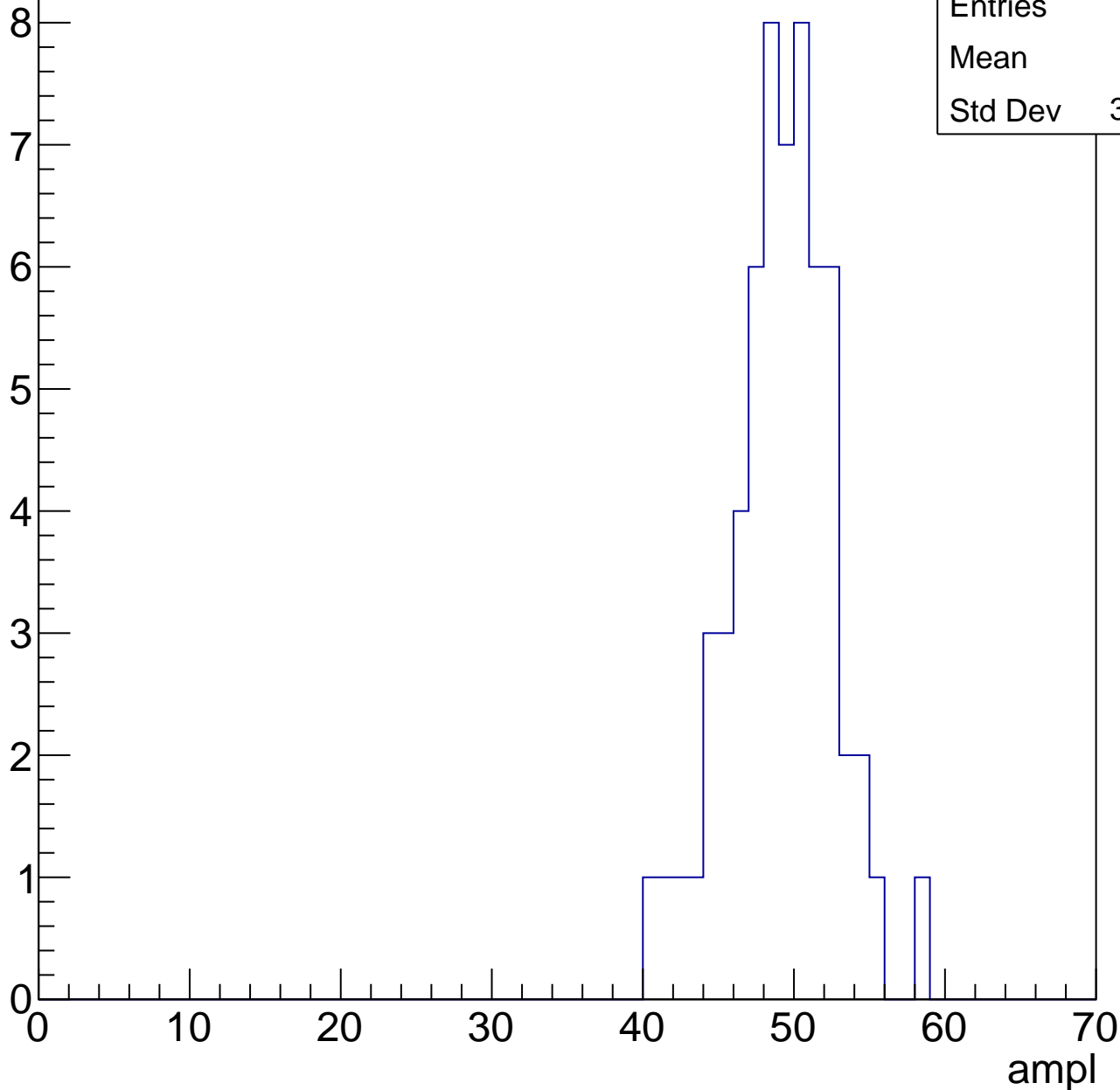
Entries	53
Mean	42.64
Std Dev	2.985



B1L100S, U1-ch52, adc3

calib_packv5_042523_0143.root, FC#4, port A2

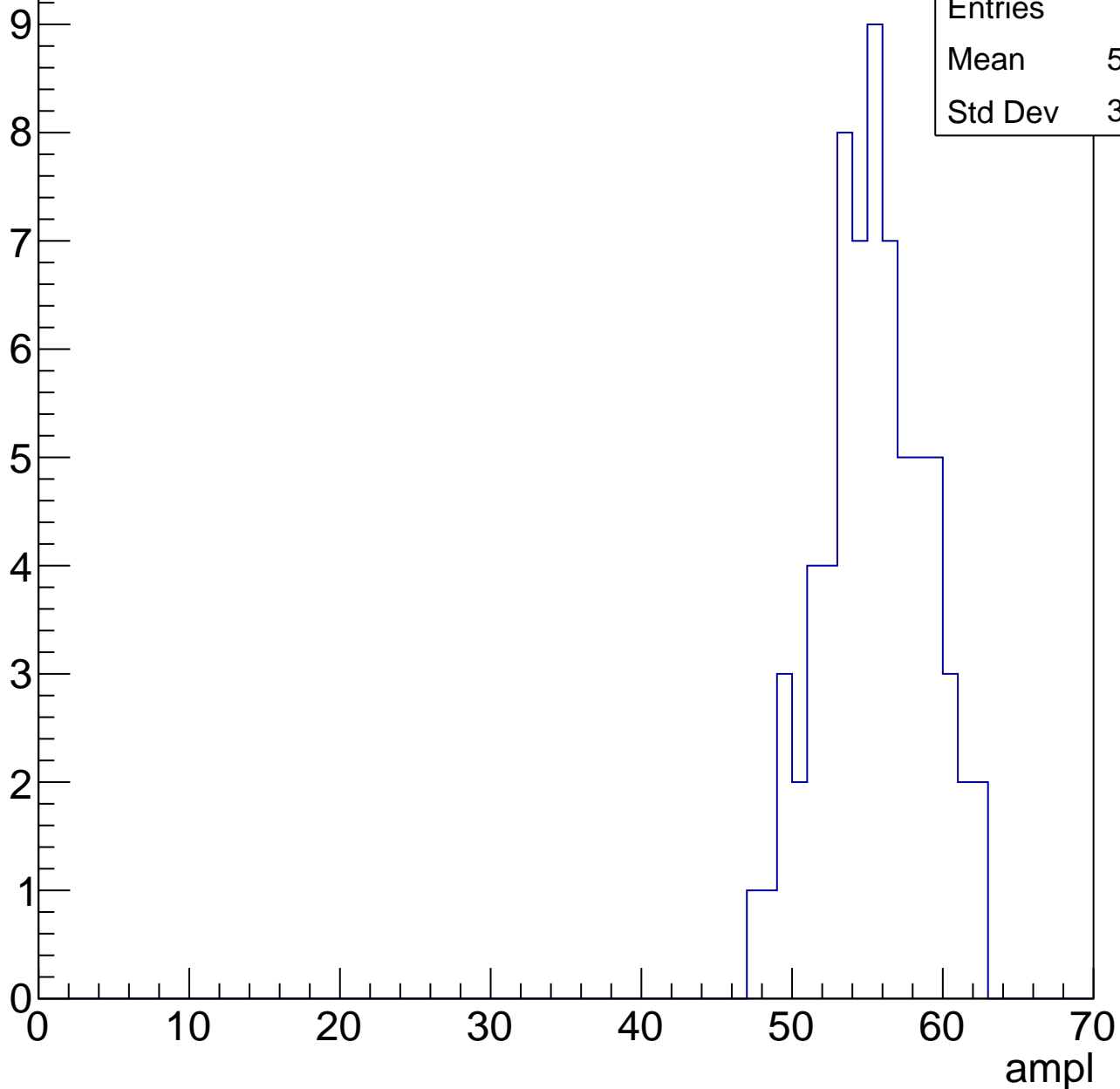
Entry



B1L100S, U1-ch52, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

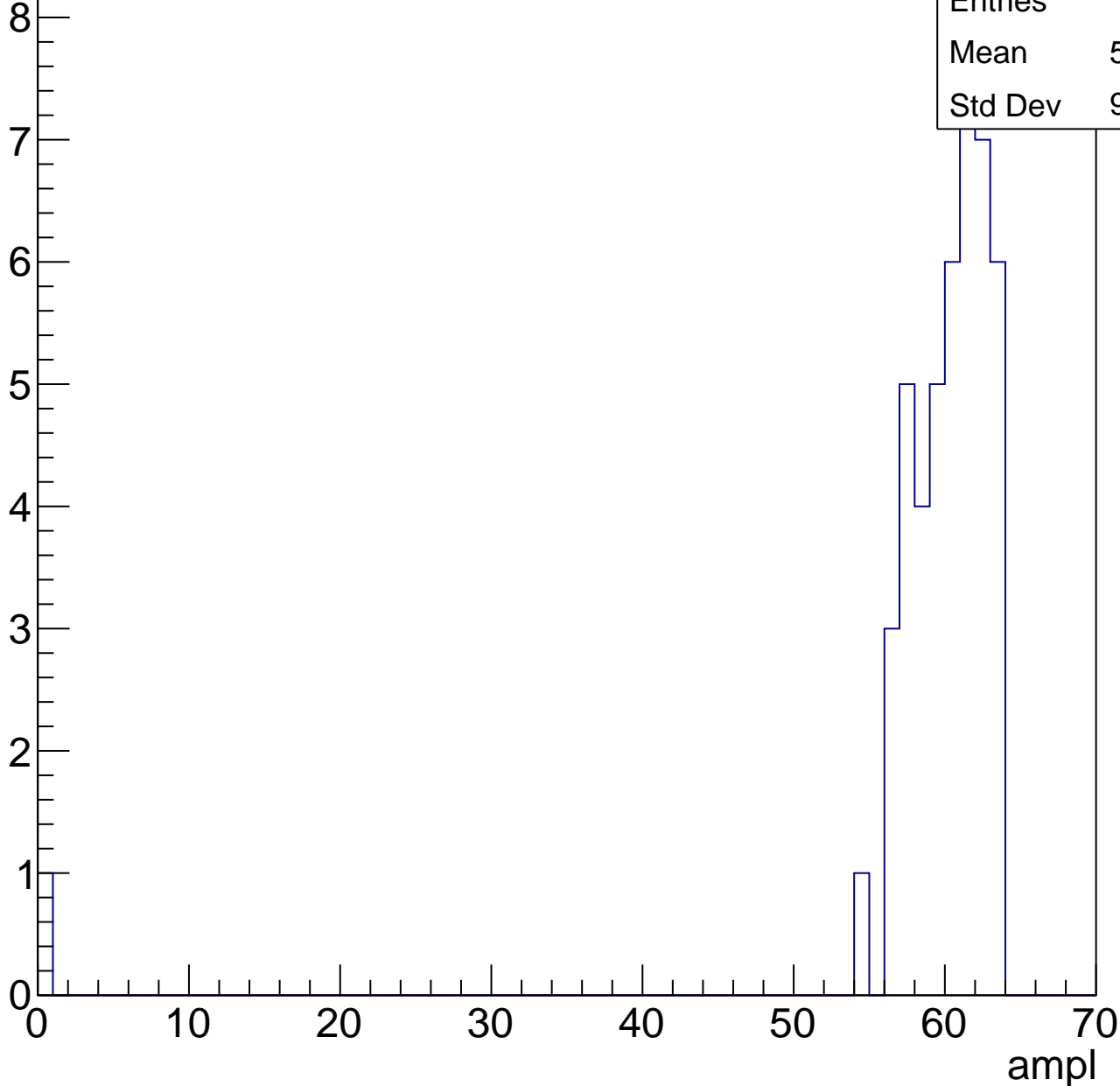


B1L100S, U1-ch52, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

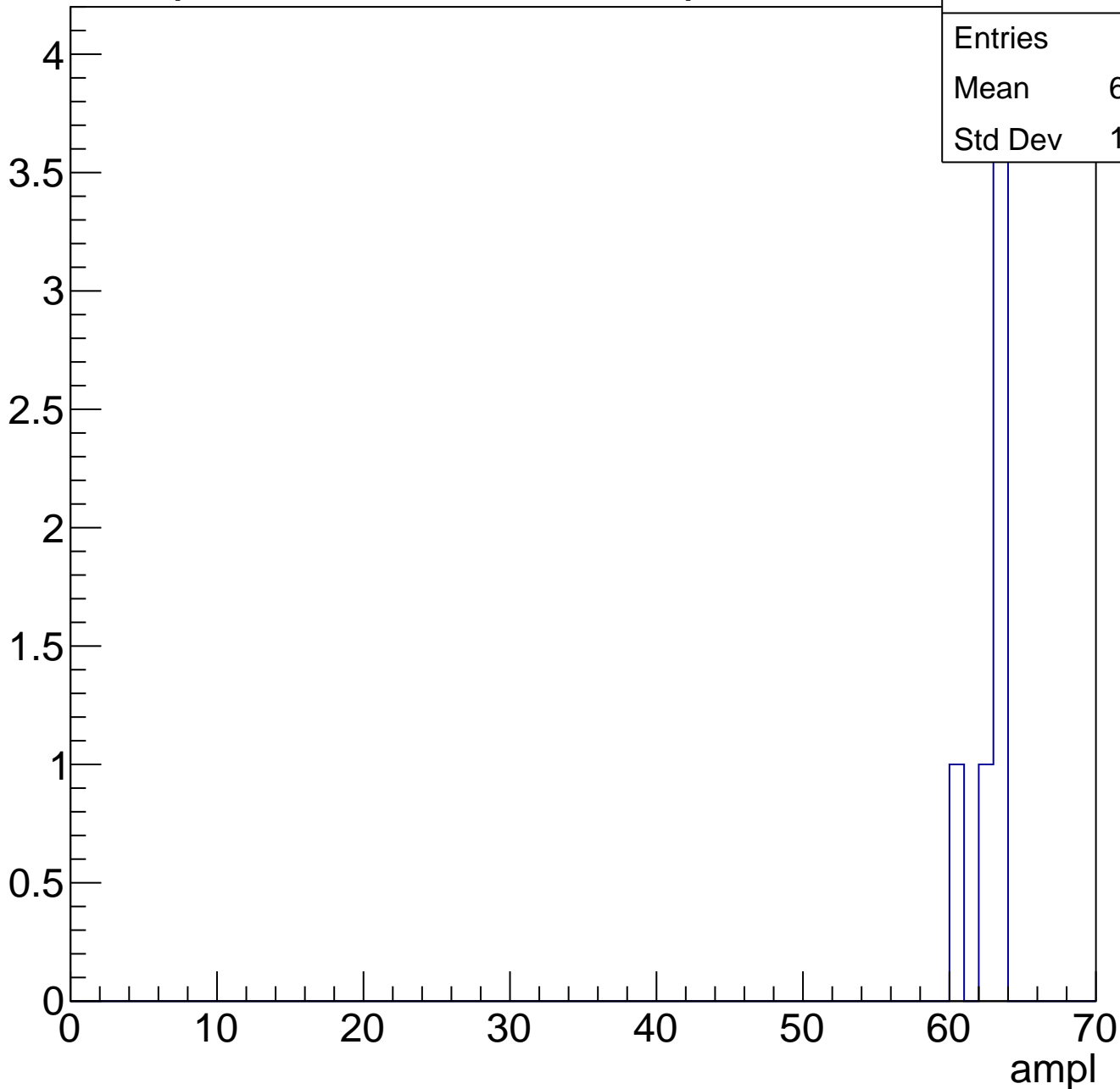
Entries	46
Mean	58.57
Std Dev	9.023



B1L100S, U1-ch52, adc6

calib_packv5_042523_0143.root, FC#4, port A2

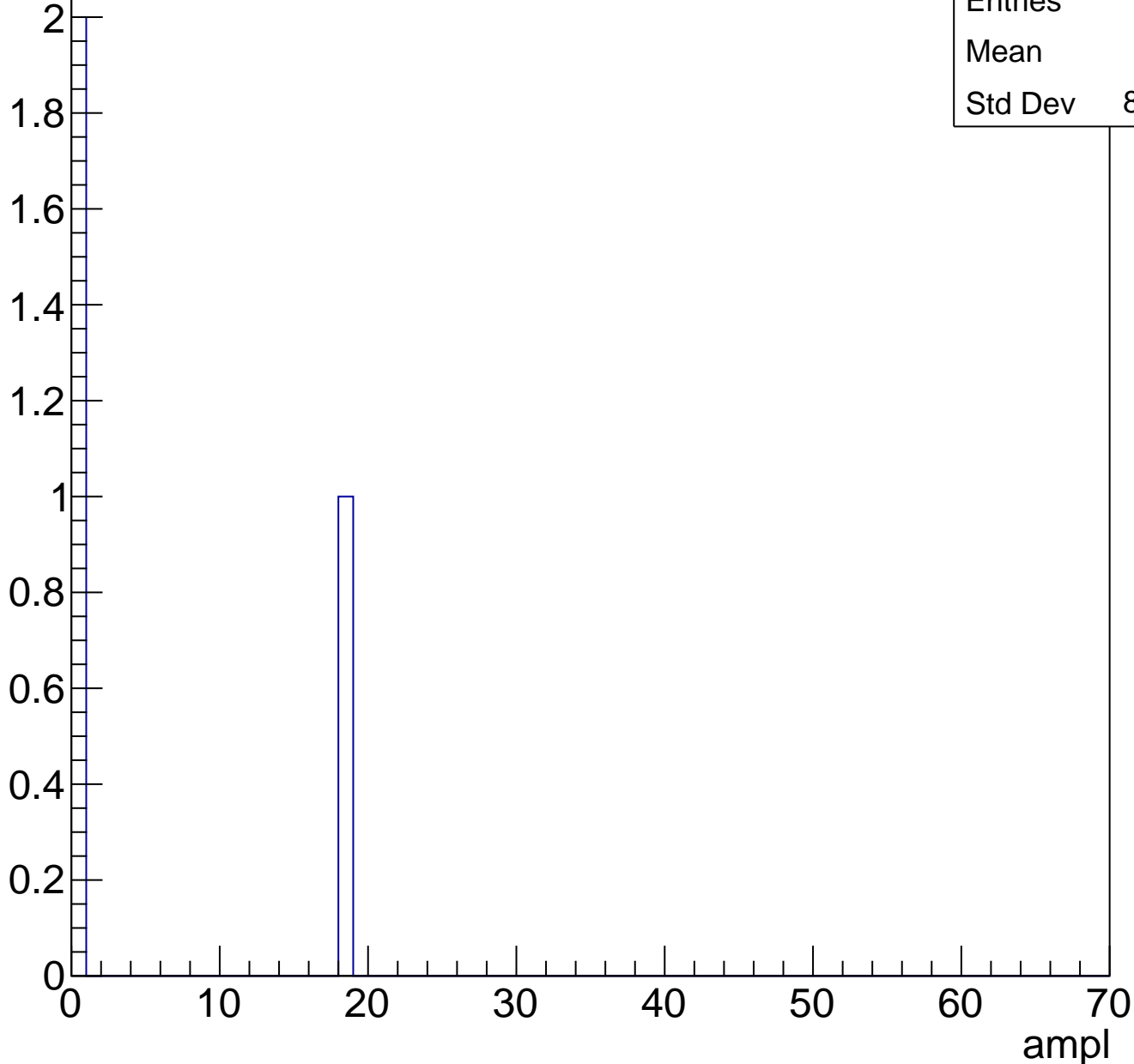
Entry



B1L100S, U1-ch52, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	6
Std Dev	8.485

B1L100S, U1-ch53, adc0

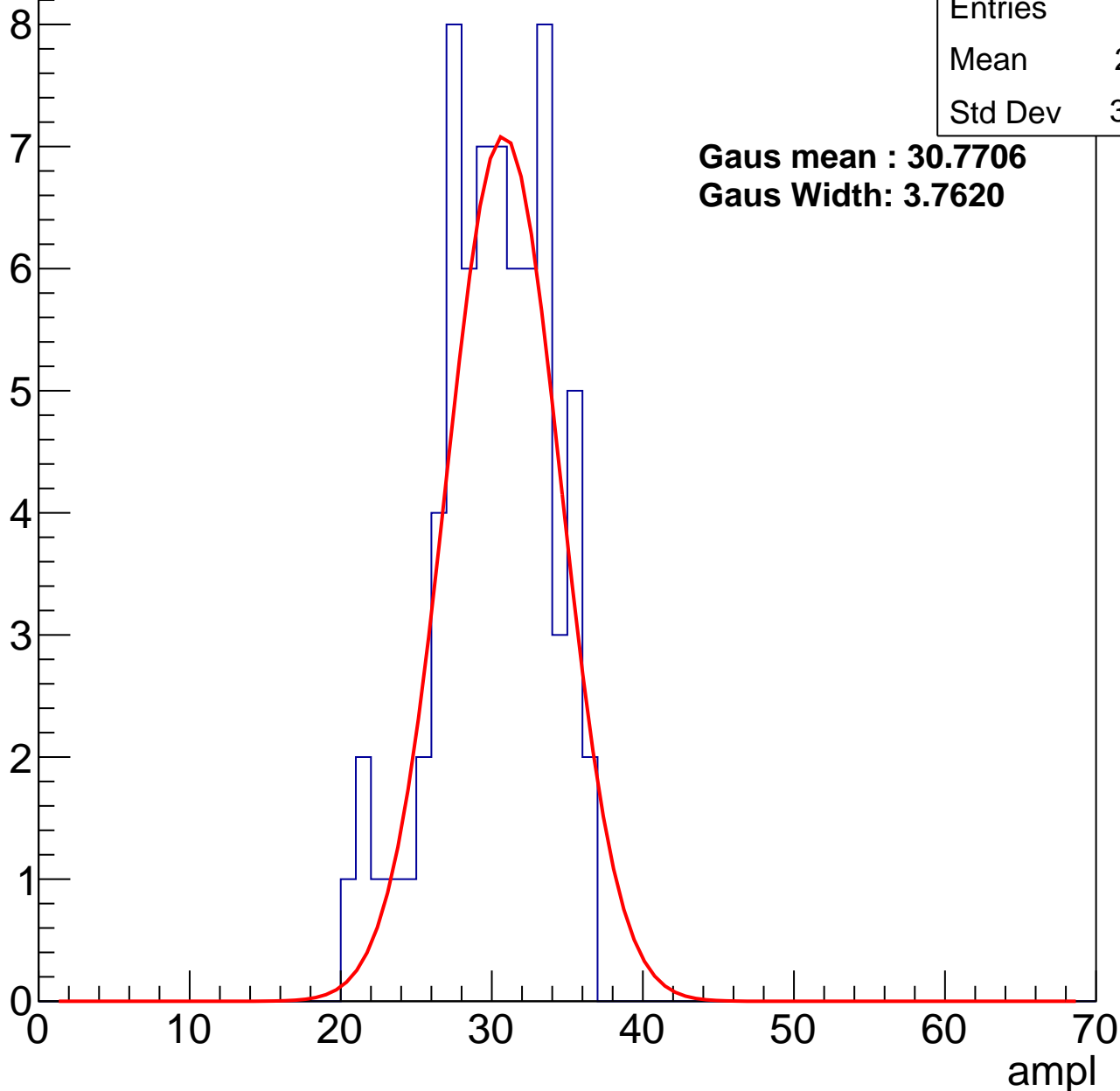
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	29.61
Std Dev	3.712

Gaus mean : 30.7706

Gaus Width: 3.7620



B1L100S, U1-ch53, adc1

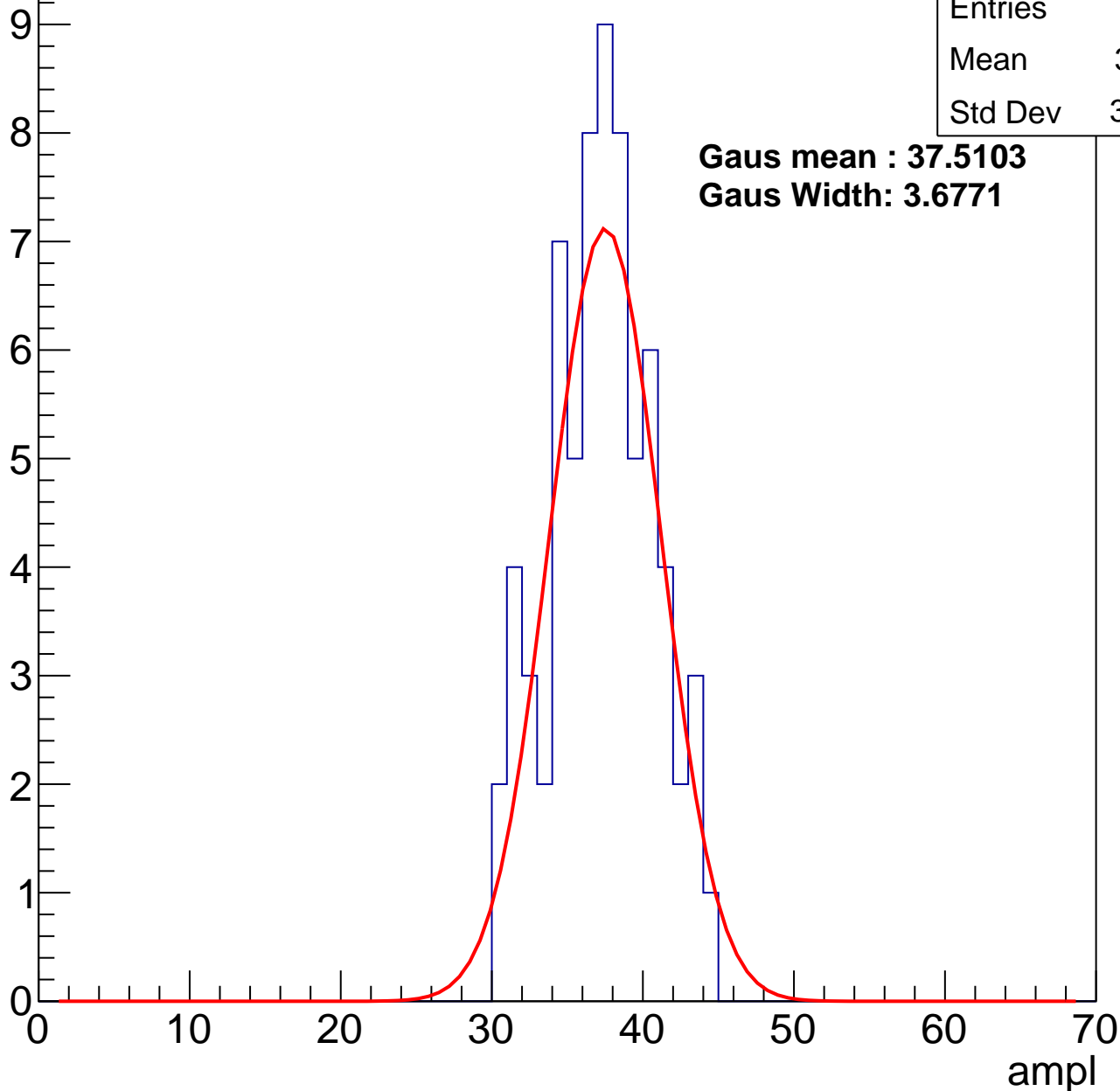
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	36.81
Std Dev	3.385

Gaus mean : 37.5103

Gaus Width: 3.6771



B1L100S, U1-ch53, adc2

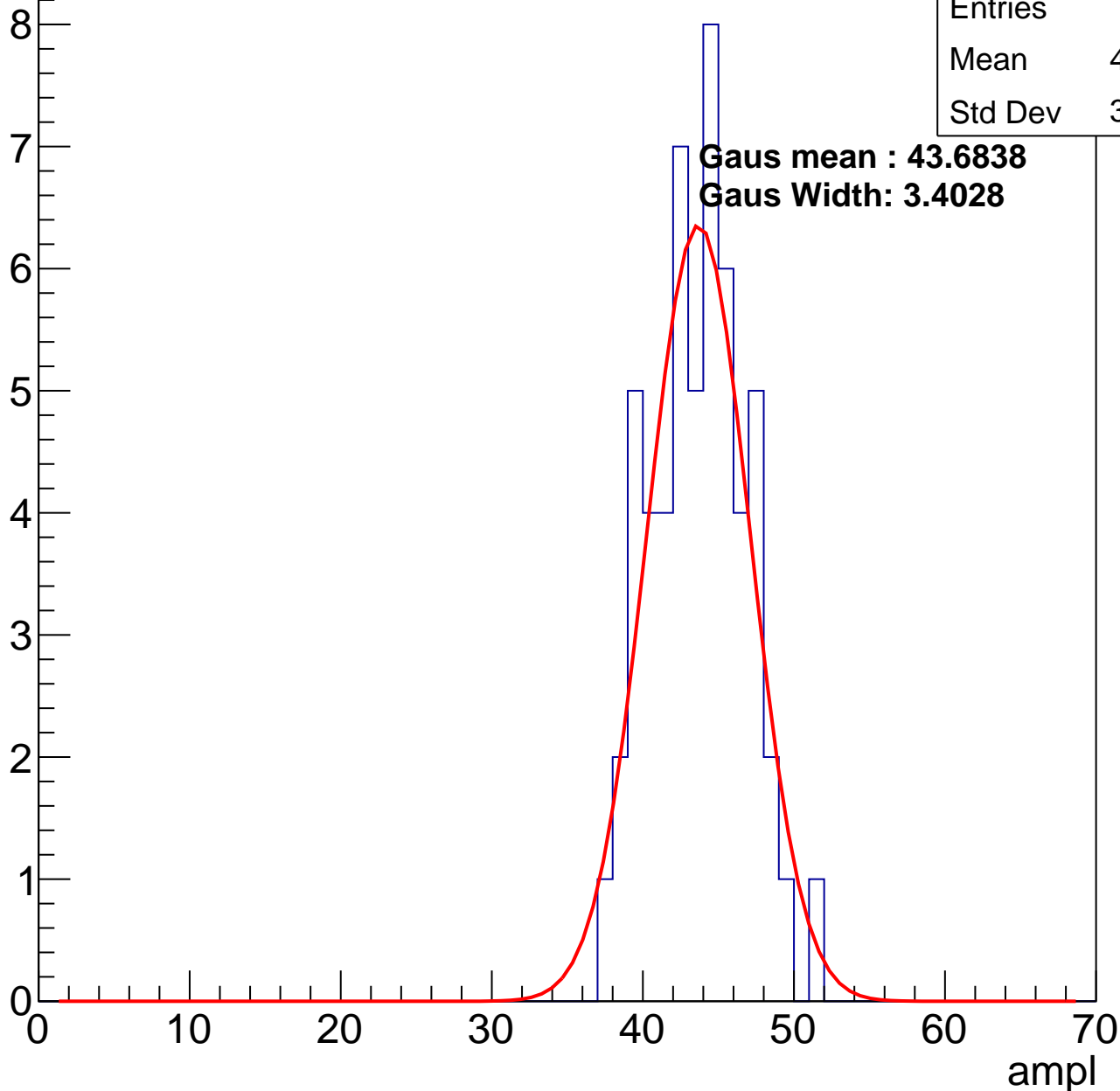
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.24
Std Dev	3.075

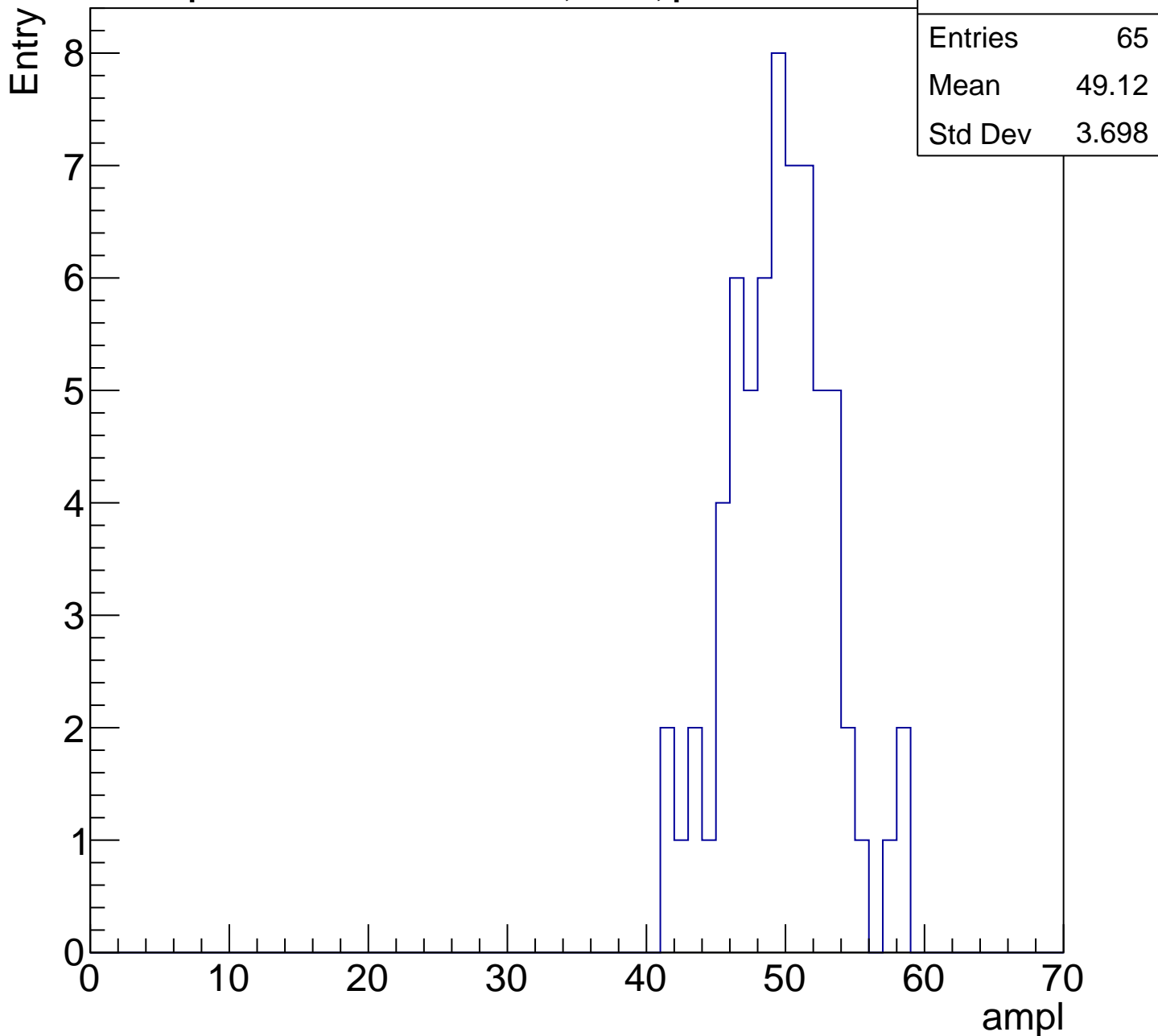
Gaus mean : 43.6838

Gaus Width: 3.4028



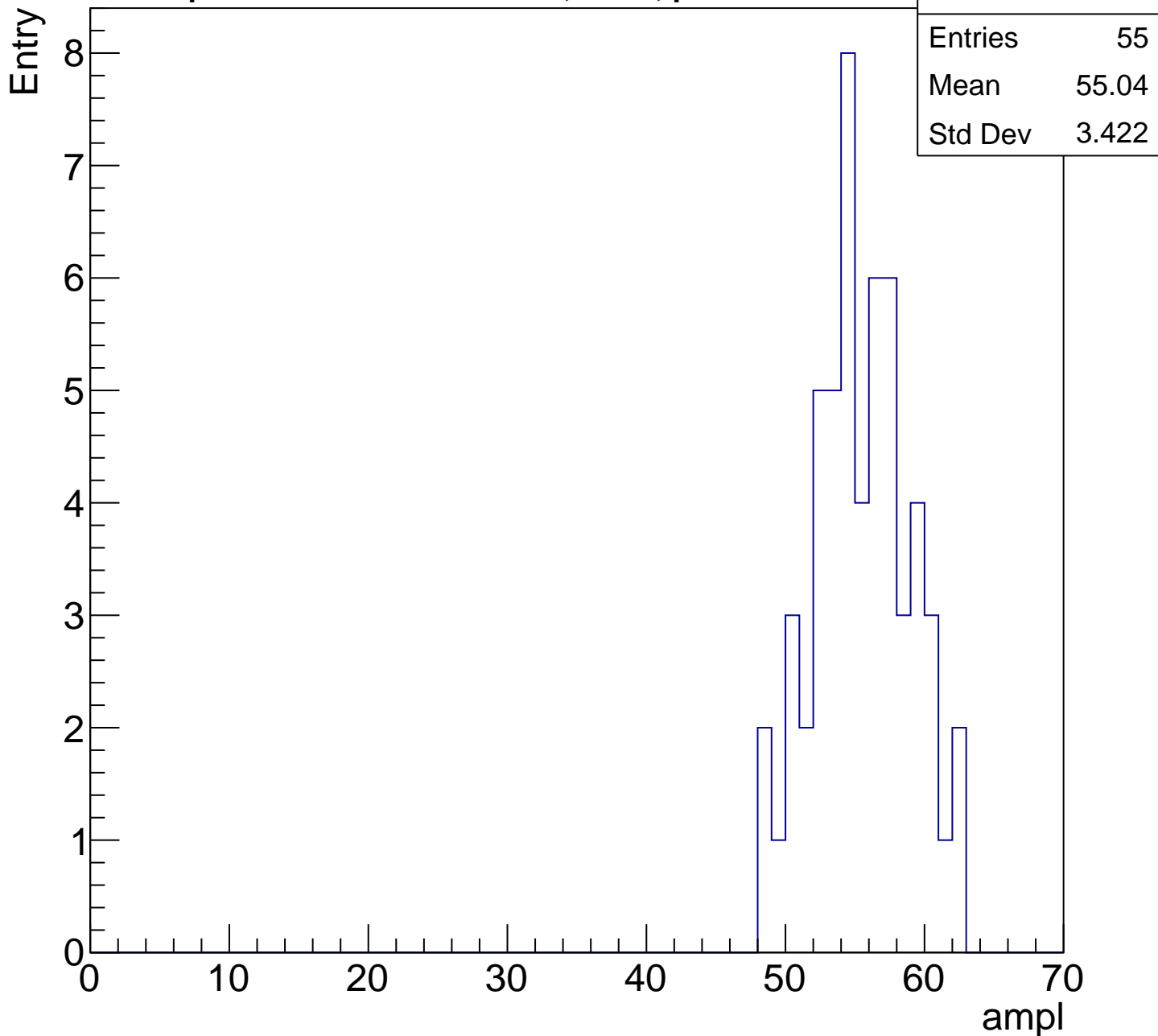
B1L100S, U1-ch53, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch53, adc4

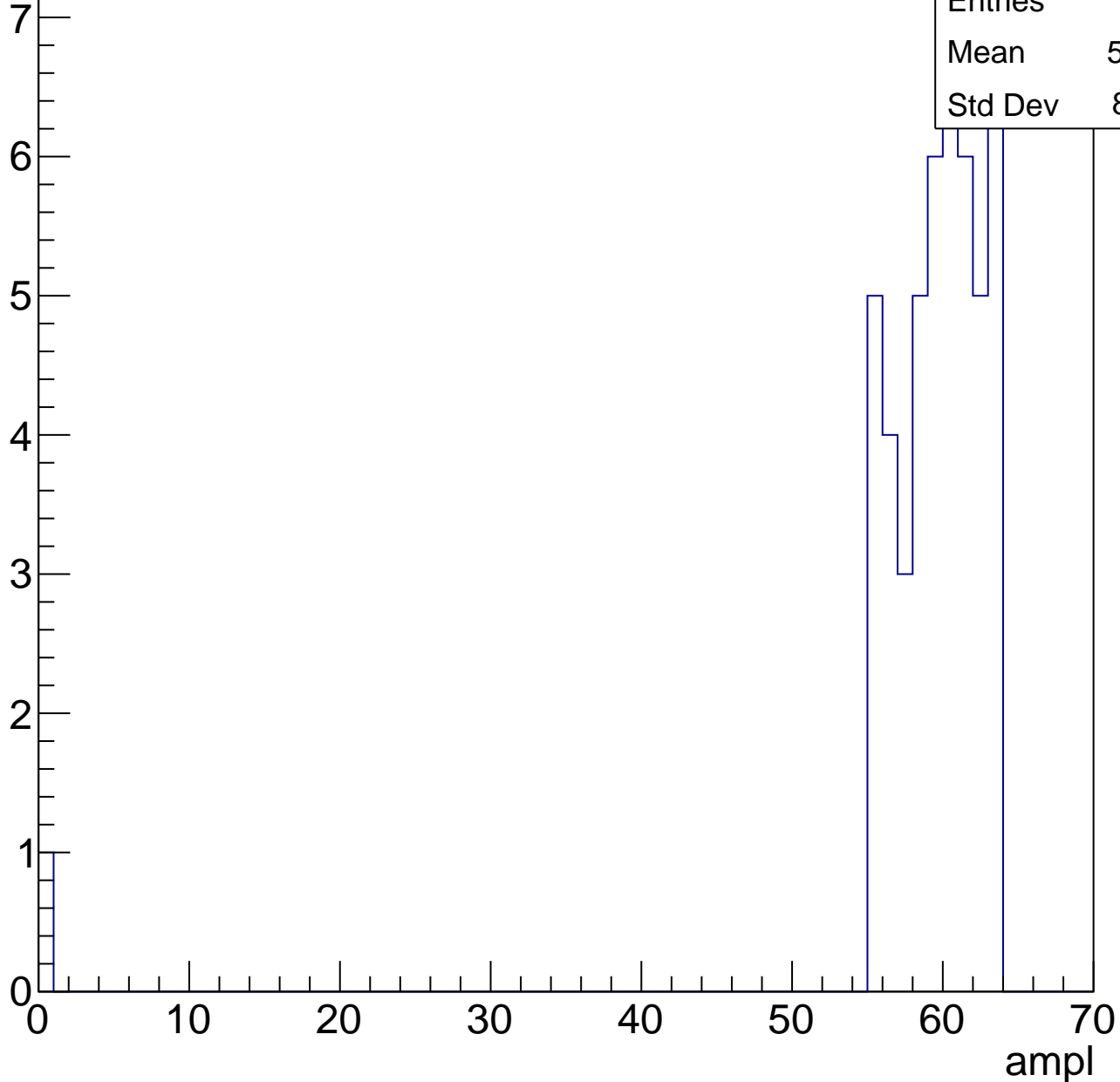
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch53, adc5

calib_packv5_042523_0143.root, FC#4, port A2

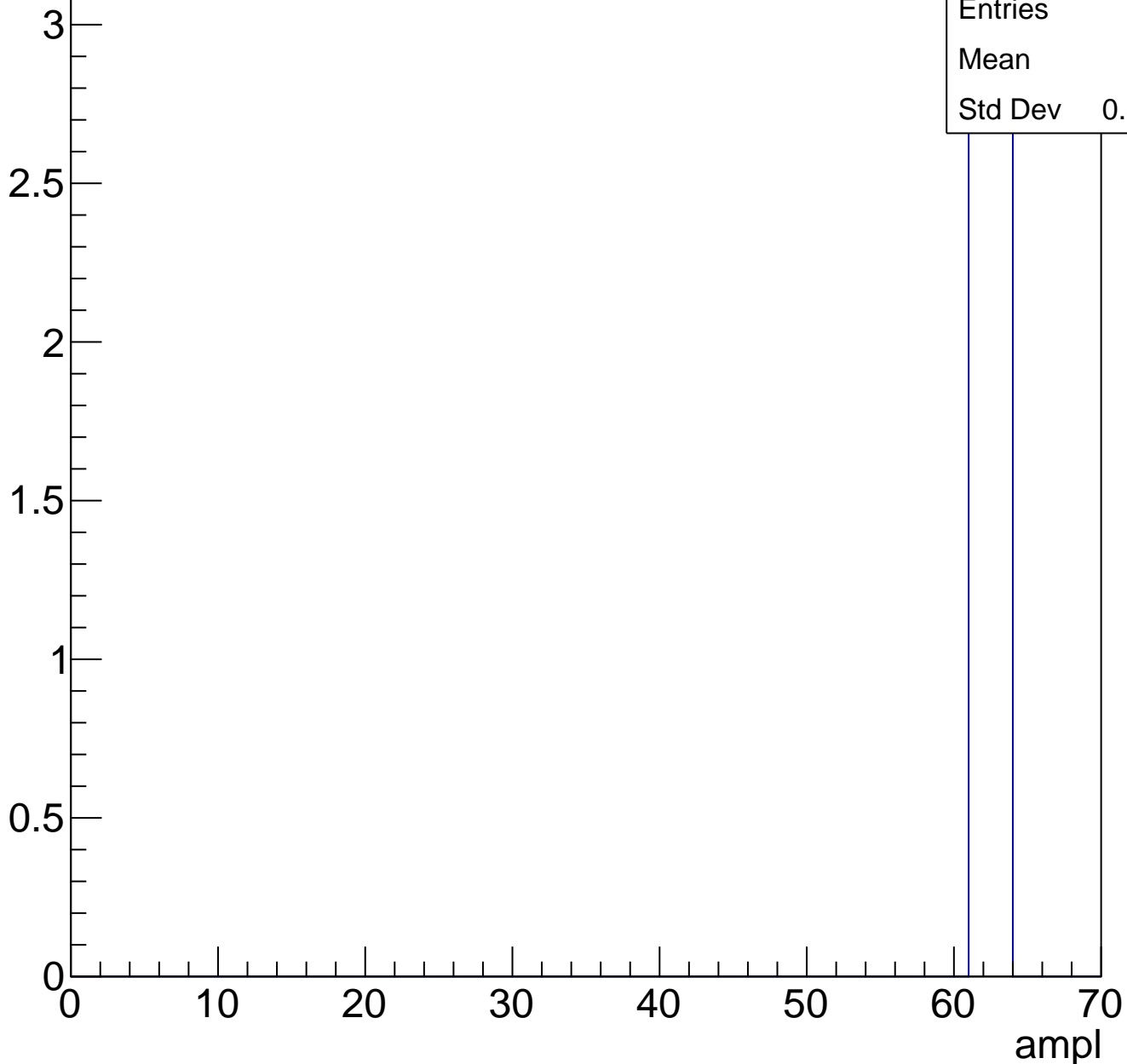
Entry



B1L100S, U1-ch53, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch53, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch54, adc0

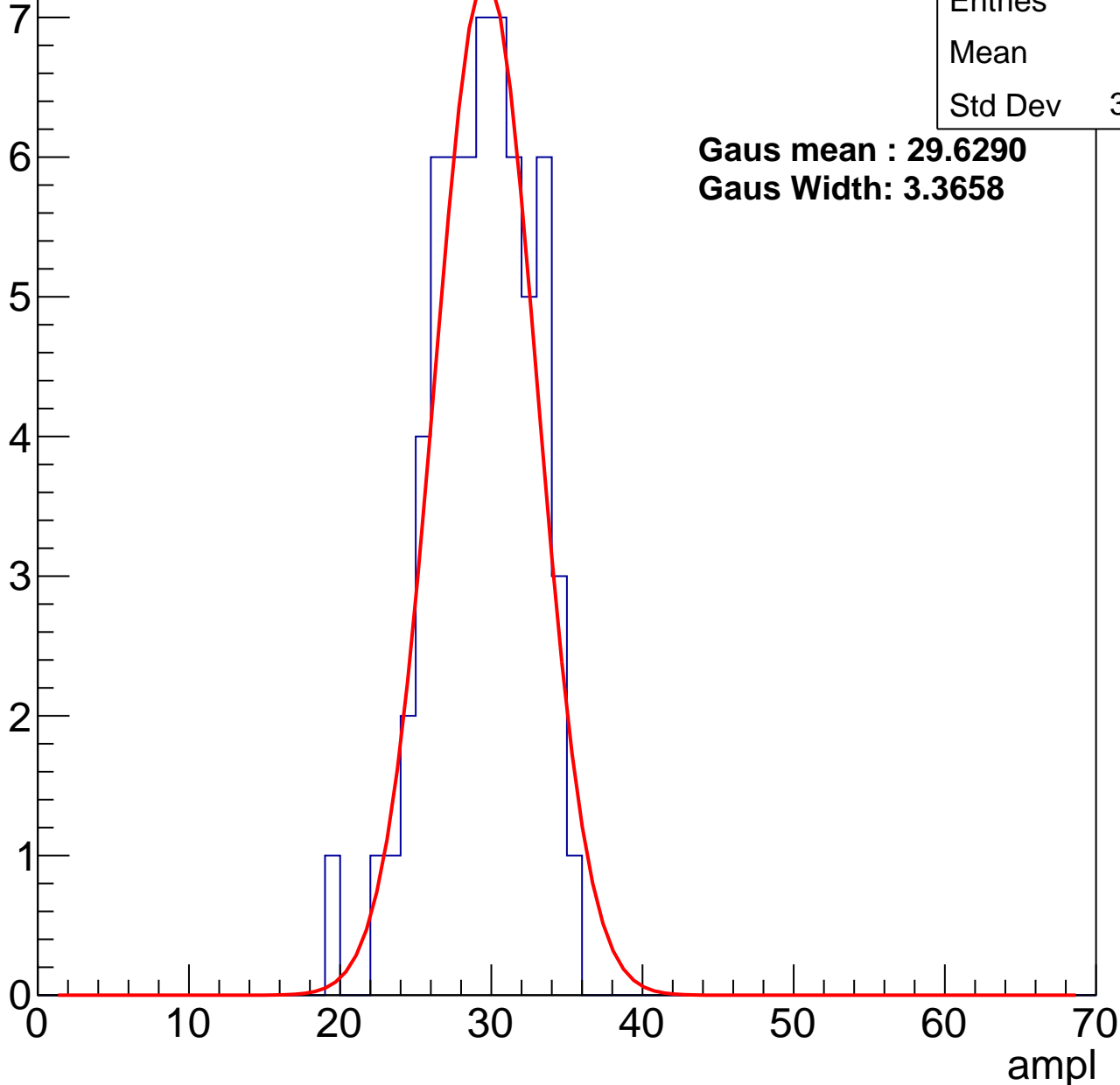
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.9
Std Dev	3.276

Gaus mean : 29.6290

Gaus Width: 3.3658



B1L100S, U1-ch54, adc1

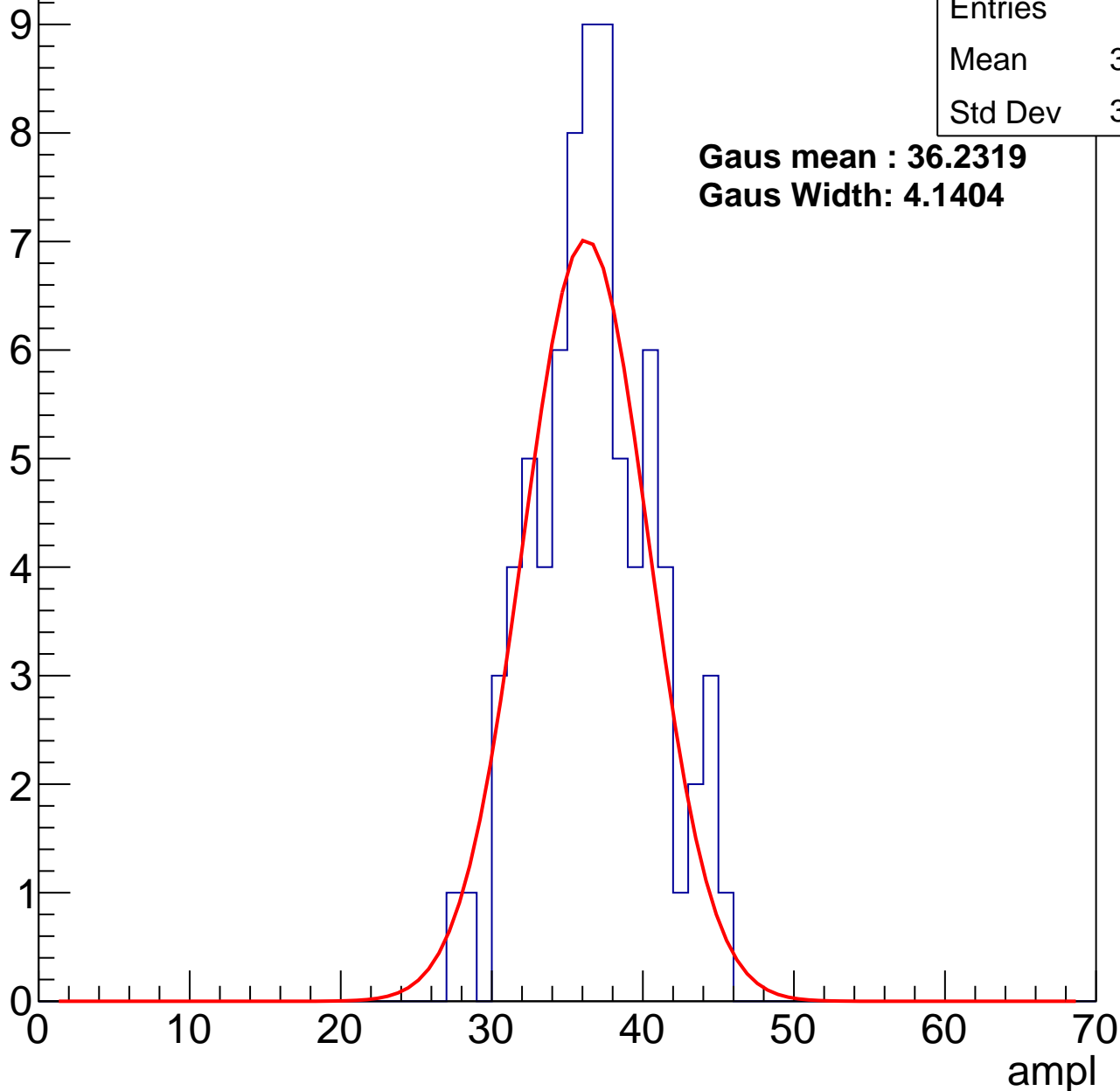
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	36.28
Std Dev	3.912

Gaus mean : 36.2319

Gaus Width: 4.1404



B1L100S, U1-ch54, adc2

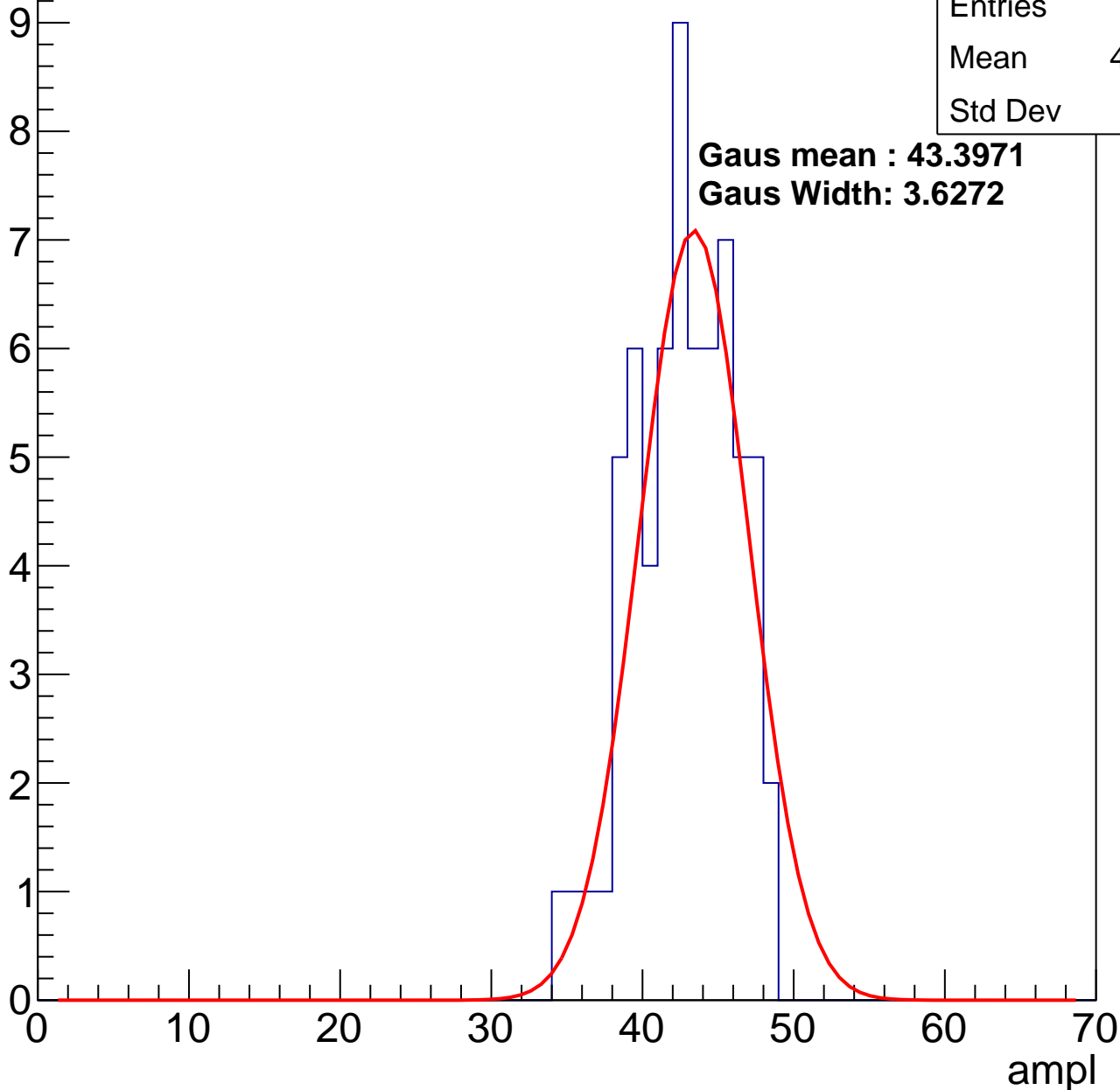
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.28
Std Dev	3.27

Gaus mean : 43.3971

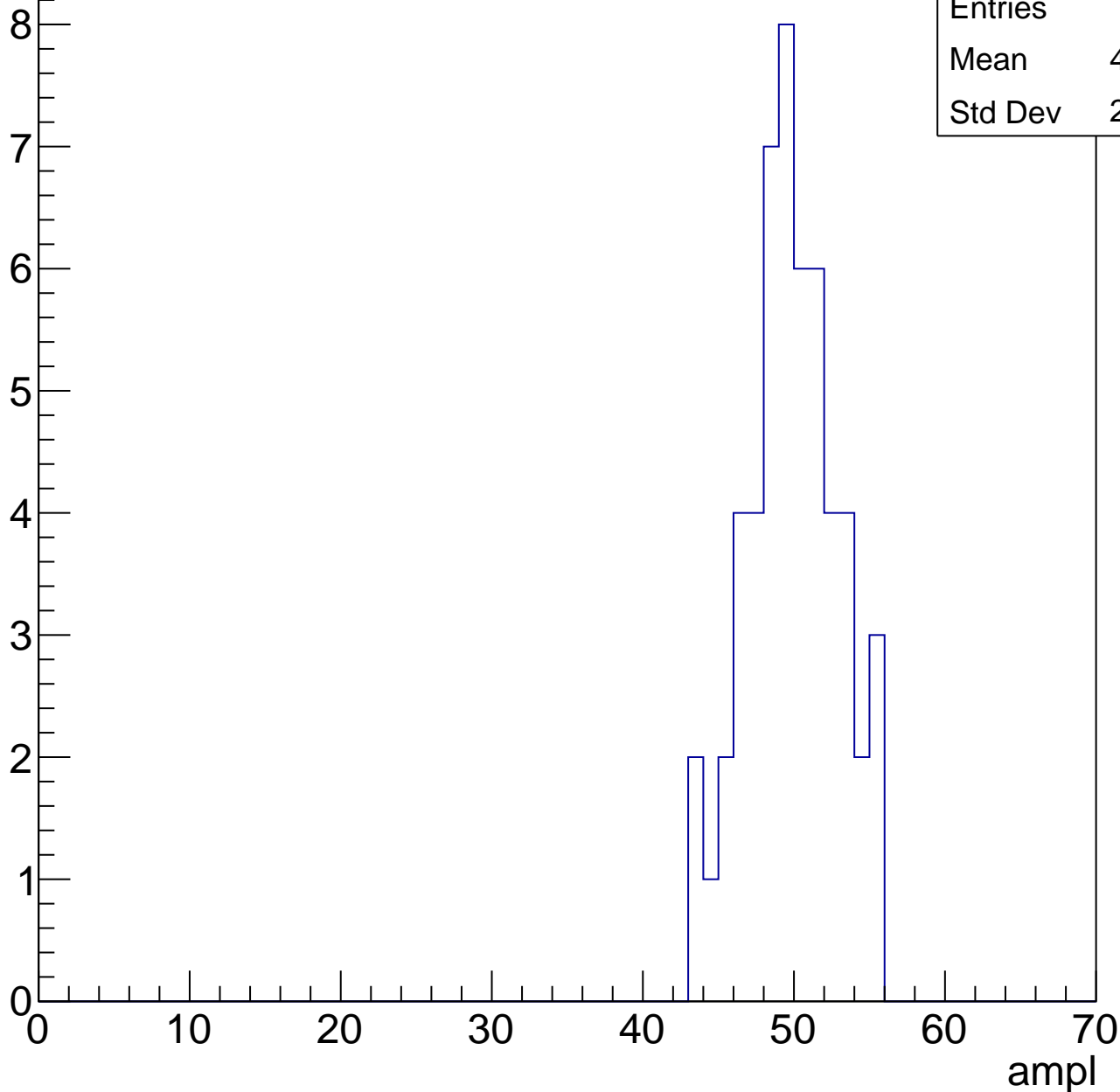
Gaus Width: 3.6272



B1L100S, U1-ch54, adc3

calib_packv5_042523_0143.root, FC#4, port A2

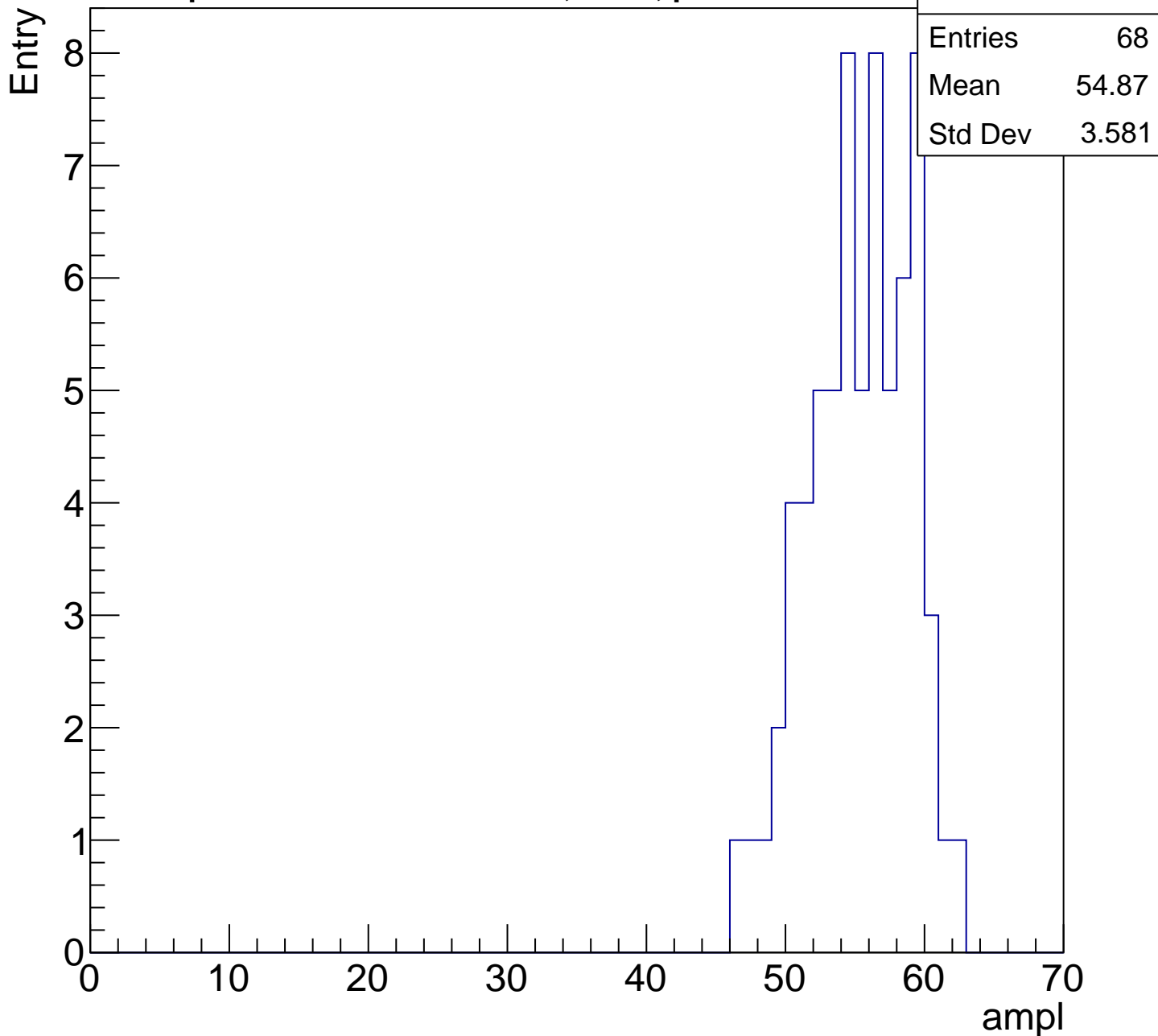
Entry



Entries	53
Mean	49.42
Std Dev	2.968

B1L100S, U1-ch54, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch54, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

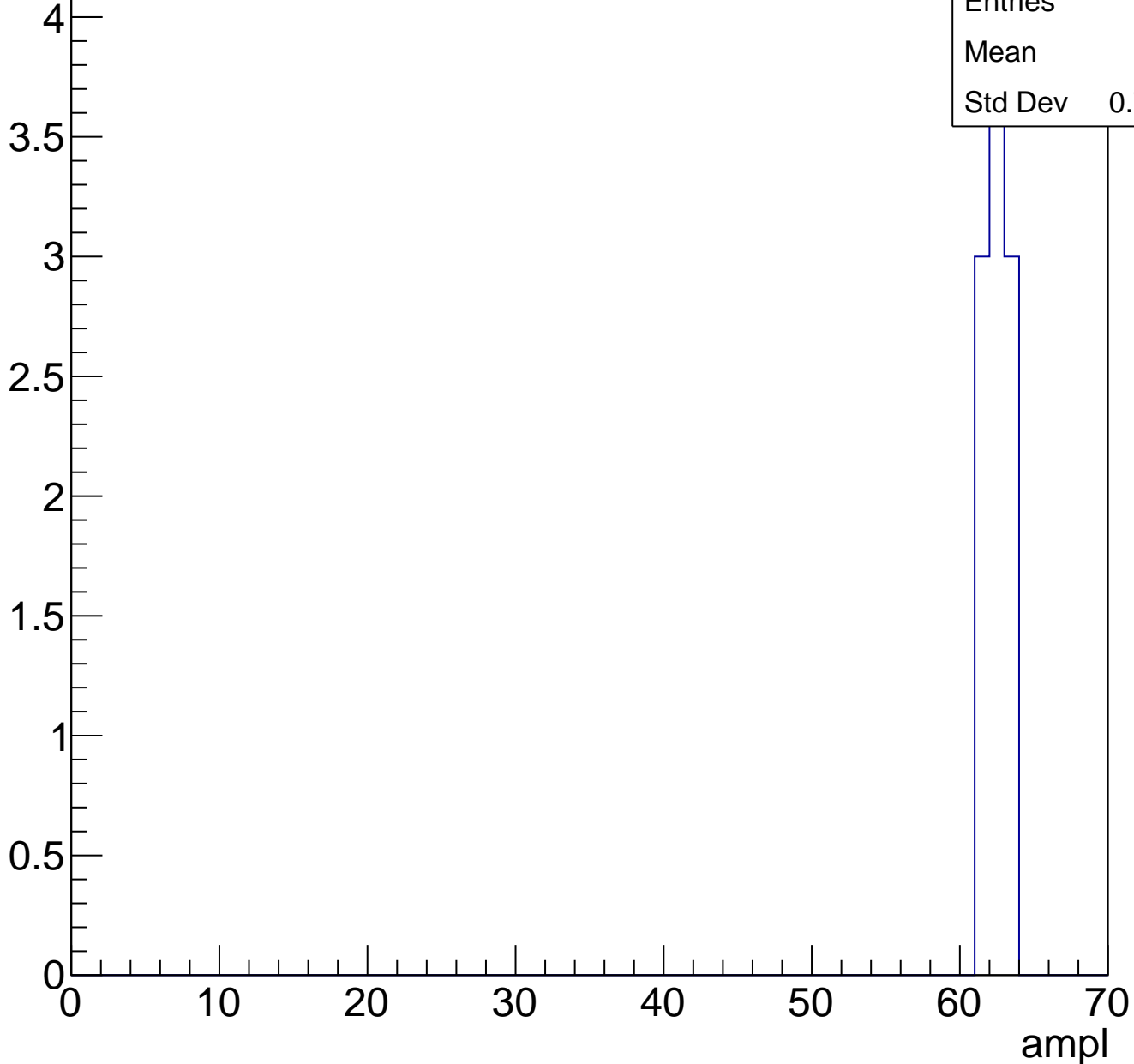
Entries	43
Mean	58.12
Std Dev	9.394

ampl

B1L100S, U1-ch54, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch54, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch55, adc0

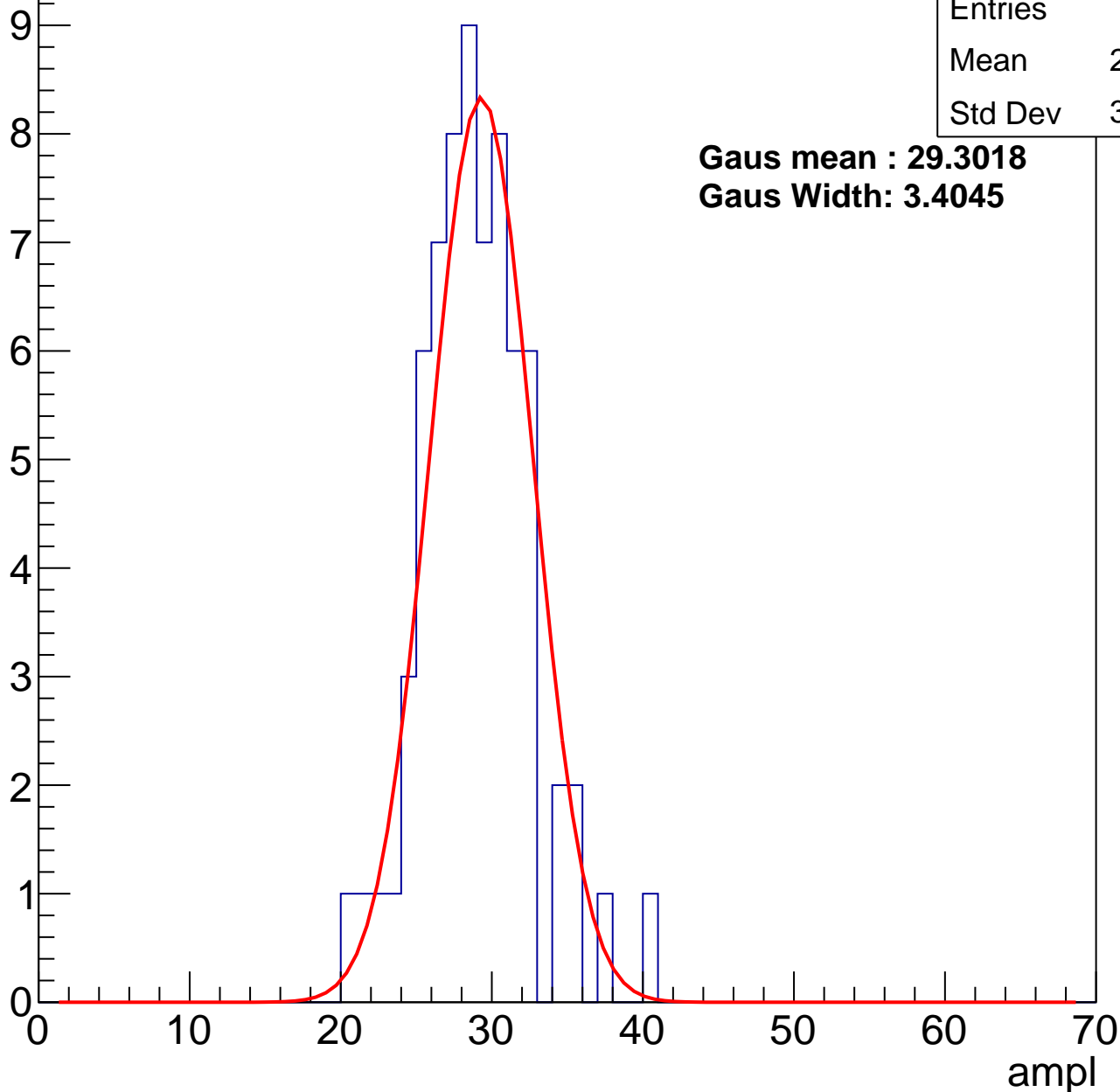
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	28.49
Std Dev	3.545

Gaus mean : 29.3018

Gaus Width: 3.4045



B1L100S, U1-ch55, adc1

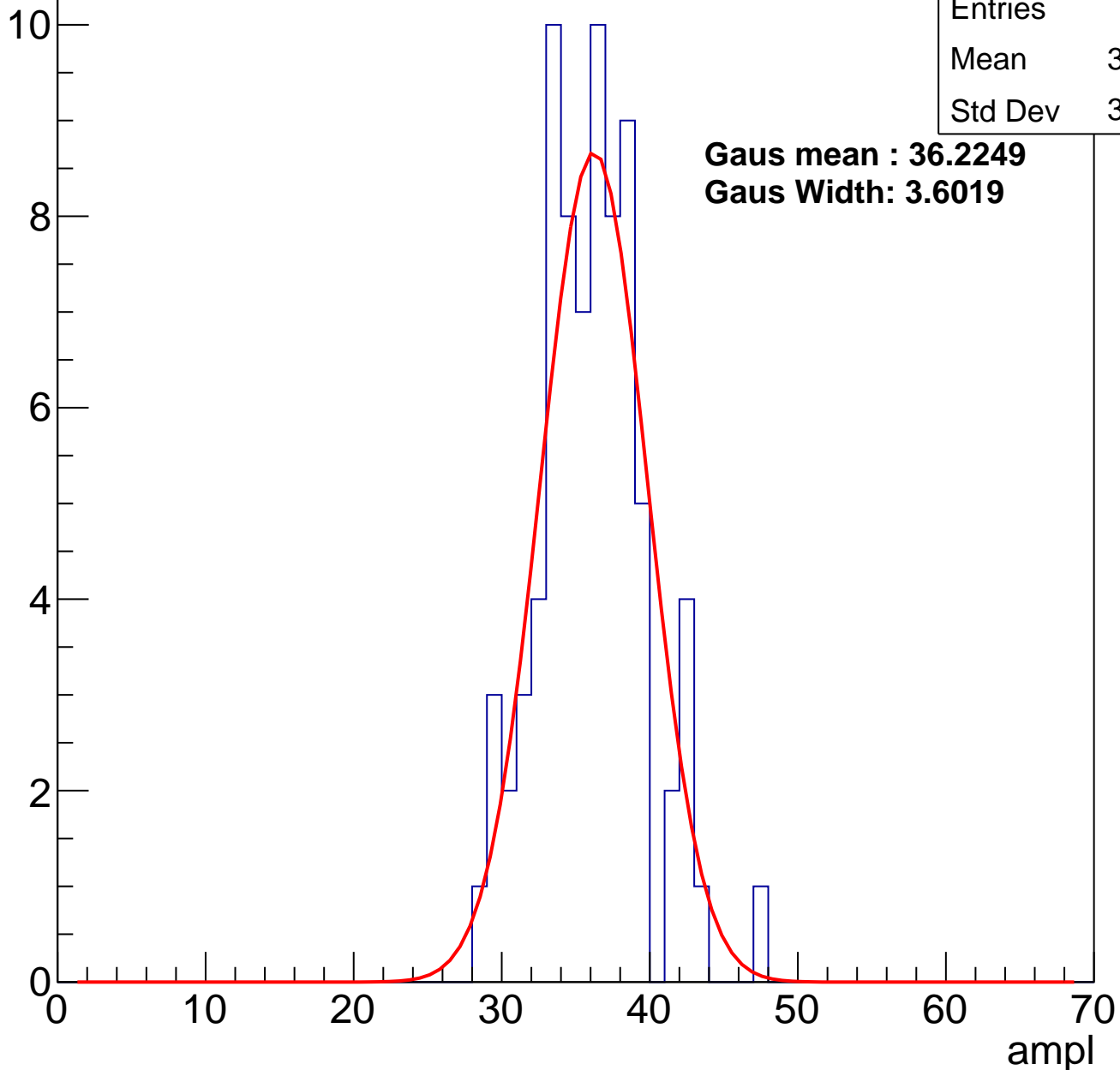
calib_packv5_042523_0143.root, FC#4, port A2

Entries	78
Mean	35.59
Std Dev	3.564

Gaus mean : 36.2249

Gaus Width: 3.6019

Entry



B1L100S, U1-ch55, adc2

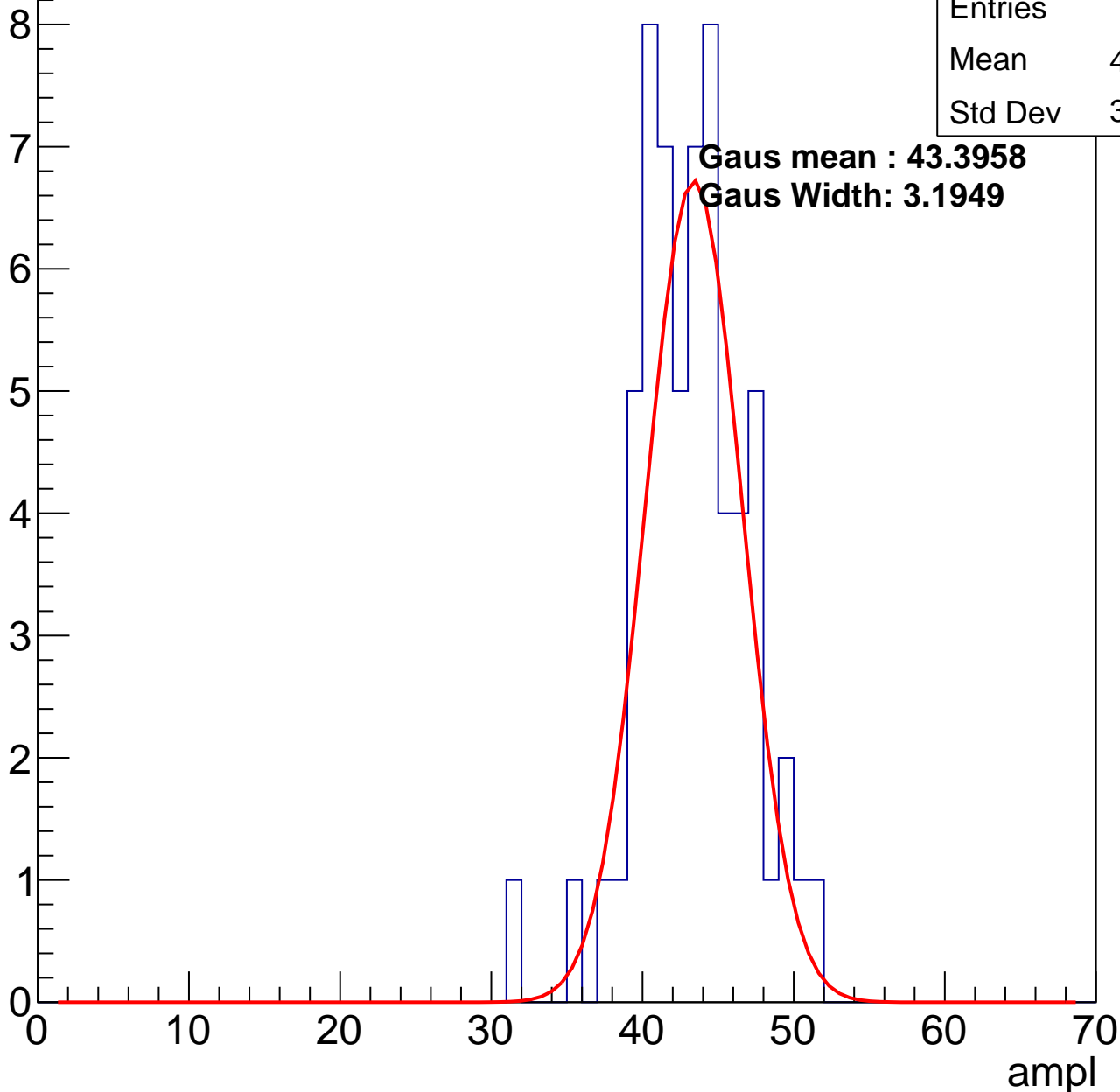
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.77
Std Dev	3.594

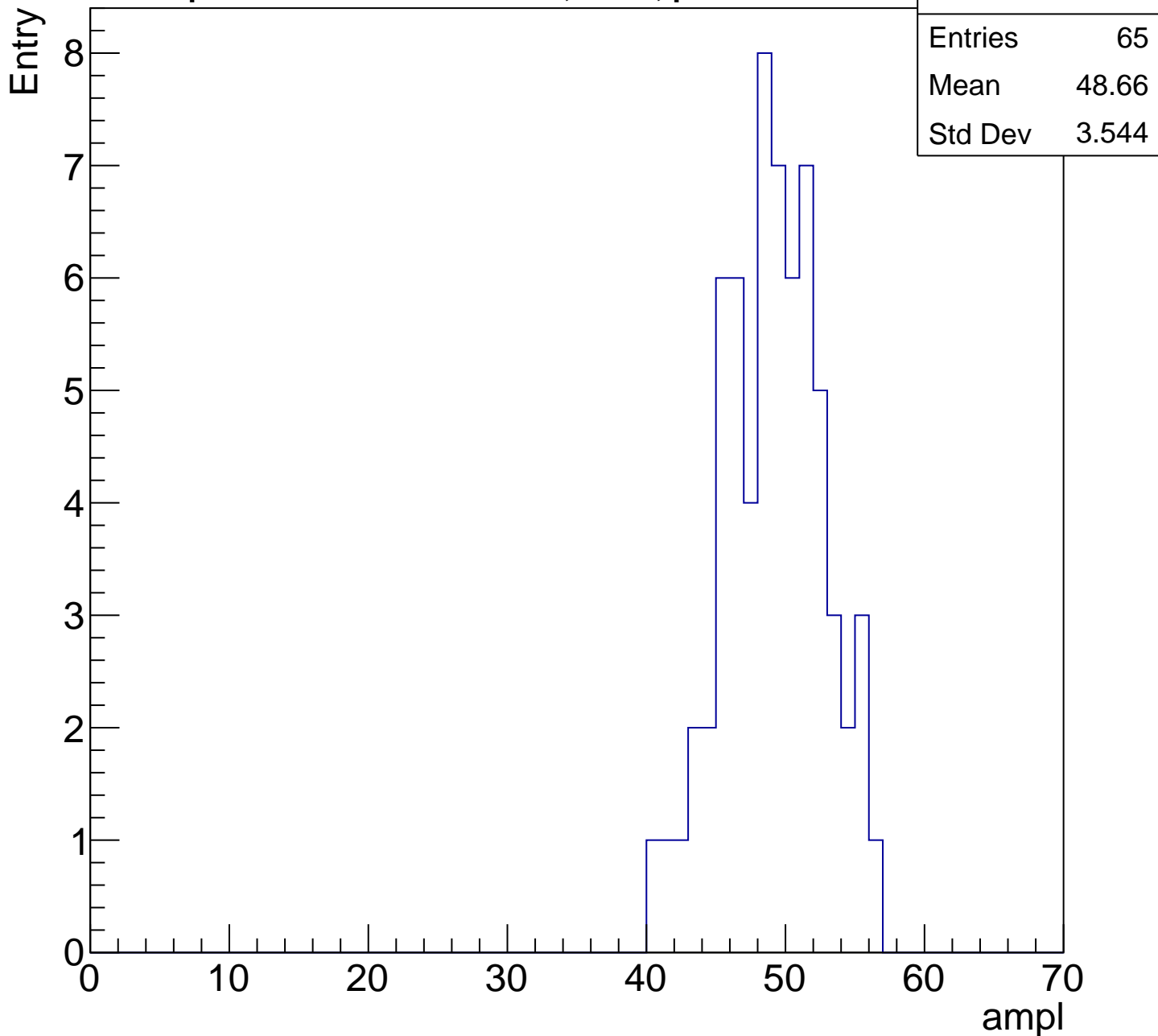
Gaus mean : 43.3958

Gaus Width: 3.1949



B1L100S, U1-ch55, adc3

calib_packv5_042523_0143.root, FC#4, port A2

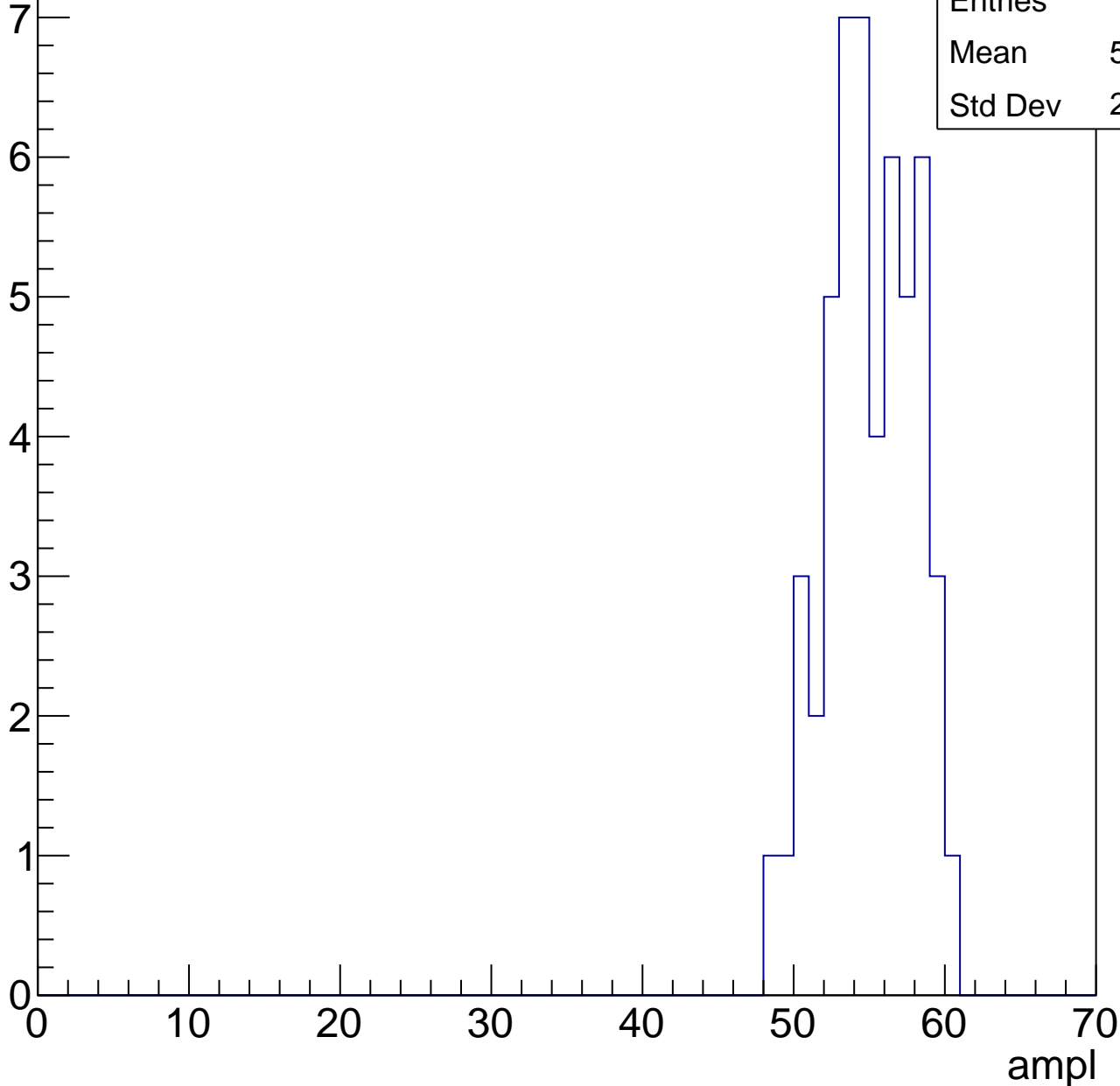


B1L100S, U1-ch55, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	54.59
Std Dev	2.857



B1L100S, U1-ch55, adc5

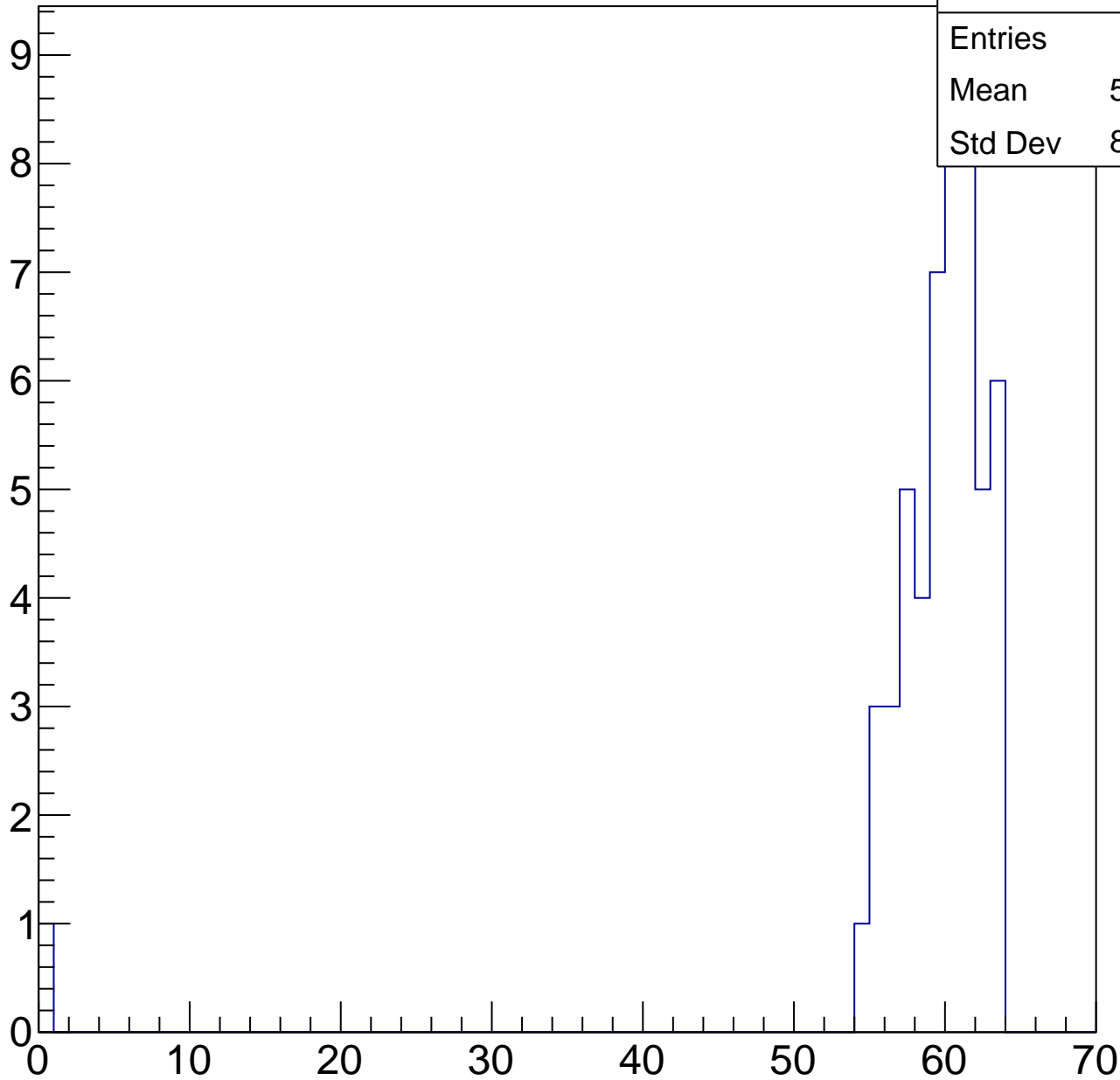
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.35
Std Dev	8.512

ampl

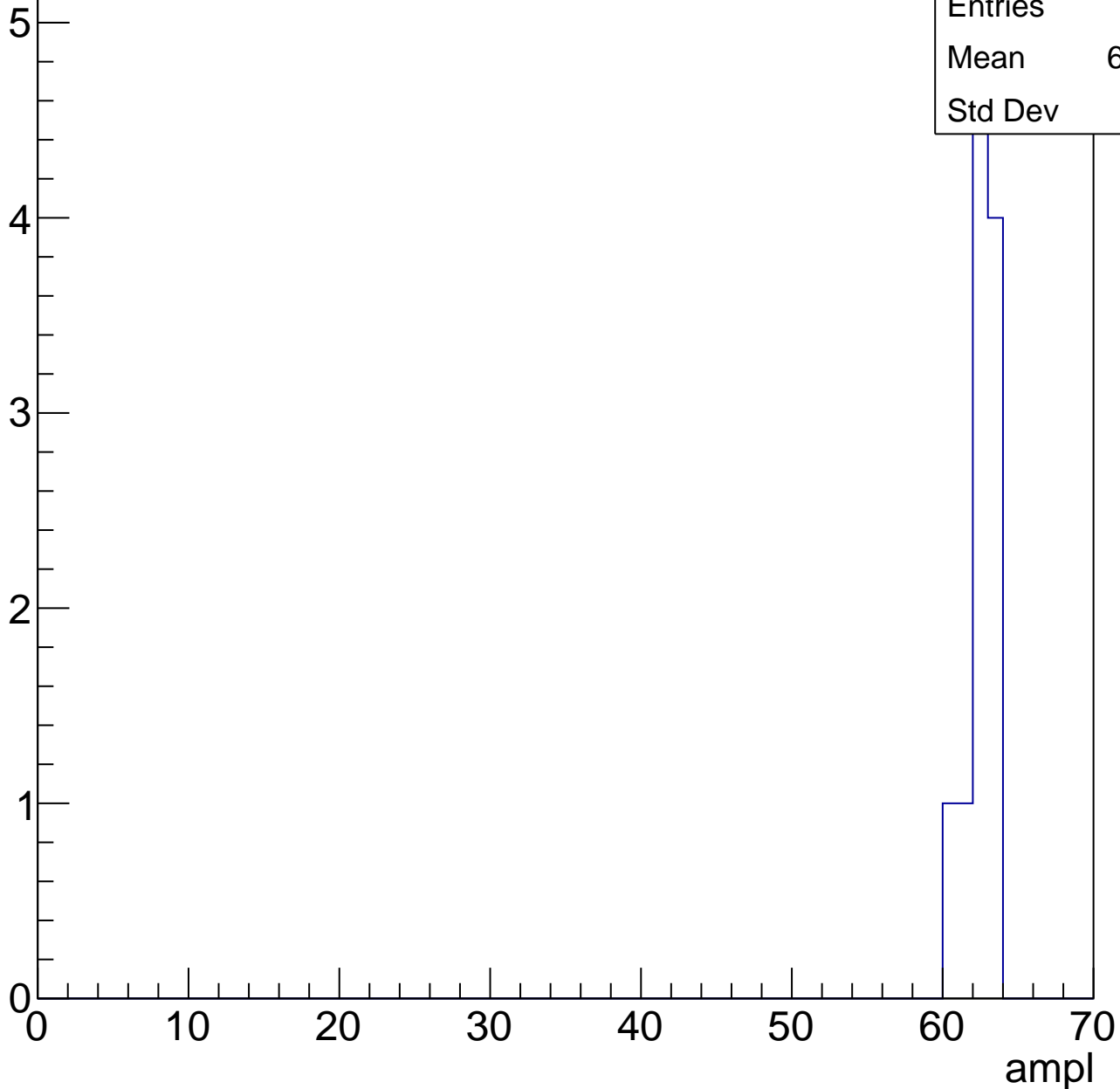


B1L100S, U1-ch55, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	62.09
Std Dev	0.9



B1L100S, U1-ch55, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch56, adc0

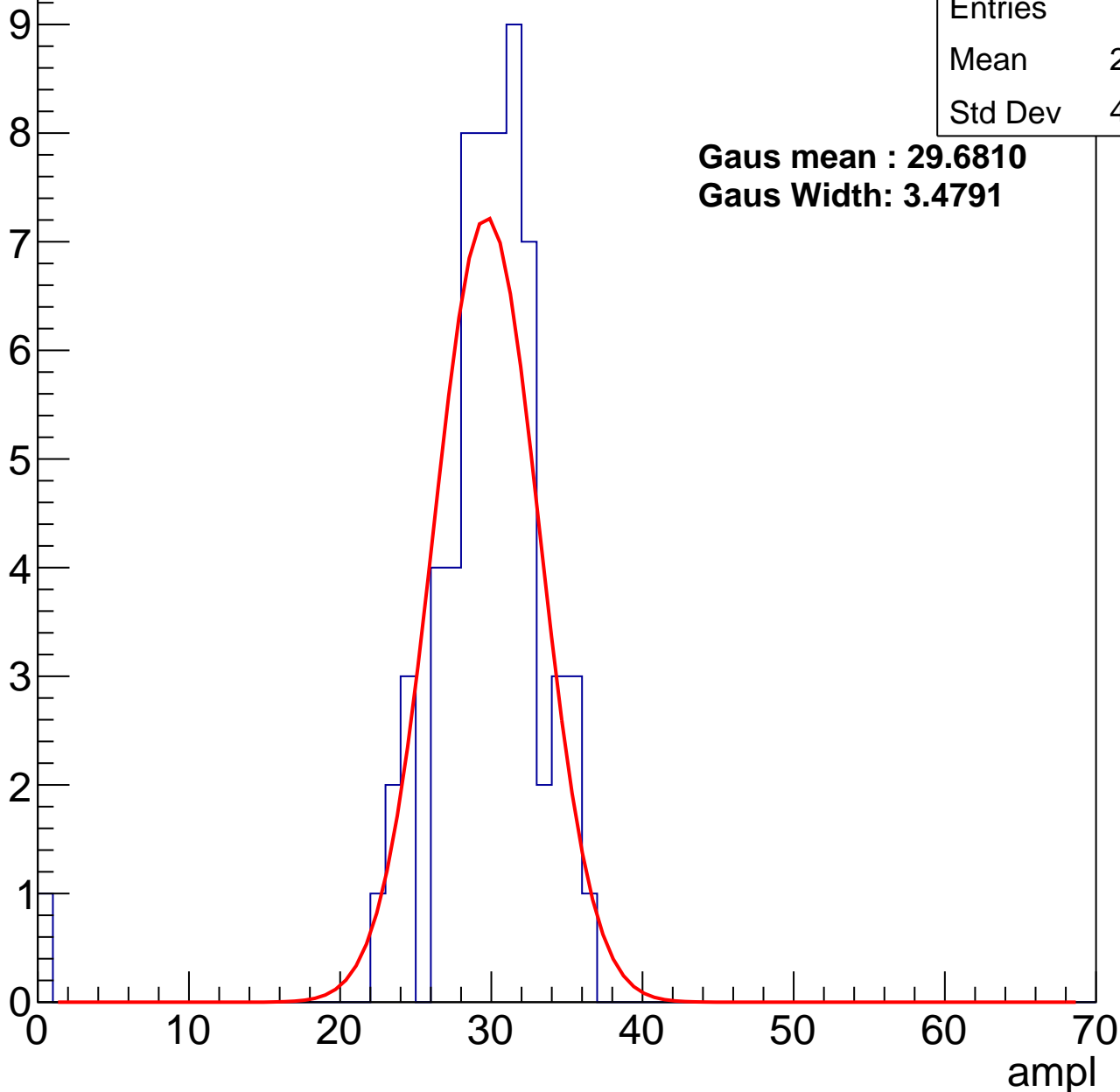
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.06
Std Dev	4.789

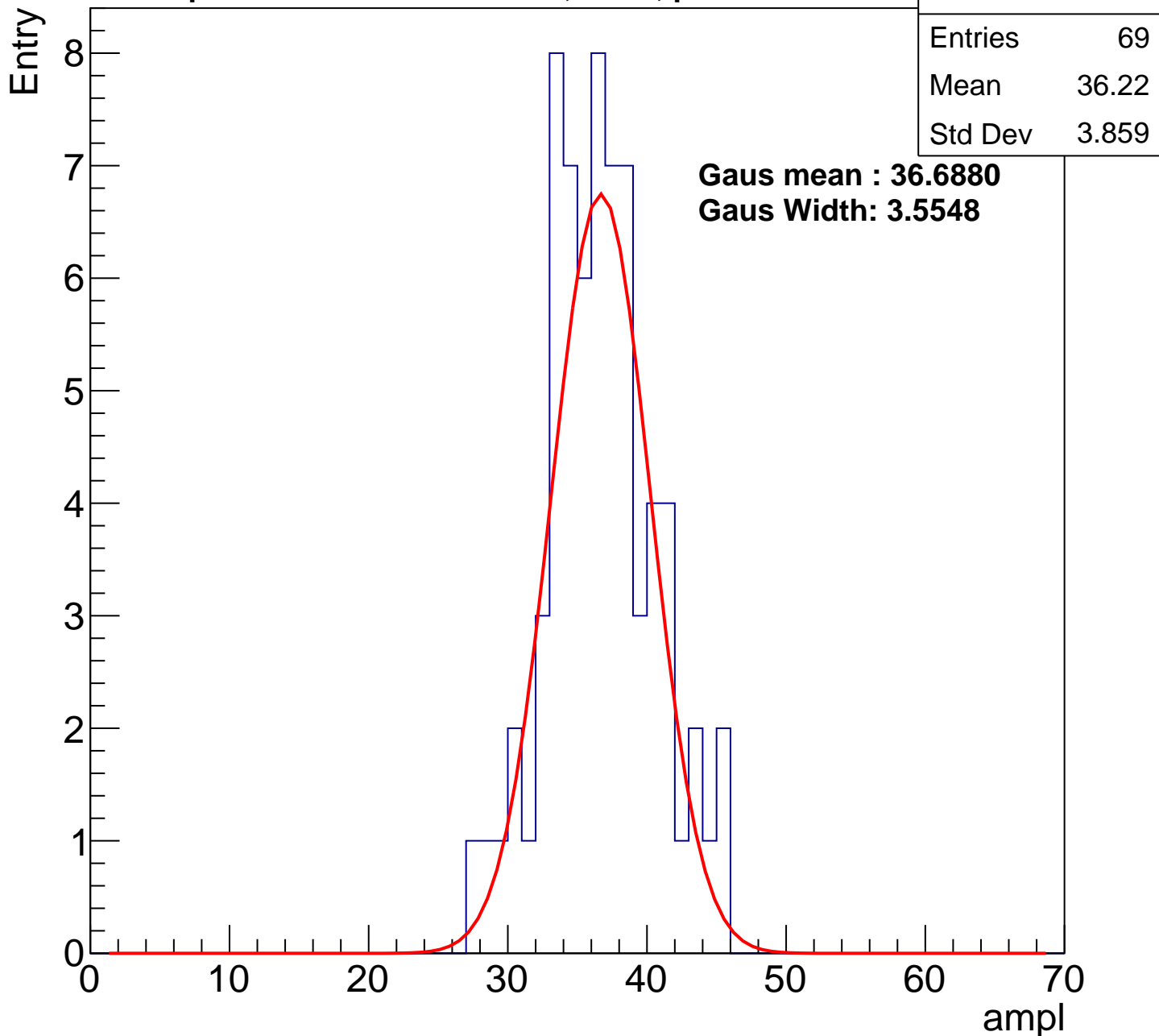
Gaus mean : 29.6810

Gaus Width: 3.4791



B1L100S, U1-ch56, adc1

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch56, adc2

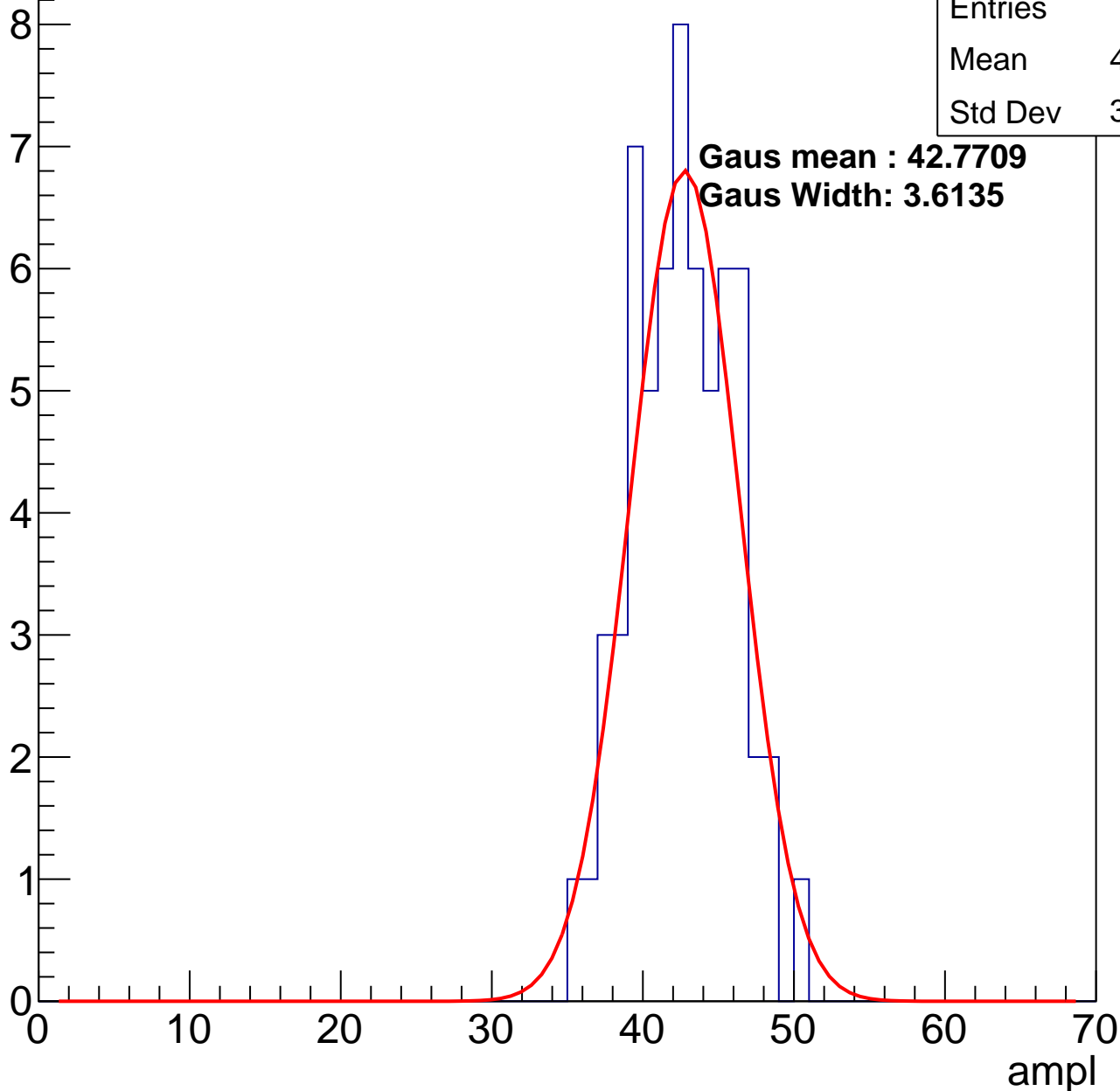
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	42.18
Std Dev	3.255

Gaus mean : 42.7709

Gaus Width: 3.6135

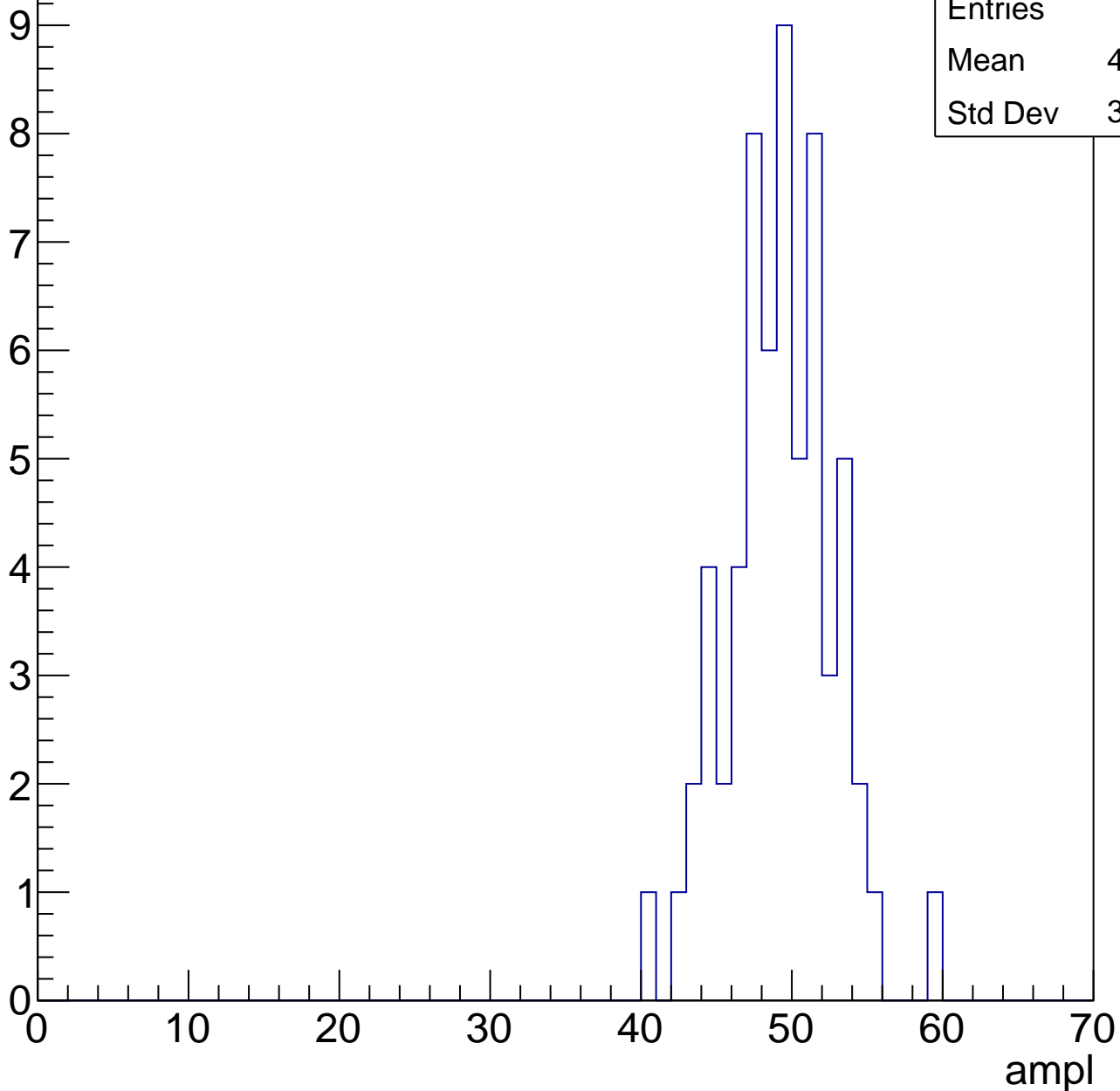


B1L100S, U1-ch56, adc3

calib_packv5_042523_0143.root, FC#4, port A2

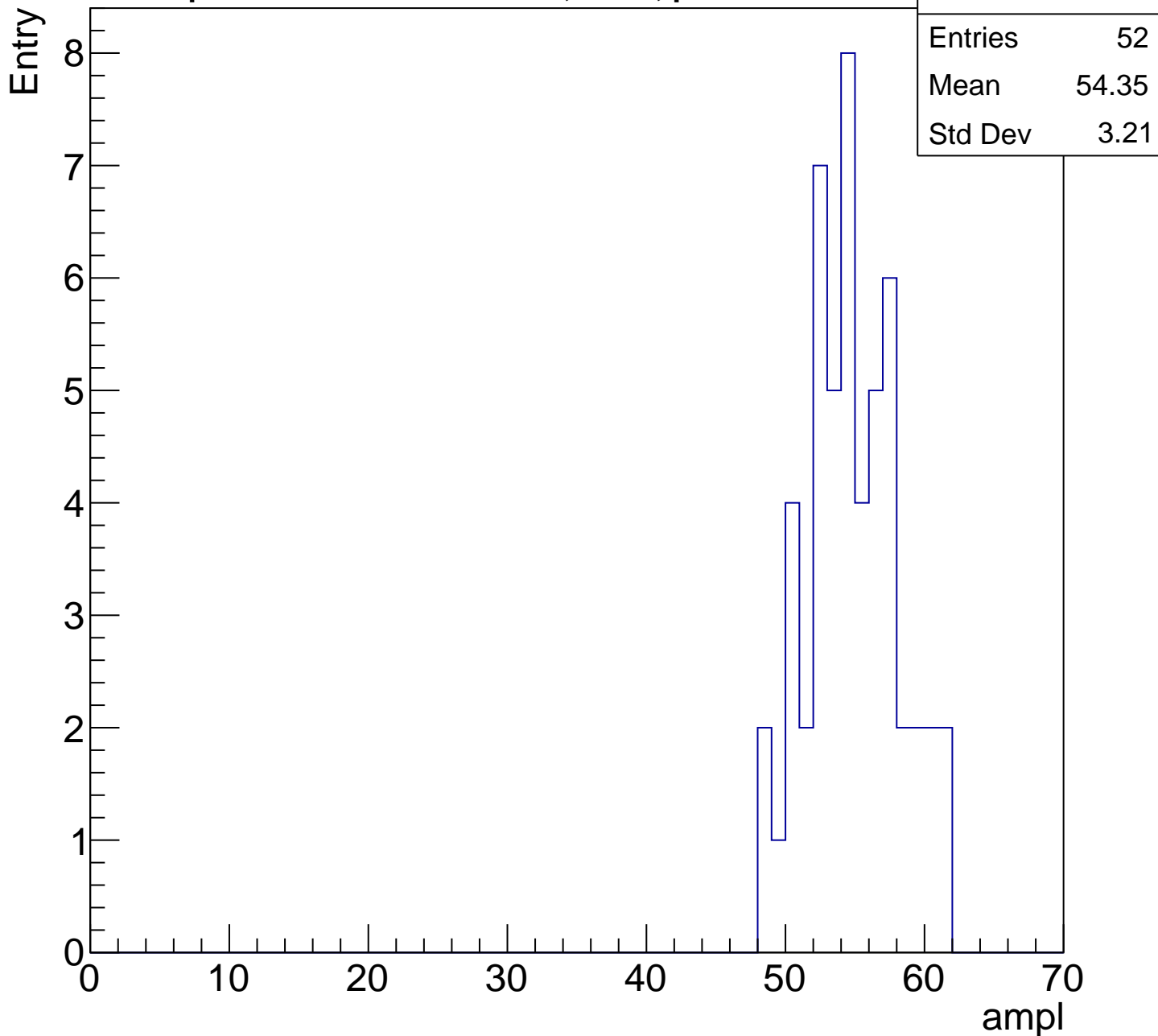
Entry

Entries	62
Mean	48.77
Std Dev	3.443



B1L100S, U1-ch56, adc4

calib_packv5_042523_0143.root, FC#4, port A2

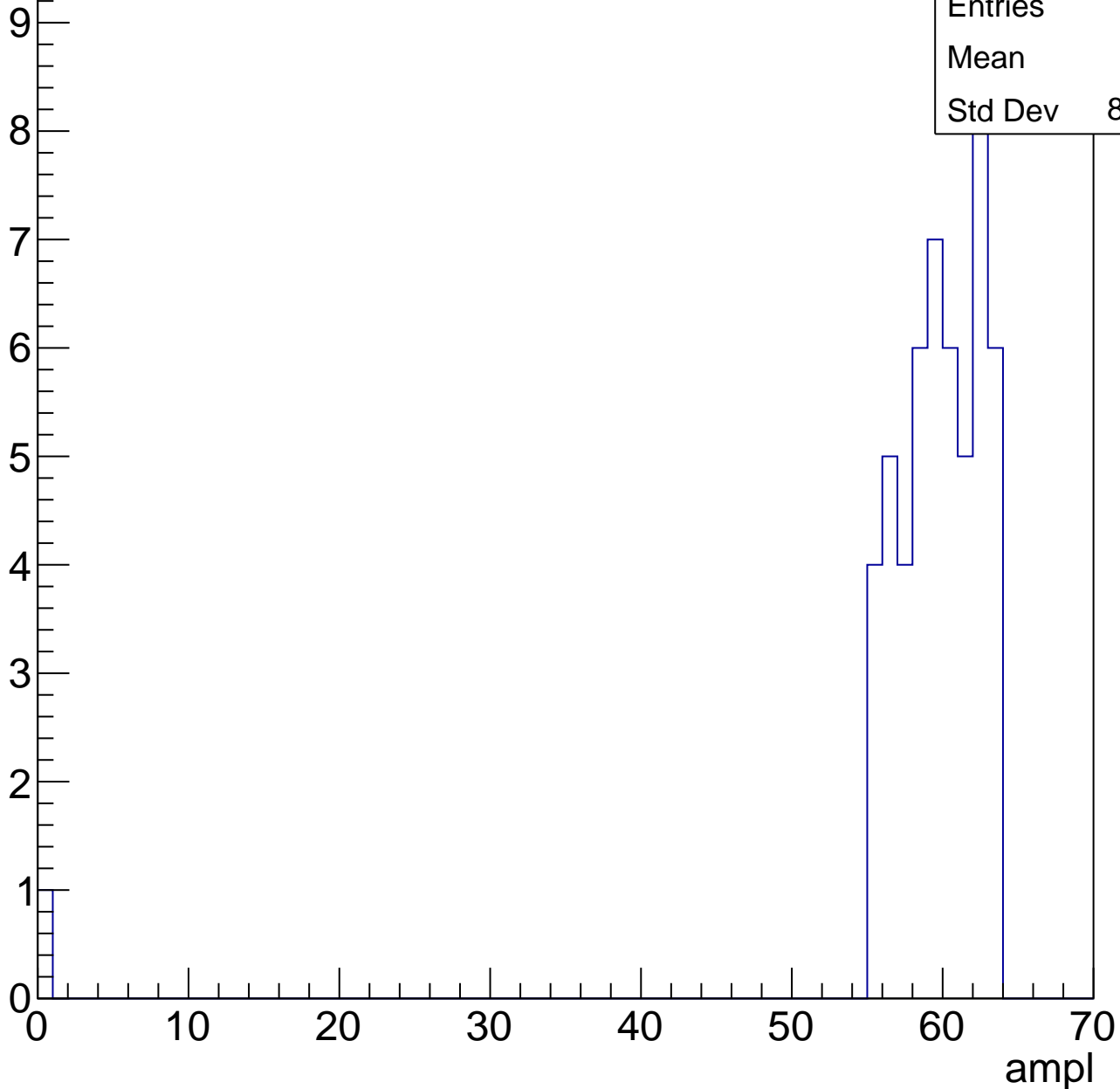


B1L100S, U1-ch56, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

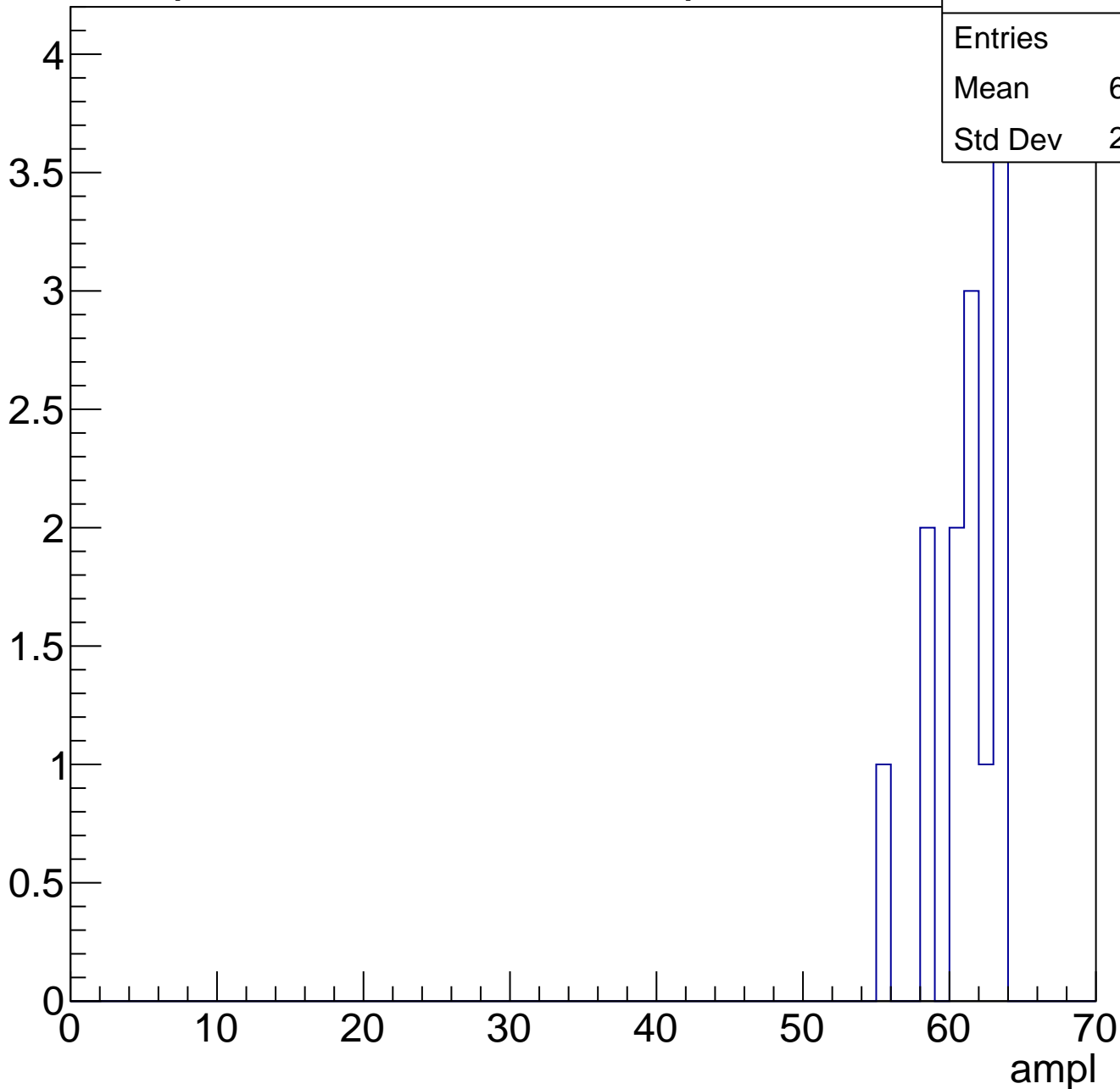
Entries	53
Mean	58.3
Std Dev	8.455



B1L100S, U1-ch56, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch56, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch57, adc0

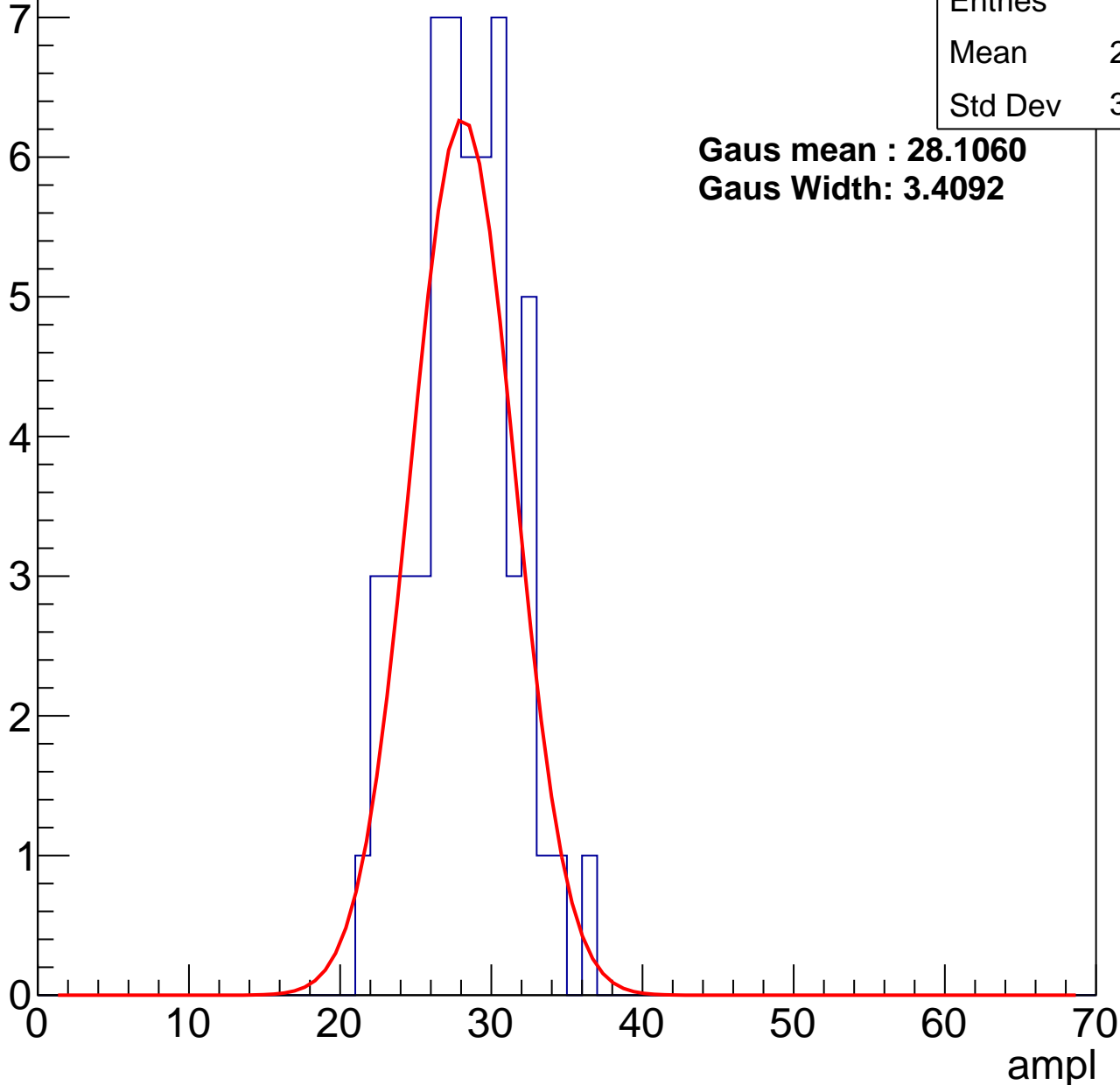
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	27.75
Std Dev	3.257

Gaus mean : 28.1060

Gaus Width: 3.4092



B1L100S, U1-ch57, adc1

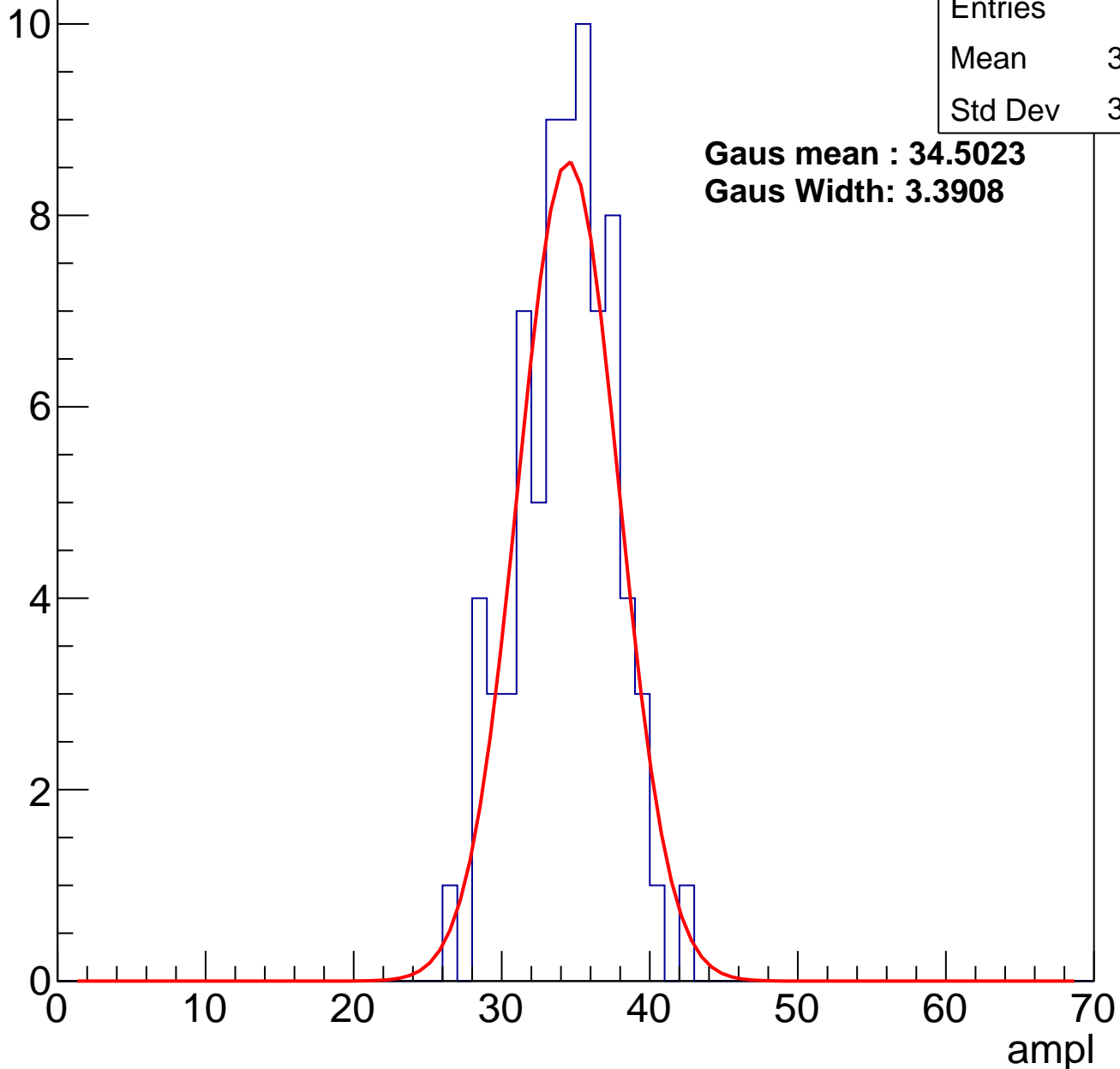
calib_packv5_042523_0143.root, FC#4, port A2

Entries	75
Mean	33.92
Std Dev	3.207

Gaus mean : 34.5023

Gaus Width: 3.3908

Entry



B1L100S, U1-ch57, adc2

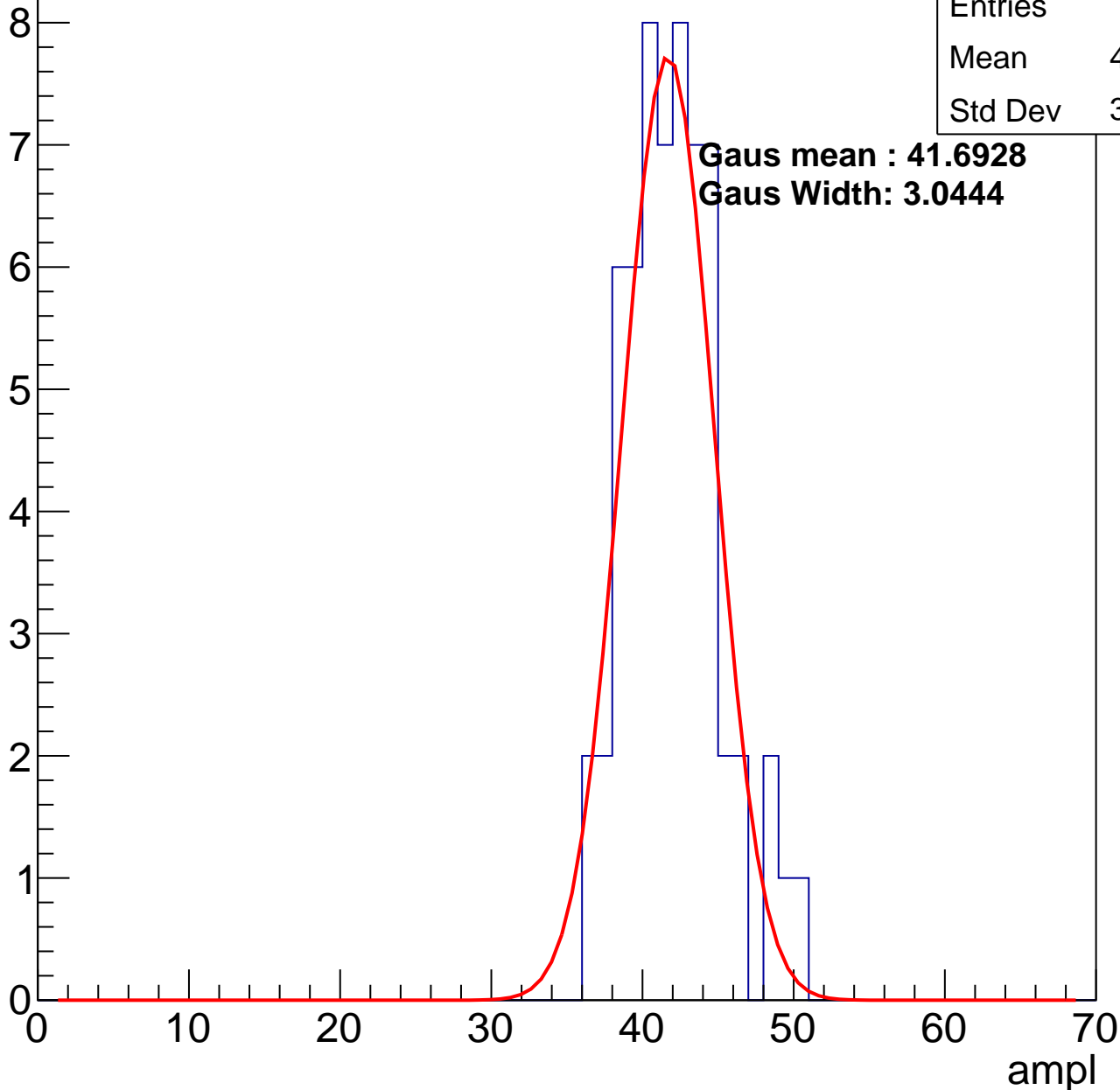
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	41.59
Std Dev	3.059

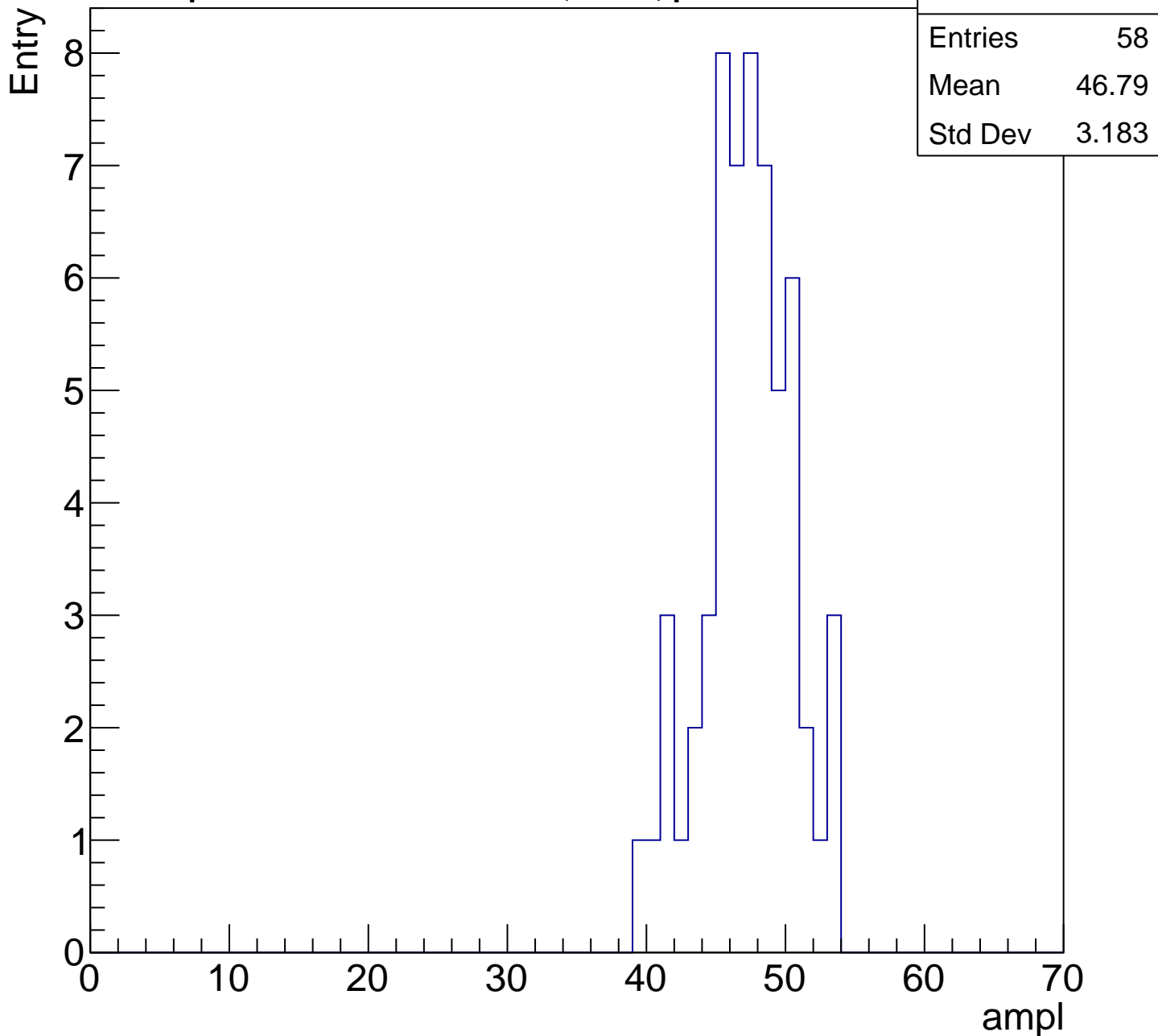
Gaus mean : 41.6928

Gaus Width: 3.0444



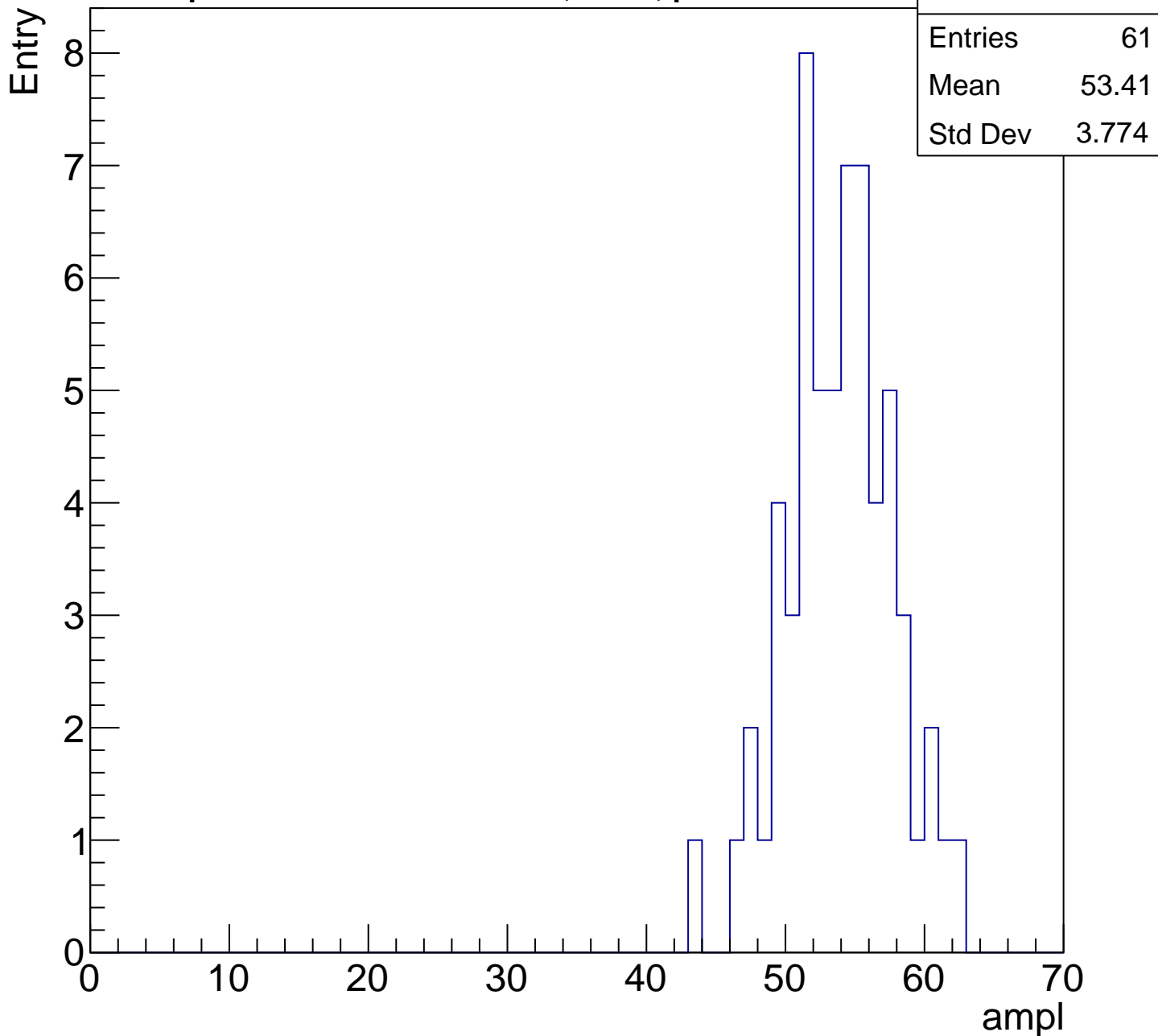
B1L100S, U1-ch57, adc3

calib_packv5_042523_0143.root, FC#4, port A2



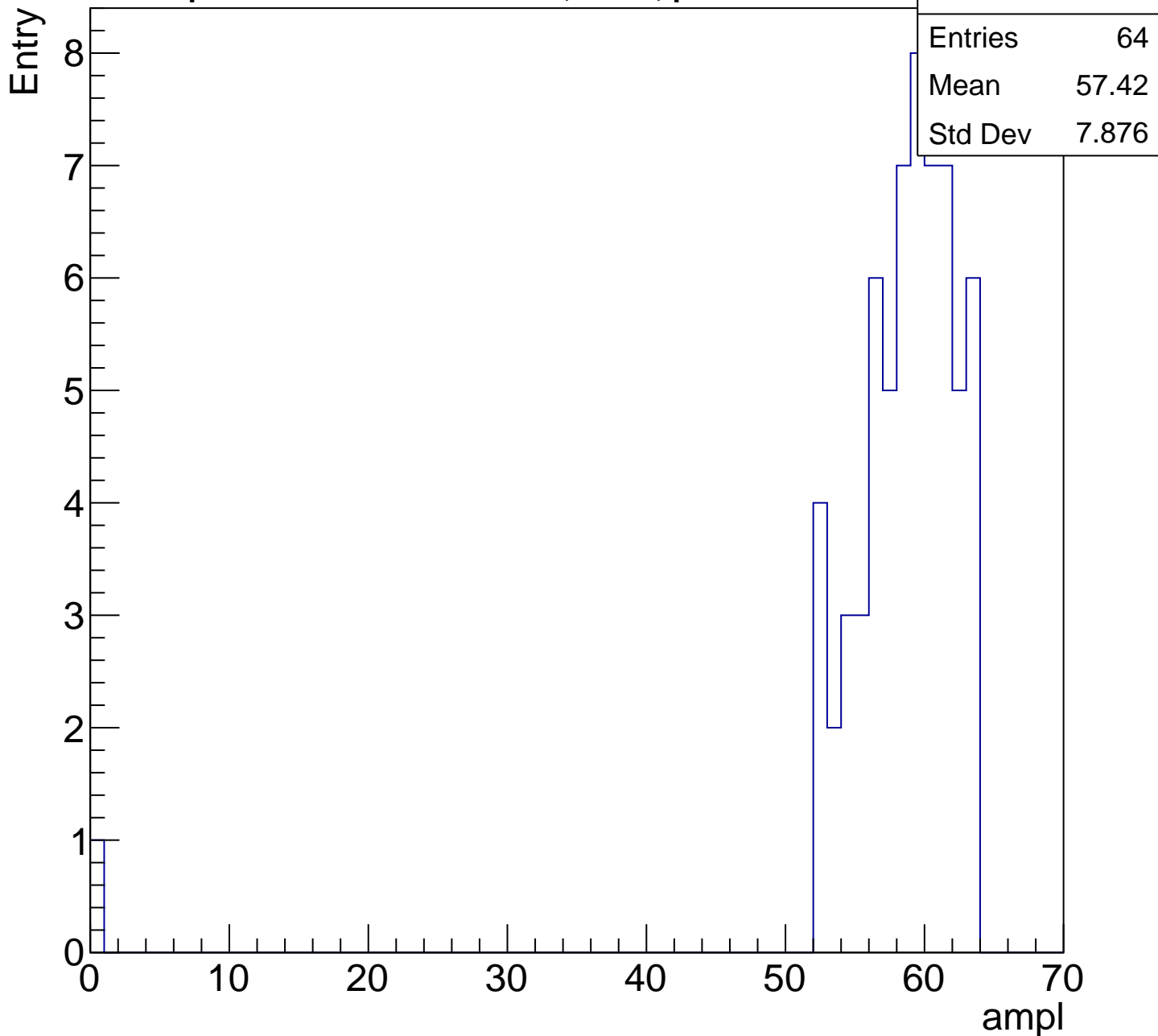
B1L100S, U1-ch57, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch57, adc5

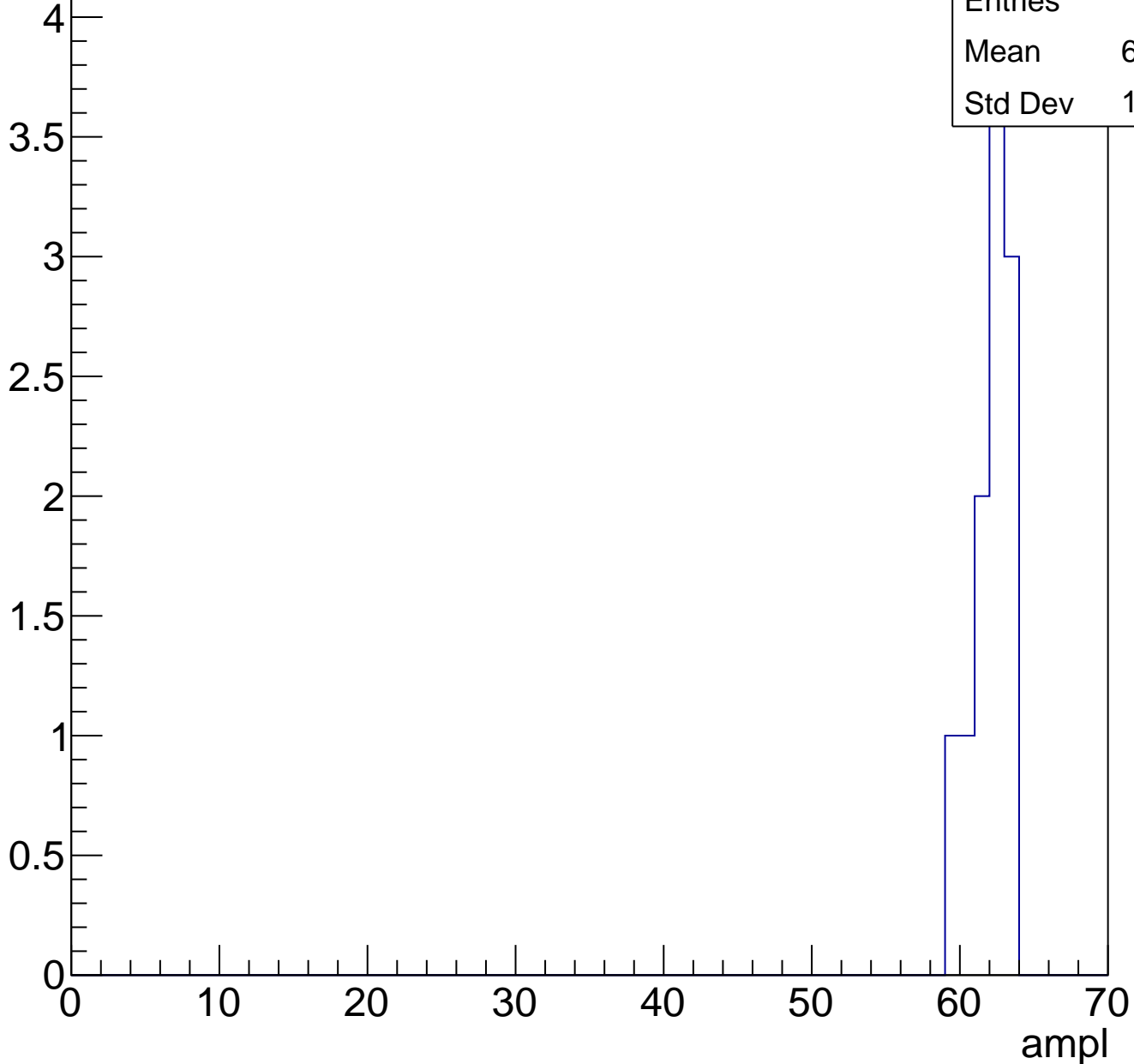
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch57, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch57, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch58, adc0

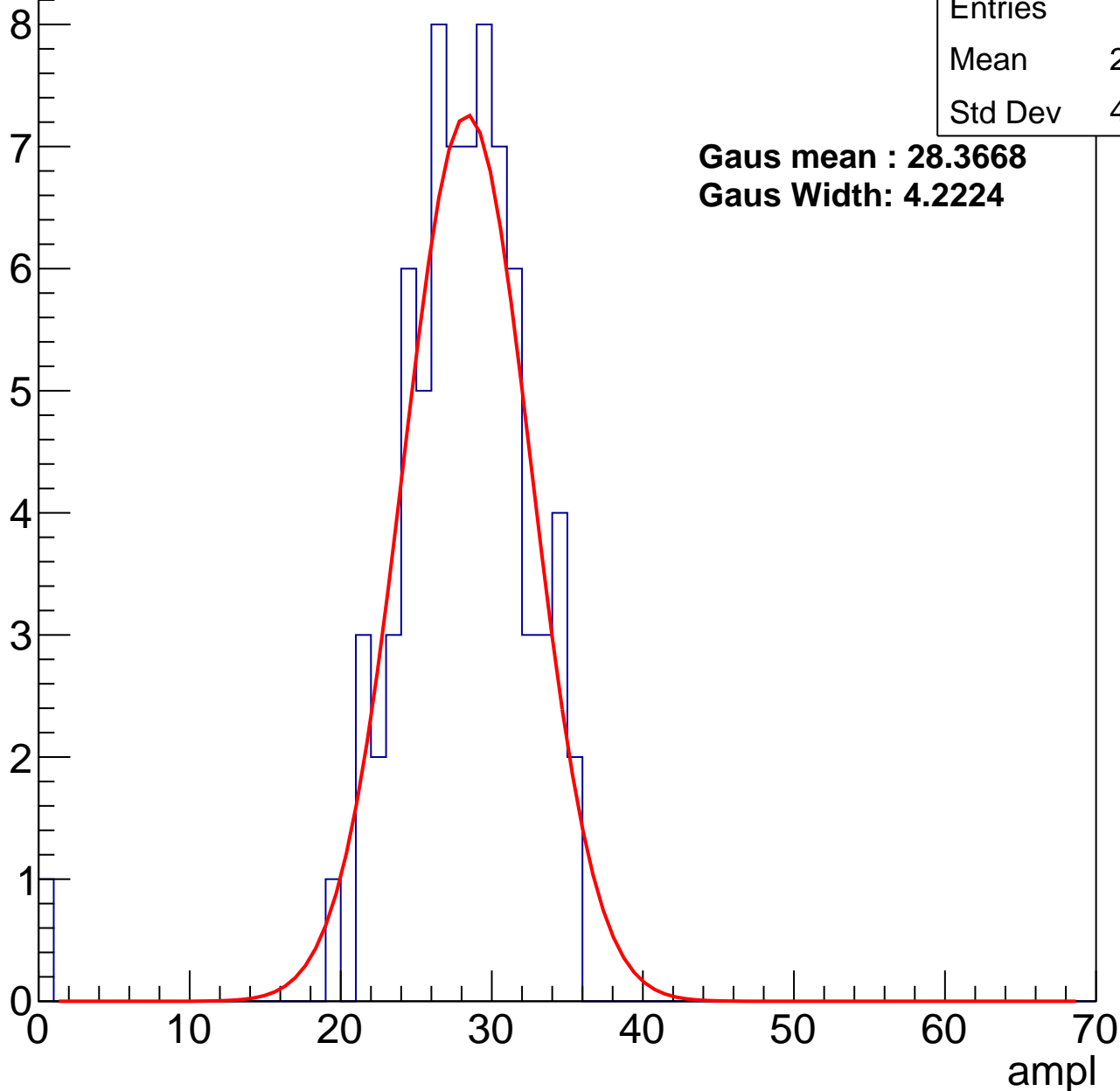
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	27.45
Std Dev	4.827

Gaus mean : 28.3668

Gaus Width: 4.2224



B1L100S, U1-ch58, adc1

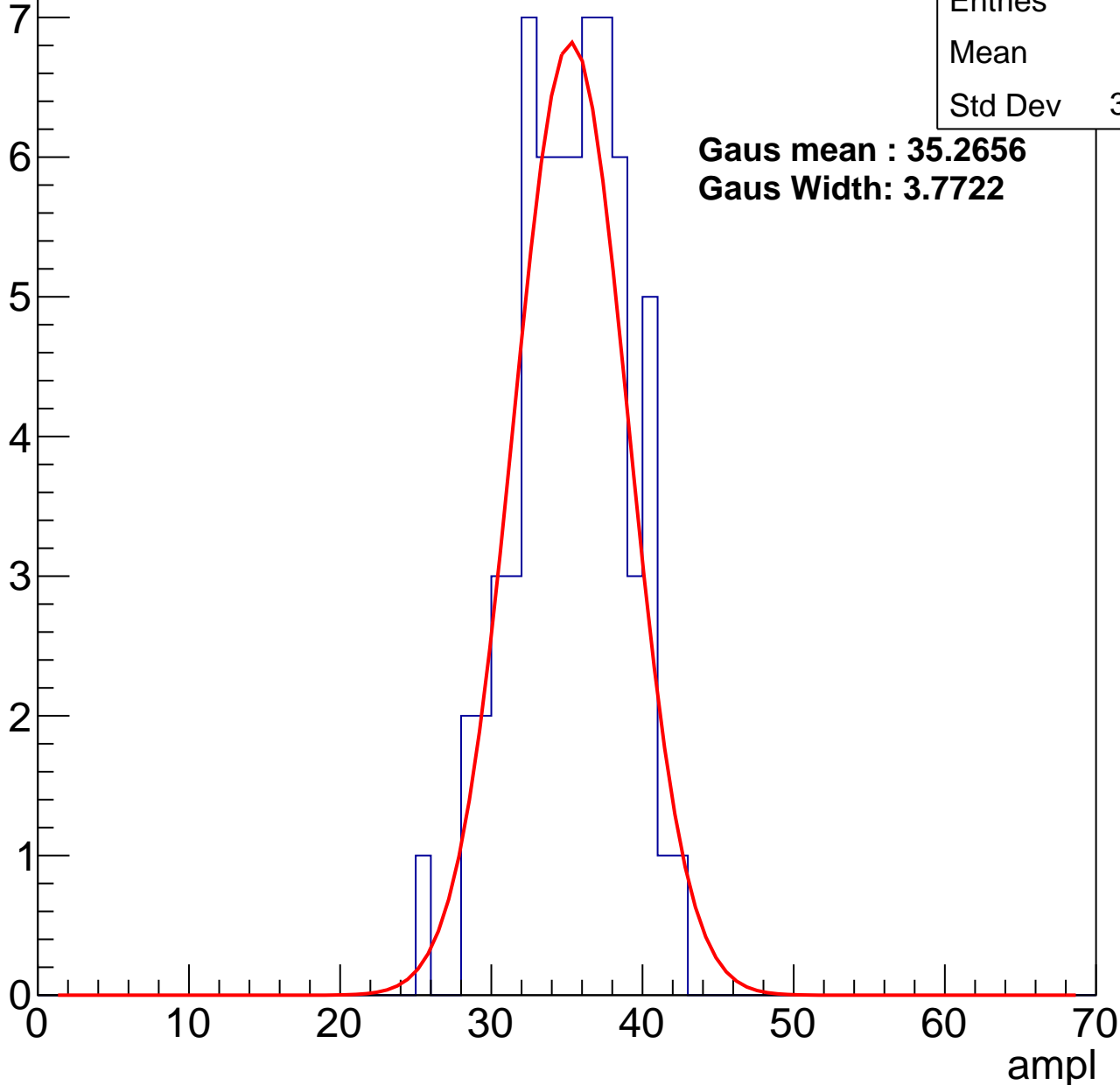
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	34.8
Std Dev	3.547

Gaus mean : 35.2656

Gaus Width: 3.7722



B1L100S, U1-ch58, adc2

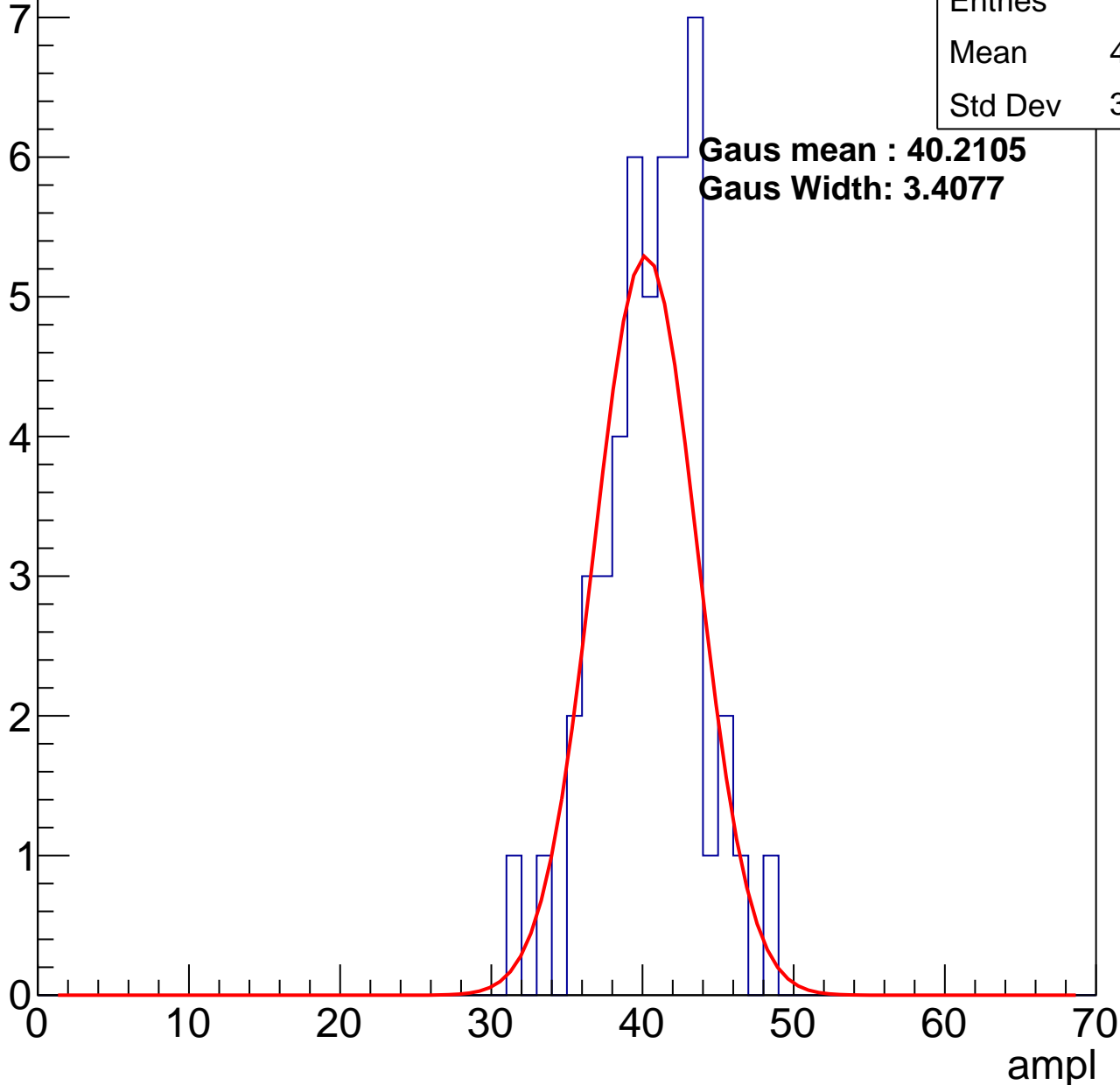
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	40.12
Std Dev	3.305

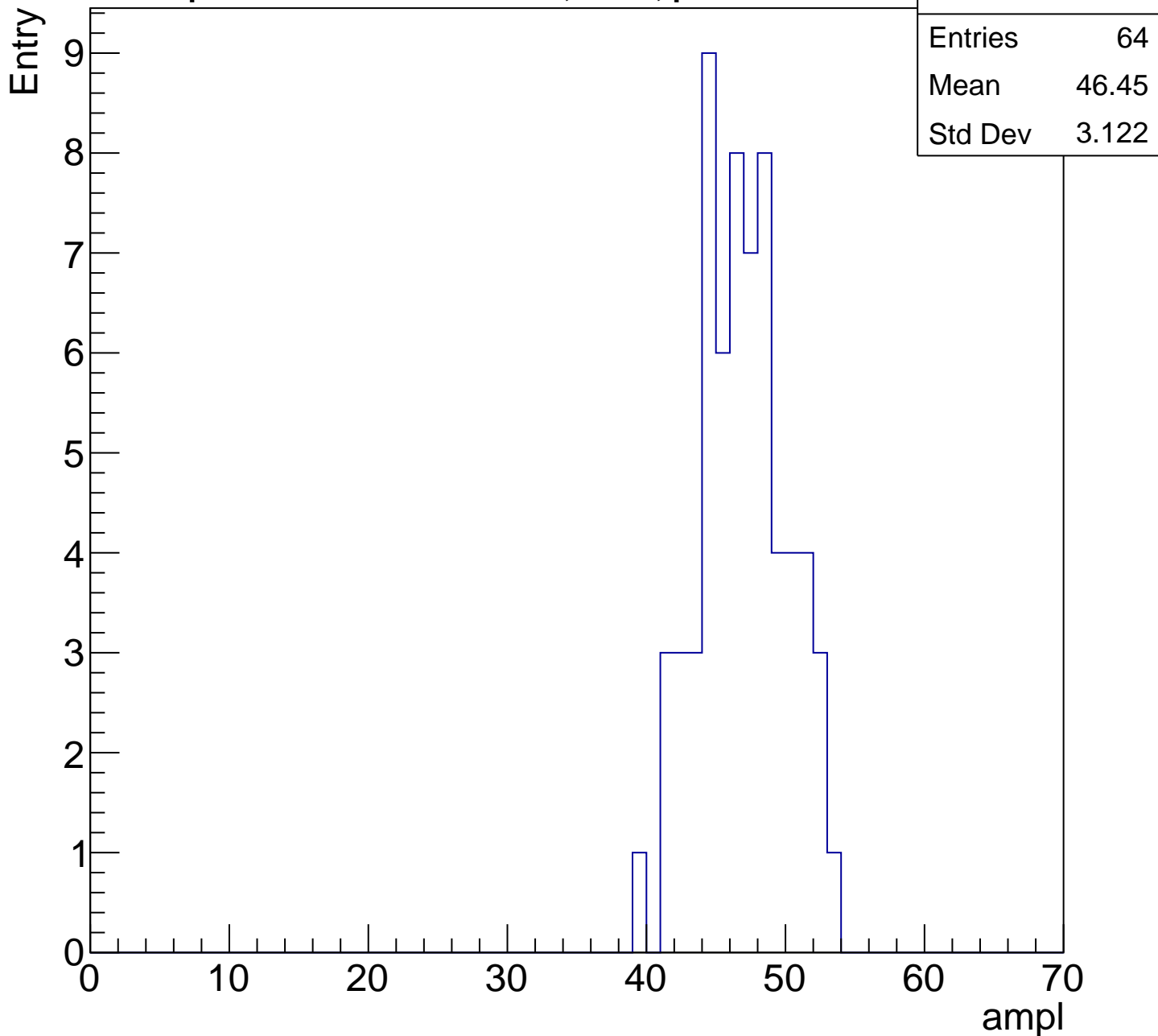
Gaus mean : 40.2105

Gaus Width: 3.4077



B1L100S, U1-ch58, adc3

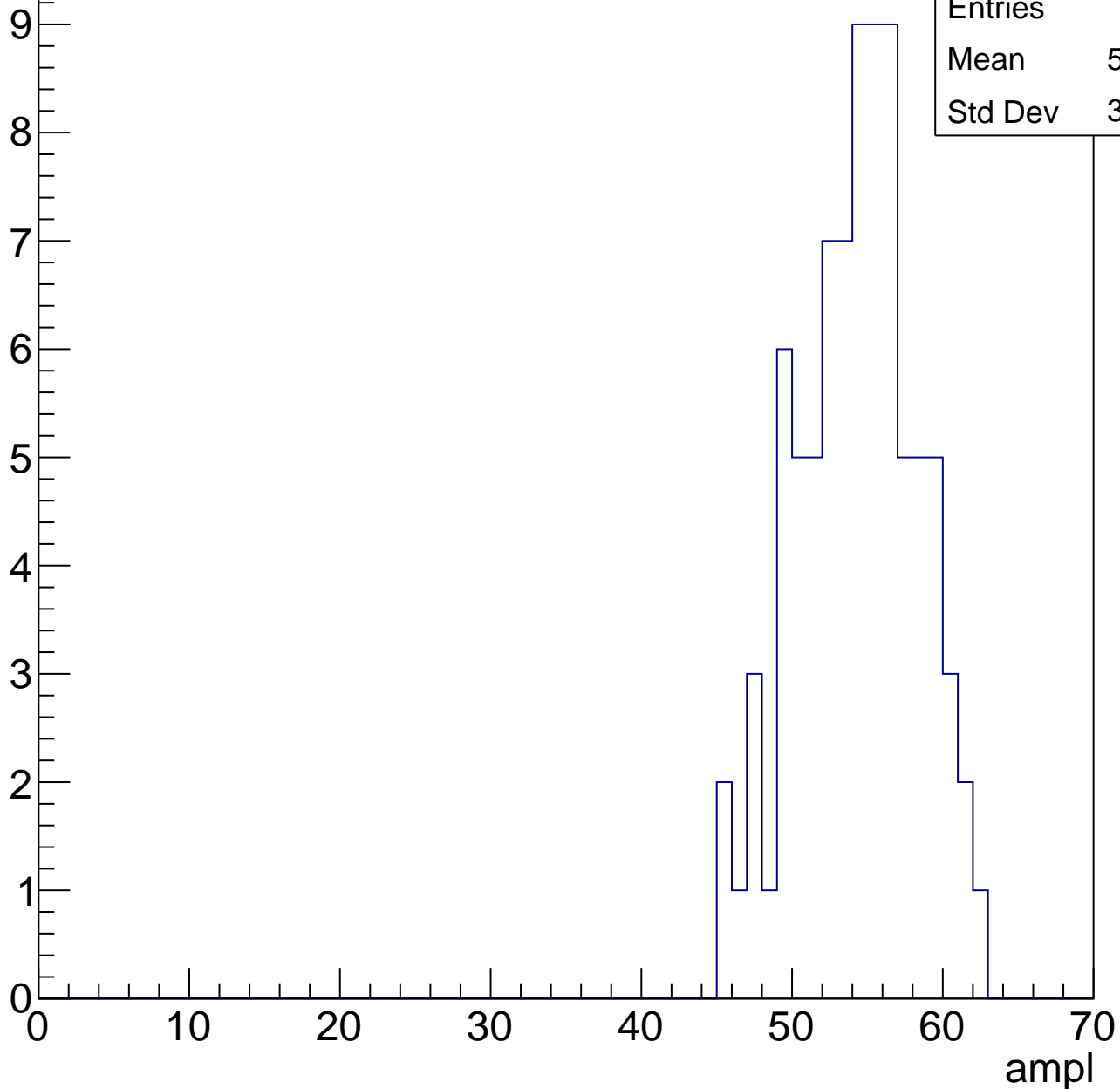
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch58, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

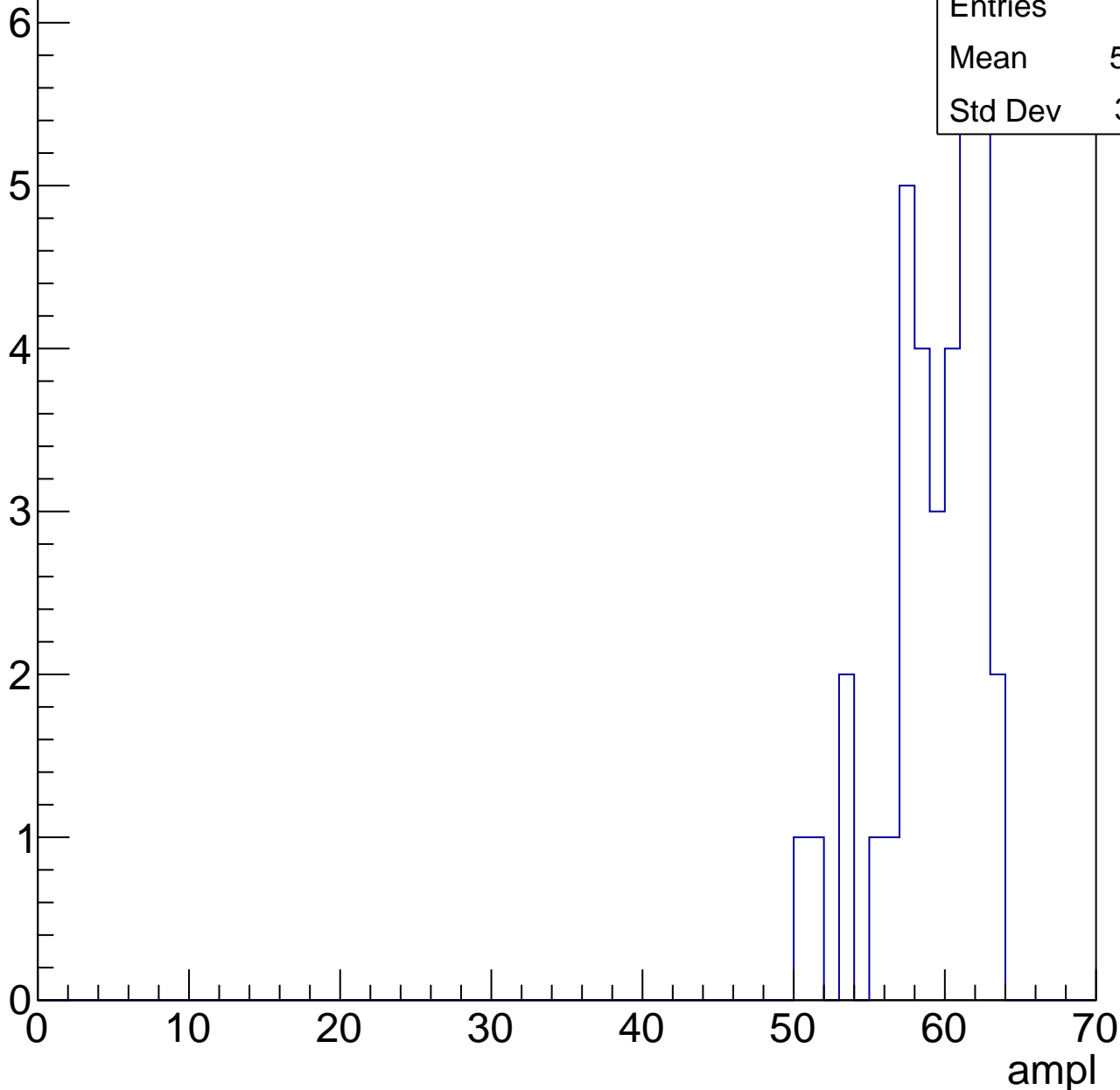


B1L100S, U1-ch58, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.78
Std Dev	3.241

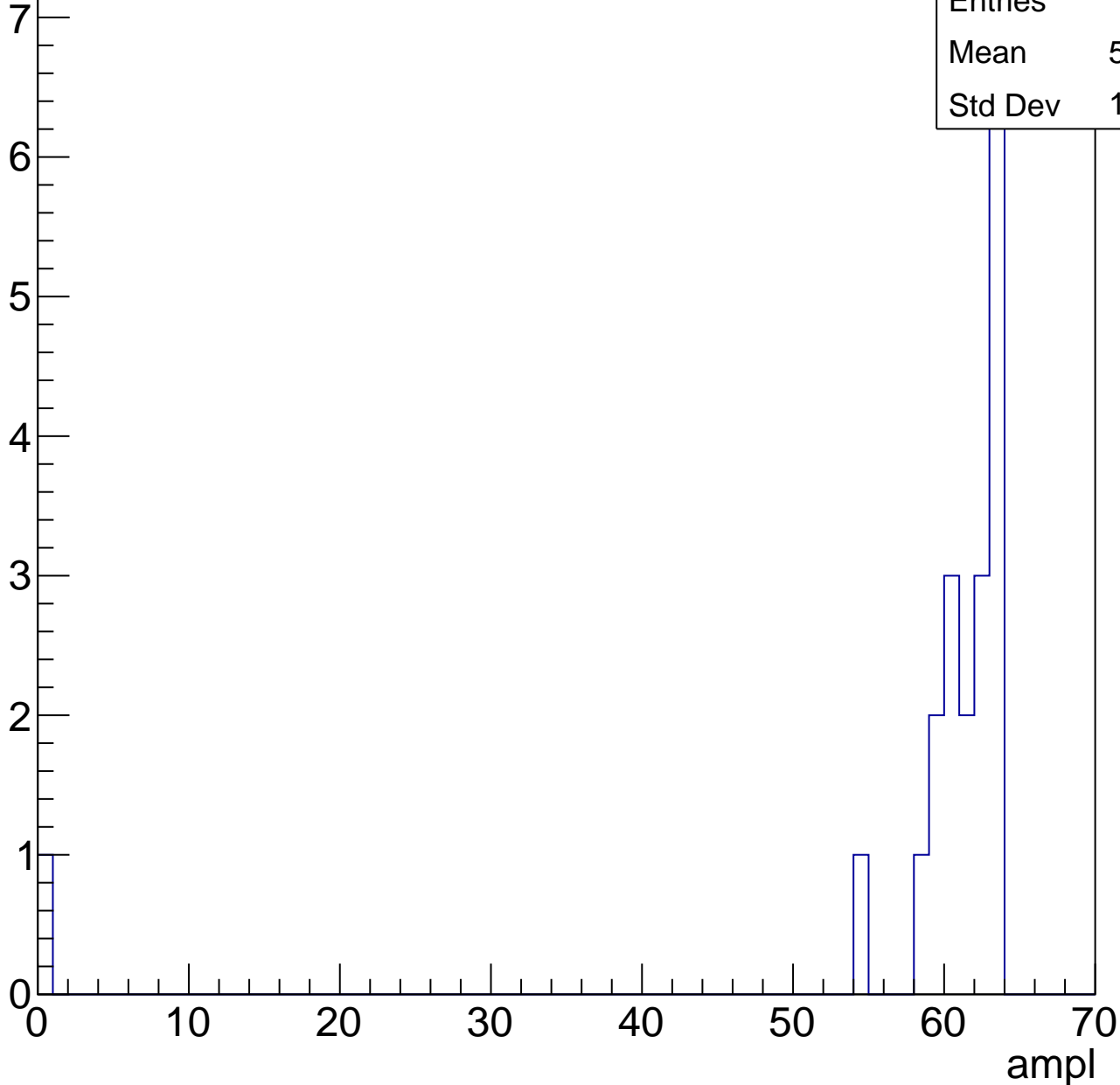


B1L100S, U1-ch58, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	57.95
Std Dev	13.48



B1L100S, U1-ch58, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch59, adc0

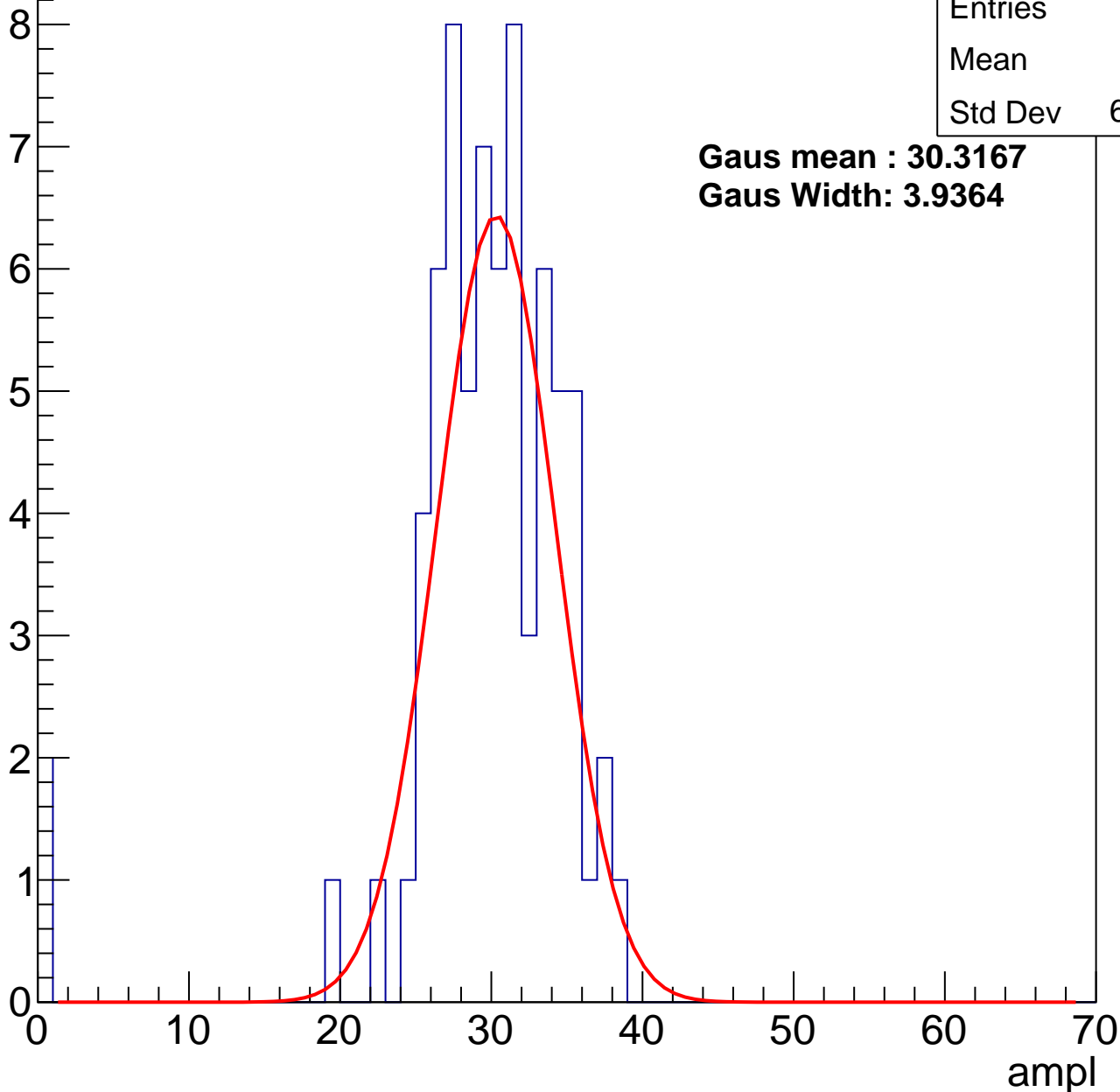
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	29.1
Std Dev	6.163

Gaus mean : 30.3167

Gaus Width: 3.9364



B1L100S, U1-ch59, adc1

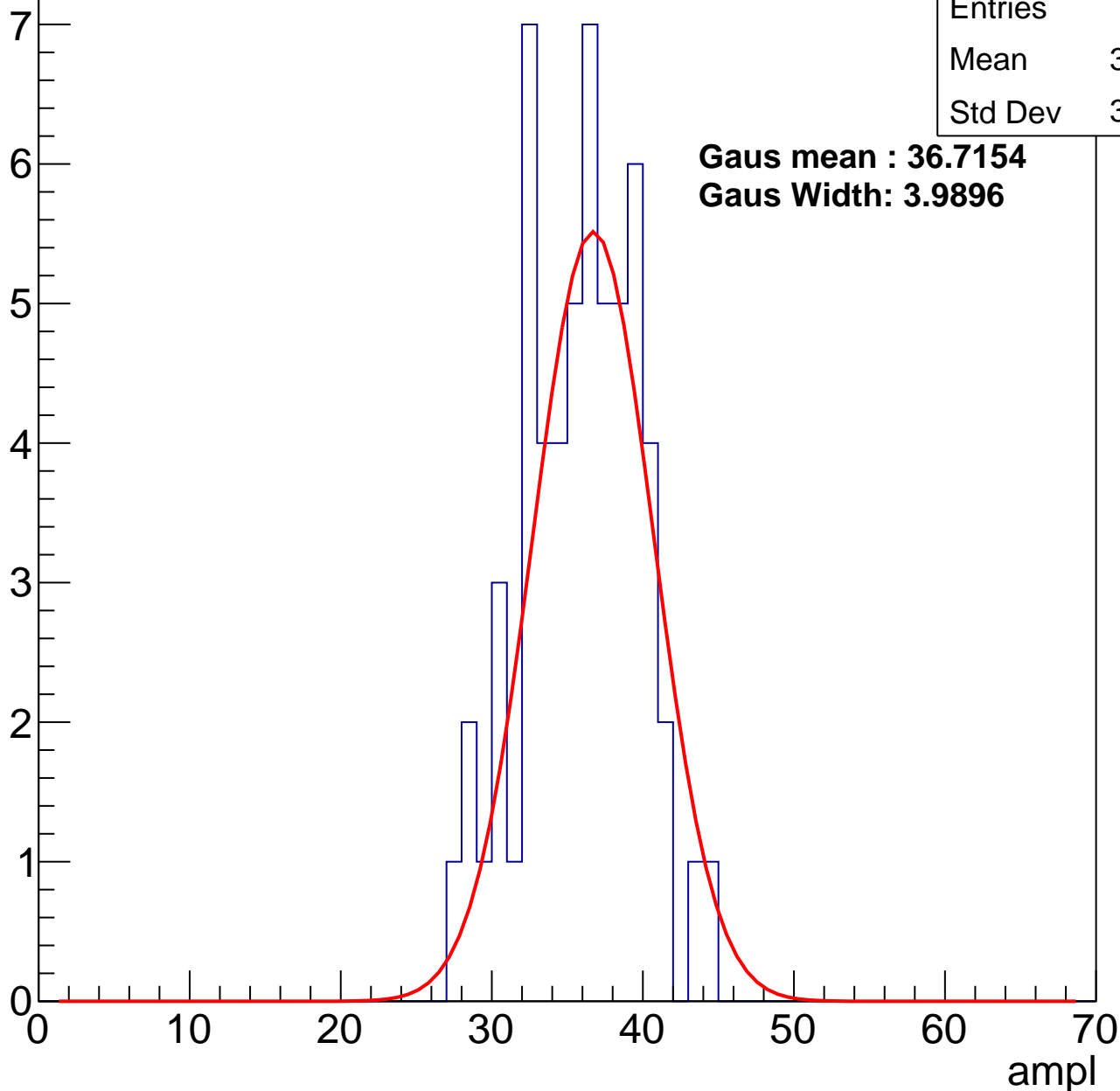
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.42
Std Dev	3.788

Gaus mean : 36.7154

Gaus Width: 3.9896



B1L100S, U1-ch59, adc2

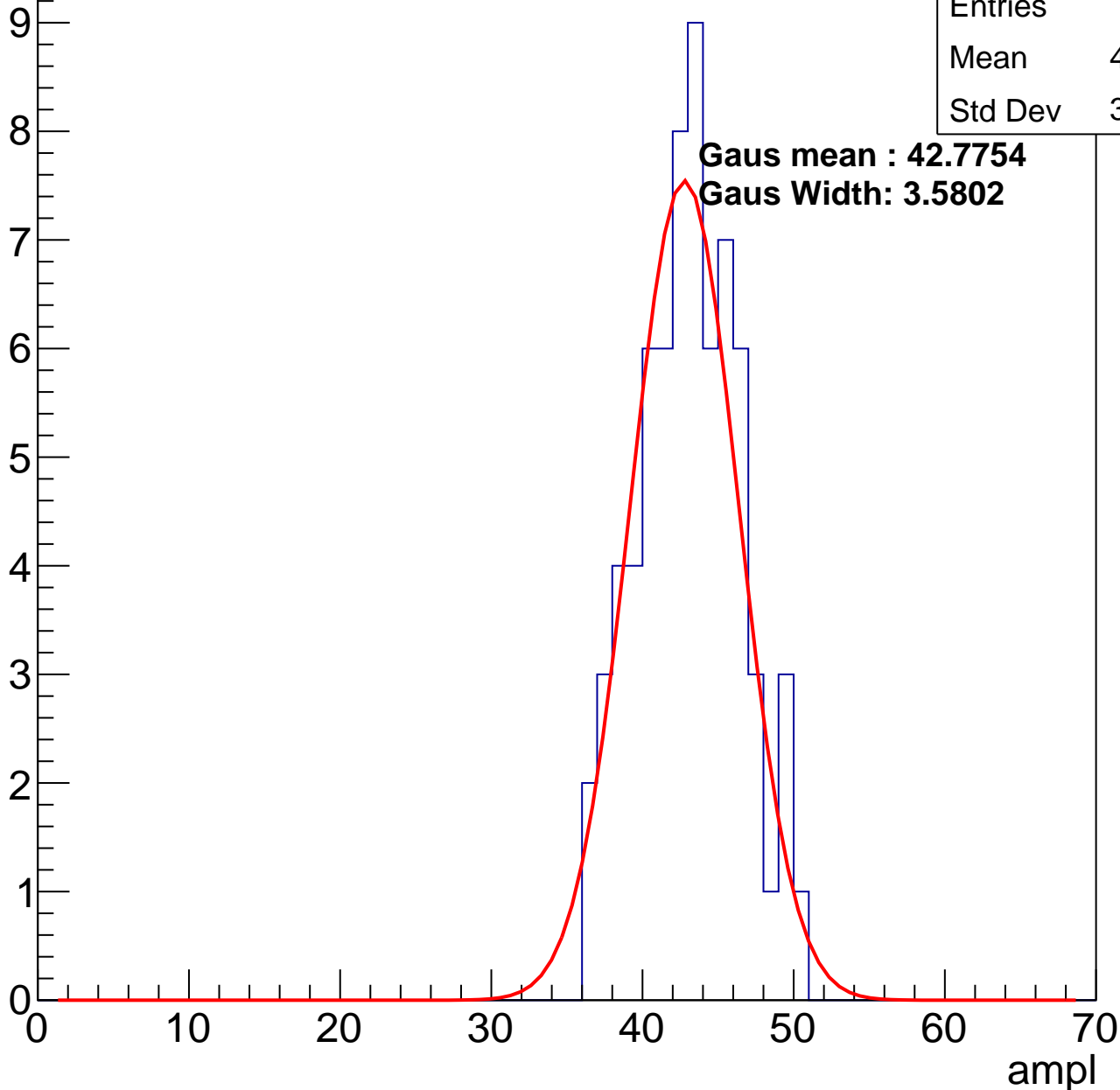
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	42.62
Std Dev	3.328

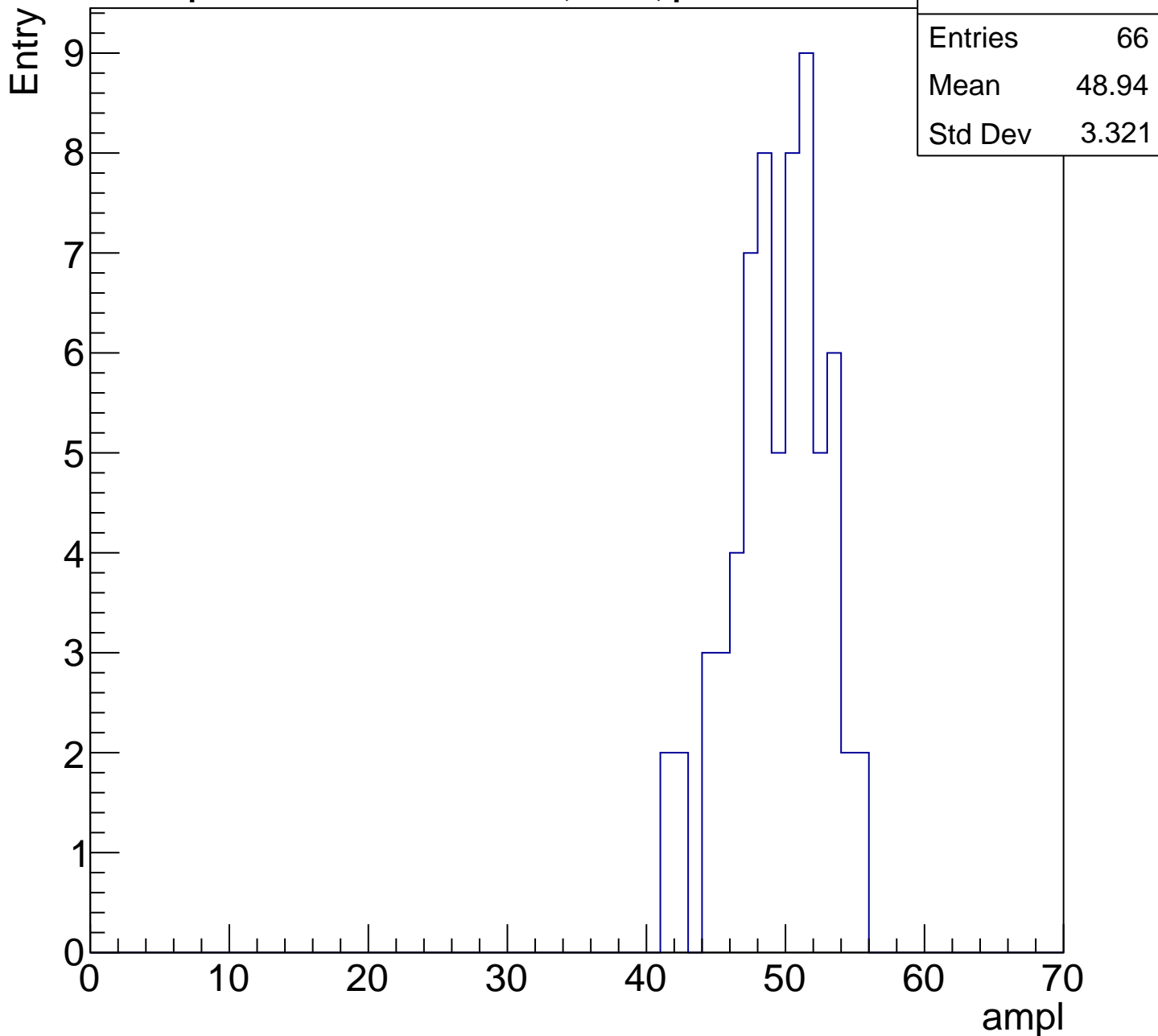
Gaus mean : 42.7754

Gaus Width: 3.5802



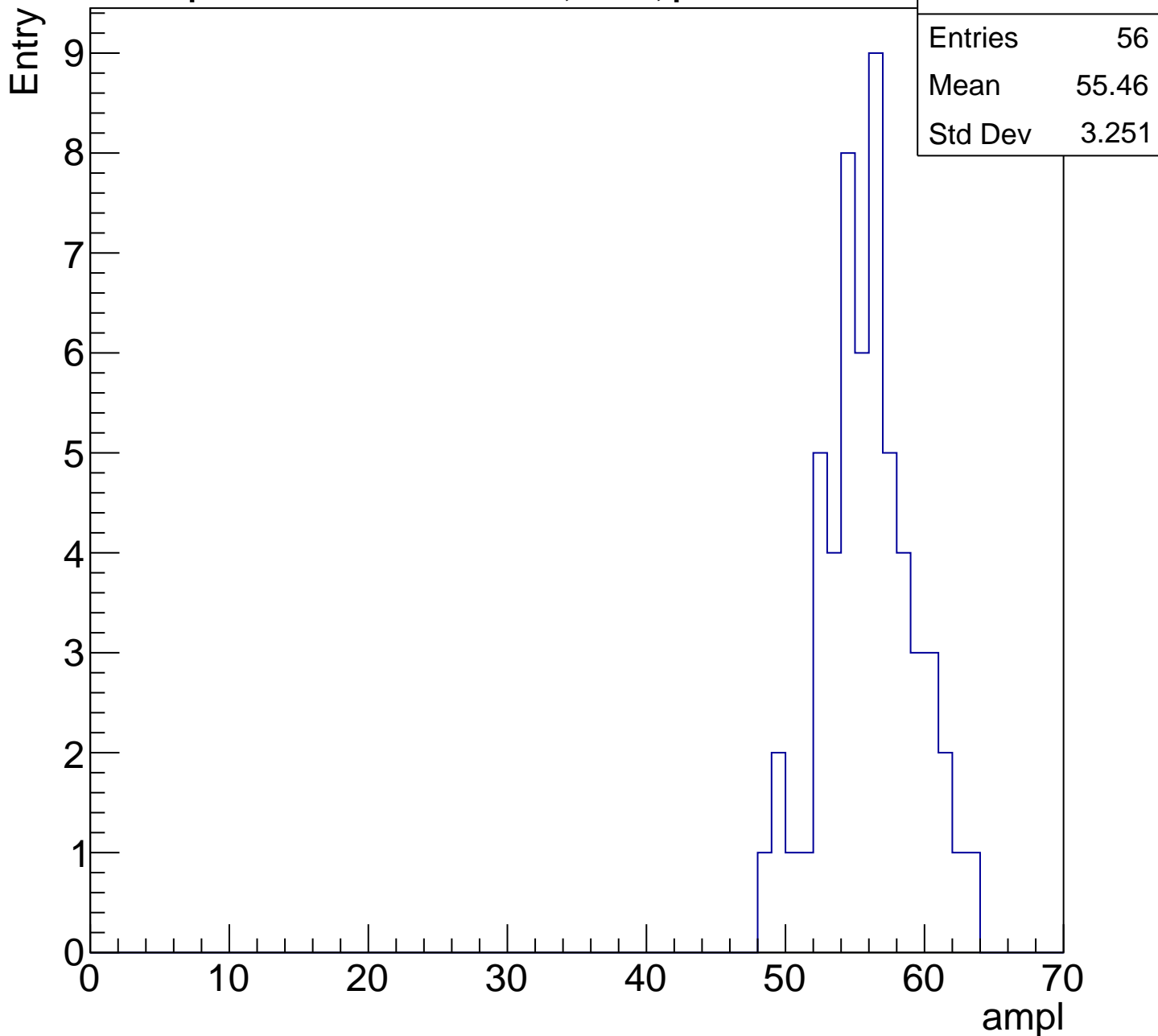
B1L100S, U1-ch59, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch59, adc4

calib_packv5_042523_0143.root, FC#4, port A2

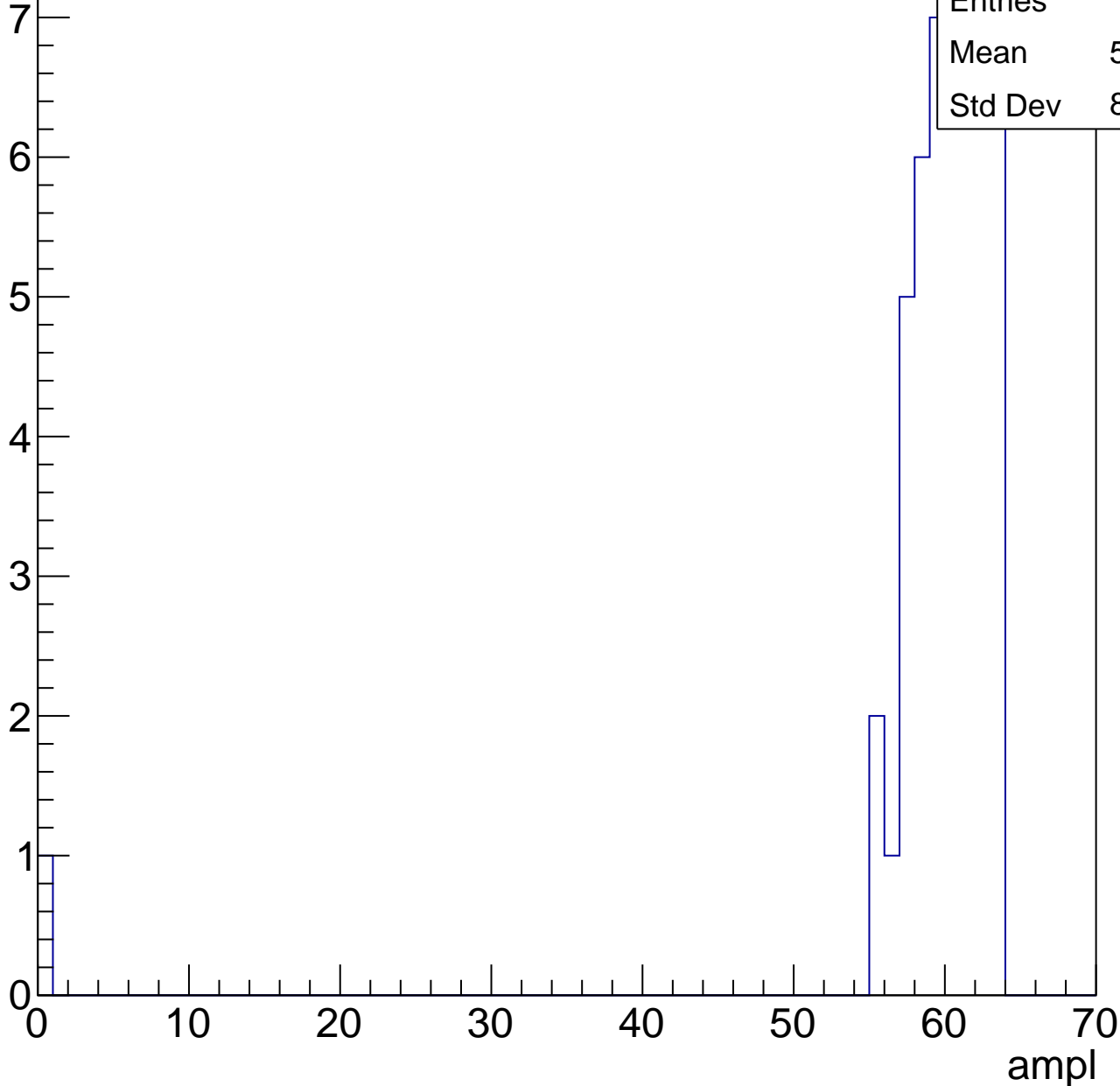


B1L100S, U1-ch59, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.68
Std Dev	8.664



B1L100S, U1-ch59, adc6

calib_packv5_042523_0143.root, FC#4, port A2

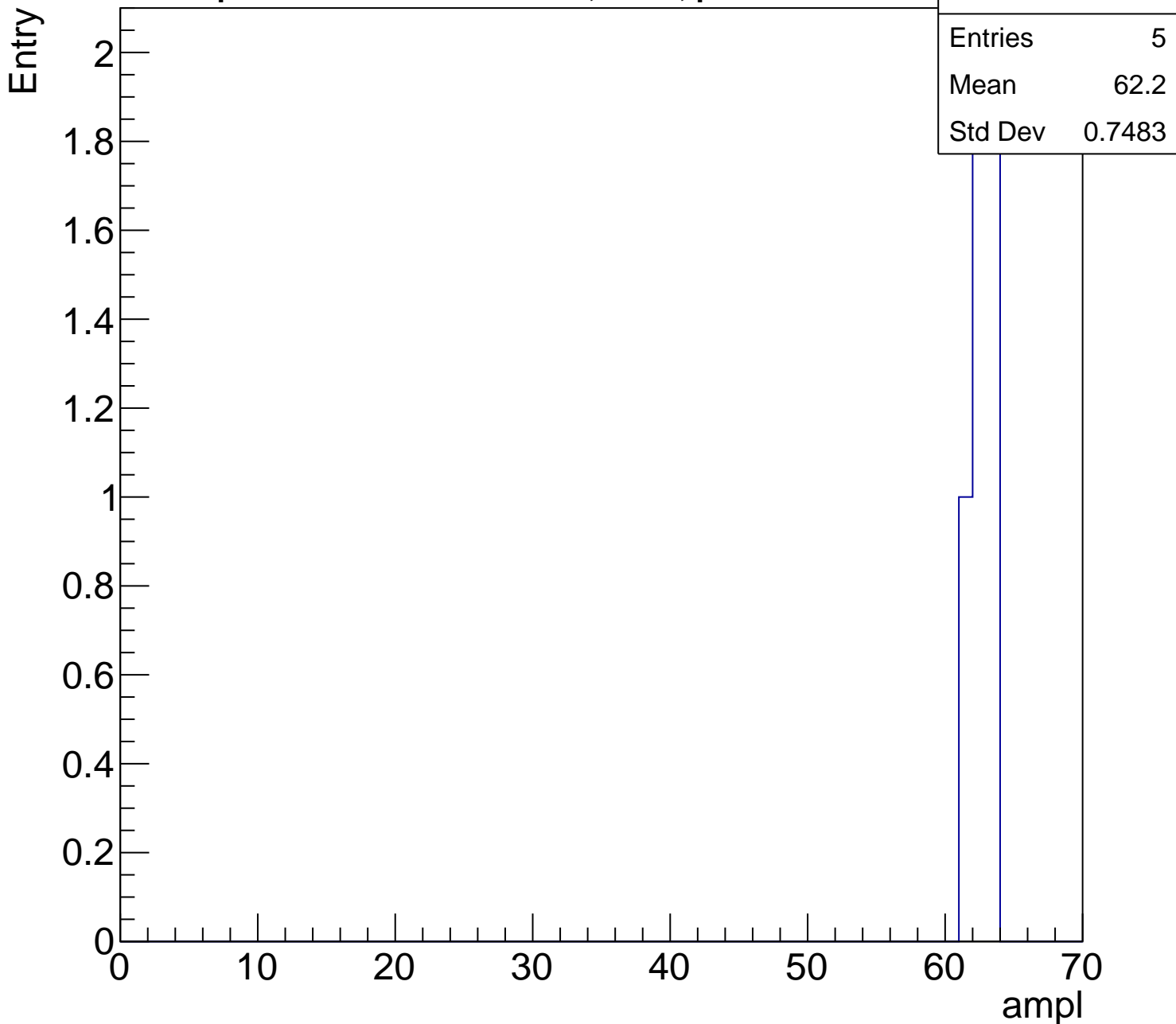
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch59, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch60, adc0

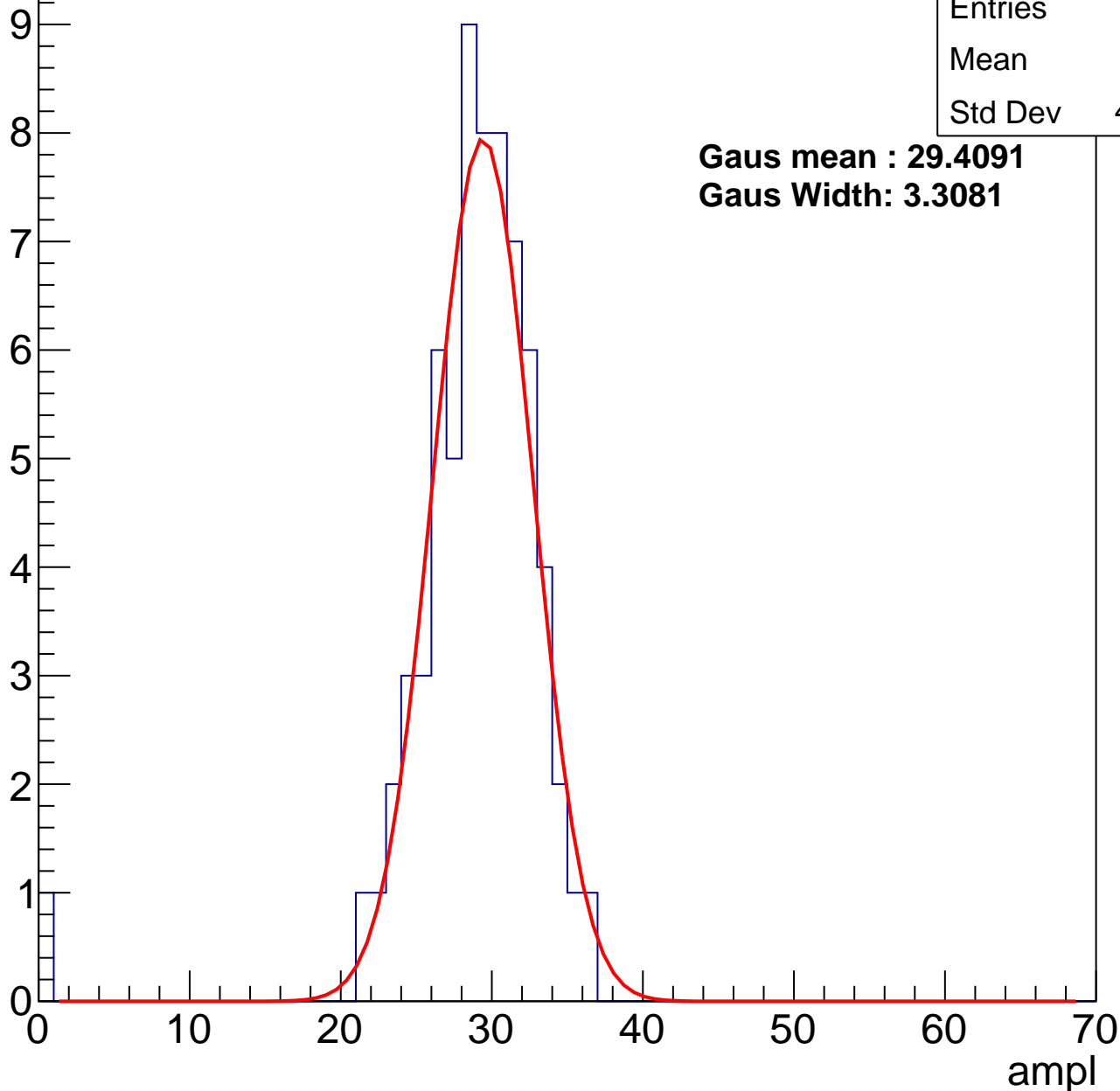
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	28.4
Std Dev	4.691

Gaus mean : 29.4091

Gaus Width: 3.3081



B1L100S, U1-ch60, adc1

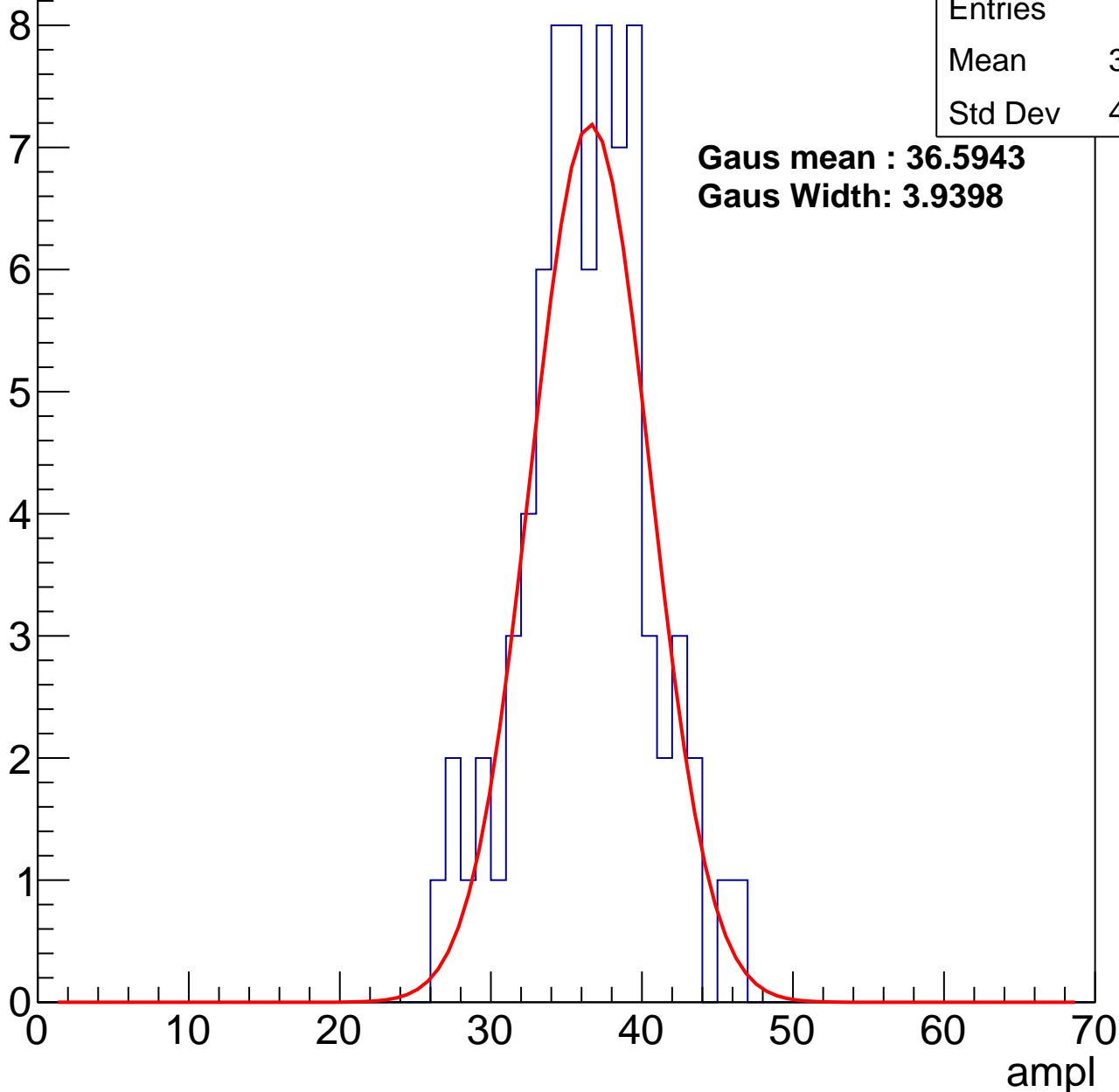
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	35.87
Std Dev	4.085

Gaus mean : 36.5943

Gaus Width: 3.9398



B1L100S, U1-ch60, adc2

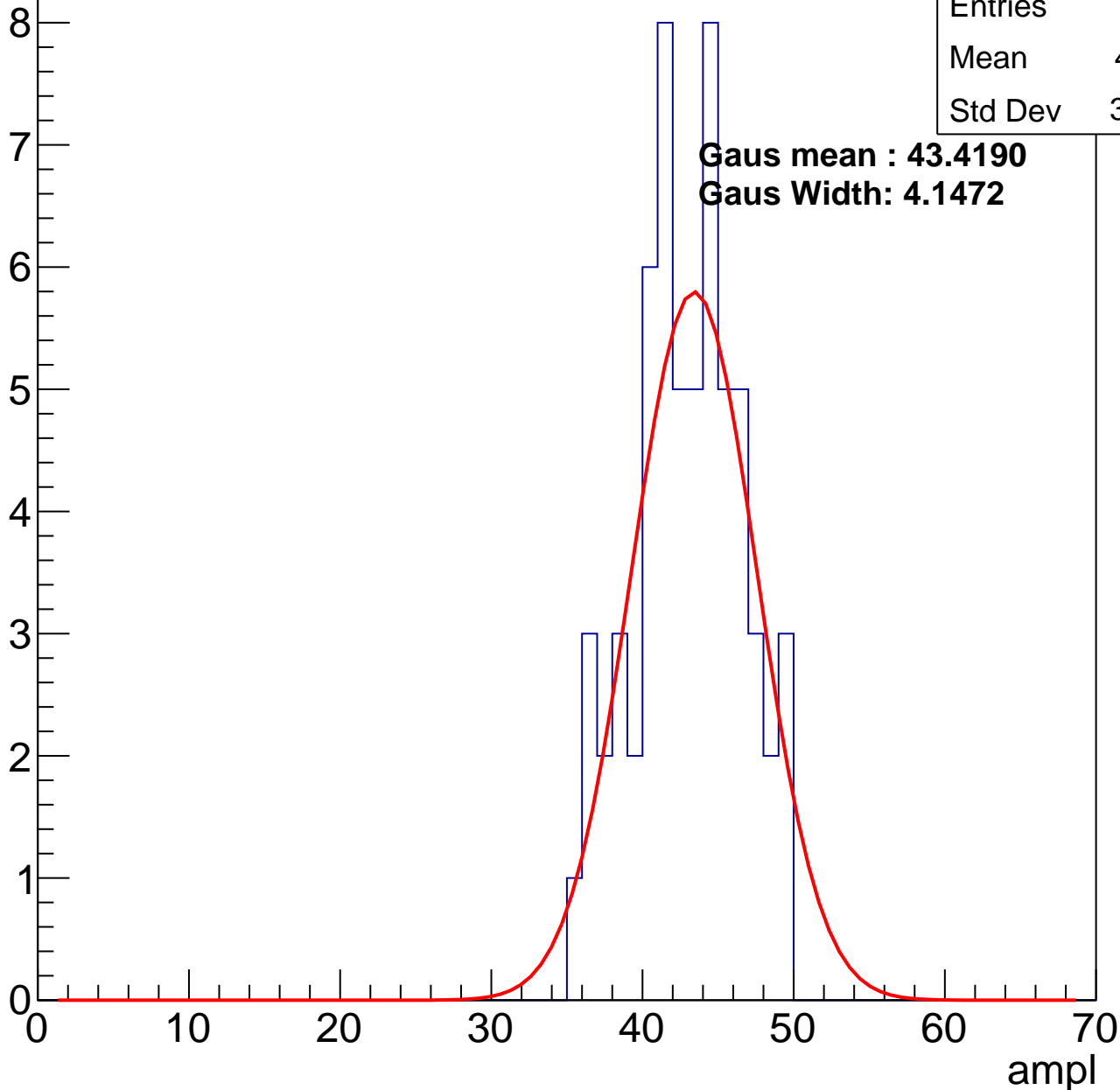
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	42.51
Std Dev	3.495

Gaus mean : 43.4190

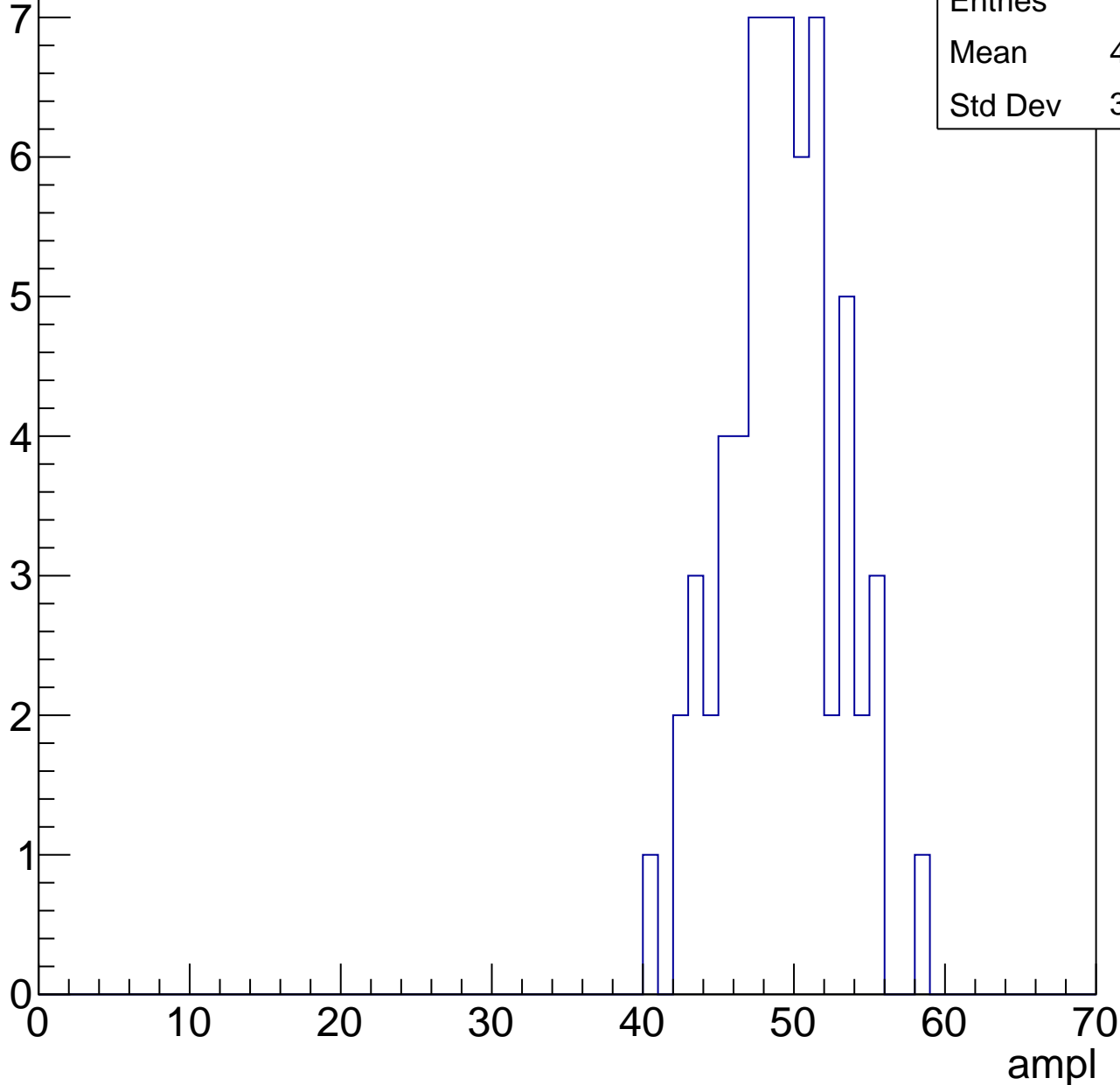
Gaus Width: 4.1472



B1L100S, U1-ch60, adc3

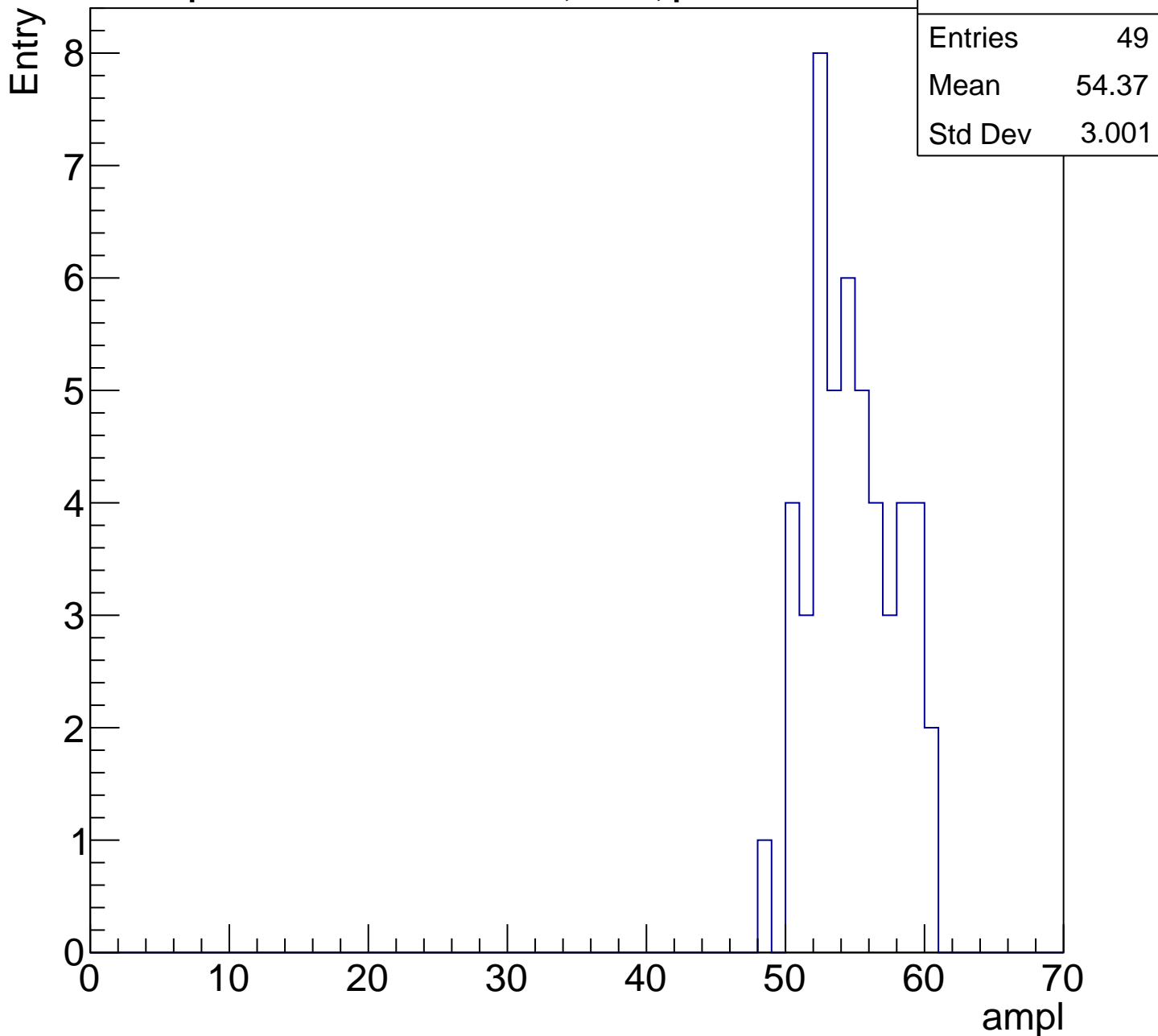
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch60, adc4

calib_packv5_042523_0143.root, FC#4, port A2

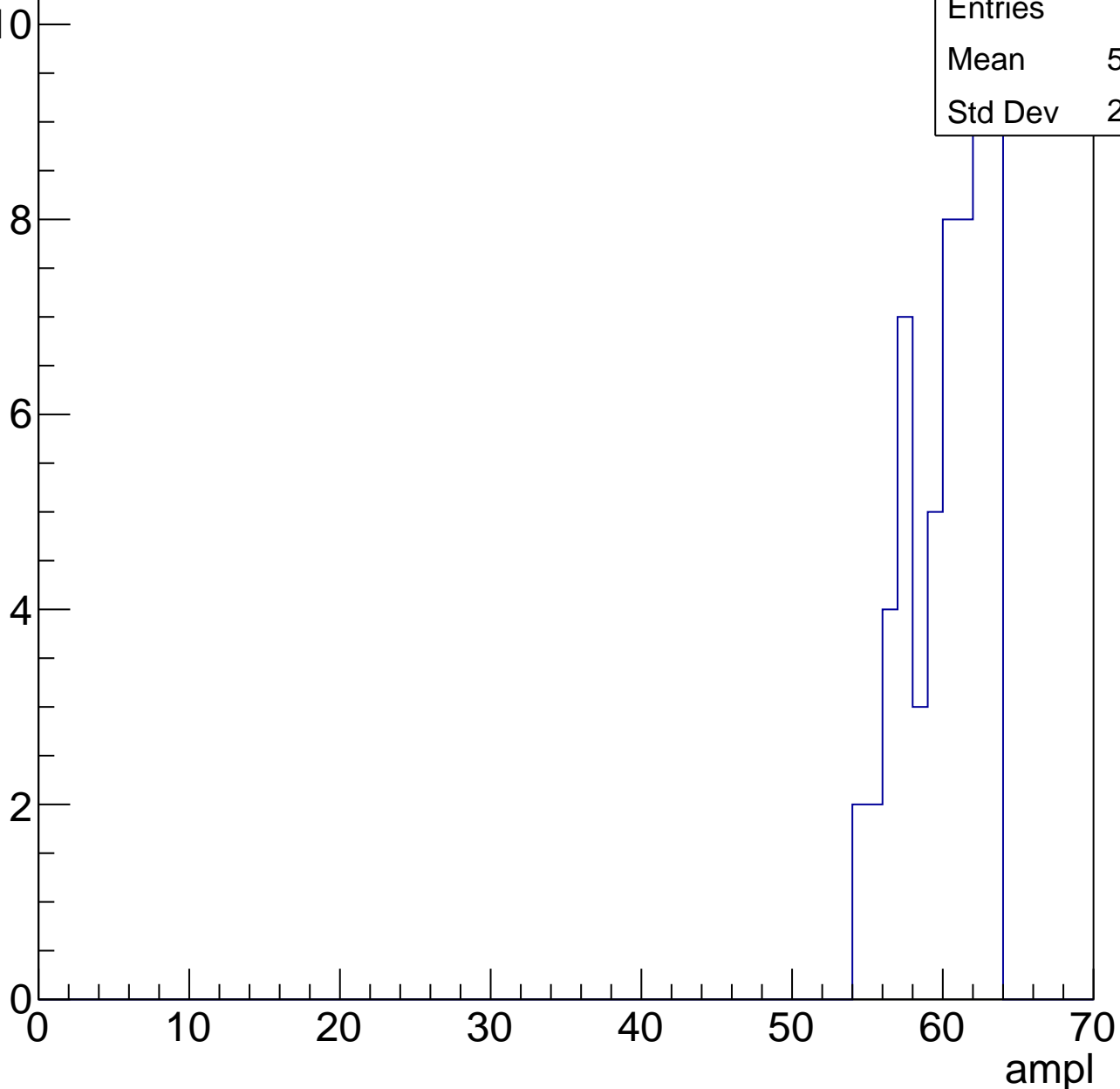


B1L100S, U1-ch60, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	59.76
Std Dev	2.615



B1L100S, U1-ch60, adc6

calib_packv5_042523_0143.root, FC#4, port A2

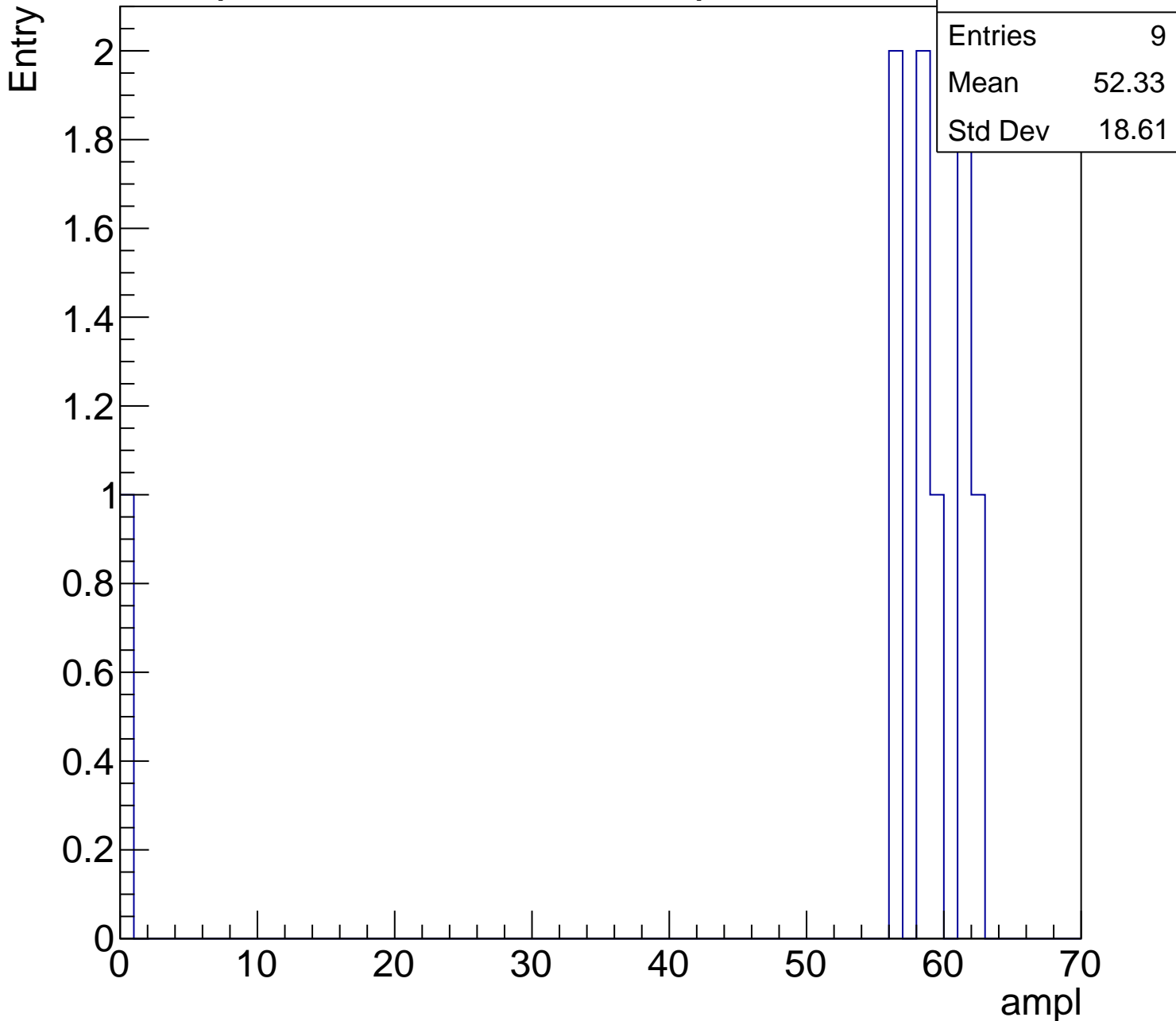
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	52.33
Std Dev	18.61

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch60, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch61, adc0

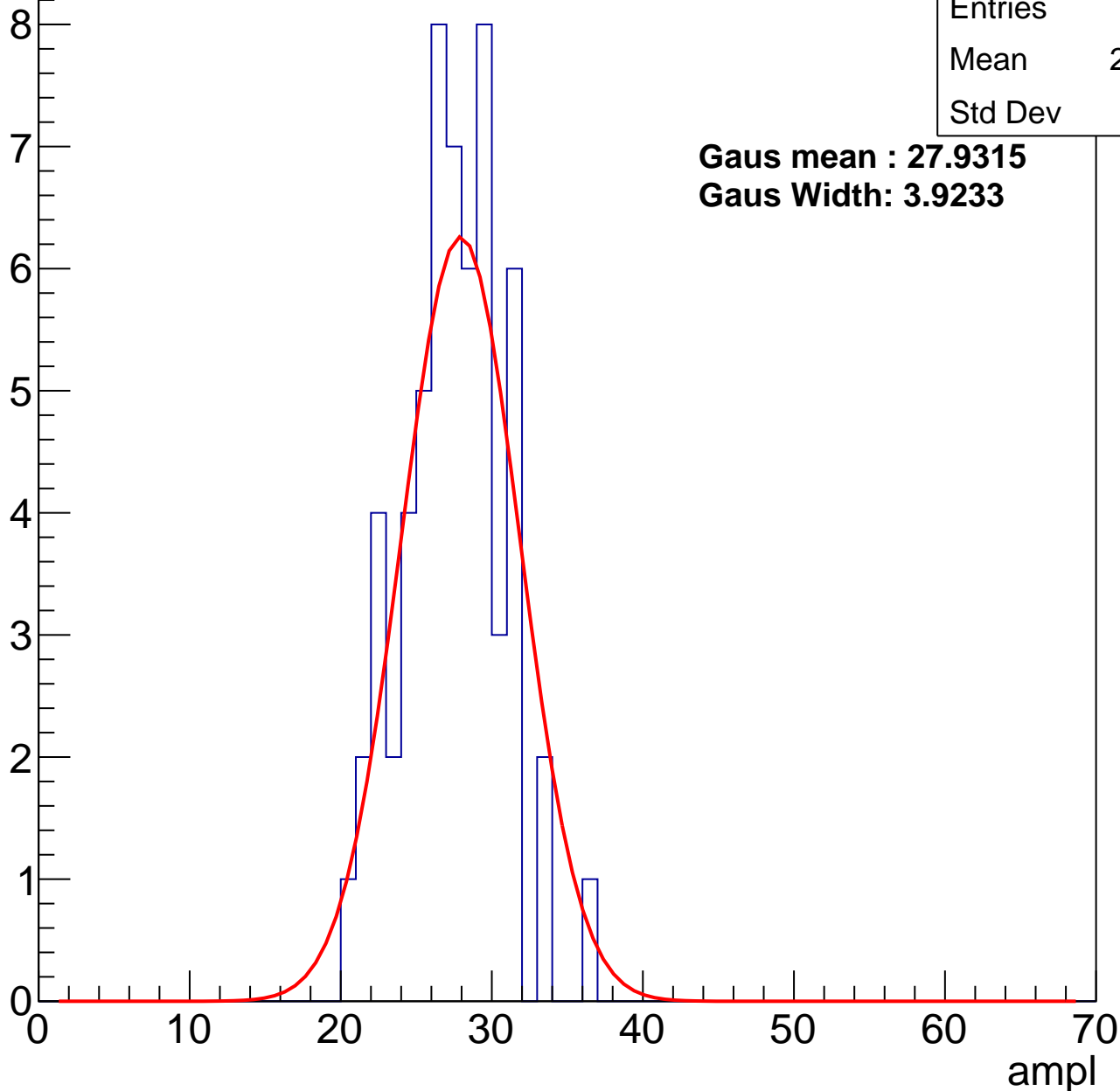
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	26.98
Std Dev	3.27

Gaus mean : 27.9315

Gaus Width: 3.9233



B1L100S, U1-ch61, adc1

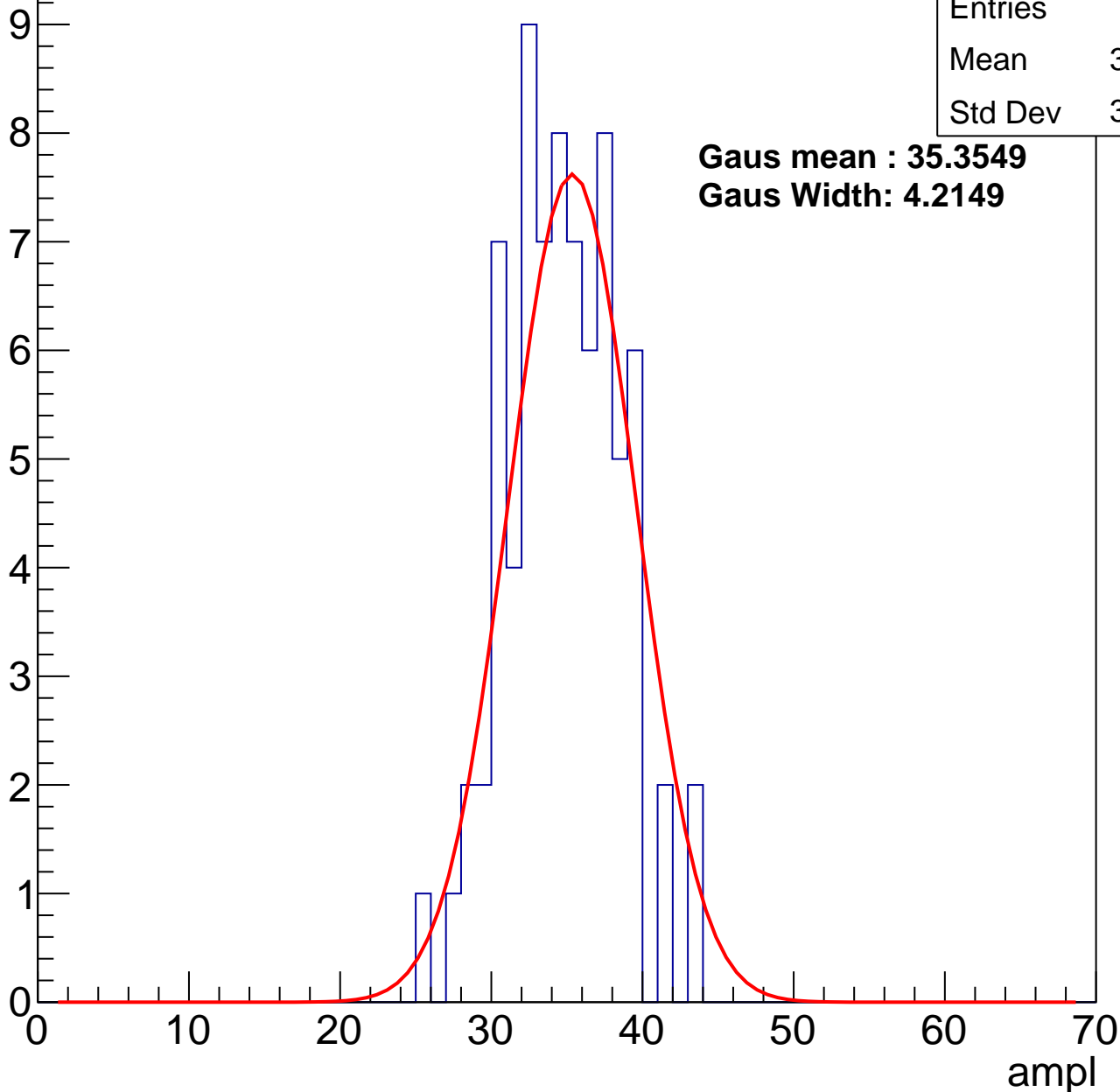
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	34.29
Std Dev	3.657

Gaus mean : 35.3549

Gaus Width: 4.2149



B1L100S, U1-ch61, adc2

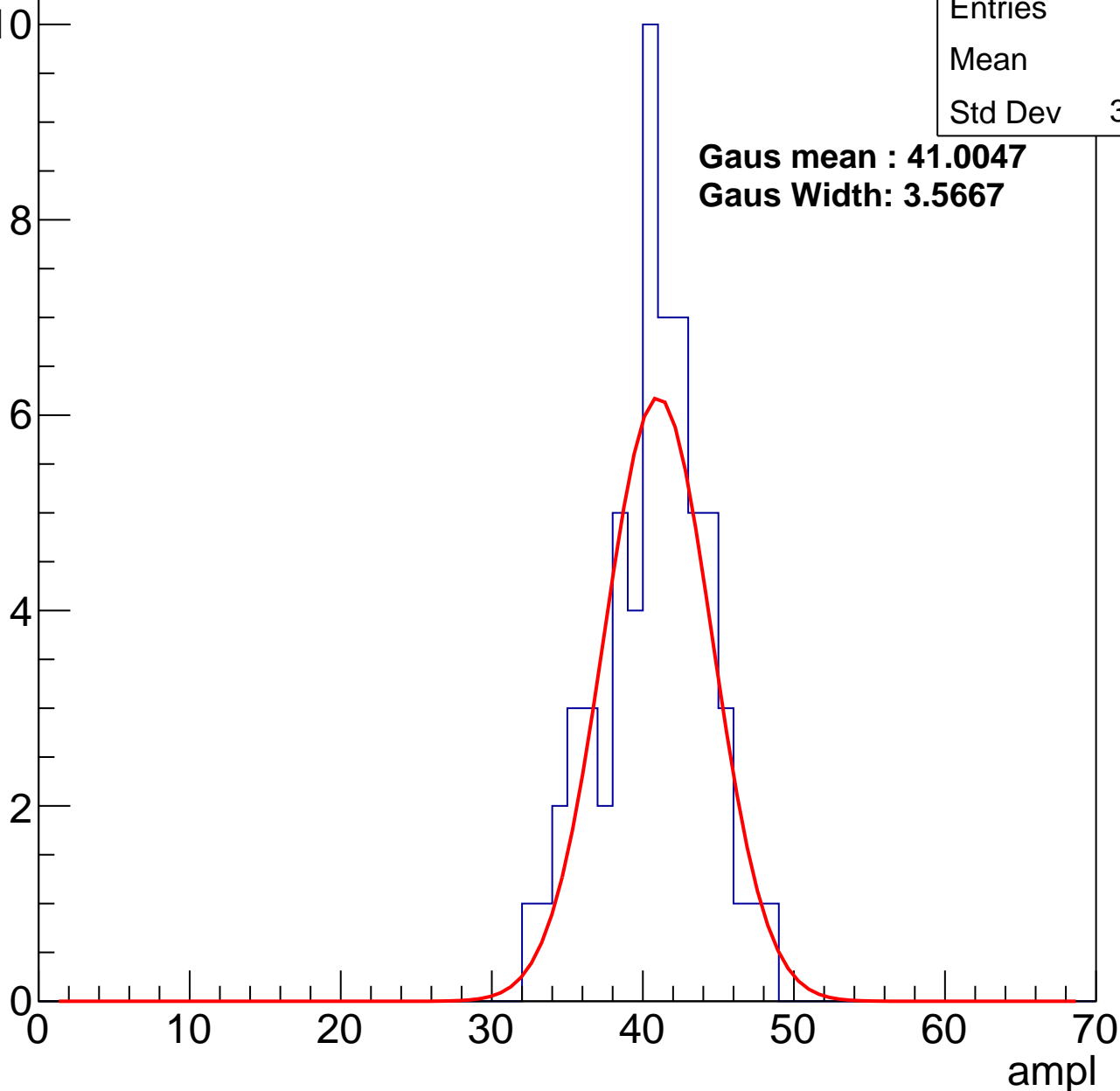
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	40.3
Std Dev	3.456

Gaus mean : 41.0047

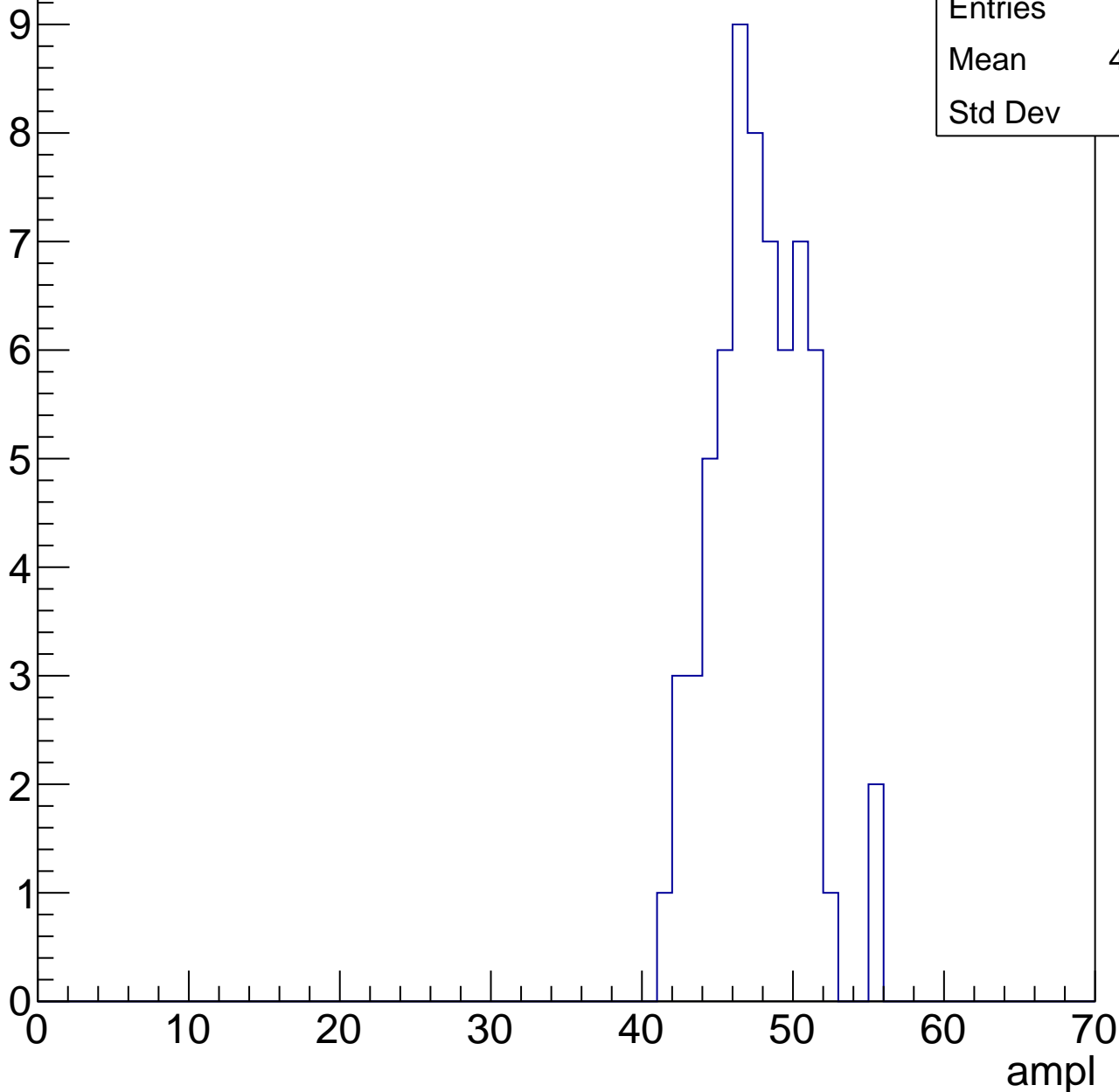
Gaus Width: 3.5667



B1L100S, U1-ch61, adc3

calib_packv5_042523_0143.root, FC#4, port A2

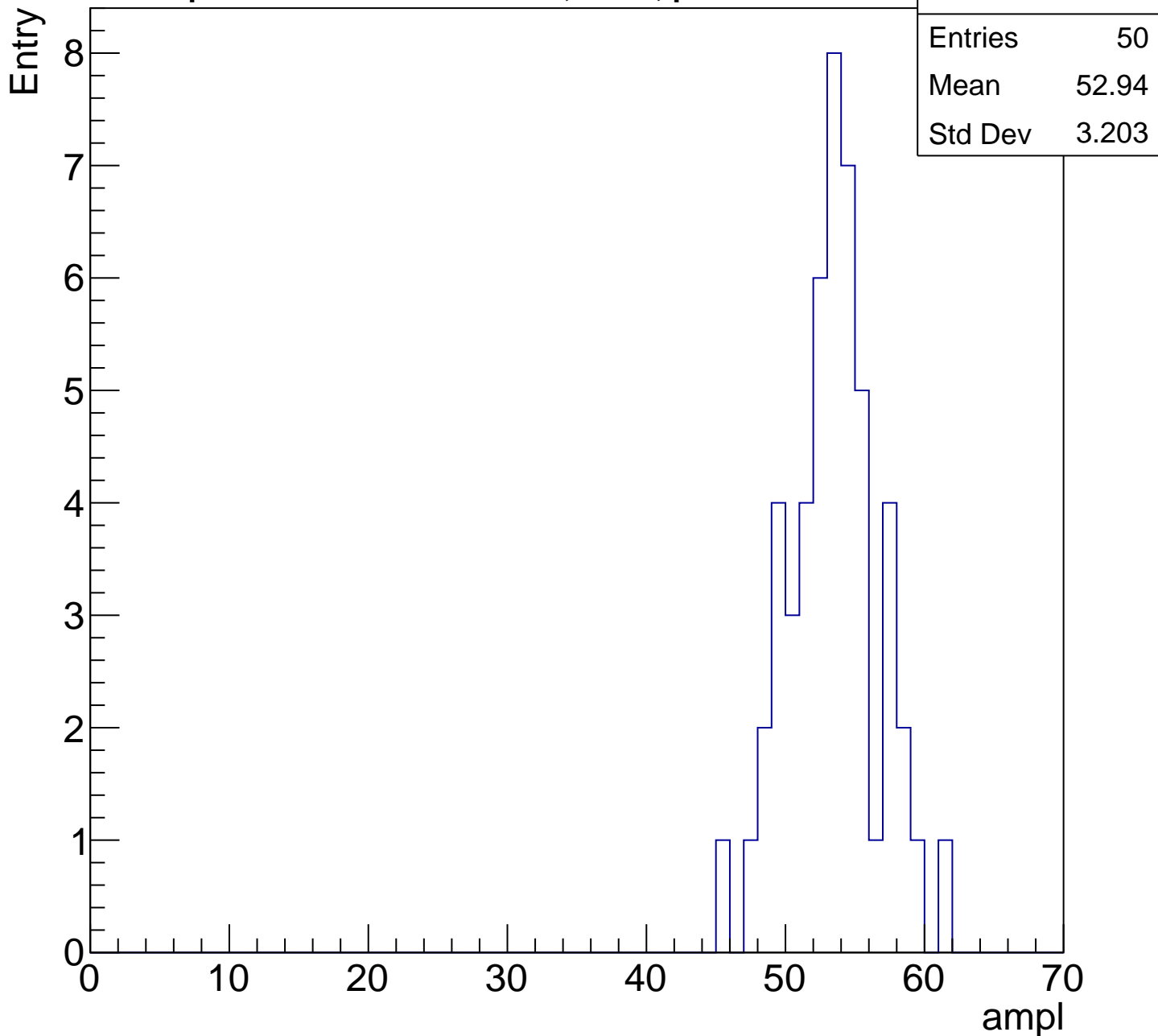
Entry



Entries	64
Mean	47.25
Std Dev	3

B1L100S, U1-ch61, adc4

calib_packv5_042523_0143.root, FC#4, port A2

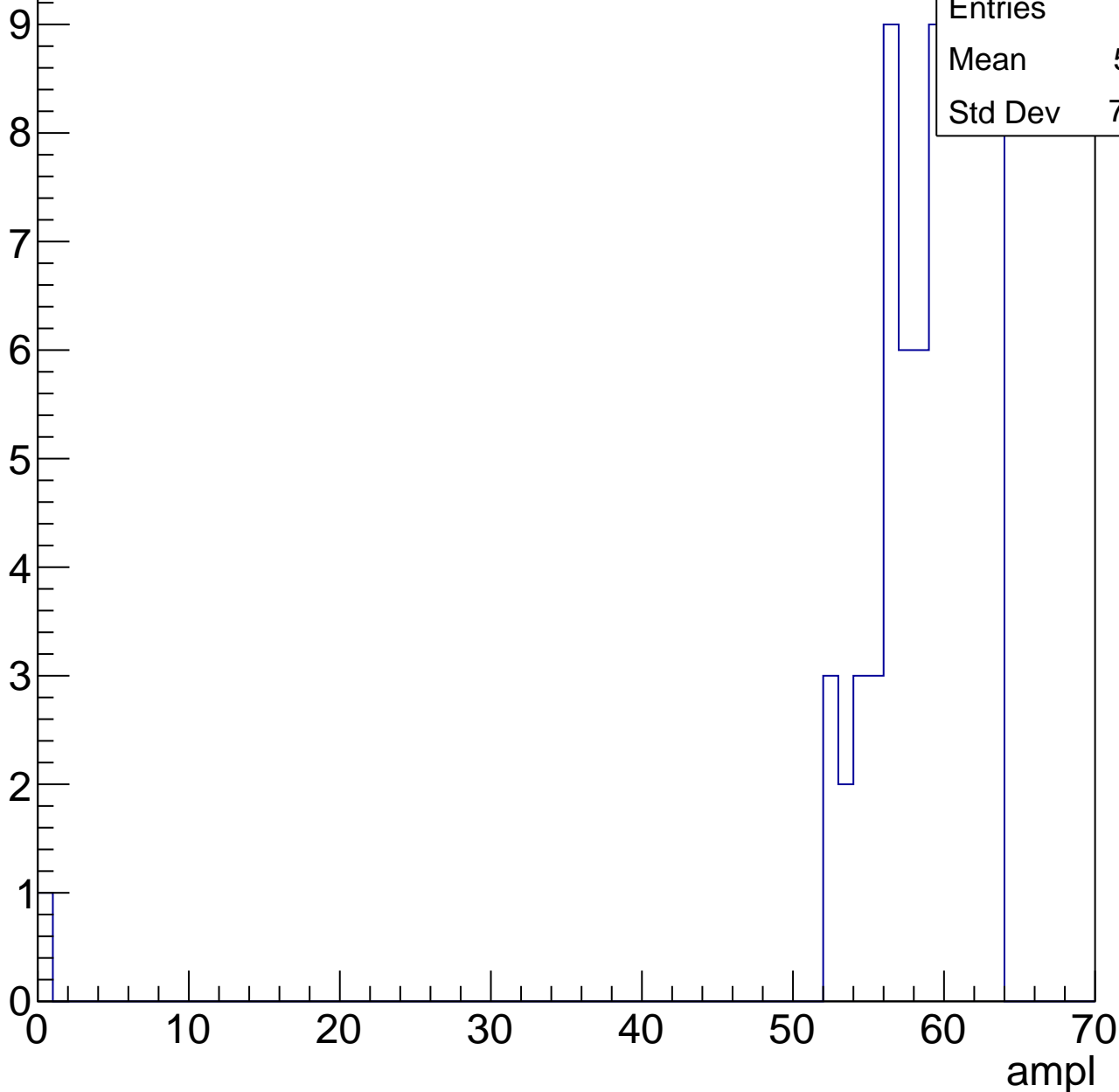


B1L100S, U1-ch61, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	58.01
Std Dev	7.319



B1L100S, U1-ch61, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

0

10

20

30

40

50

60

70

ampl

Entries	6
Mean	60.33
Std Dev	1.886

B1L100S, U1-ch61, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch62, adc0

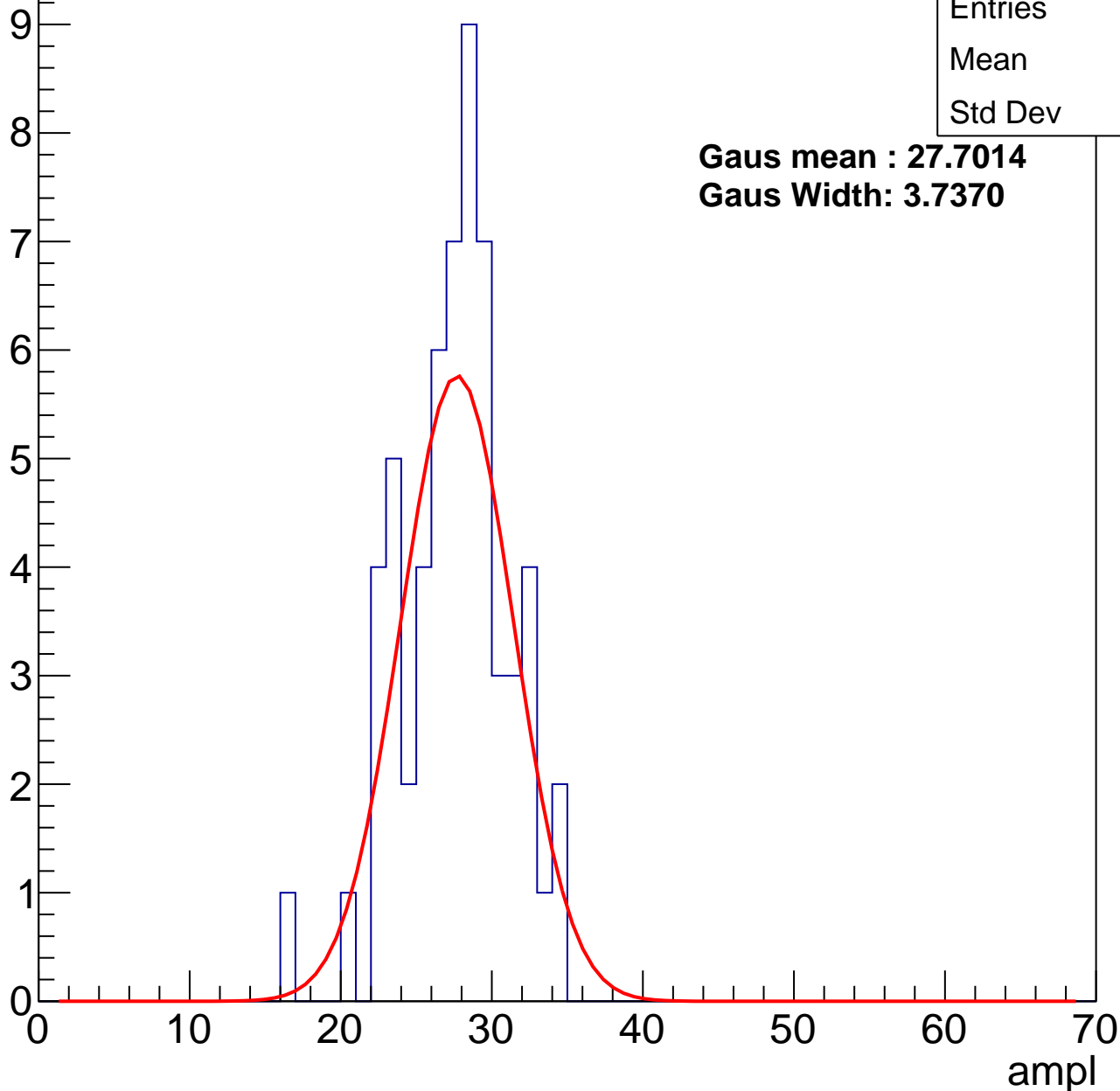
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	27.1
Std Dev	3.53

Gaus mean : 27.7014

Gaus Width: 3.7370



B1L100S, U1-ch62, adc1

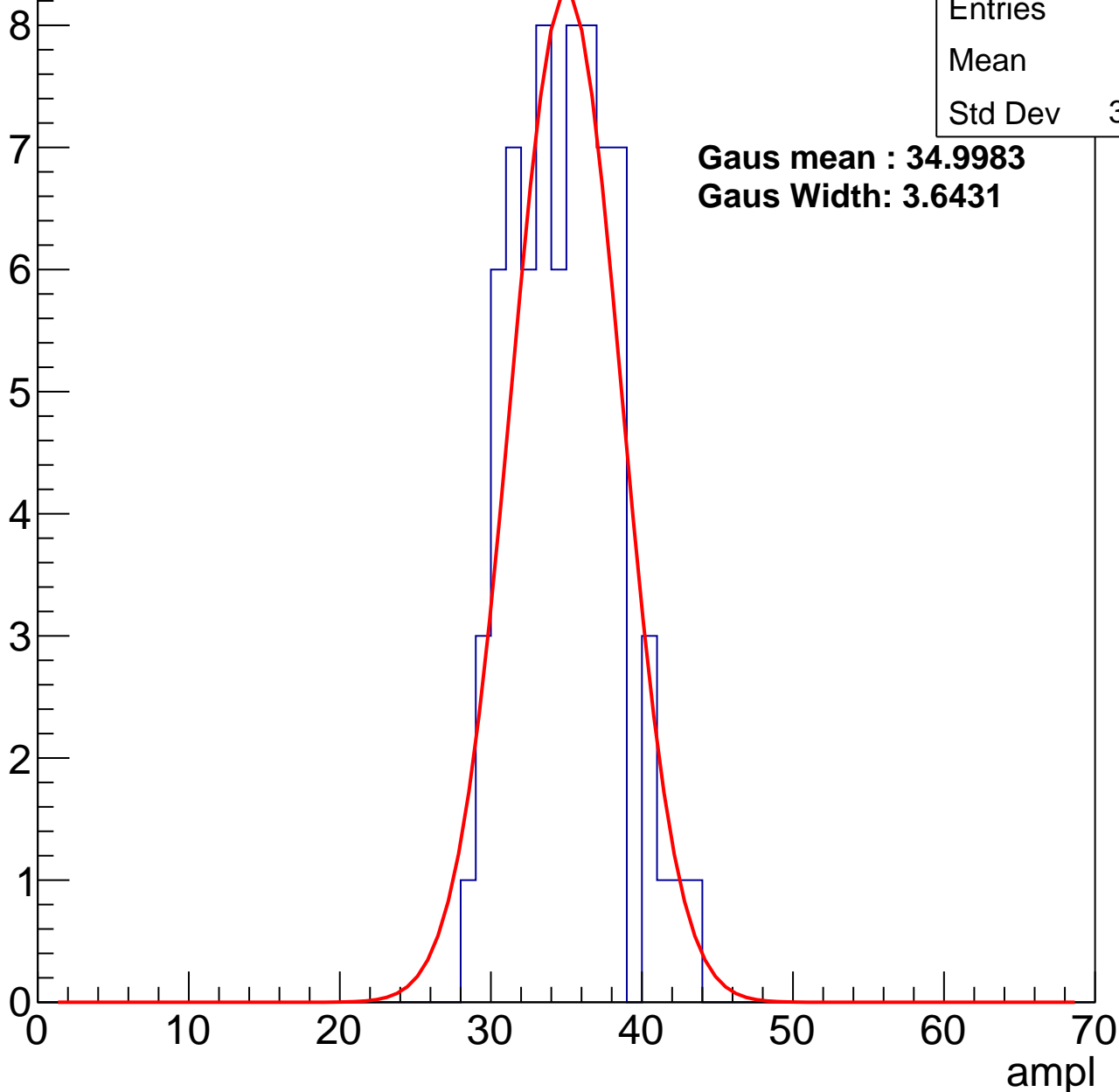
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	34.4
Std Dev	3.326

Gaus mean : 34.9983

Gaus Width: 3.6431



B1L100S, U1-ch62, adc2

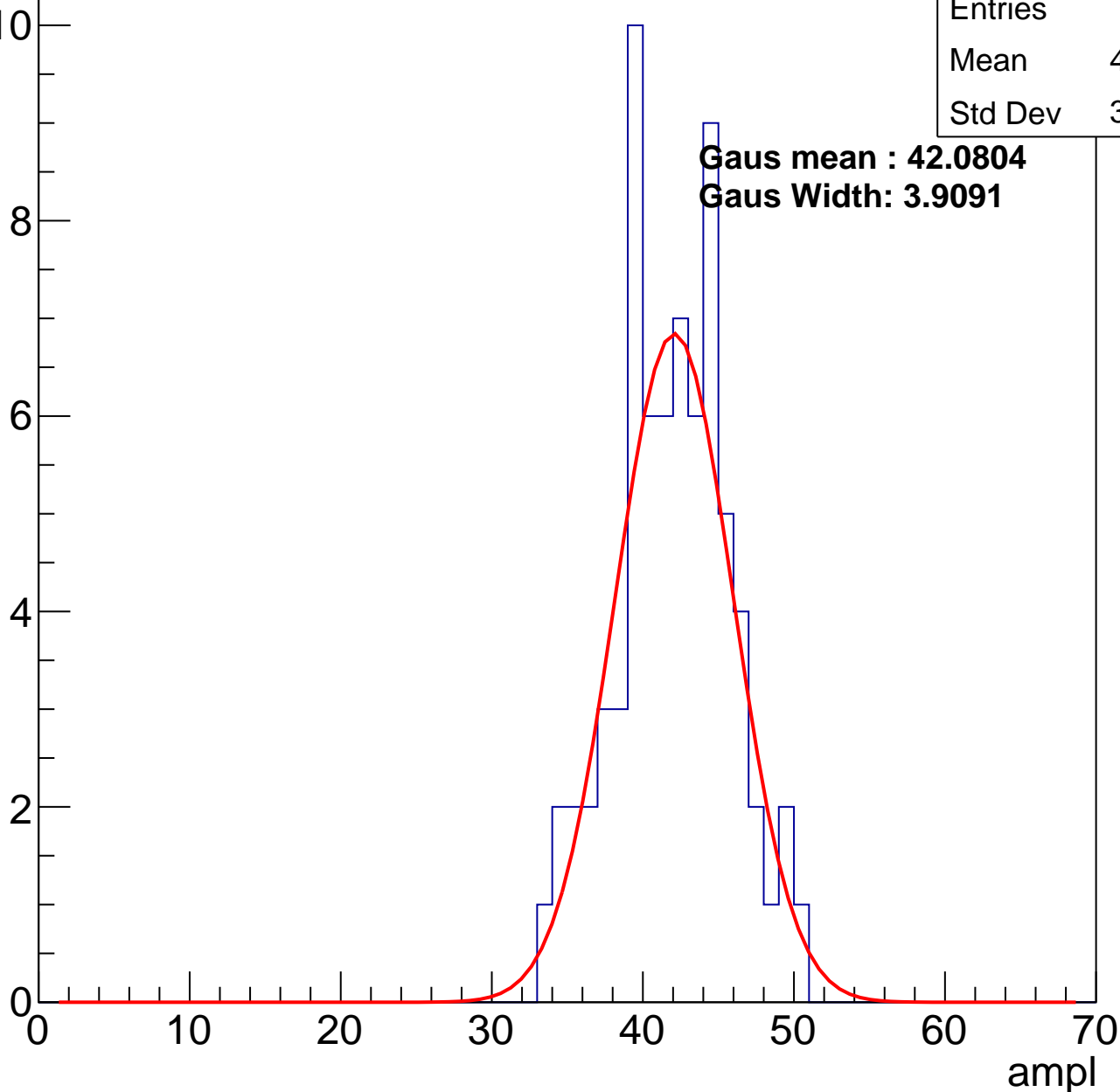
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.54
Std Dev	3.738

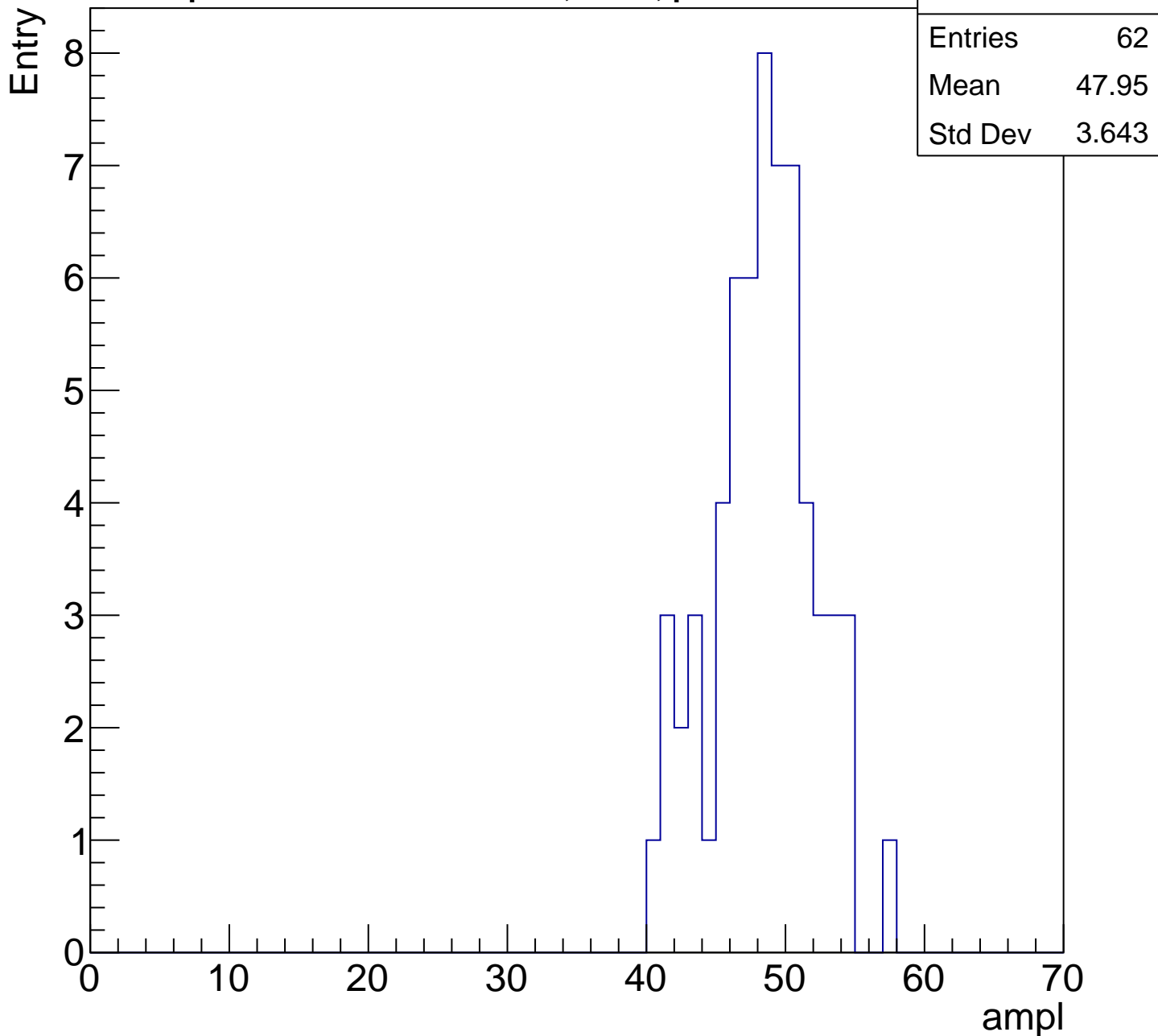
Gaus mean : 42.0804

Gaus Width: 3.9091



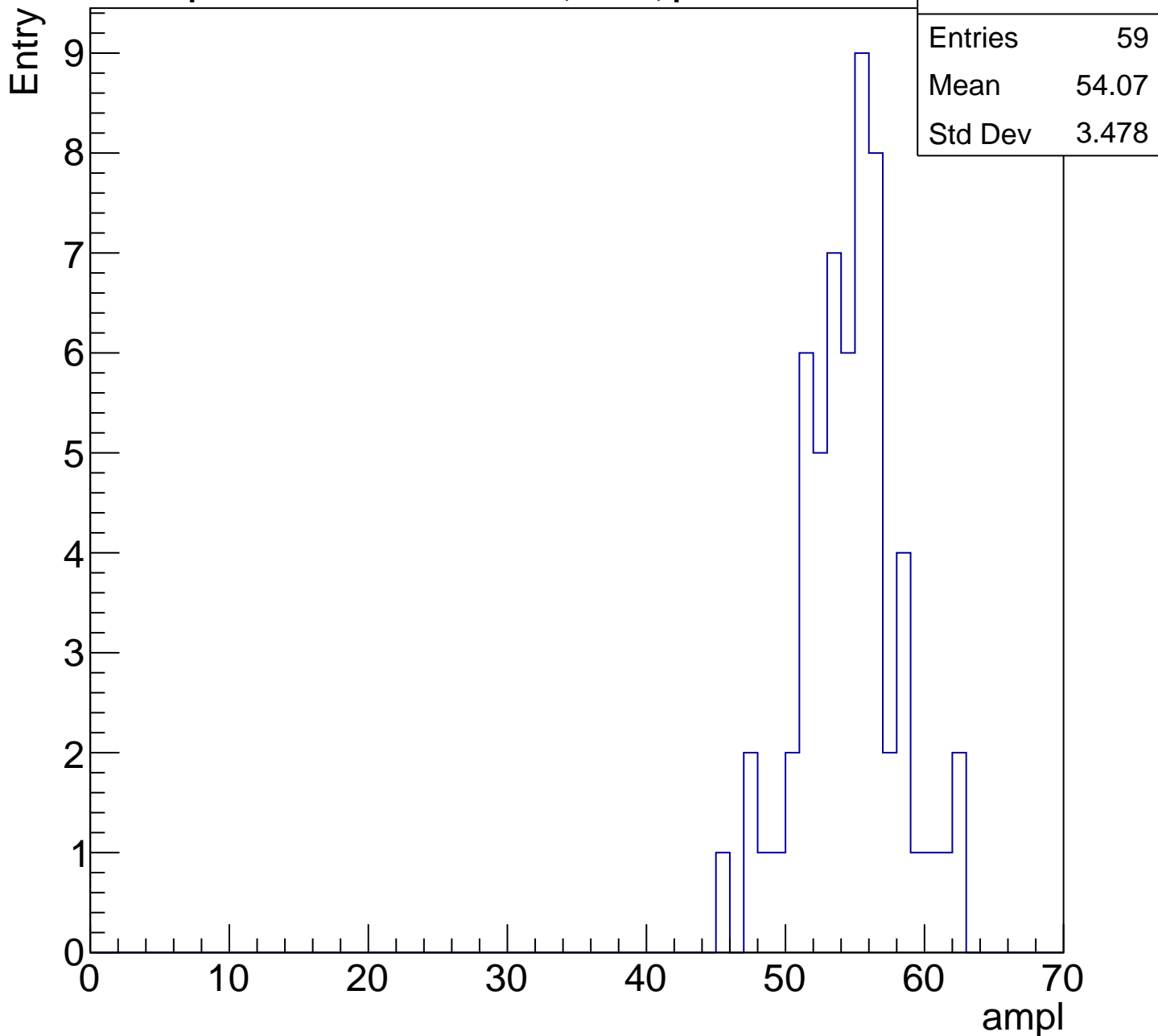
B1L100S, U1-ch62, adc3

calib_packv5_042523_0143.root, FC#4, port A2



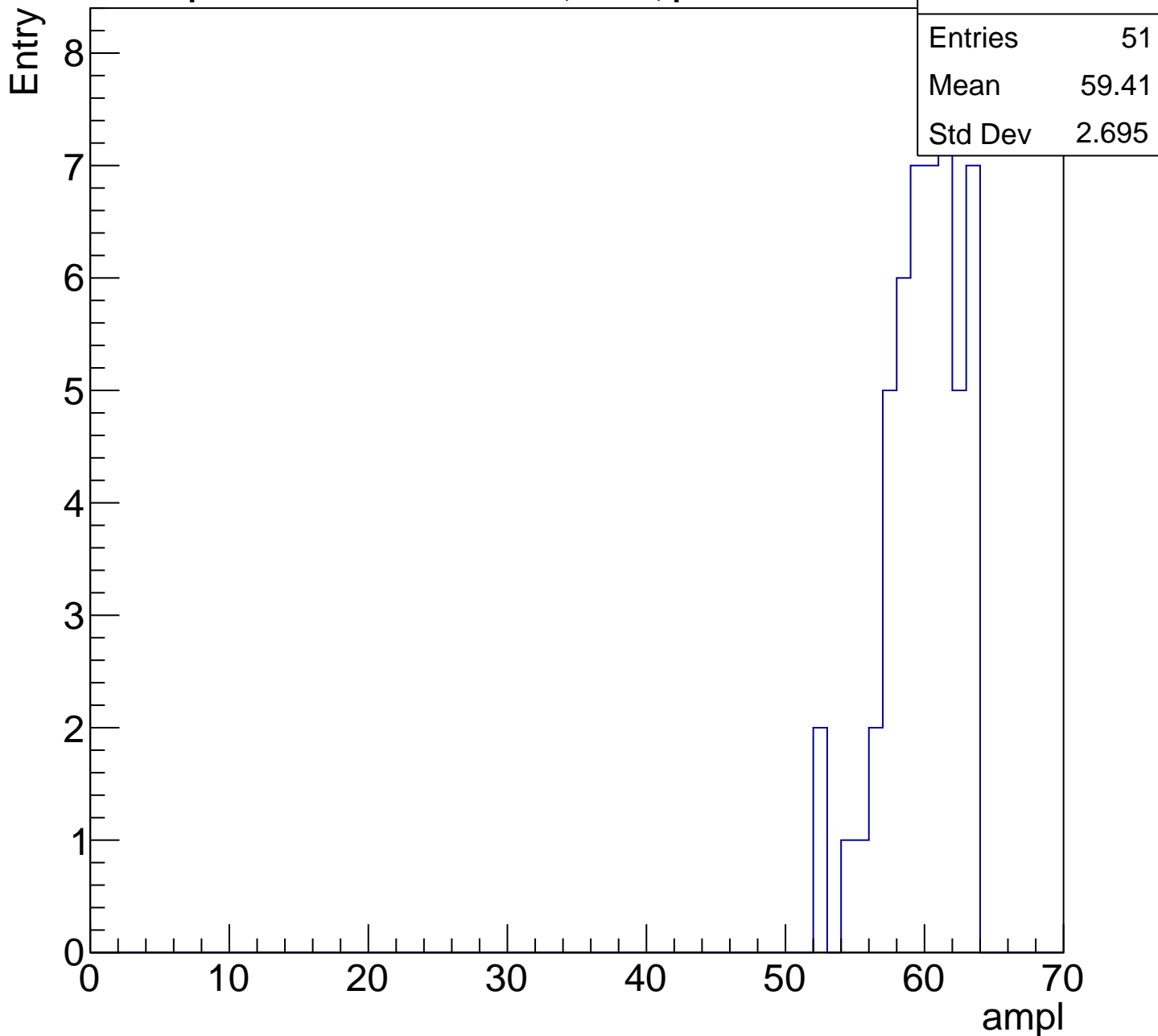
B1L100S, U1-ch62, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch62, adc5

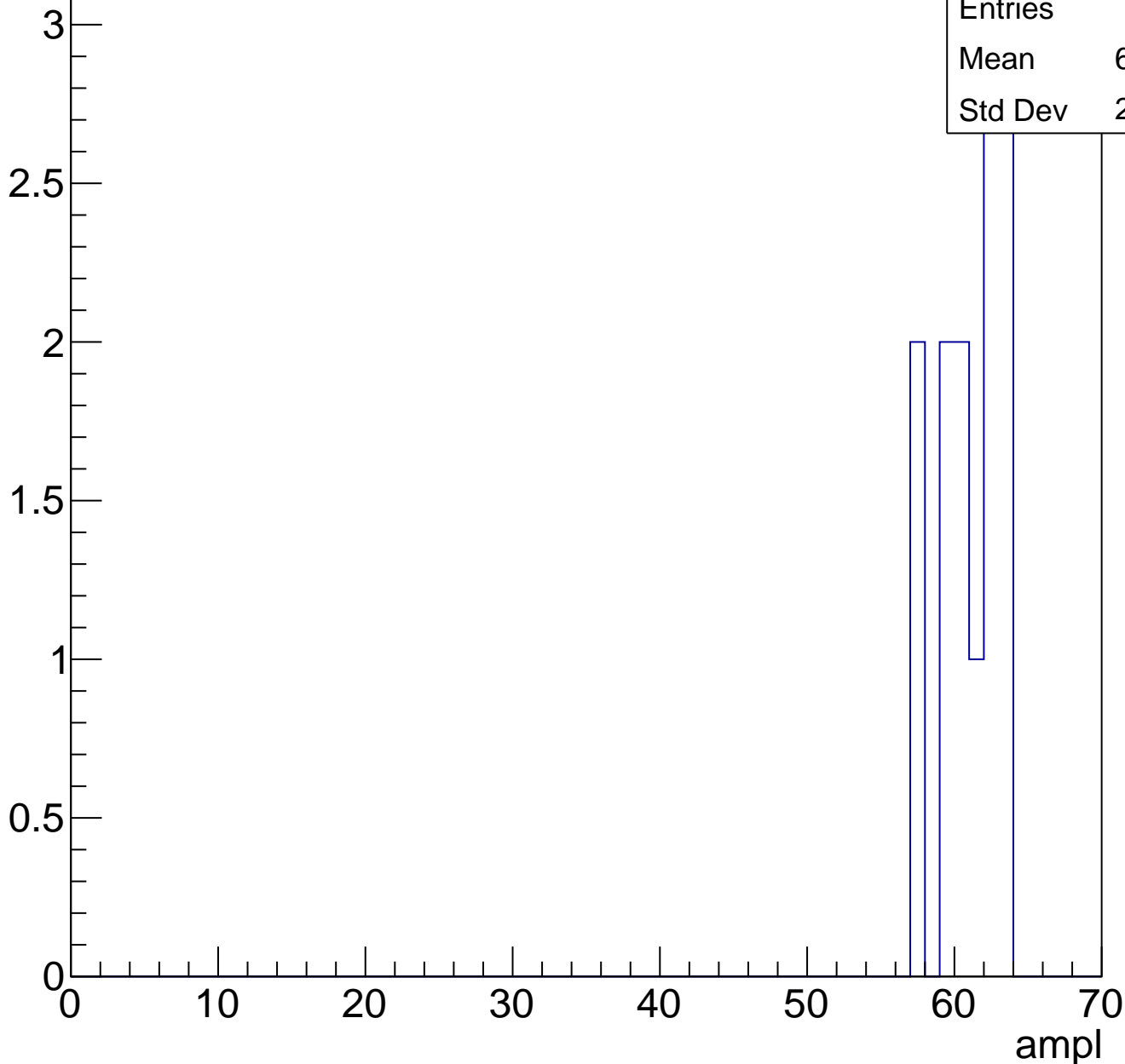
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch62, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch62, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch63, adc0

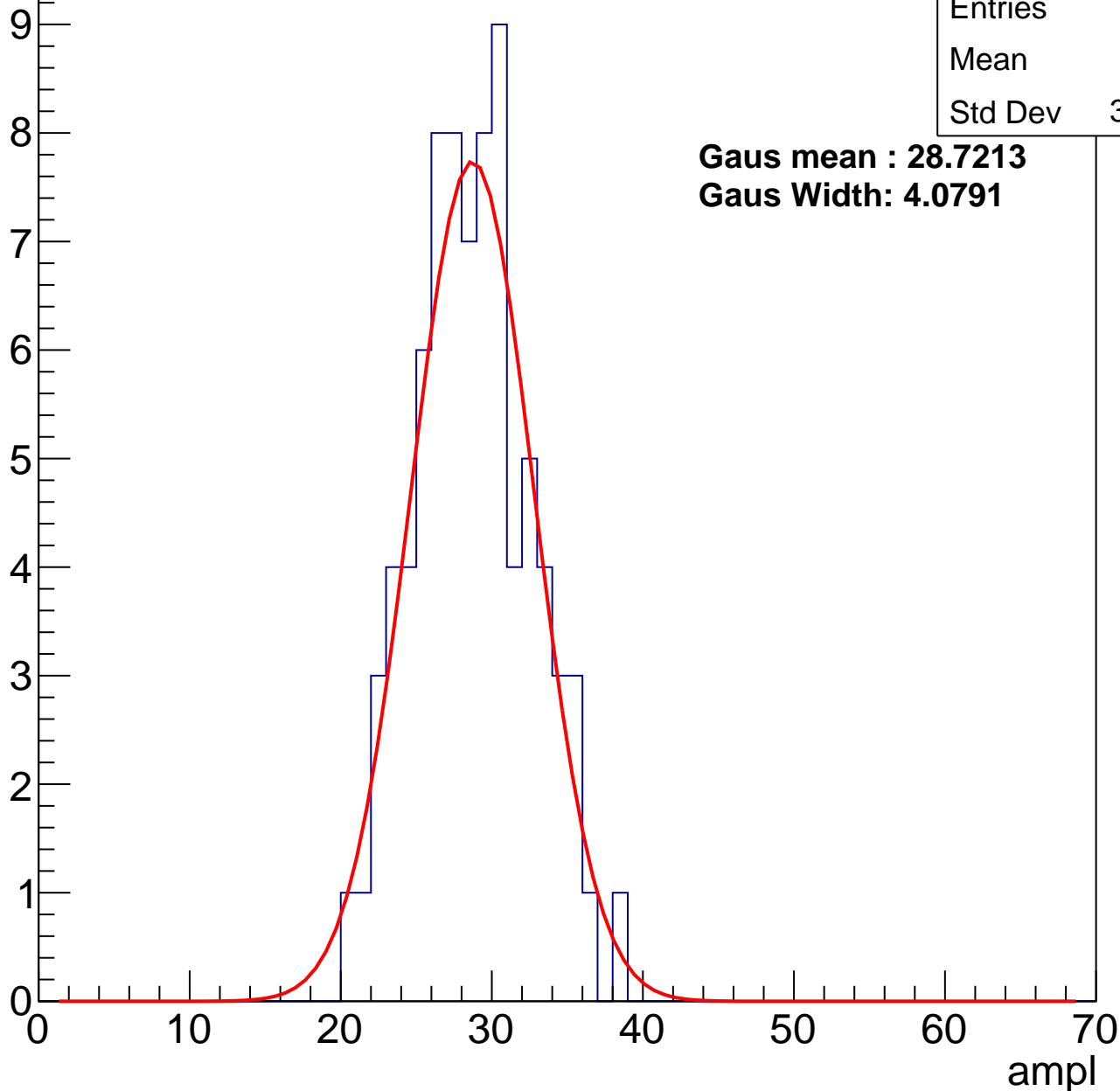
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	28.3
Std Dev	3.793

Gaus mean : 28.7213

Gaus Width: 4.0791



B1L100S, U1-ch63, adc1

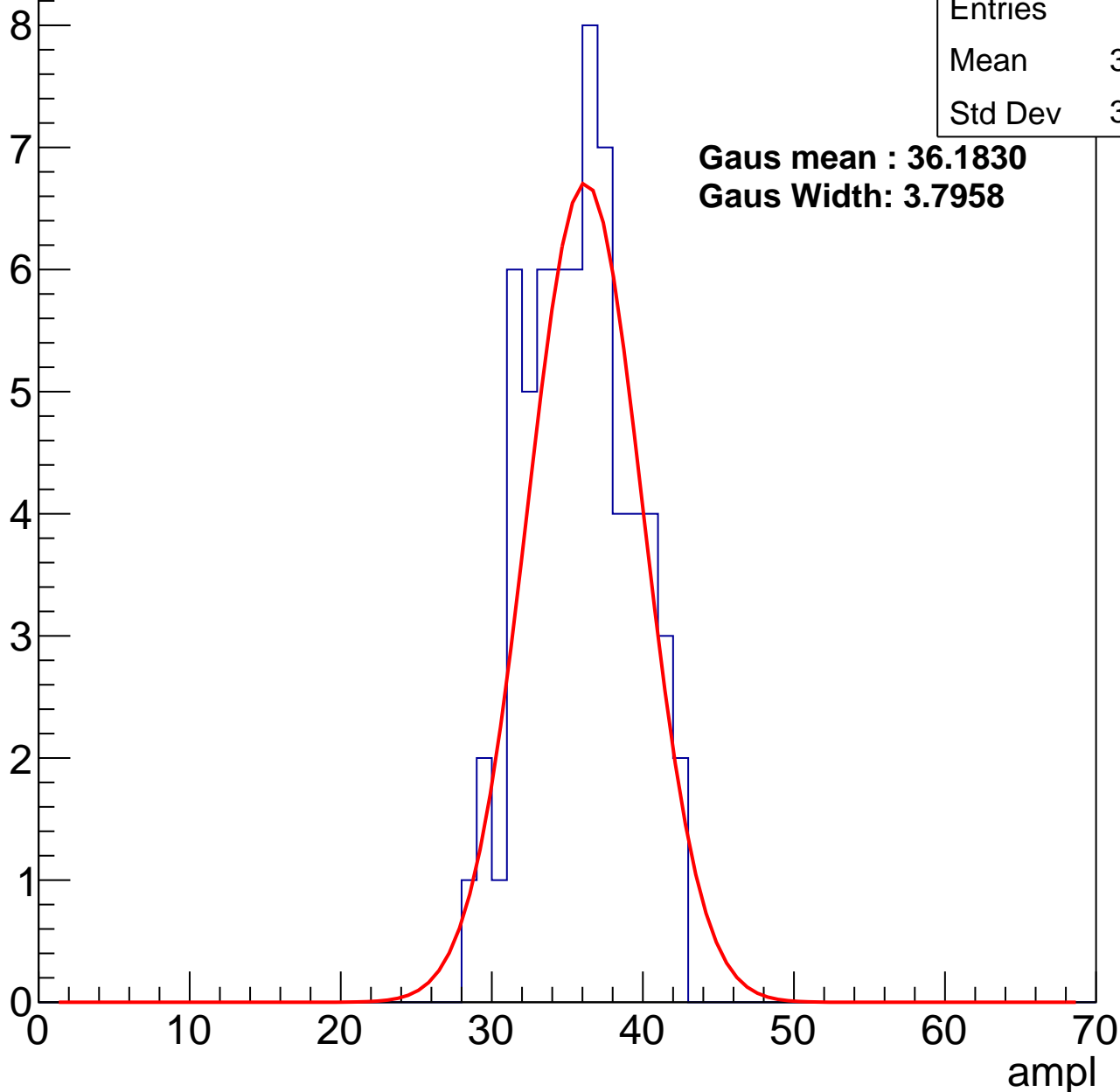
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.32
Std Dev	3.402

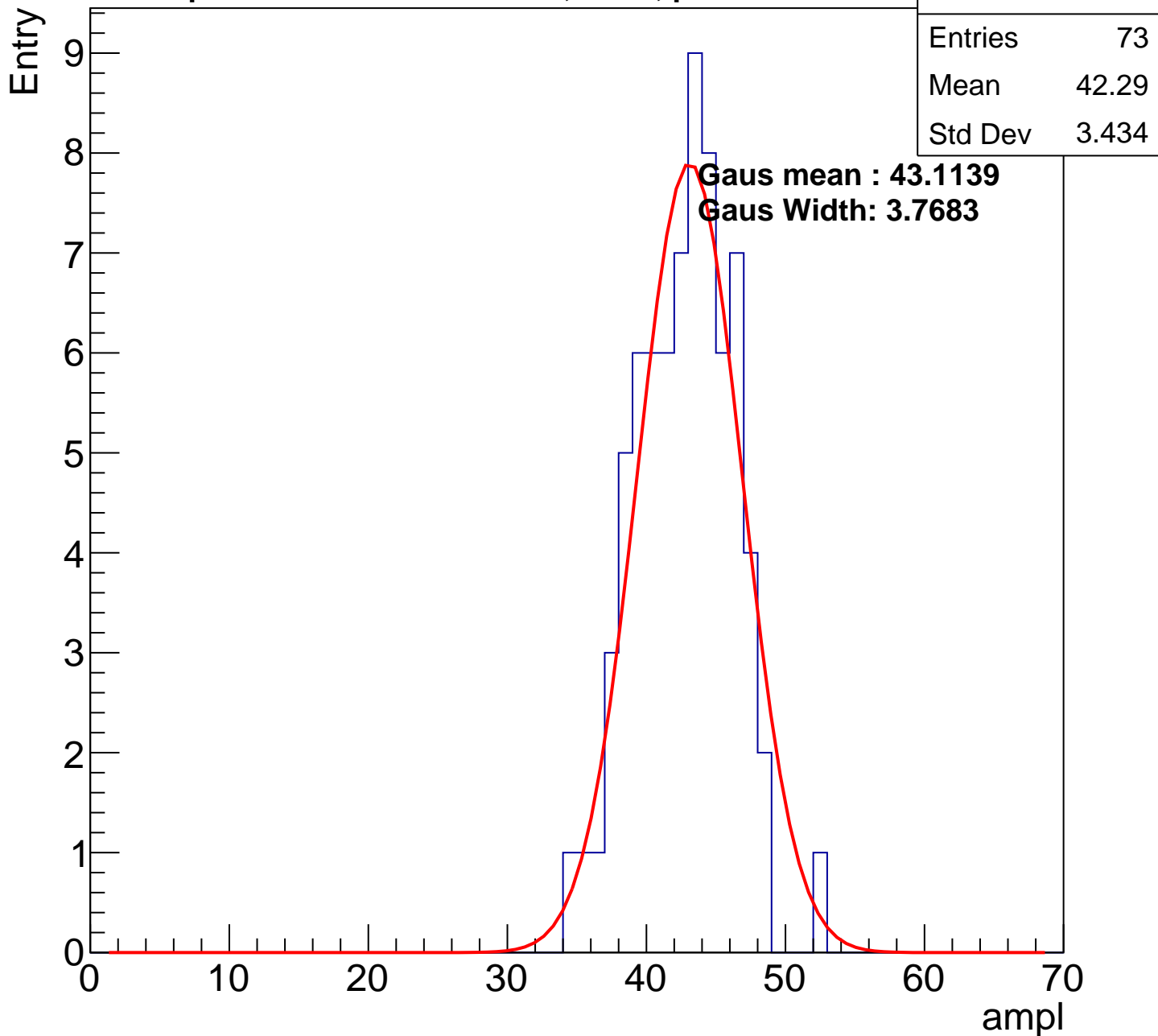
Gaus mean : 36.1830

Gaus Width: 3.7958



B1L100S, U1-ch63, adc2

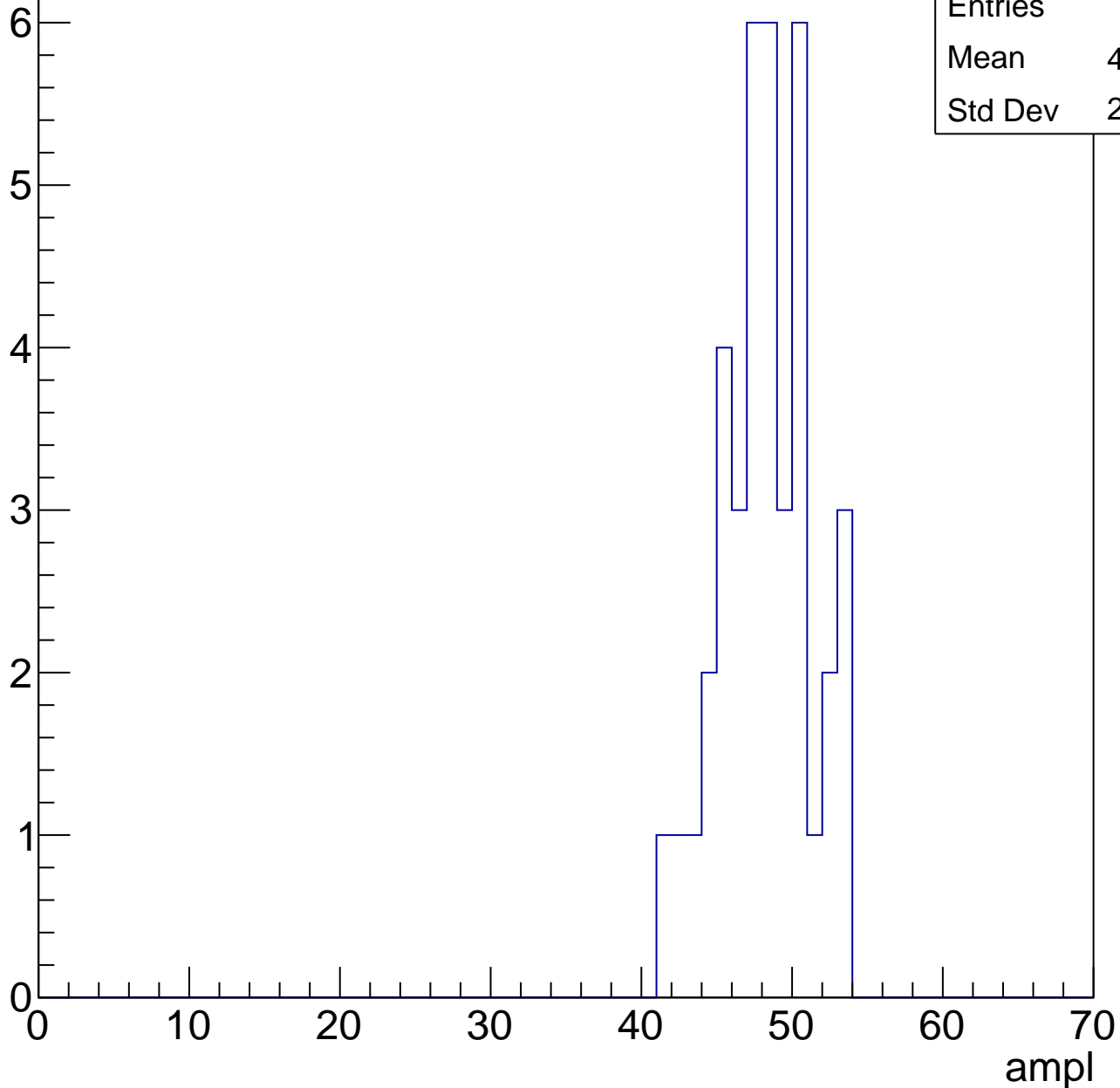
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch63, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

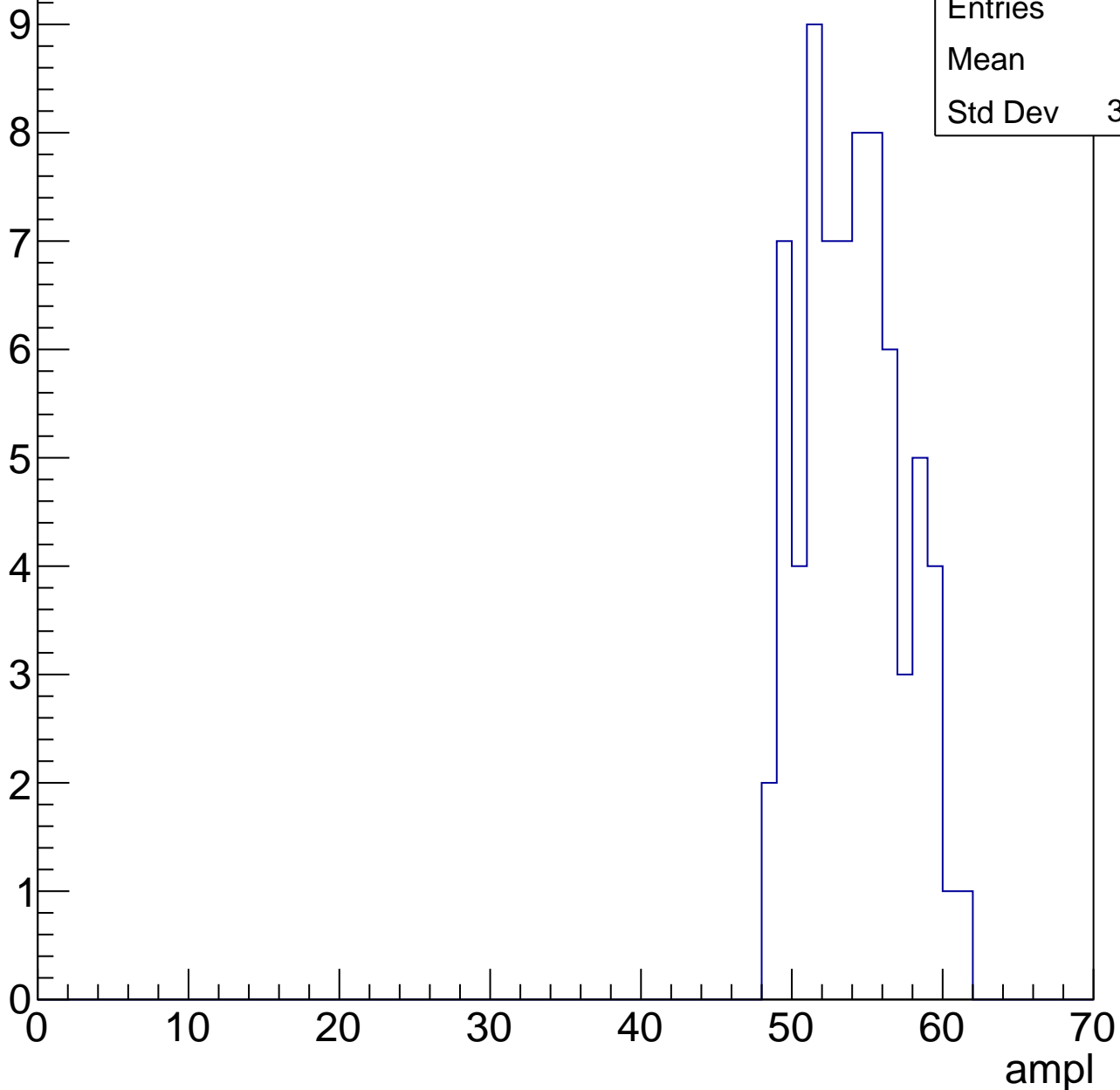


Entries	39
Mean	47.77
Std Dev	2.939

B1L100S, U1-ch63, adc4

calib_packv5_042523_0143.root, FC#4, port A2

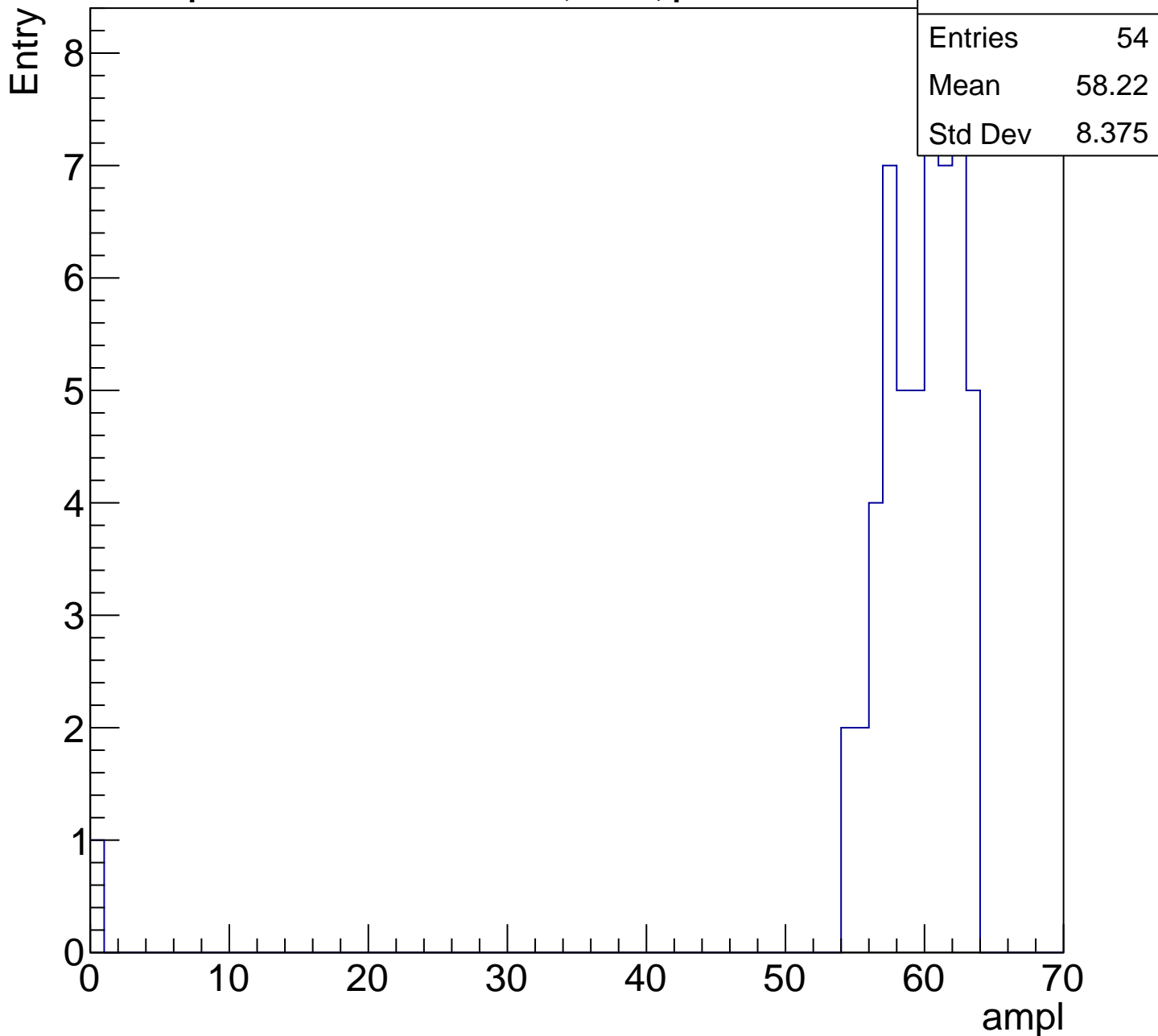
Entry



Entries	72
Mean	53.6
Std Dev	3.204

B1L100S, U1-ch63, adc5

calib_packv5_042523_0143.root, FC#4, port A2

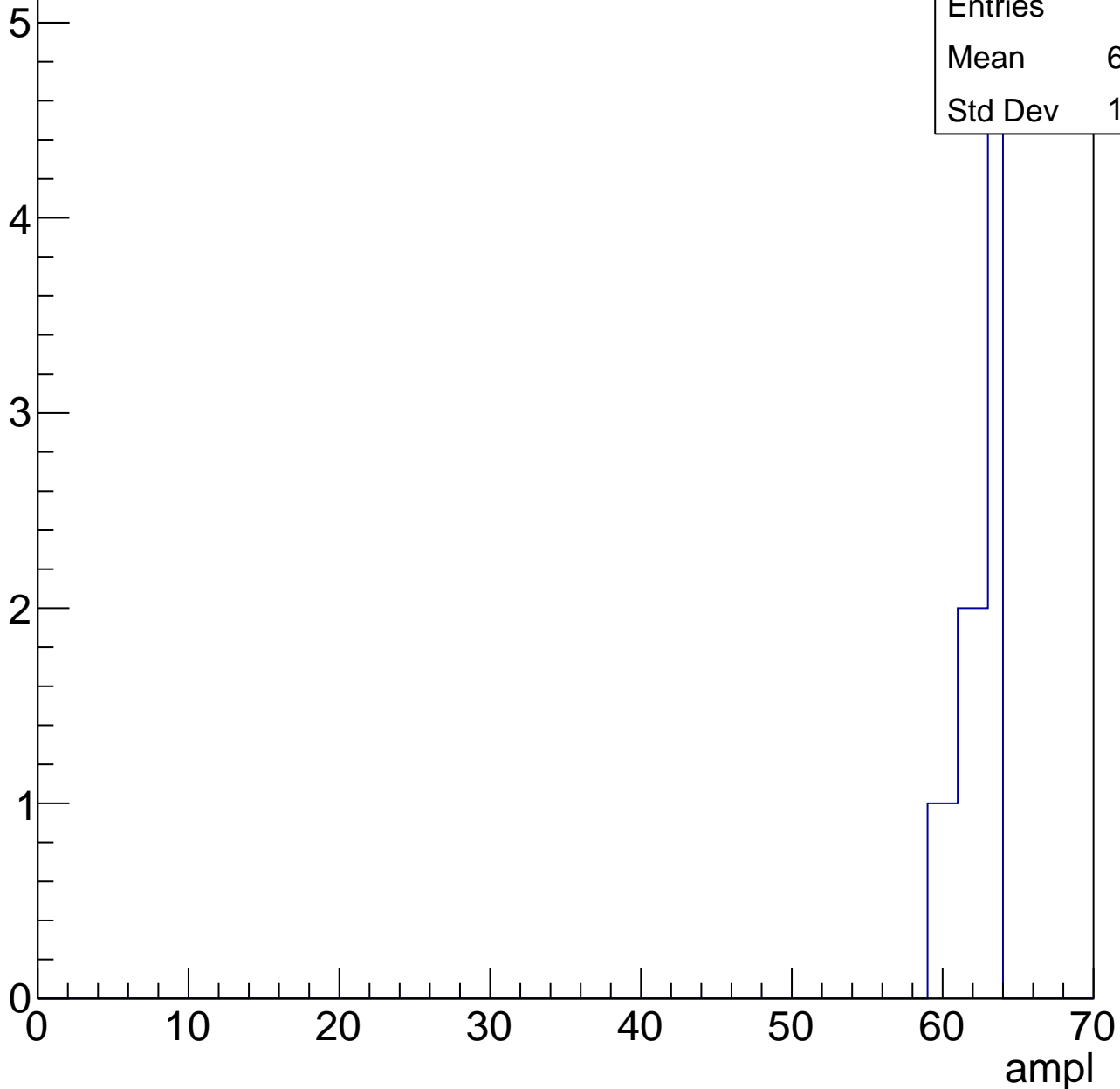


B1L100S, U1-ch63, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

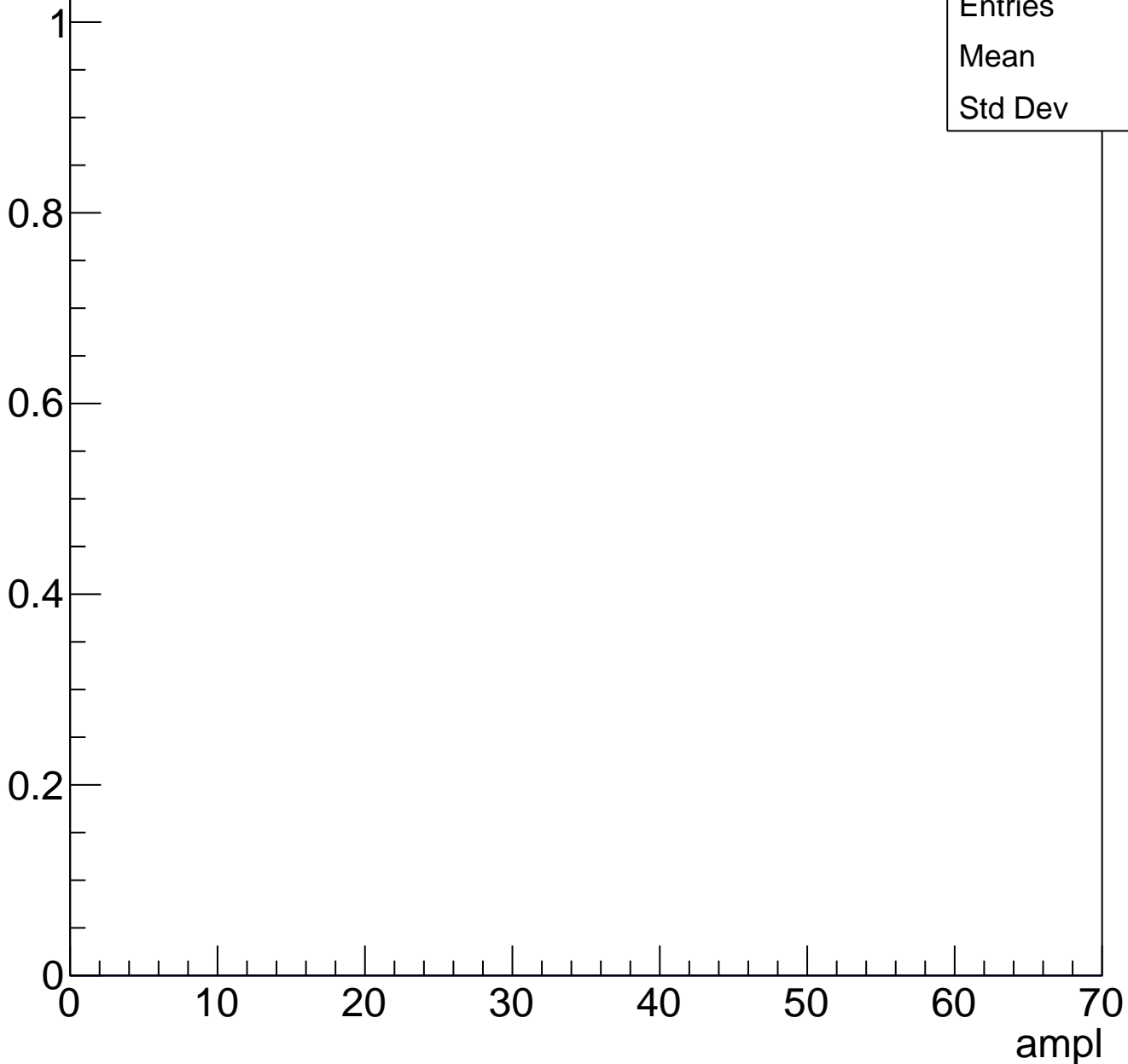
Entries	11
Mean	61.82
Std Dev	1.336



B1L100S, U1-ch63, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch64, adc0

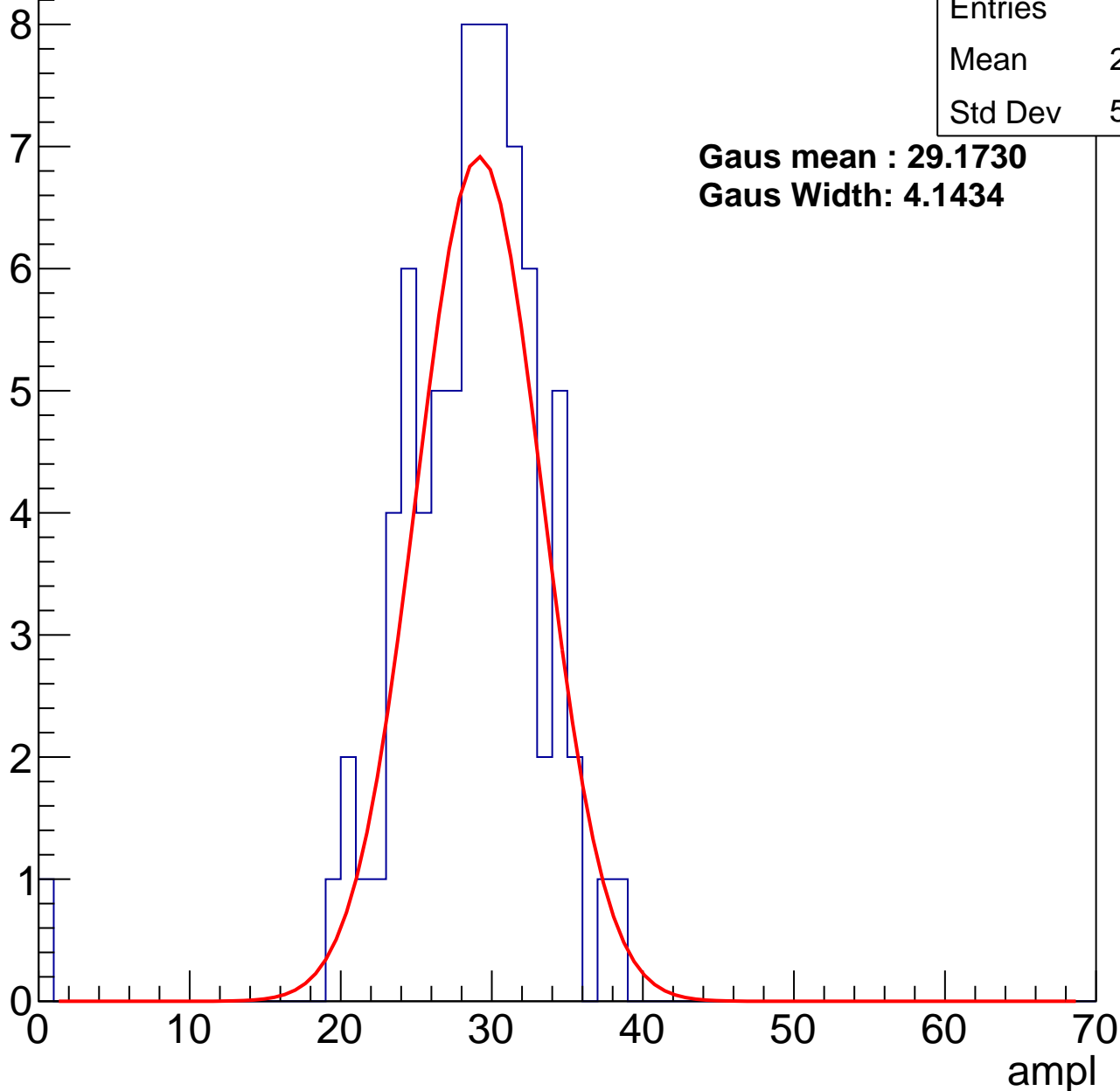
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	28.06
Std Dev	5.117

Gaus mean : 29.1730

Gaus Width: 4.1434



B1L100S, U1-ch64, adc1

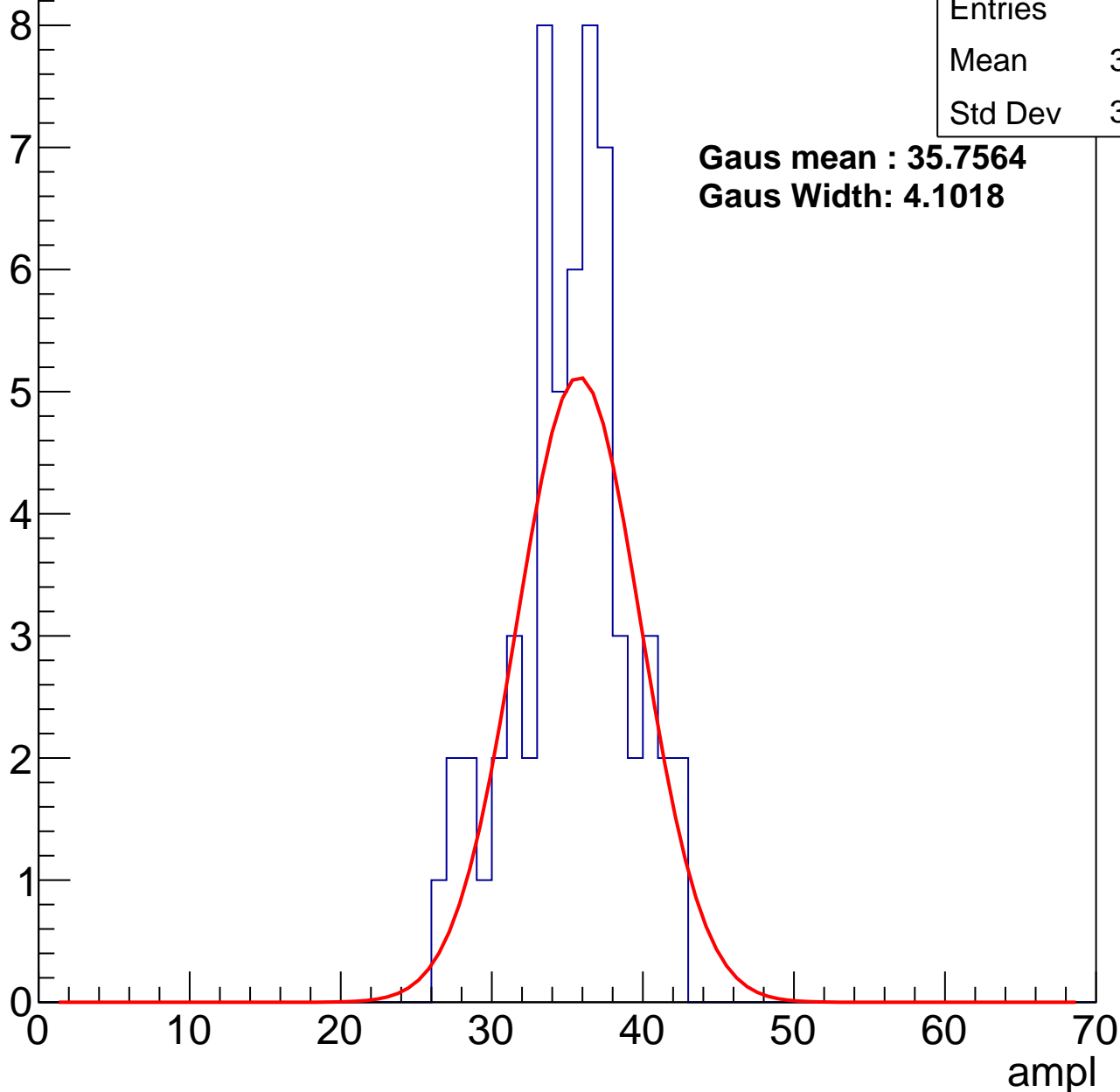
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.76
Std Dev	3.757

Gaus mean : 35.7564

Gaus Width: 4.1018



B1L100S, U1-ch64, adc2

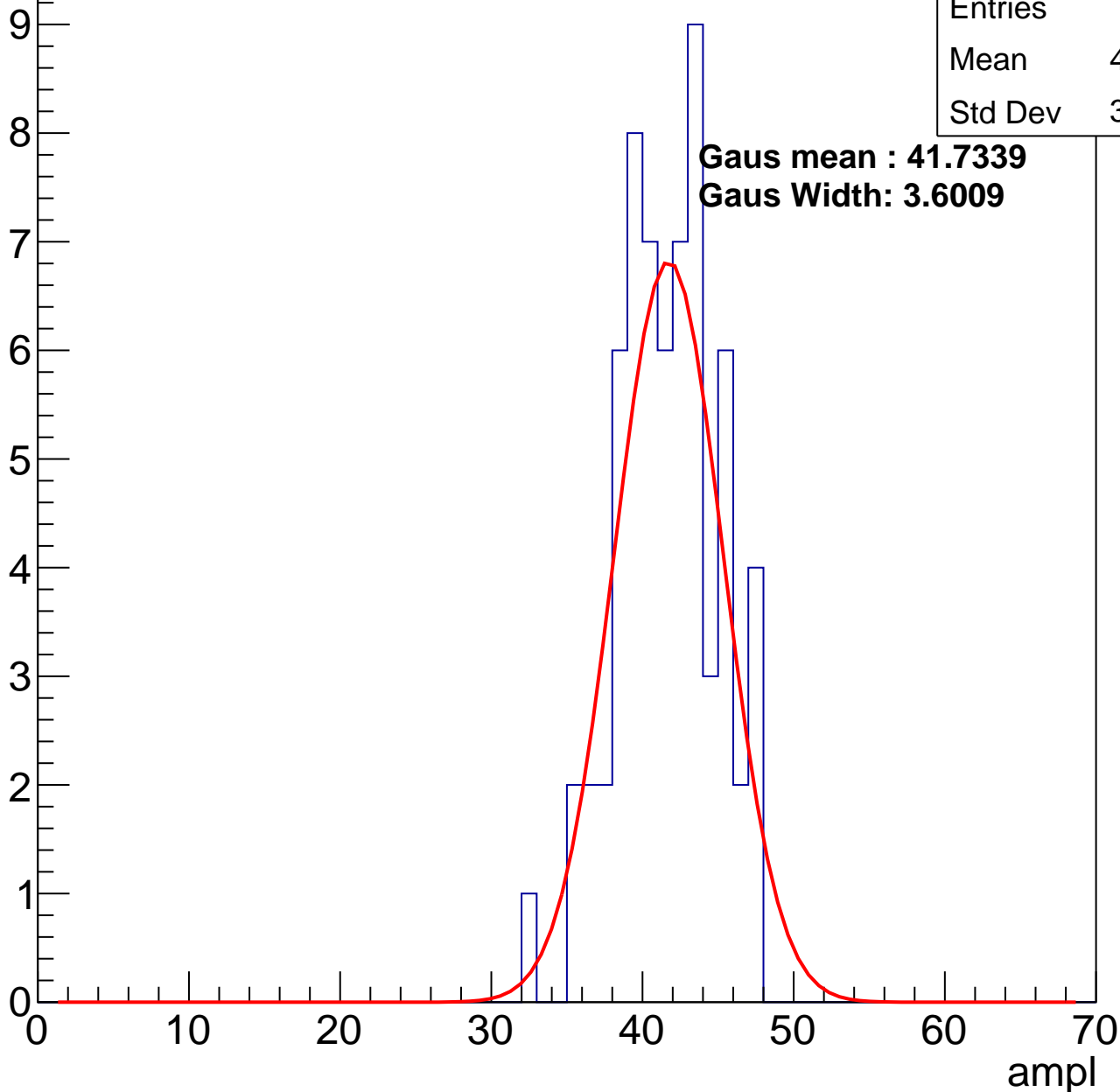
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.18
Std Dev	3.248

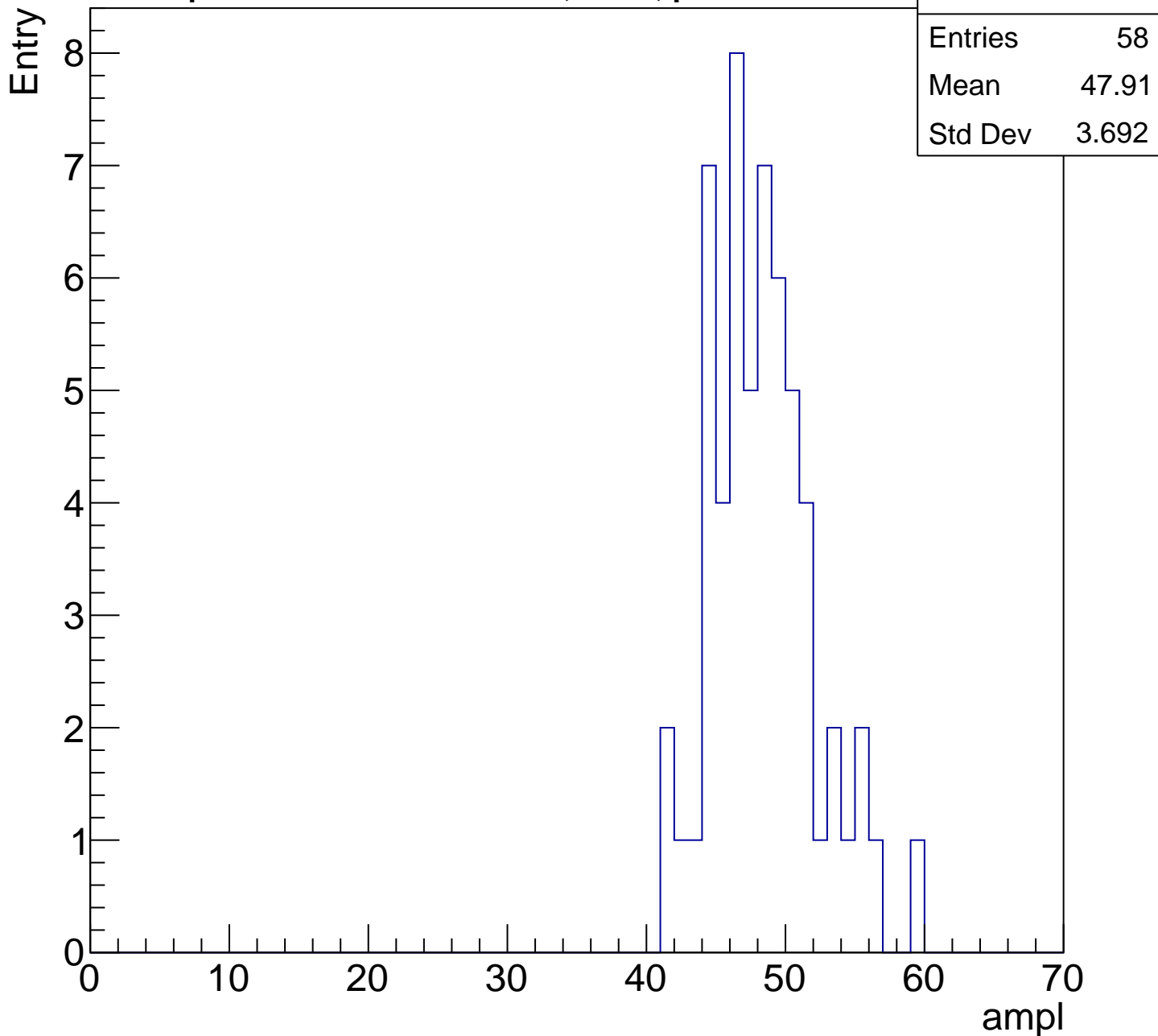
Gaus mean : 41.7339

Gaus Width: 3.6009



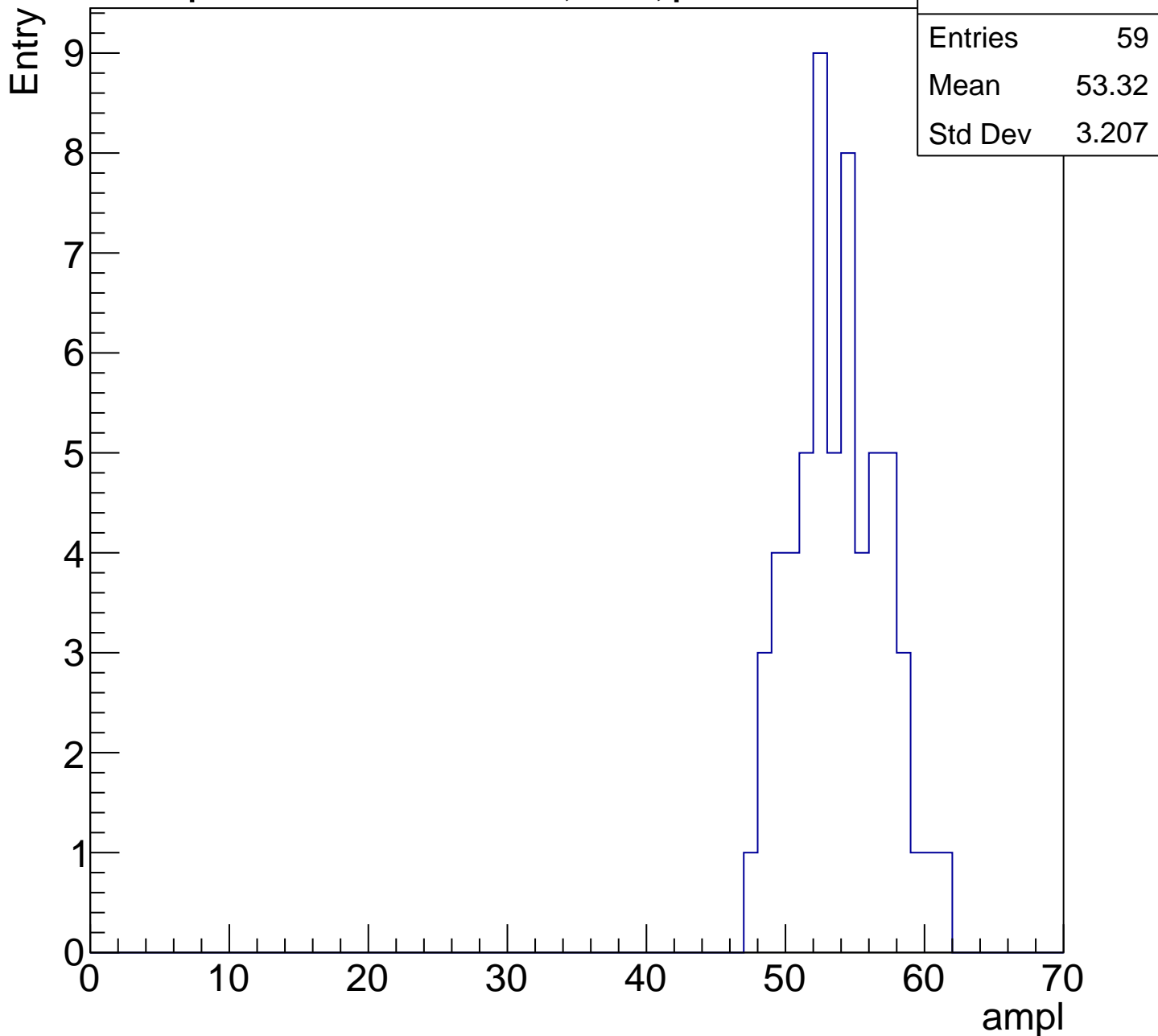
B1L100S, U1-ch64, adc3

calib_packv5_042523_0143.root, FC#4, port A2



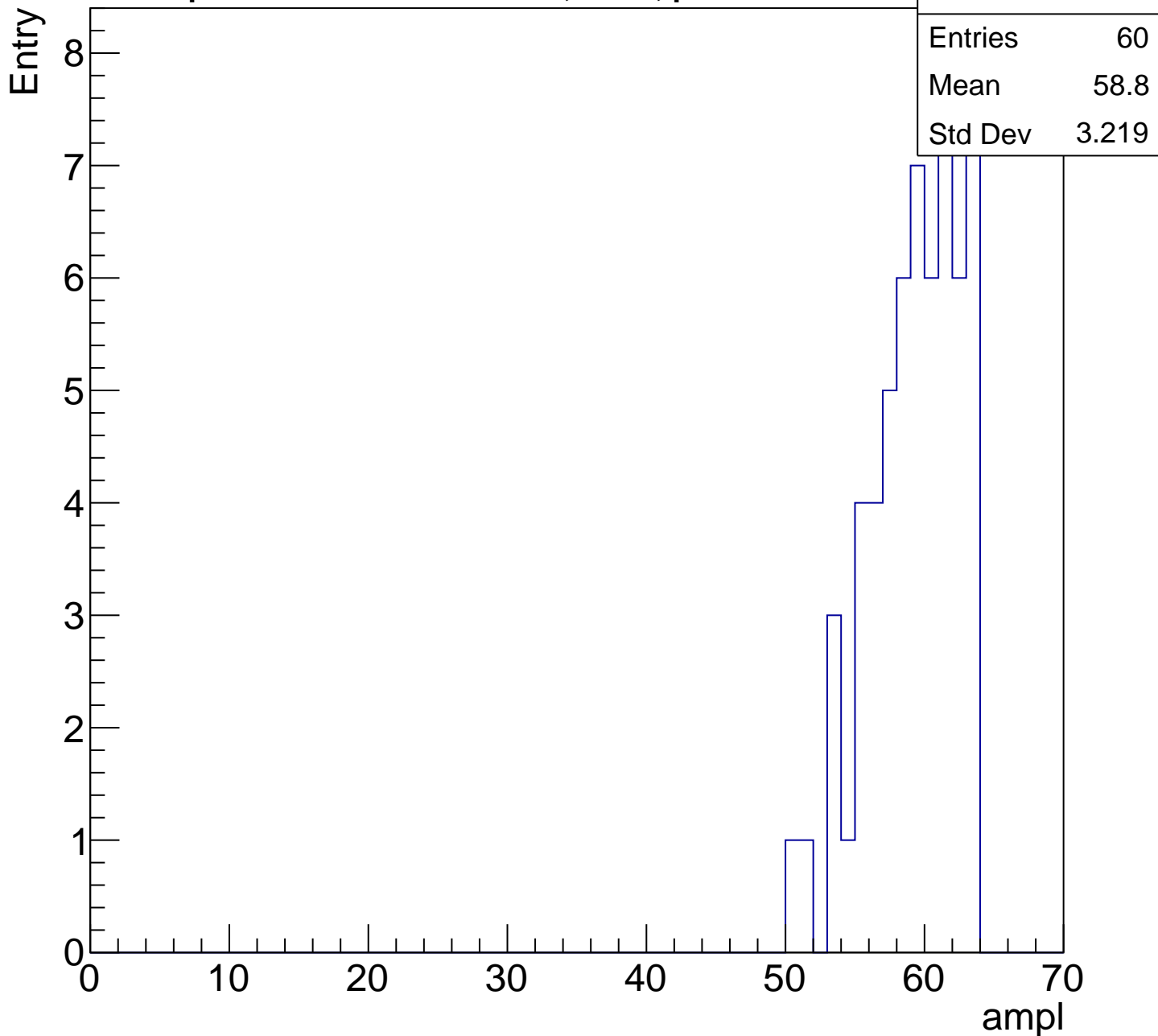
B1L100S, U1-ch64, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch64, adc5

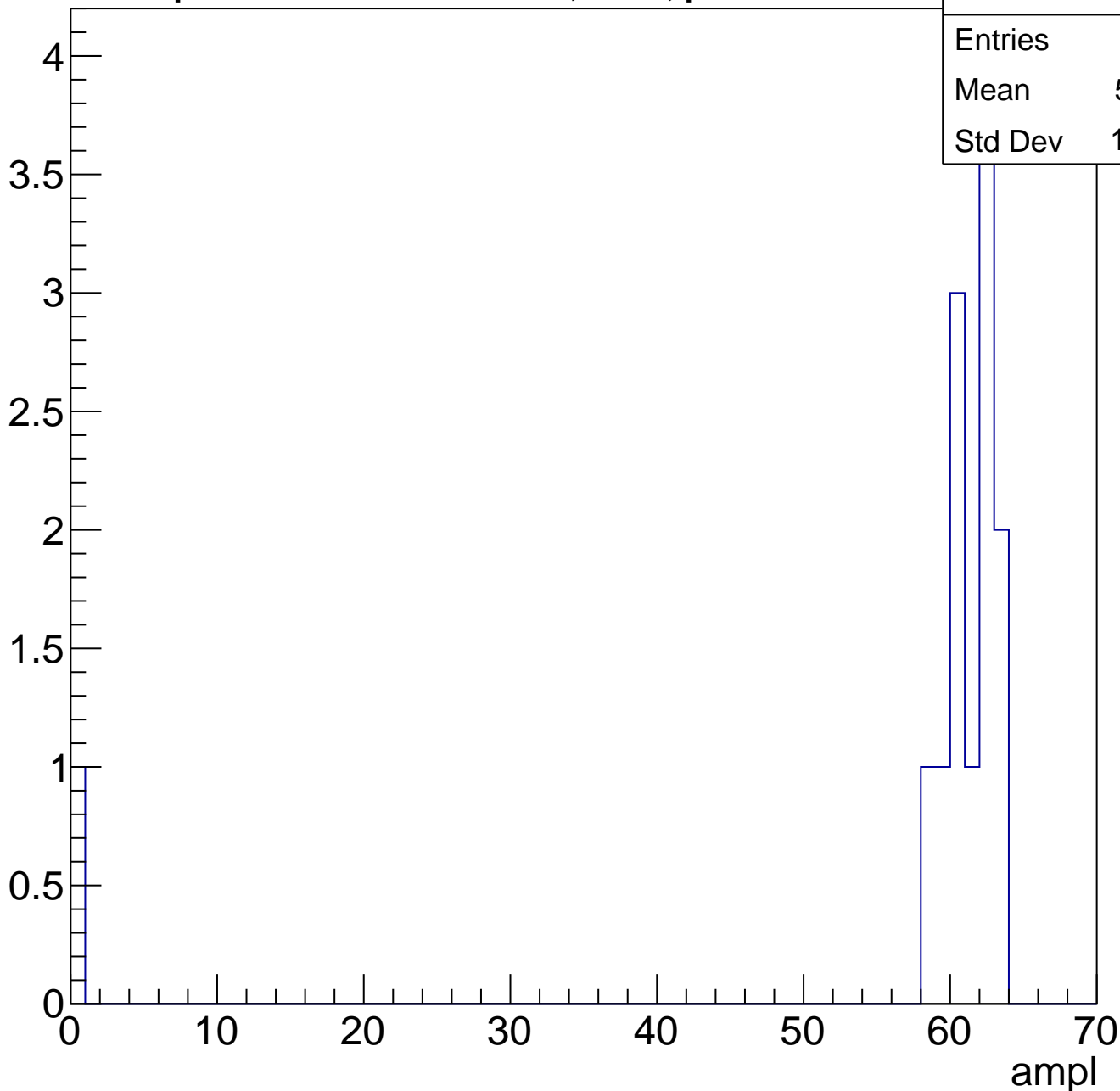
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch64, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch64, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	9
Std Dev	9

B1L100S, U1-ch65, adc0

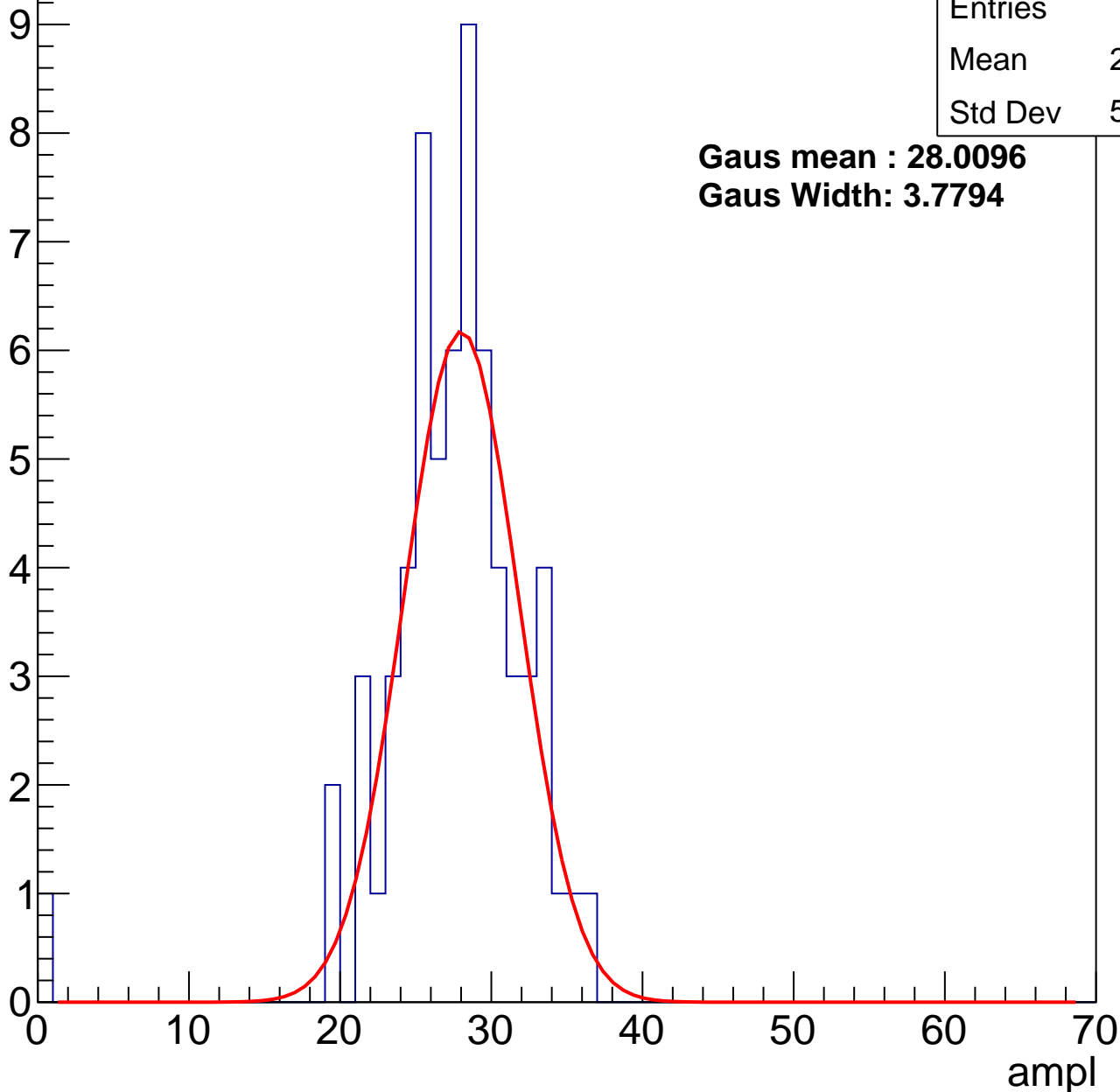
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	26.95
Std Dev	5.024

Gaus mean : 28.0096

Gaus Width: 3.7794



B1L100S, U1-ch65, adc1

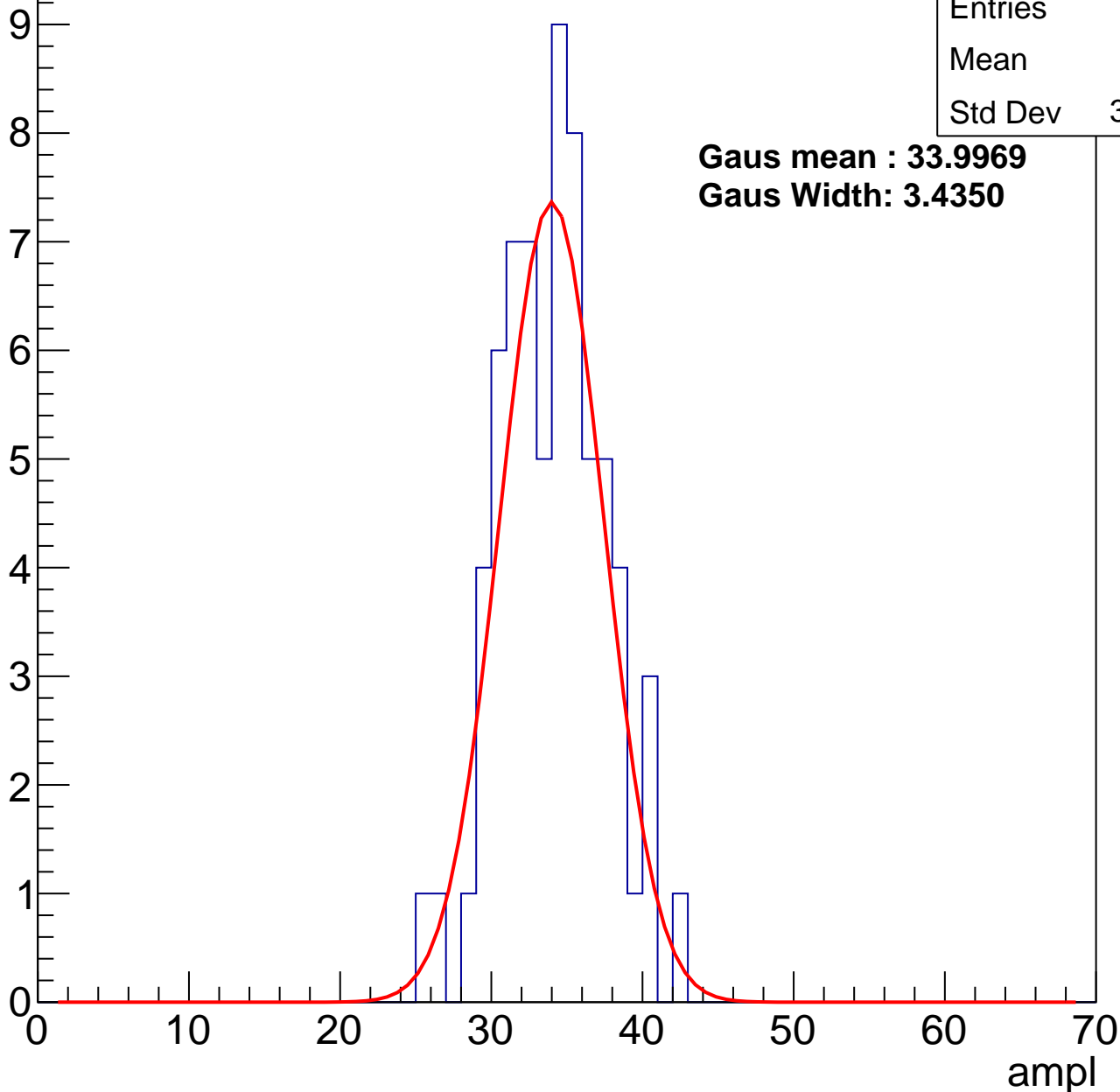
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	33.6
Std Dev	3.422

Gaus mean : 33.9969

Gaus Width: 3.4350



B1L100S, U1-ch65, adc2

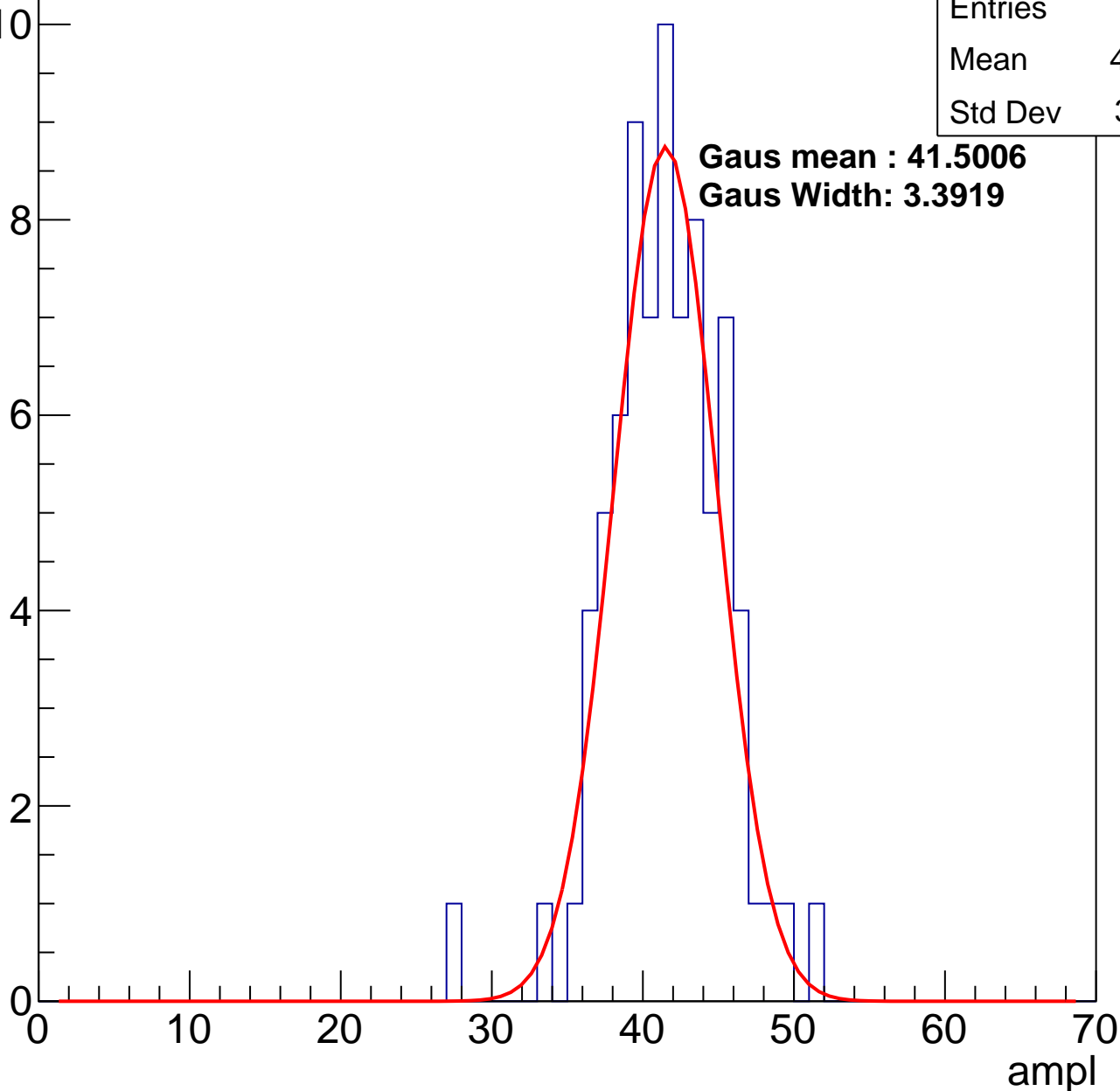
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	41.08
Std Dev	3.761

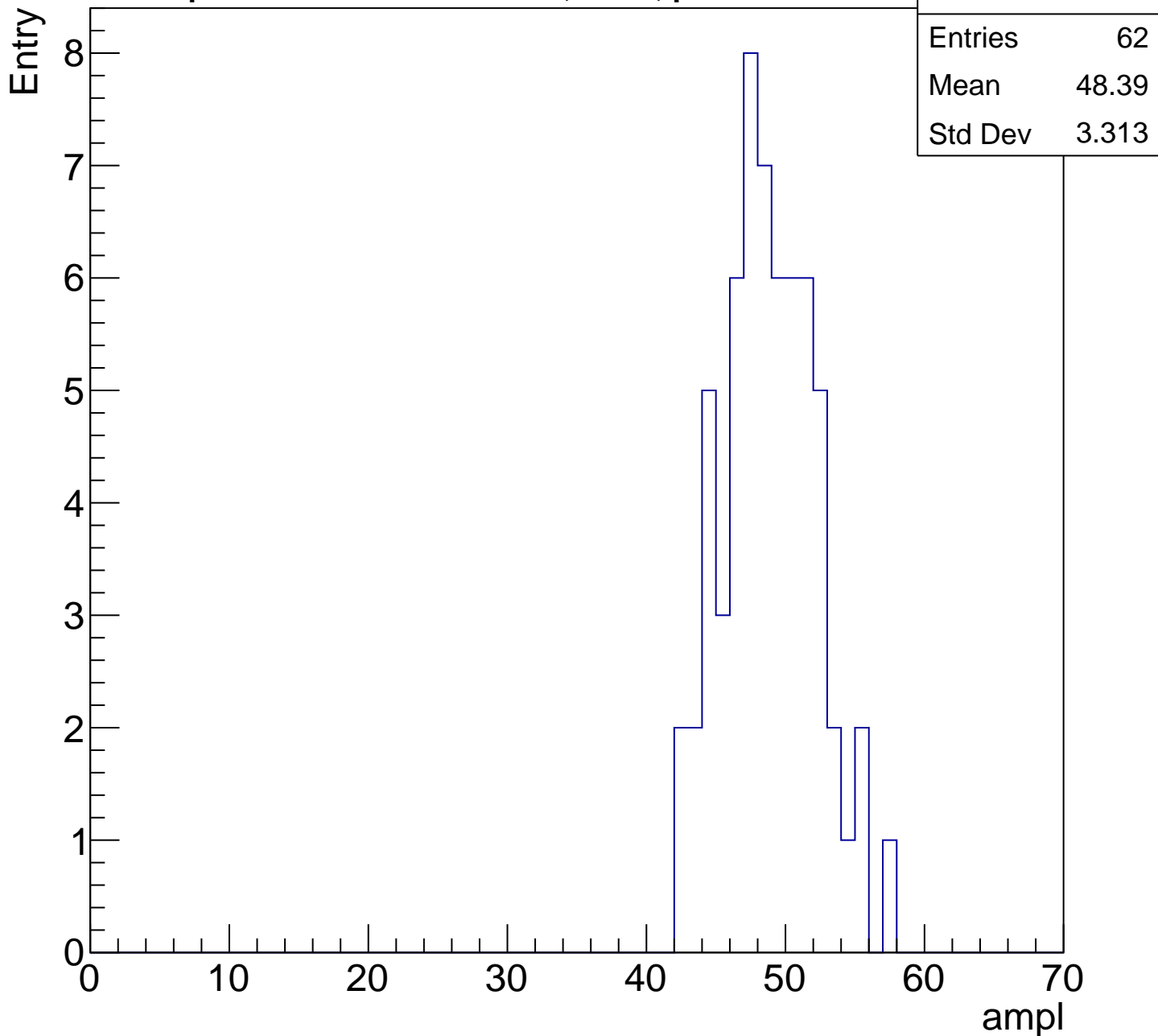
Gaus mean : 41.5006

Gaus Width: 3.3919



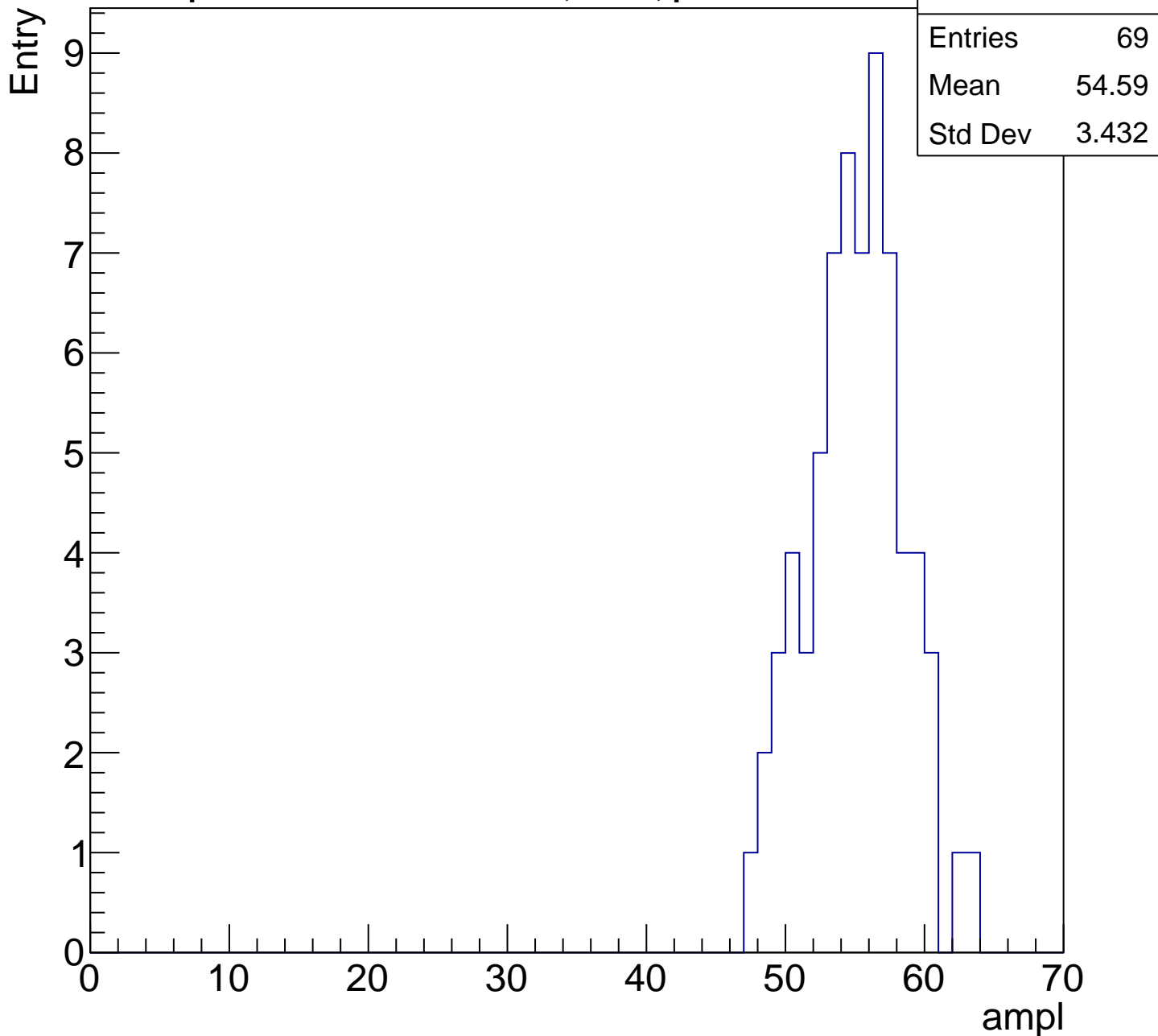
B1L100S, U1-ch65, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch65, adc4

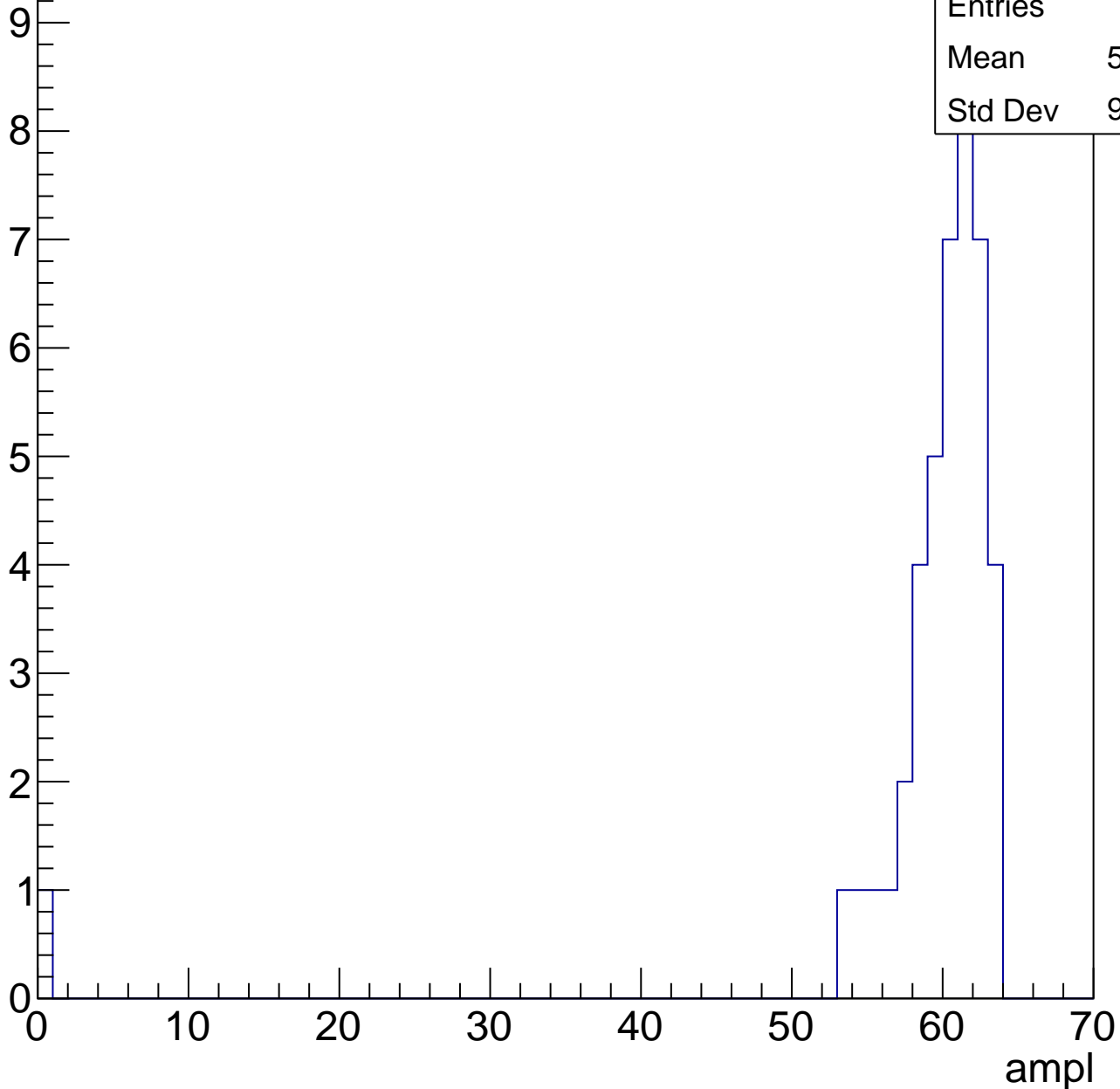
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch65, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

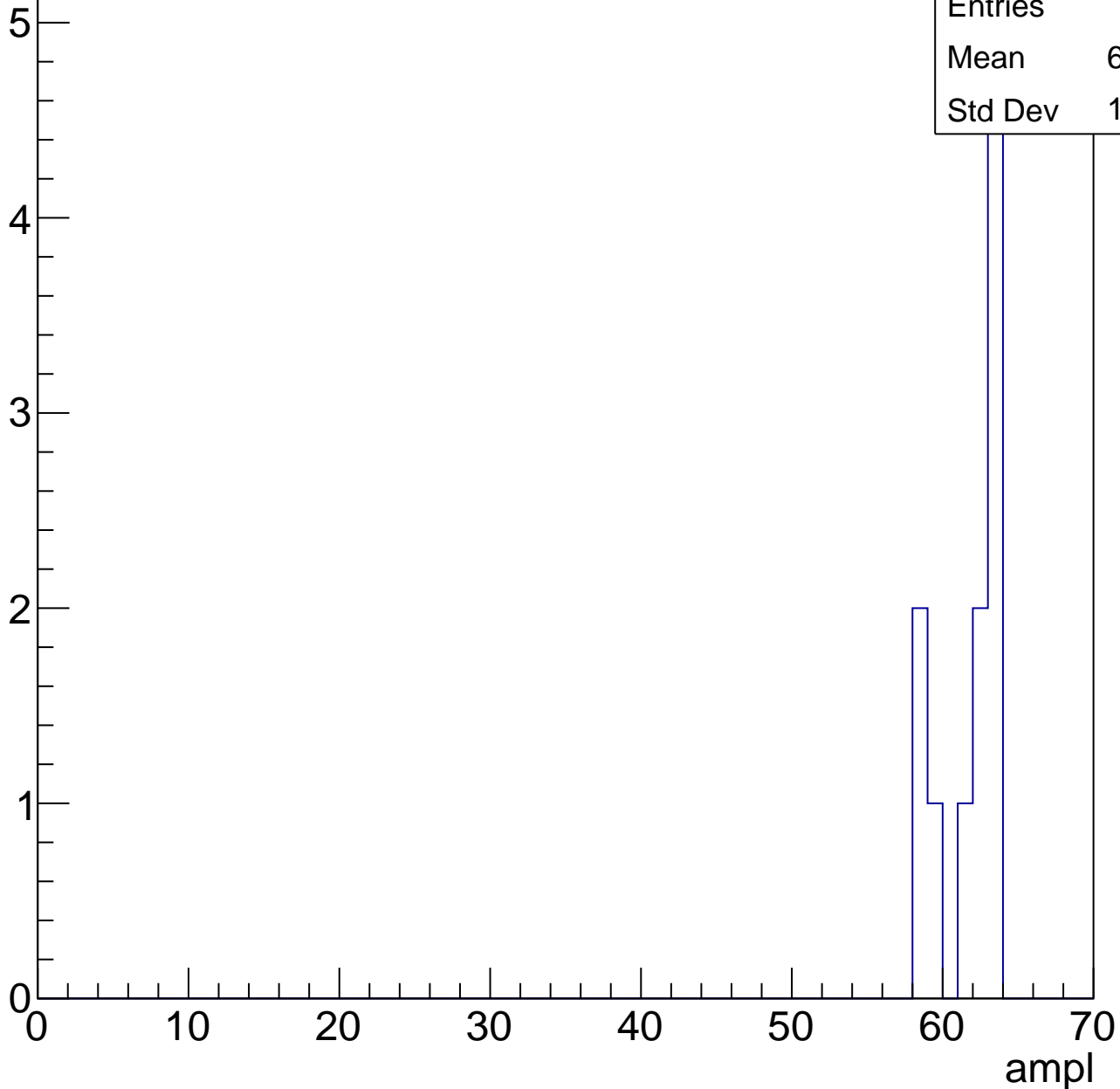


B1L100S, U1-ch65, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	11
Mean	61.36
Std Dev	1.967



B1L100S, U1-ch65, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch66, adc0

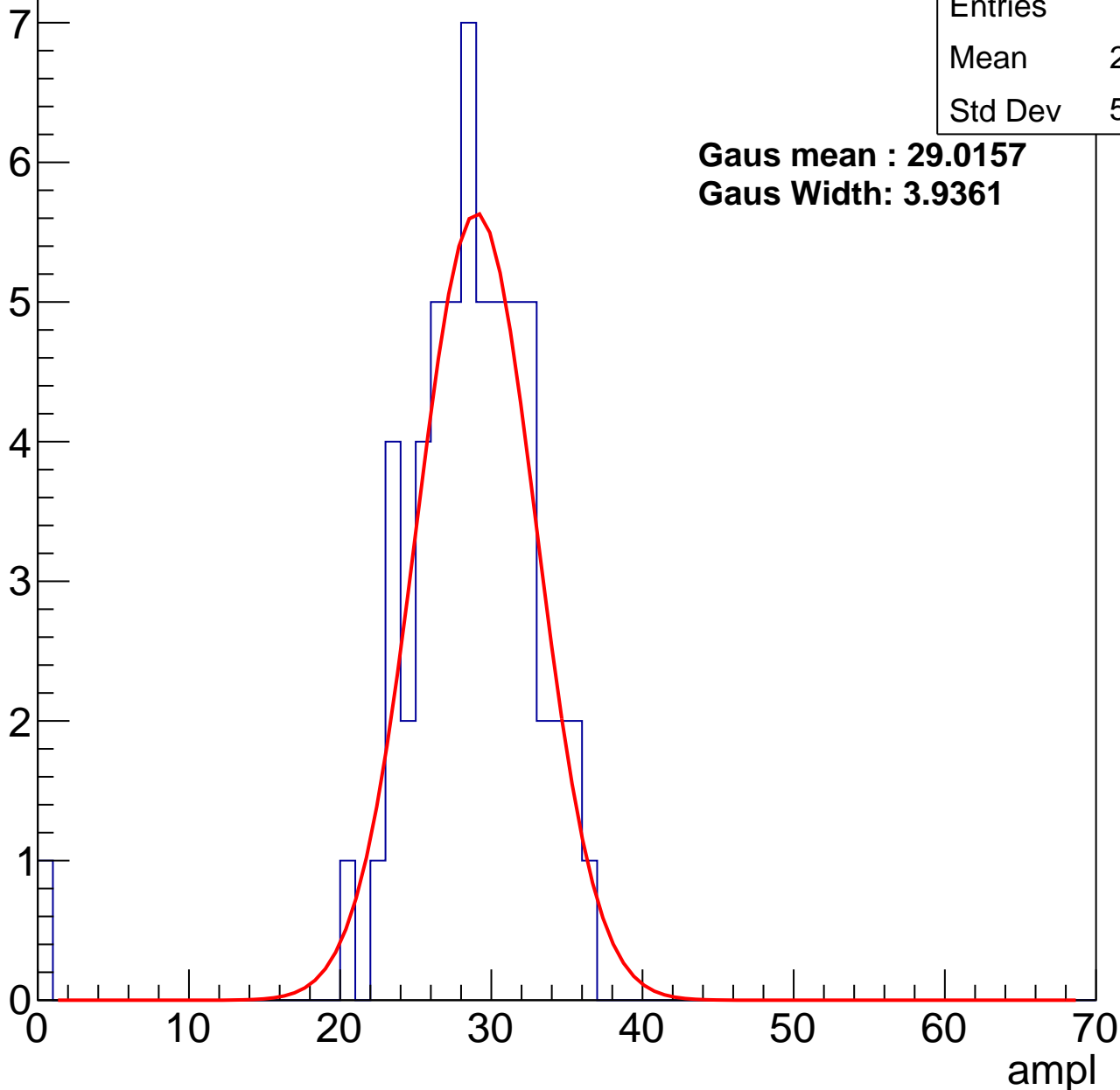
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	27.95
Std Dev	5.148

Gaus mean : 29.0157

Gaus Width: 3.9361



B1L100S, U1-ch66, adc1

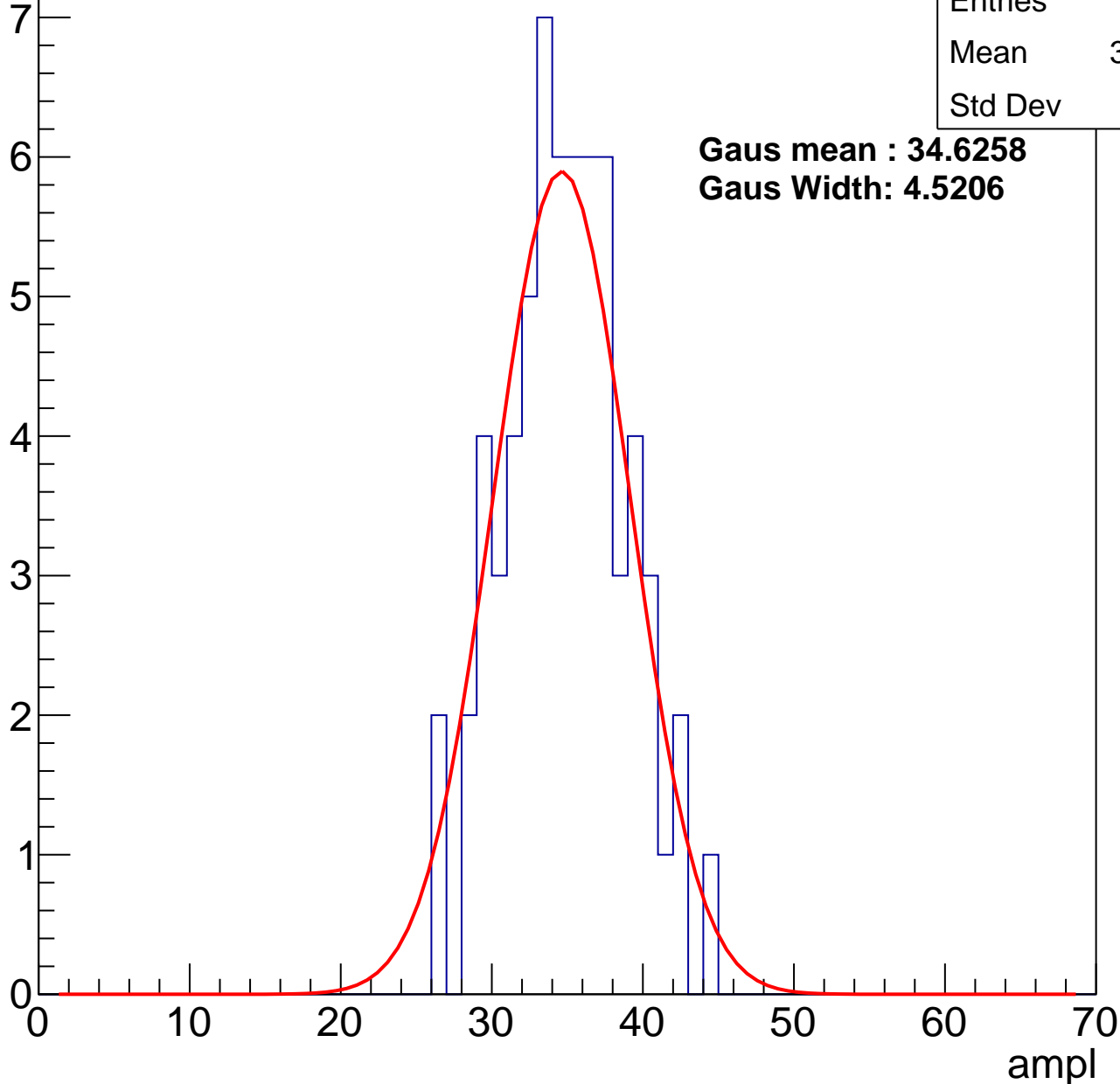
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	34.46
Std Dev	3.95

Gaus mean : 34.6258

Gaus Width: 4.5206



B1L100S, U1-ch66, adc2

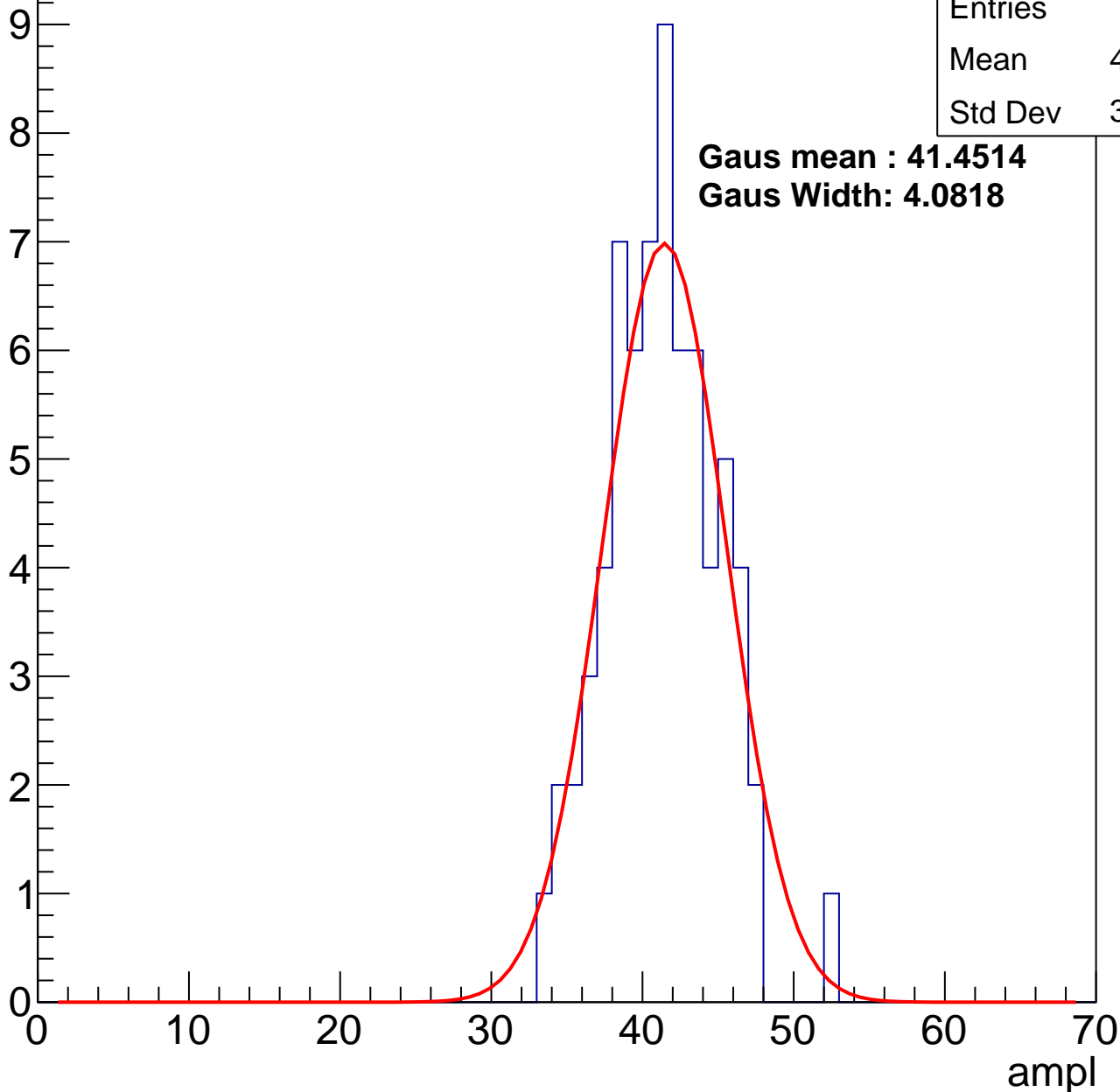
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	40.83
Std Dev	3.619

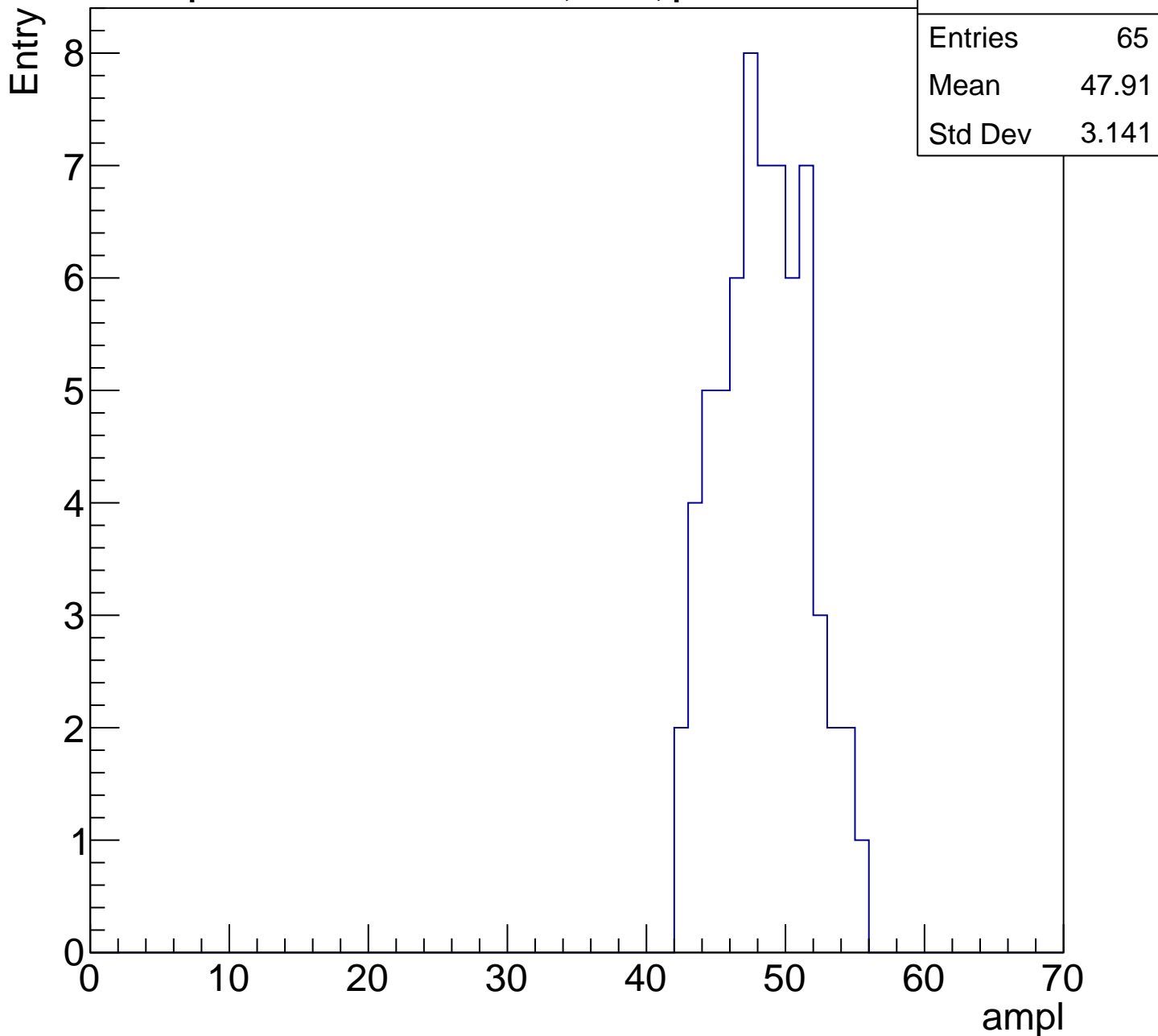
Gaus mean : 41.4514

Gaus Width: 4.0818



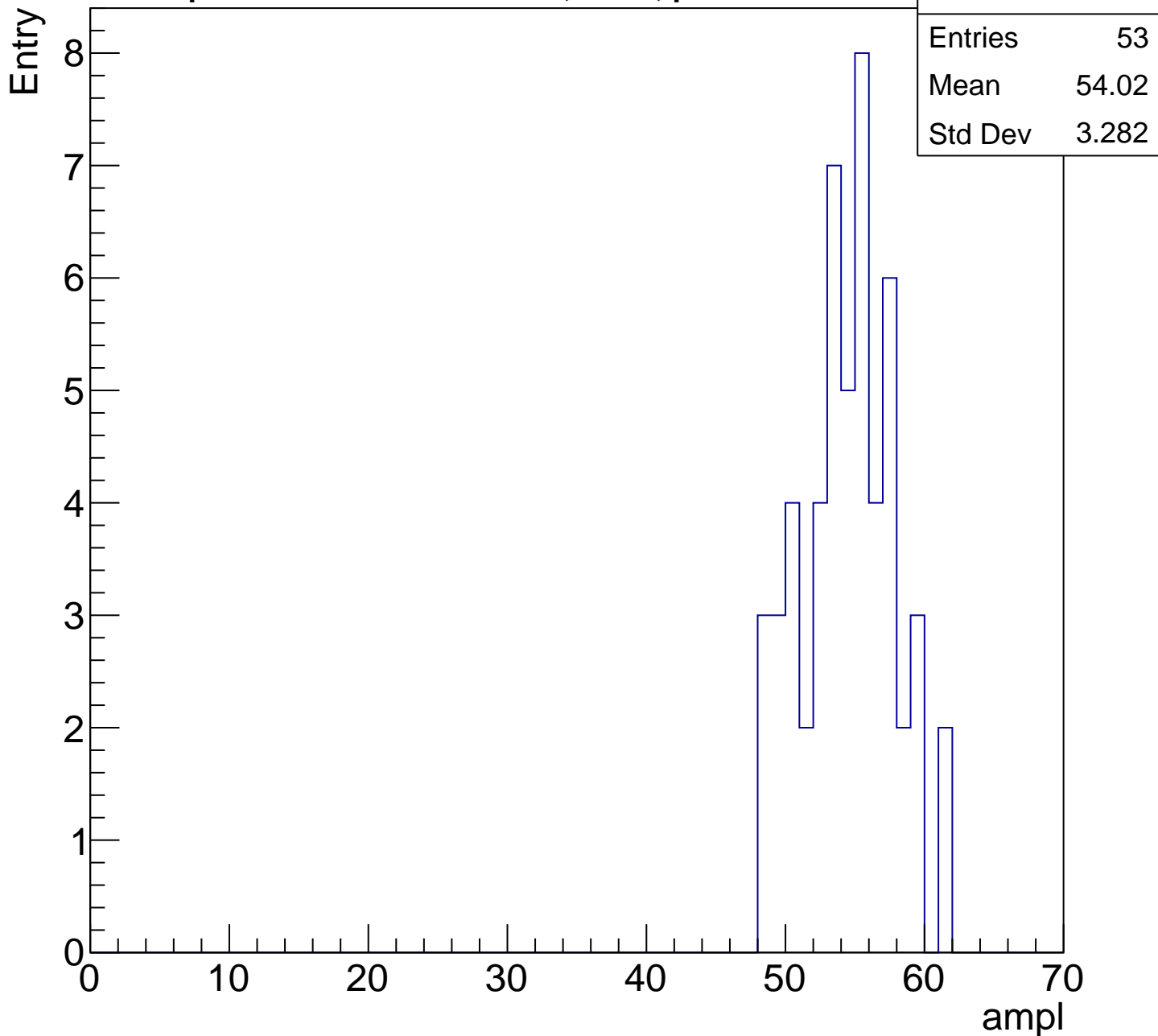
B1L100S, U1-ch66, adc3

calib_packv5_042523_0143.root, FC#4, port A2



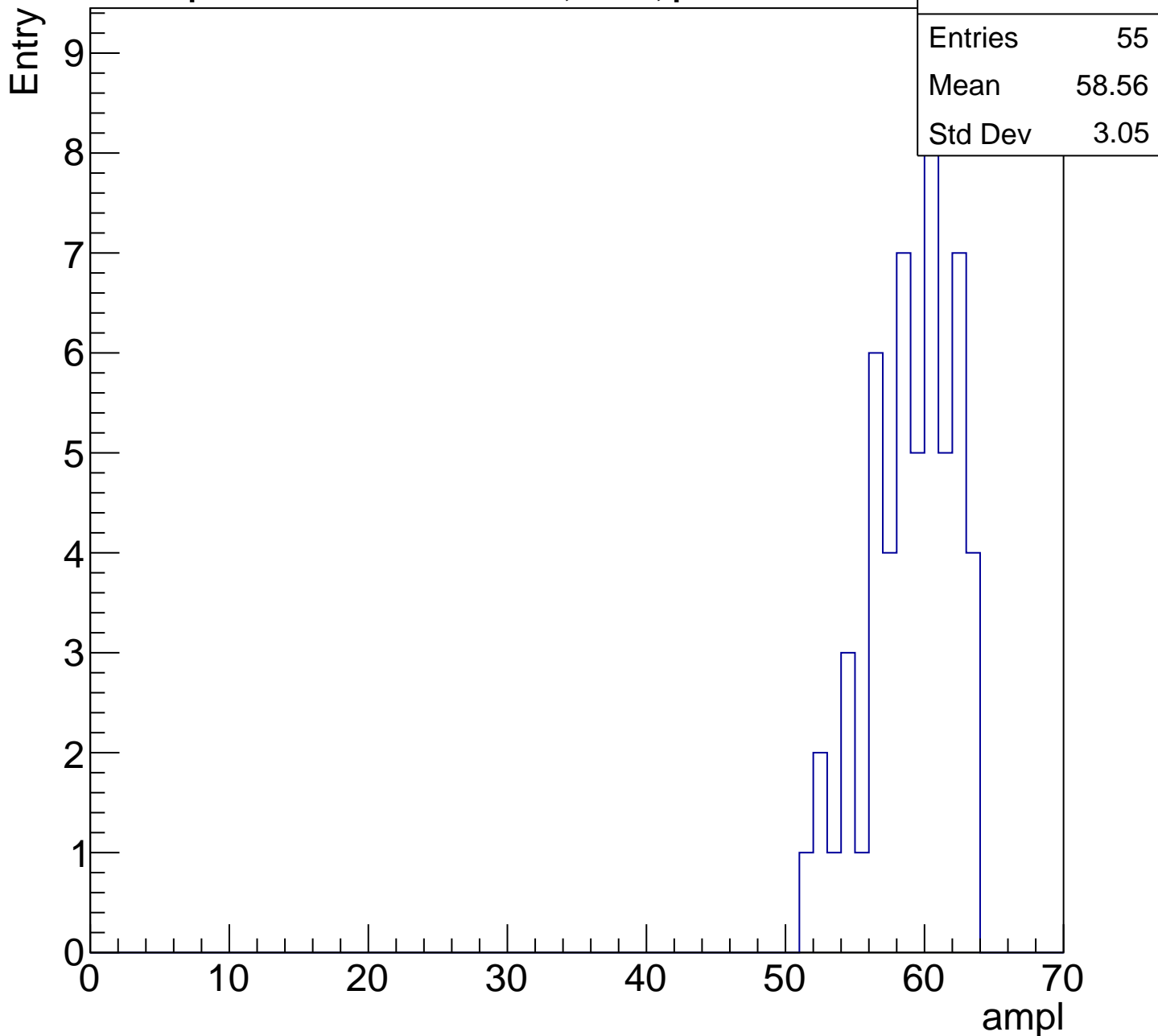
B1L100S, U1-ch66, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch66, adc5

calib_packv5_042523_0143.root, FC#4, port A2

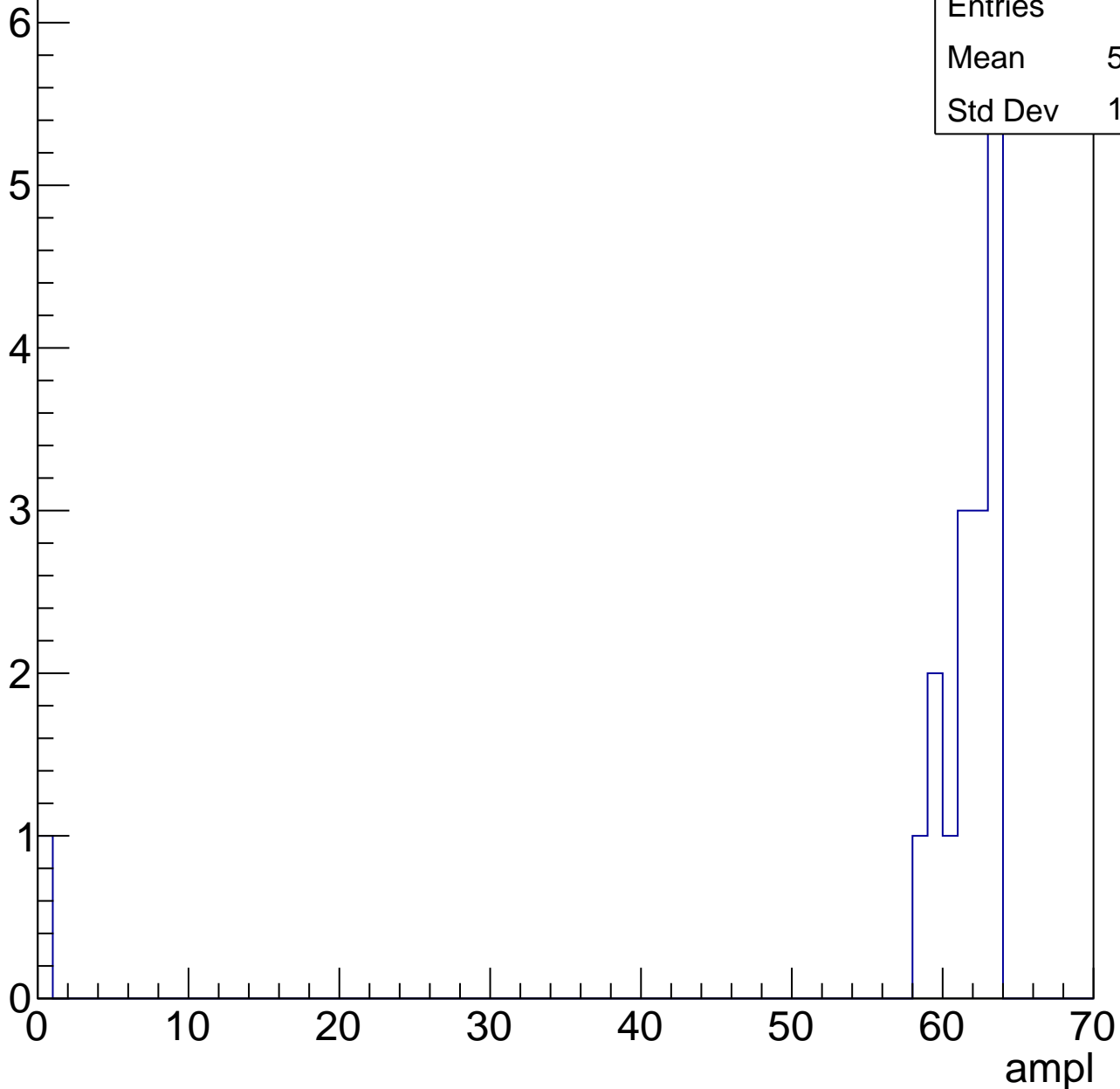


B1L100S, U1-ch66, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.82
Std Dev	14.54



B1L100S, U1-ch66, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch67, adc0

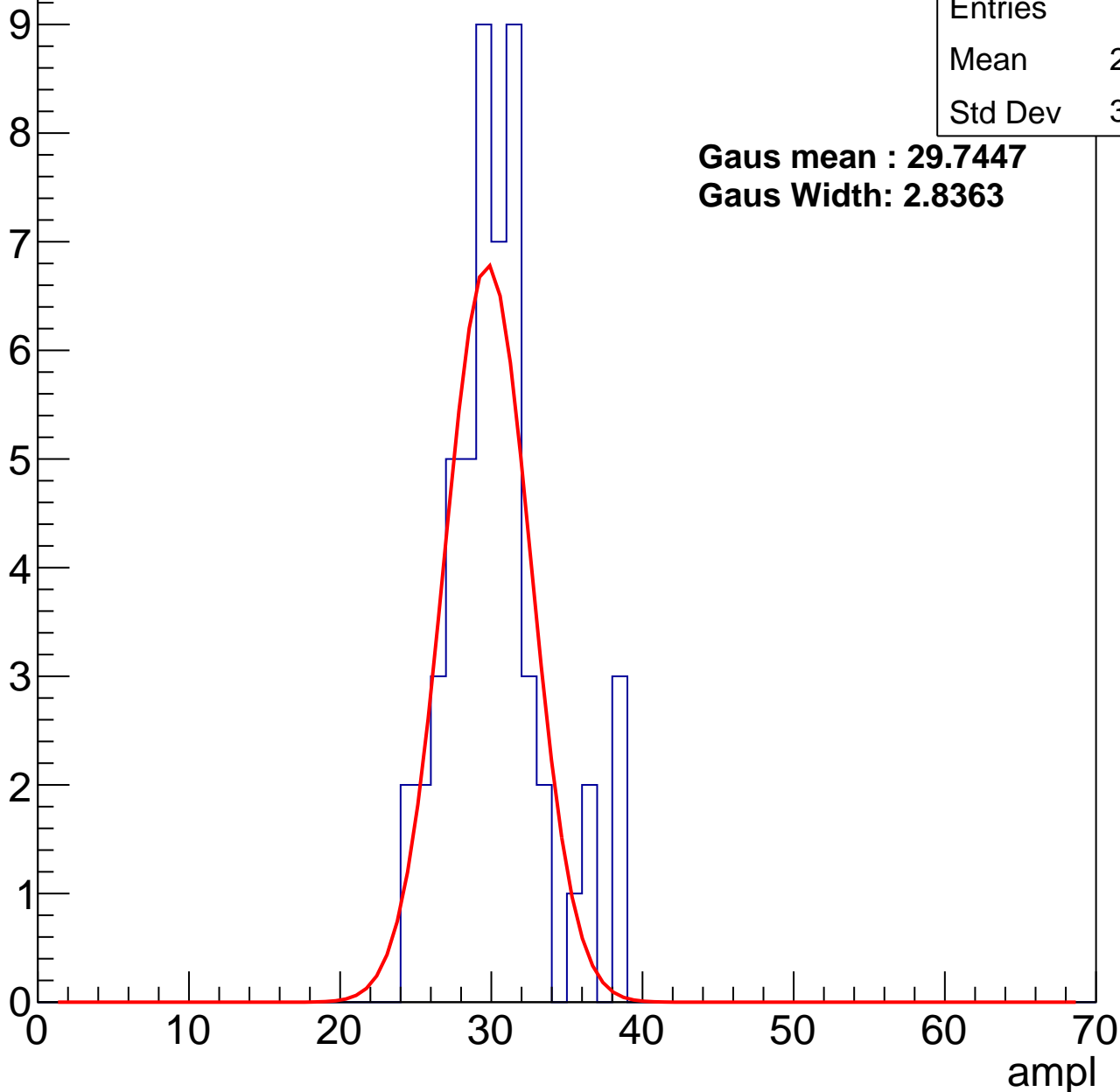
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	29.89
Std Dev	3.283

Gaus mean : 29.7447

Gaus Width: 2.8363



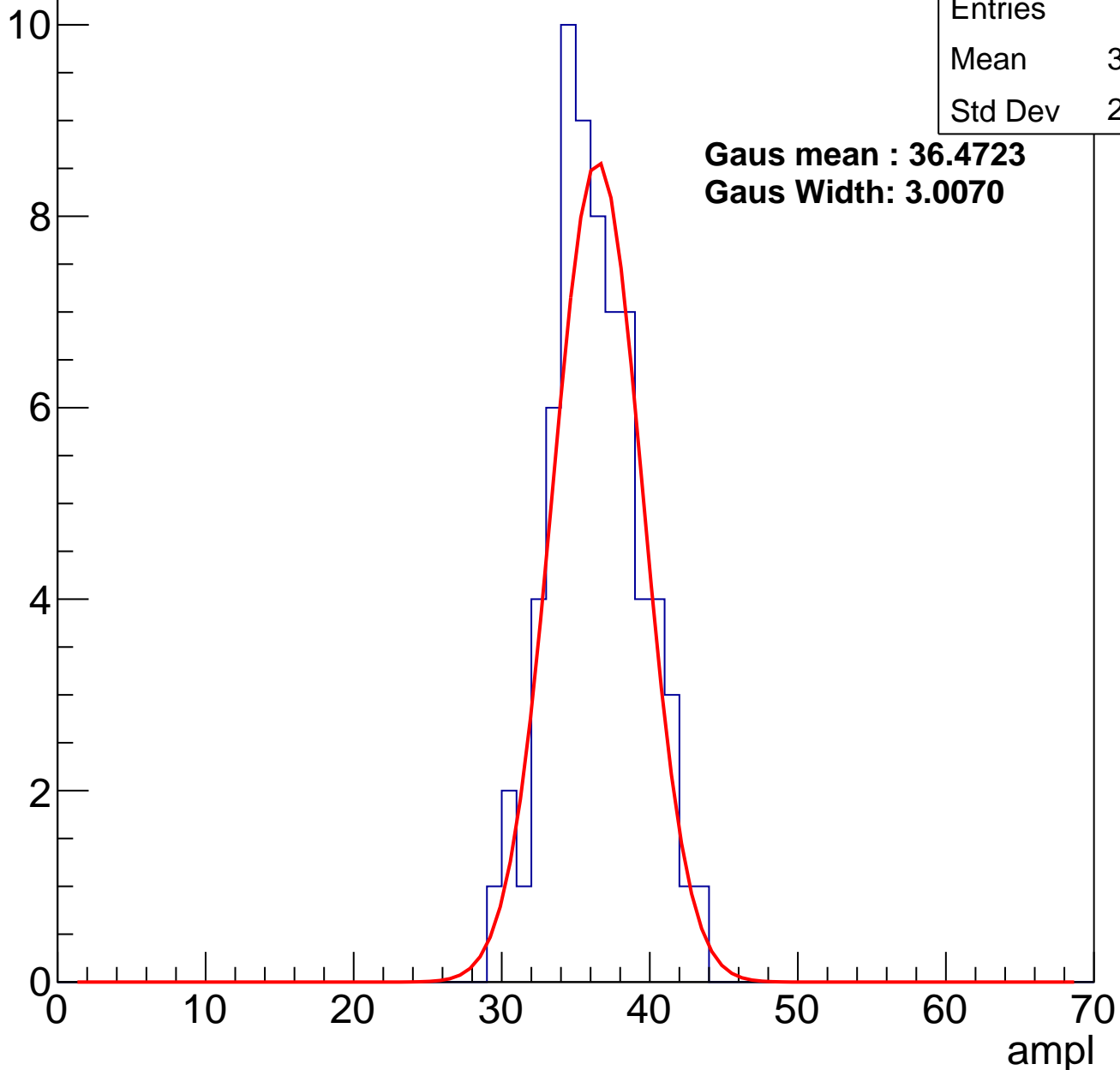
B1L100S, U1-ch67, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	35.85
Std Dev	2.987

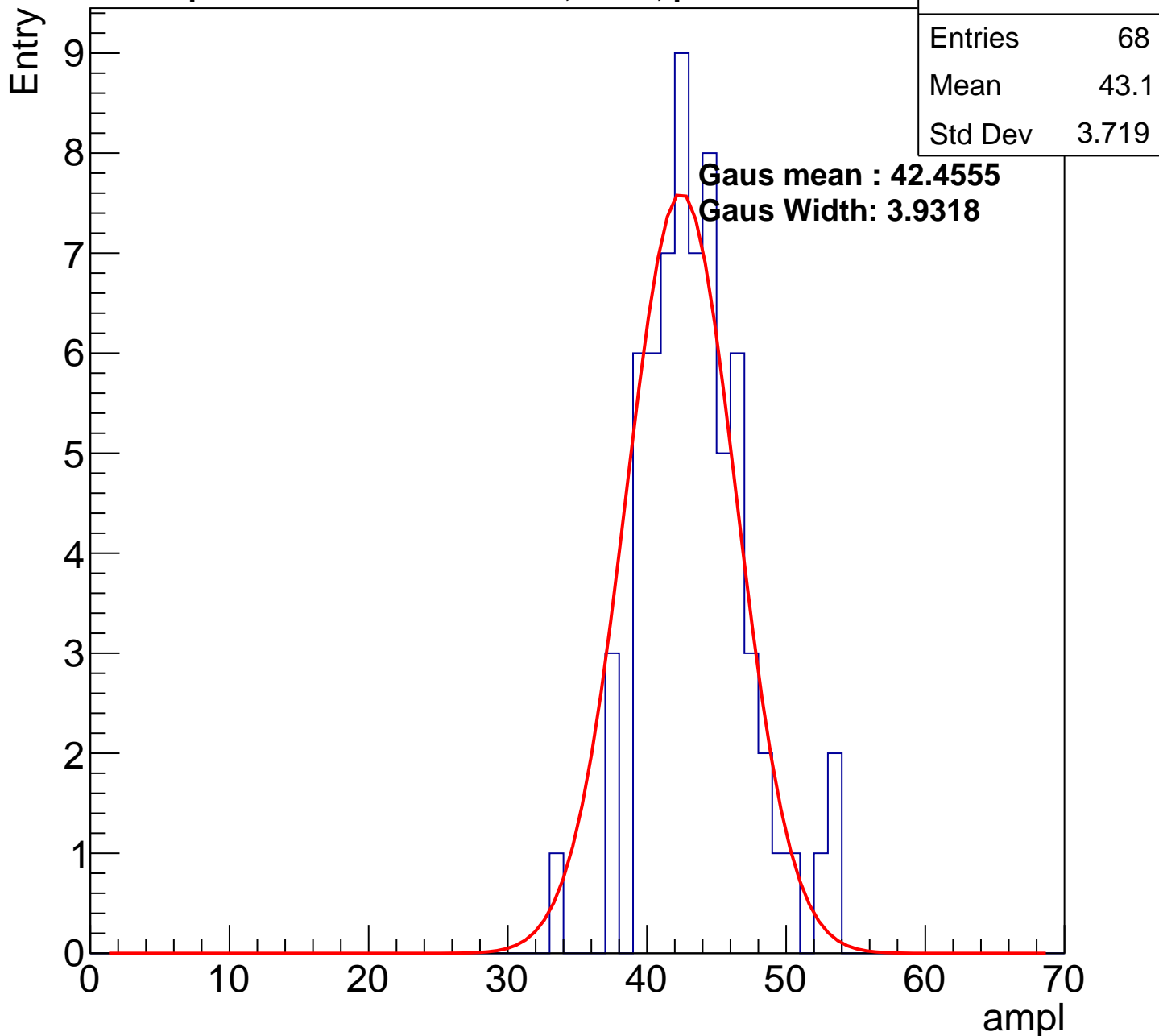
Gaus mean : 36.4723
Gaus Width: 3.0070

Entry



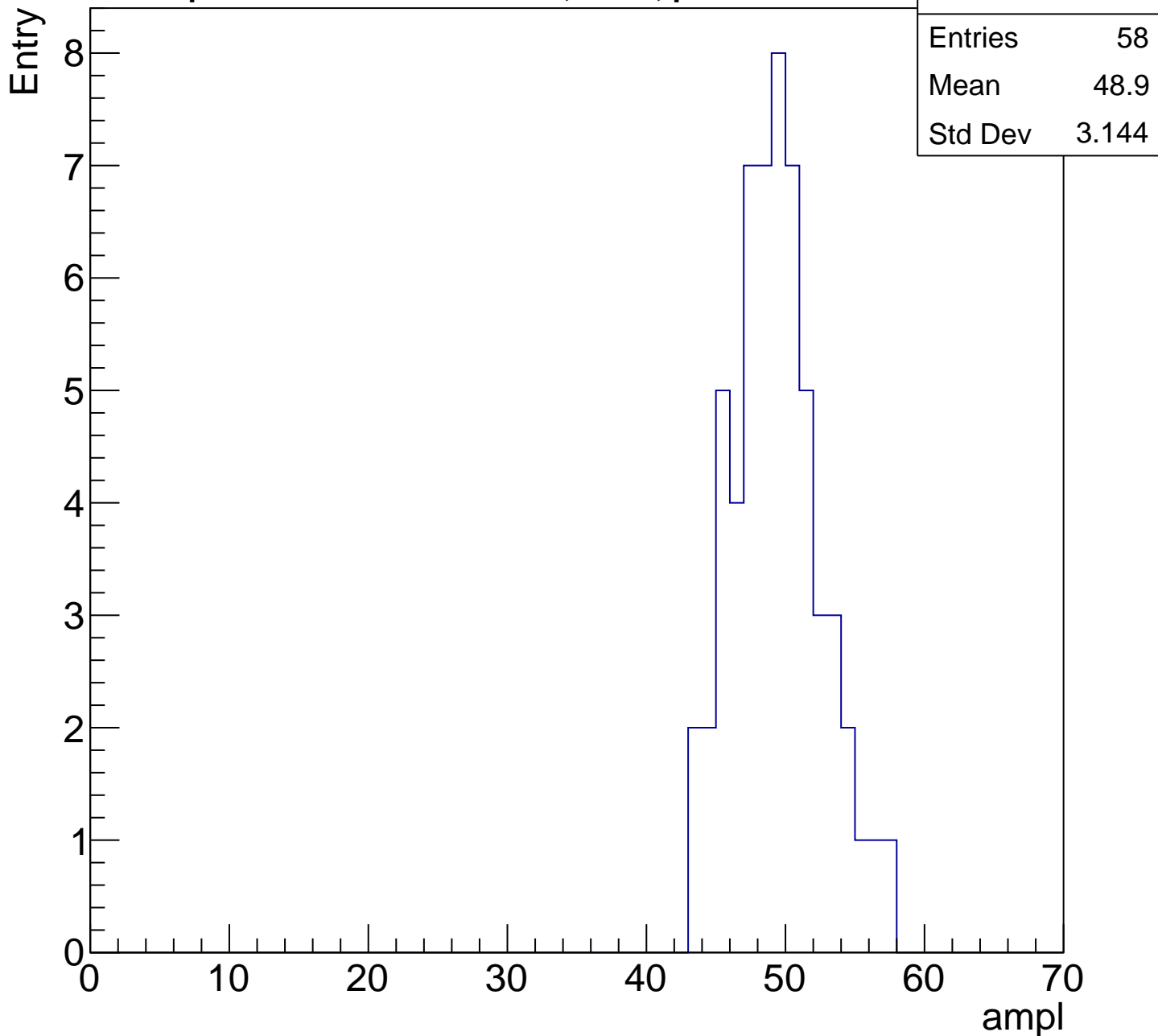
B1L100S, U1-ch67, adc2

calib_packv5_042523_0143.root, FC#4, port A2



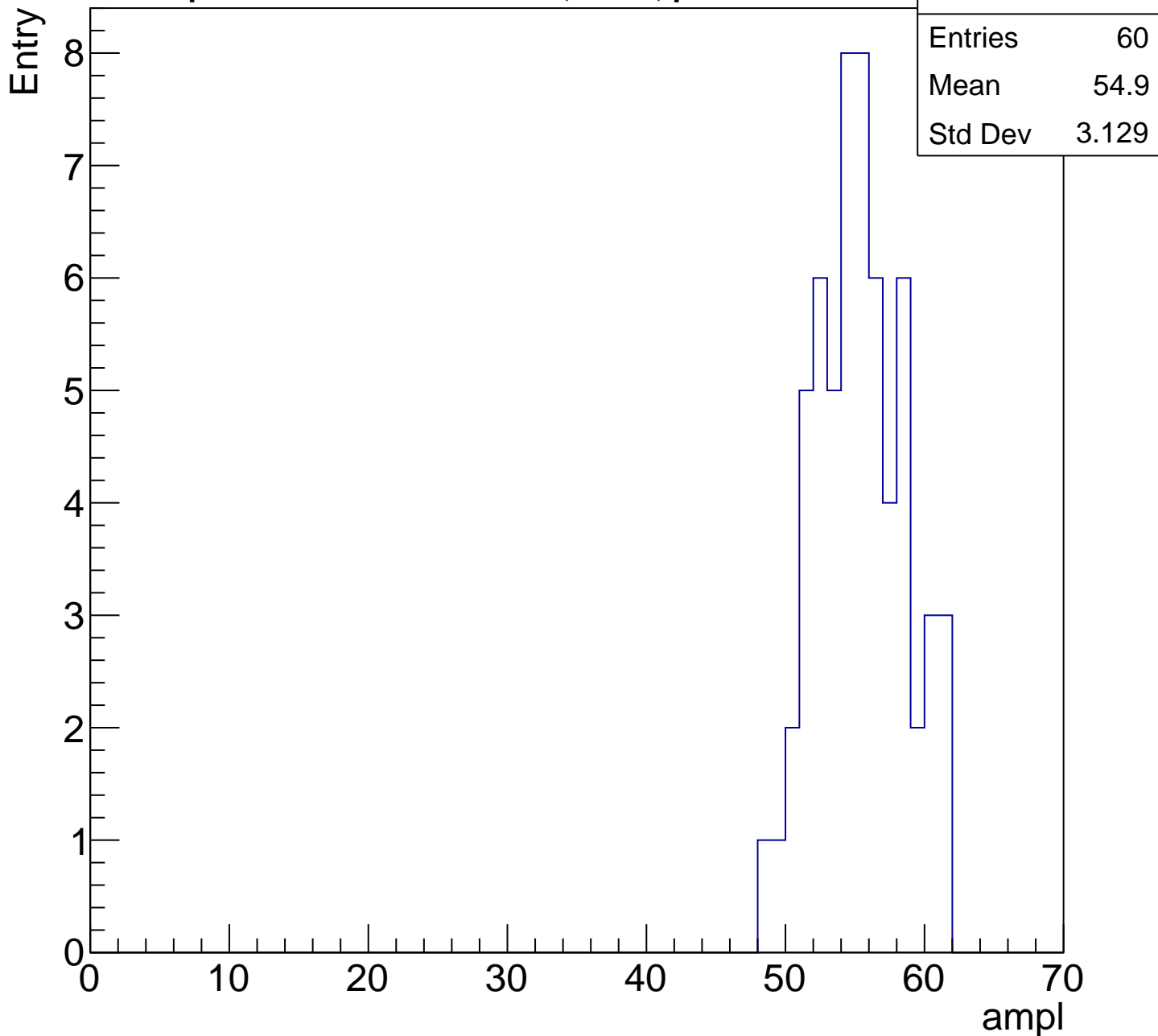
B1L100S, U1-ch67, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch67, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch67, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

10

Entries 54

Mean 60.13

Std Dev 2.253

8

6

4

2

0

0

10

20

30

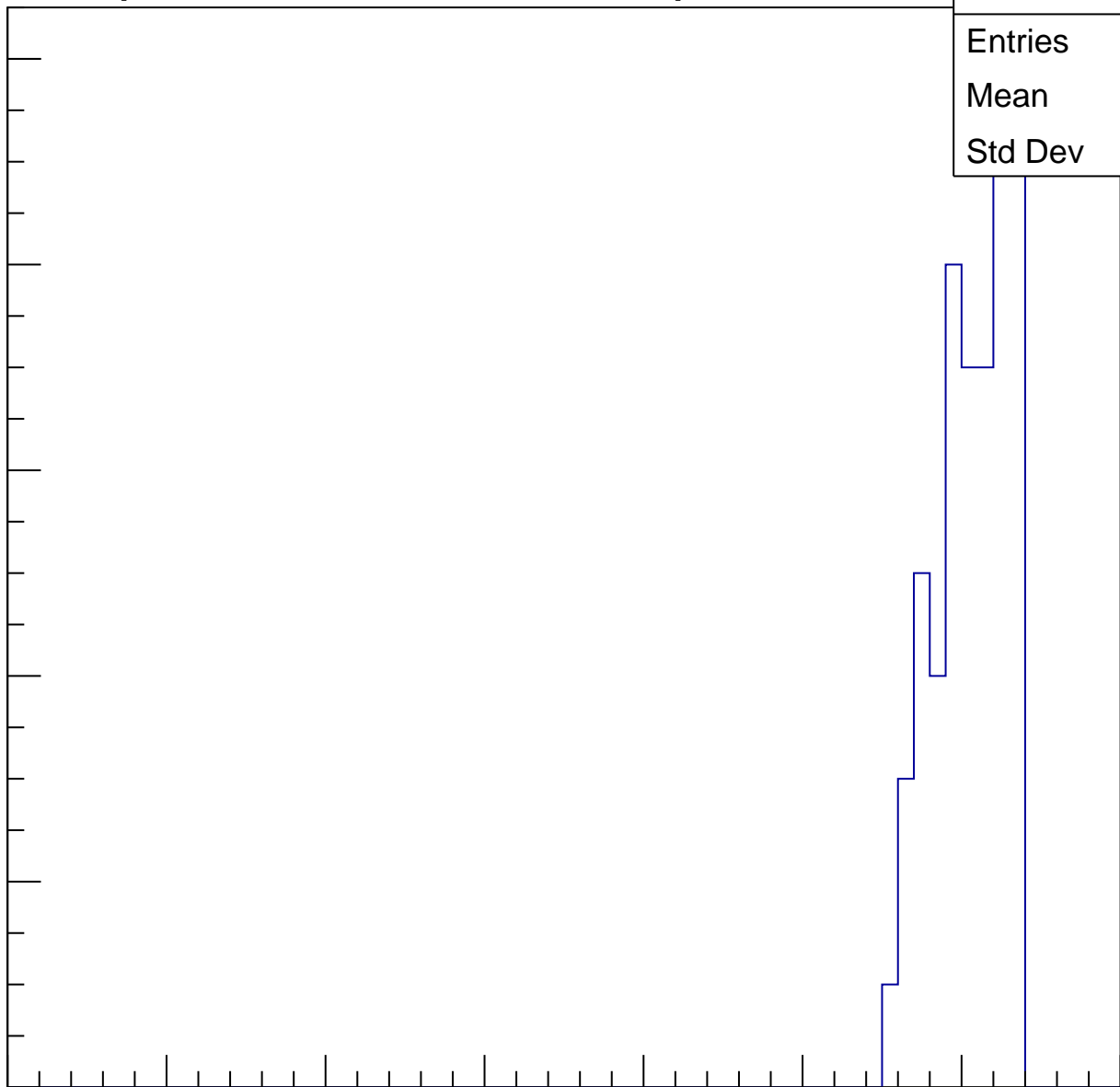
40

50

60

70

ampl



B1L100S, U1-ch67, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch67, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch68, adc0

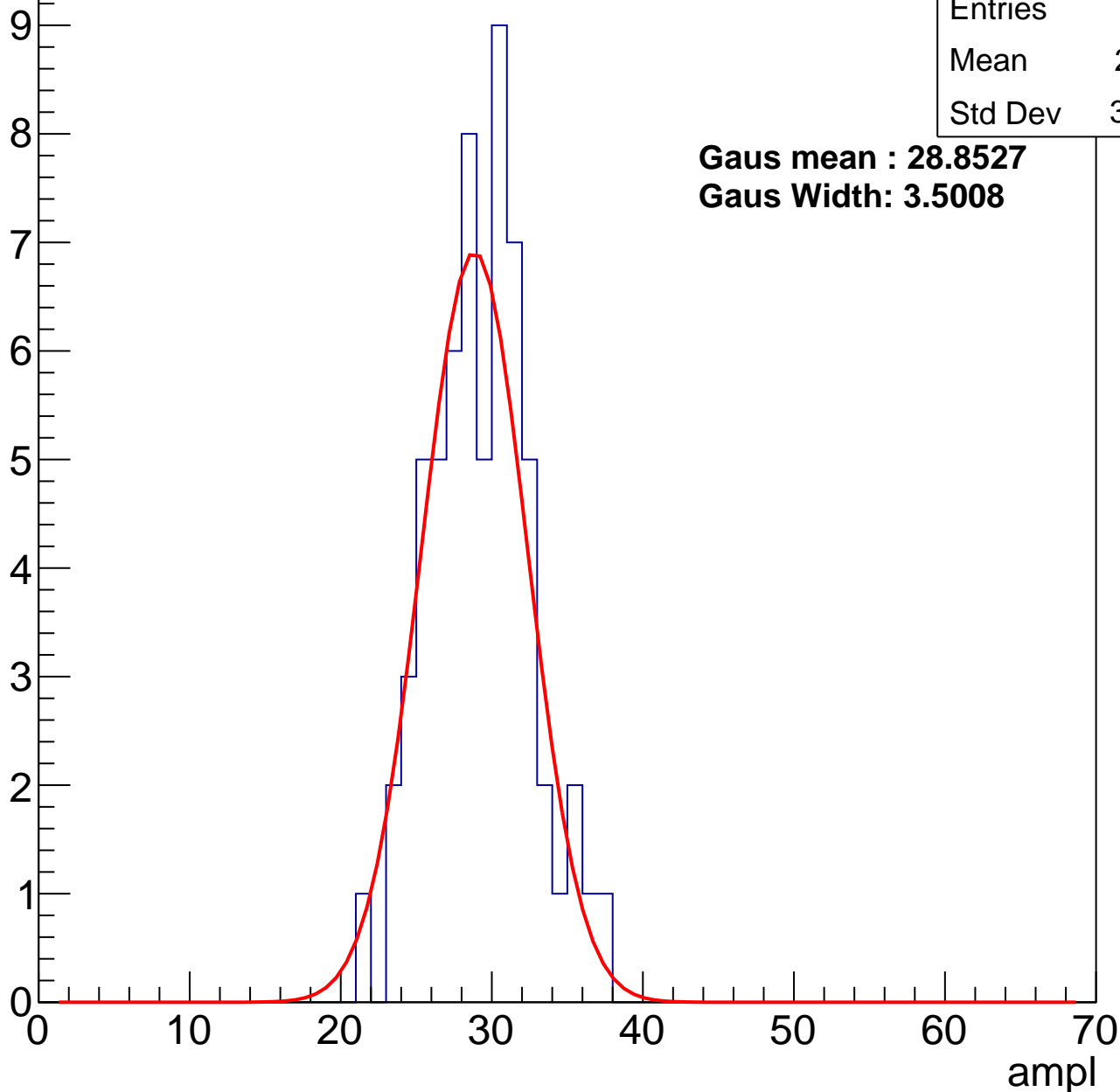
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	28.81
Std Dev	3.309

Gaus mean : 28.8527

Gaus Width: 3.5008



B1L100S, U1-ch68, adc1

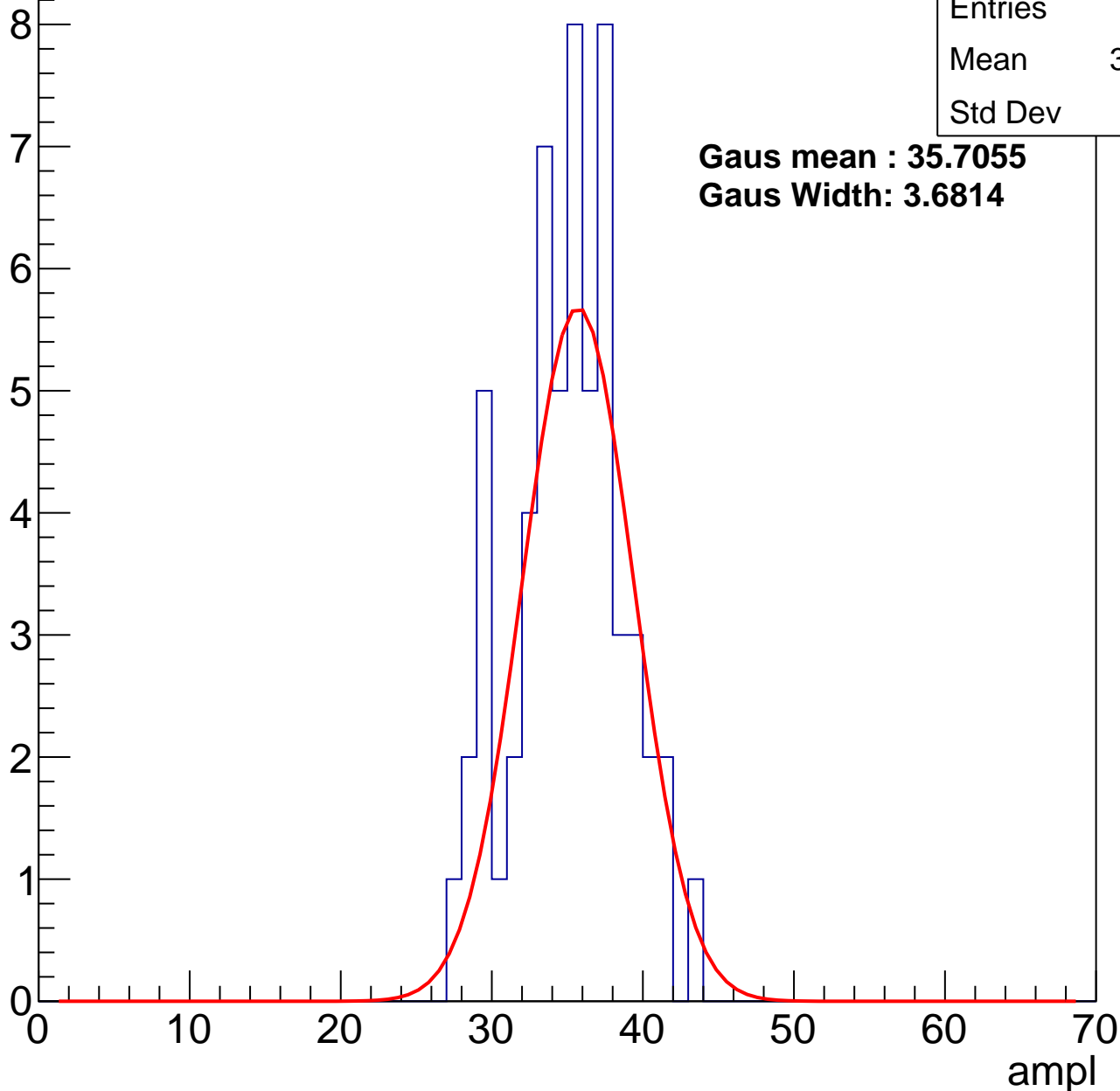
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	34.59
Std Dev	3.58

Gaus mean : 35.7055

Gaus Width: 3.6814



B1L100S, U1-ch68, adc2

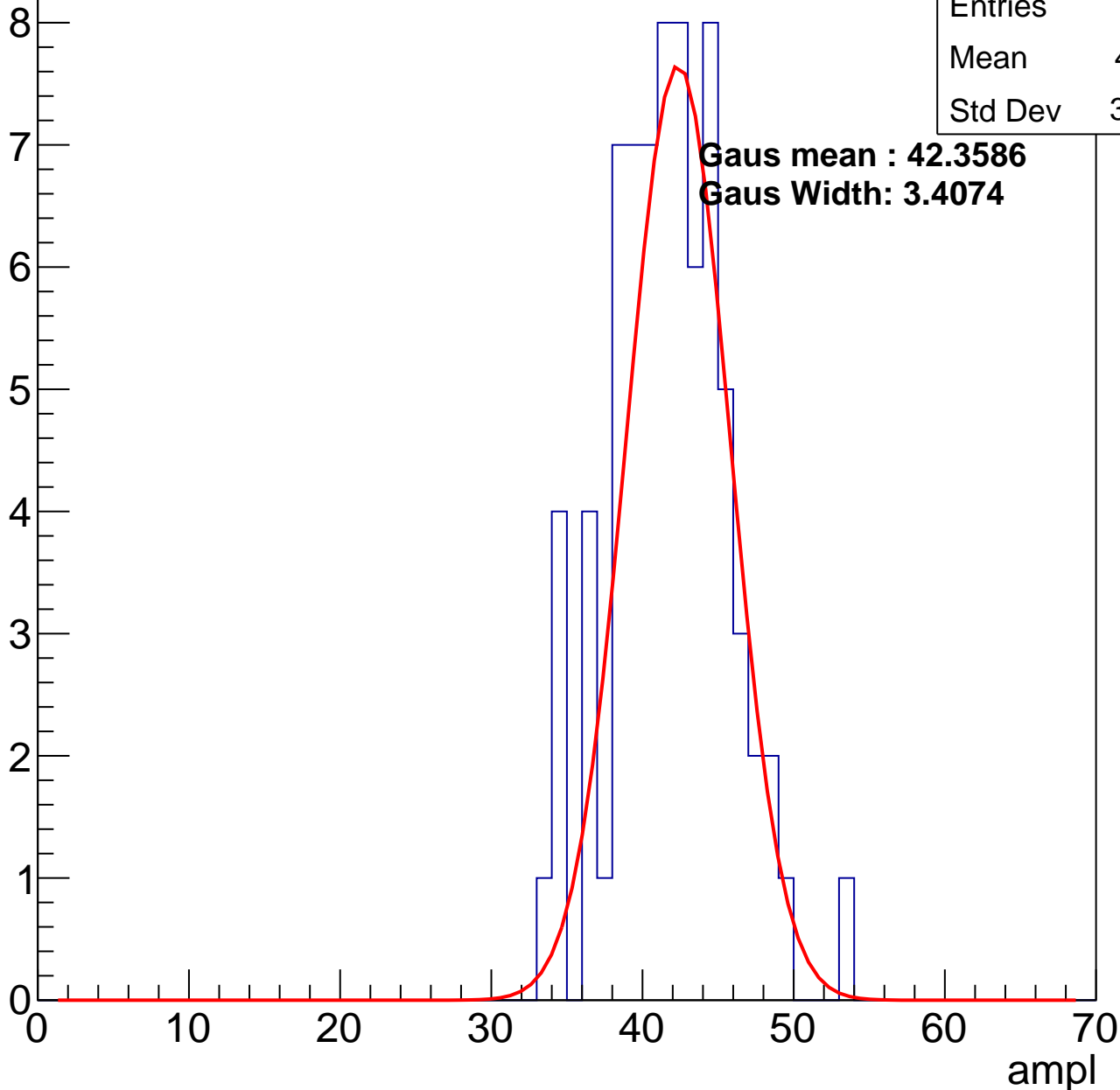
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	41.31
Std Dev	3.837

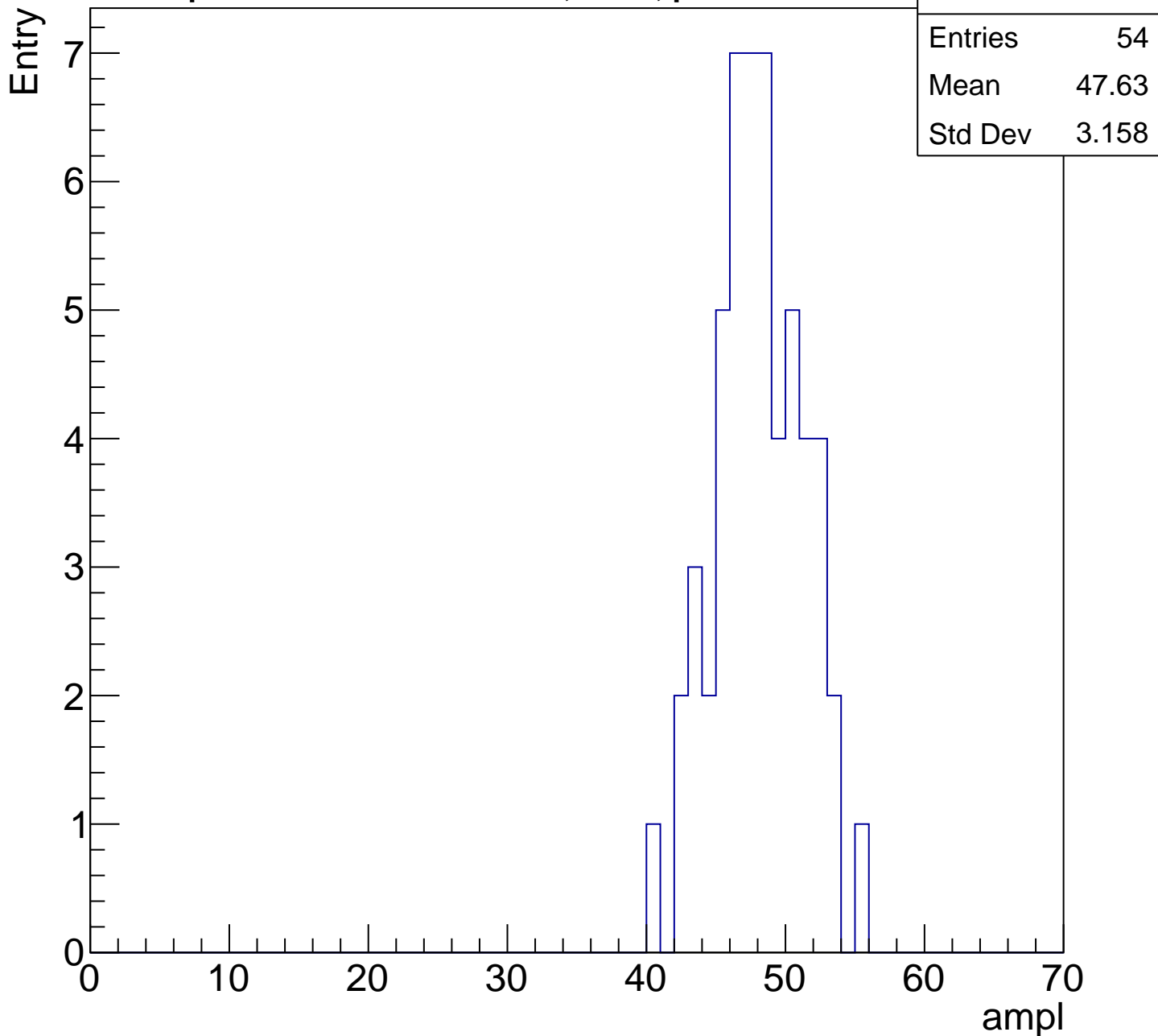
Gaus mean : 42.3586

Gaus Width: 3.4074



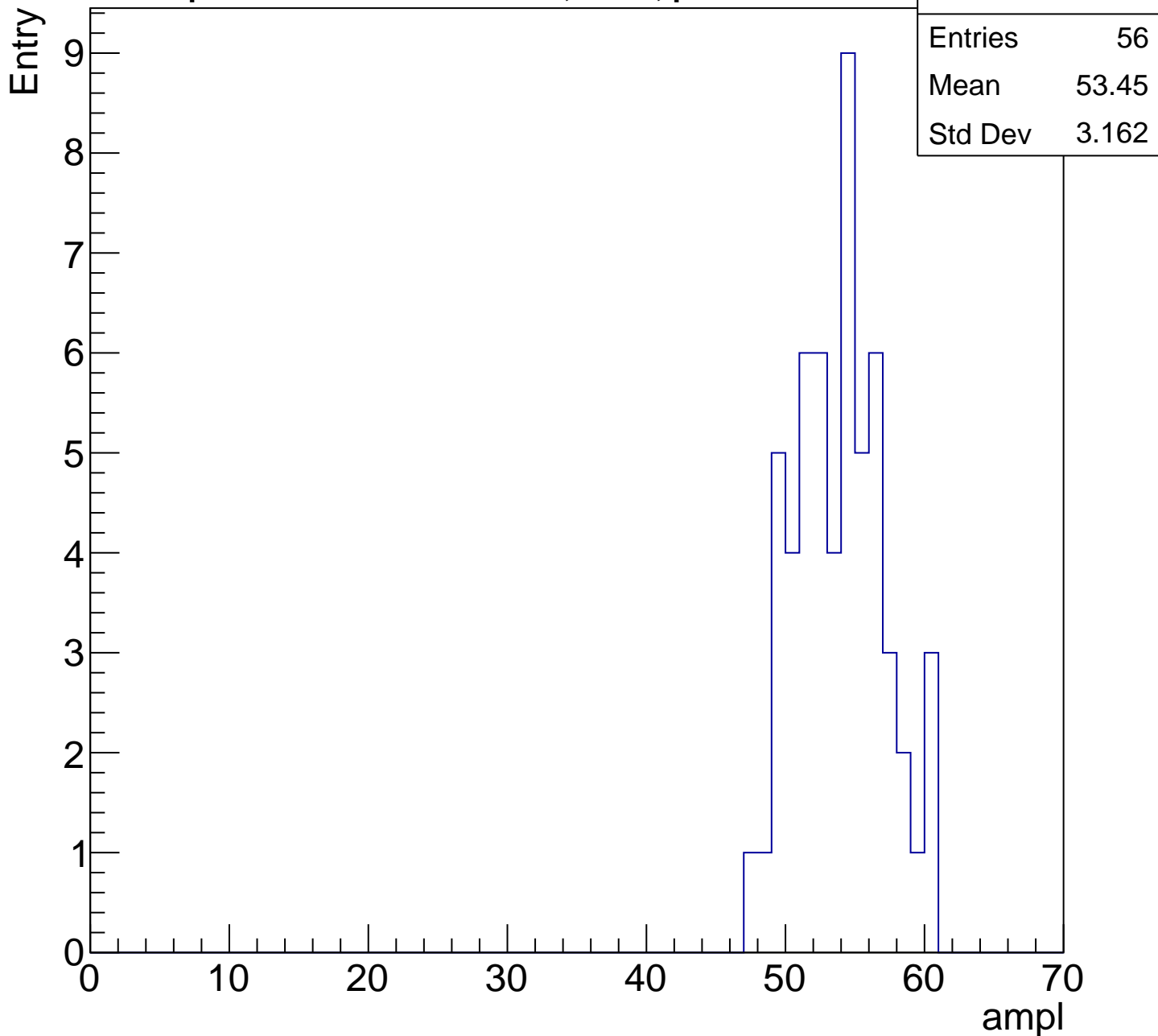
B1L100S, U1-ch68, adc3

calib_packv5_042523_0143.root, FC#4, port A2



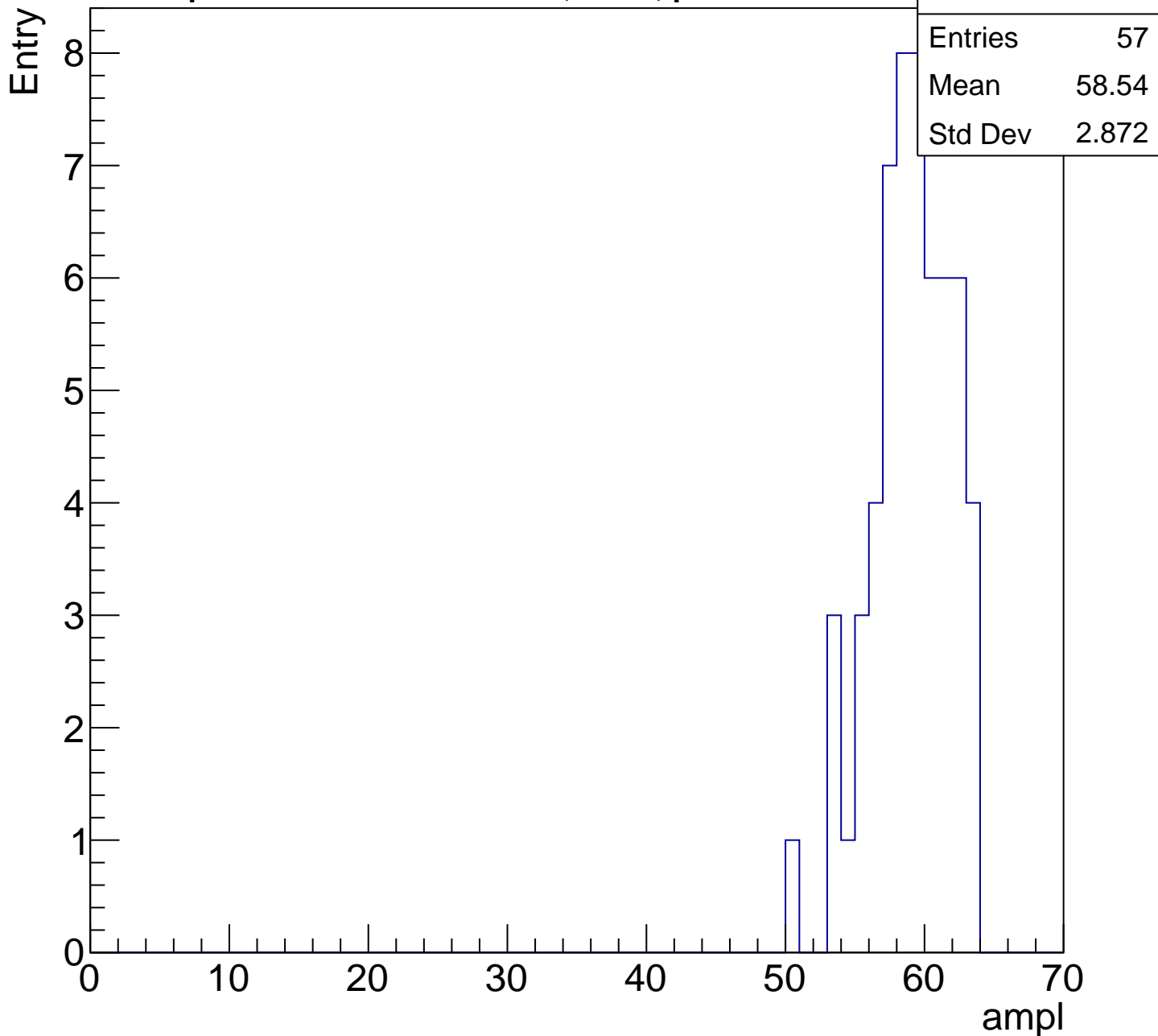
B1L100S, U1-ch68, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch68, adc5

calib_packv5_042523_0143.root, FC#4, port A2

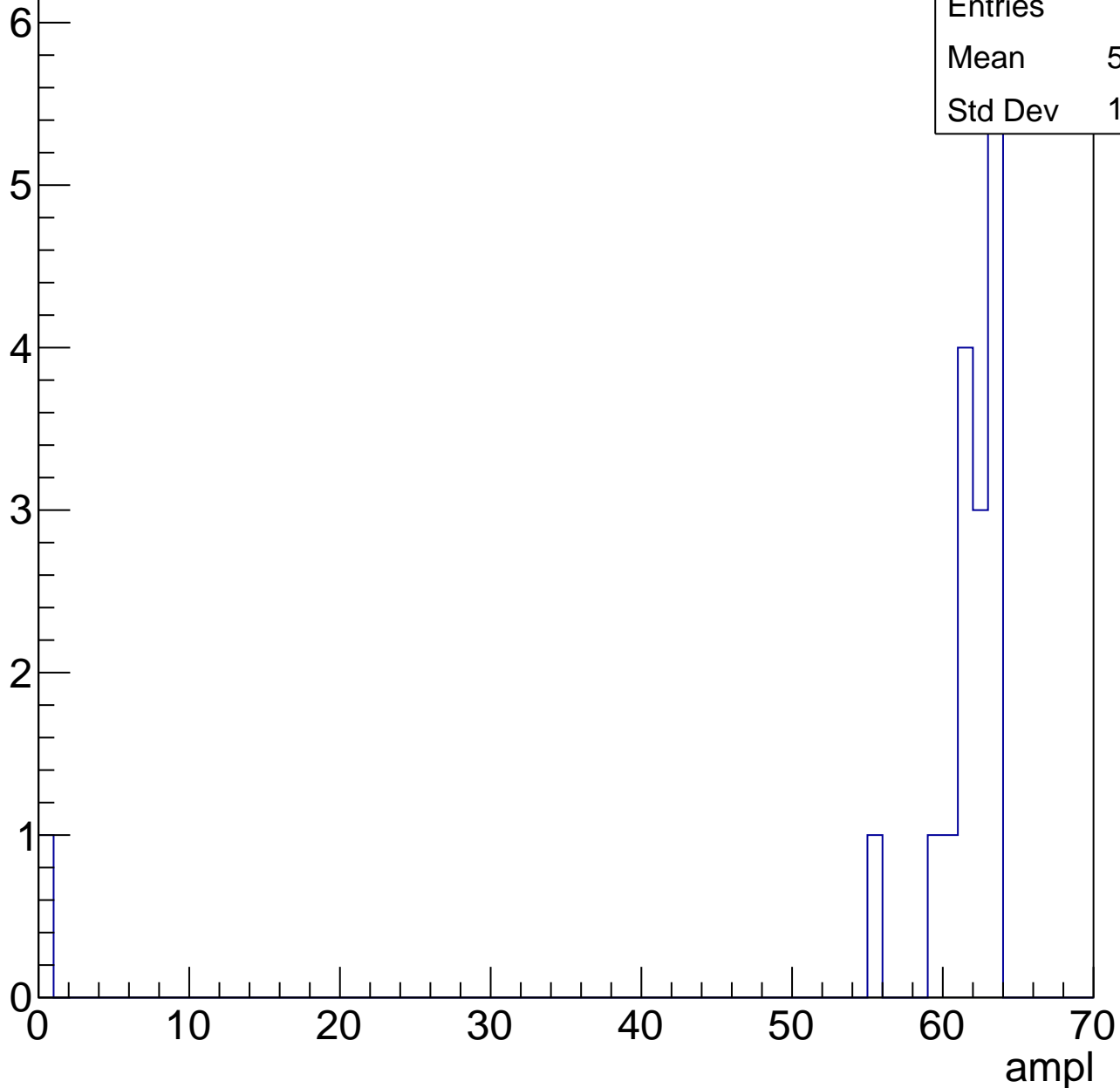


B1L100S, U1-ch68, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.76
Std Dev	14.57



B1L100S, U1-ch68, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch69, adc0

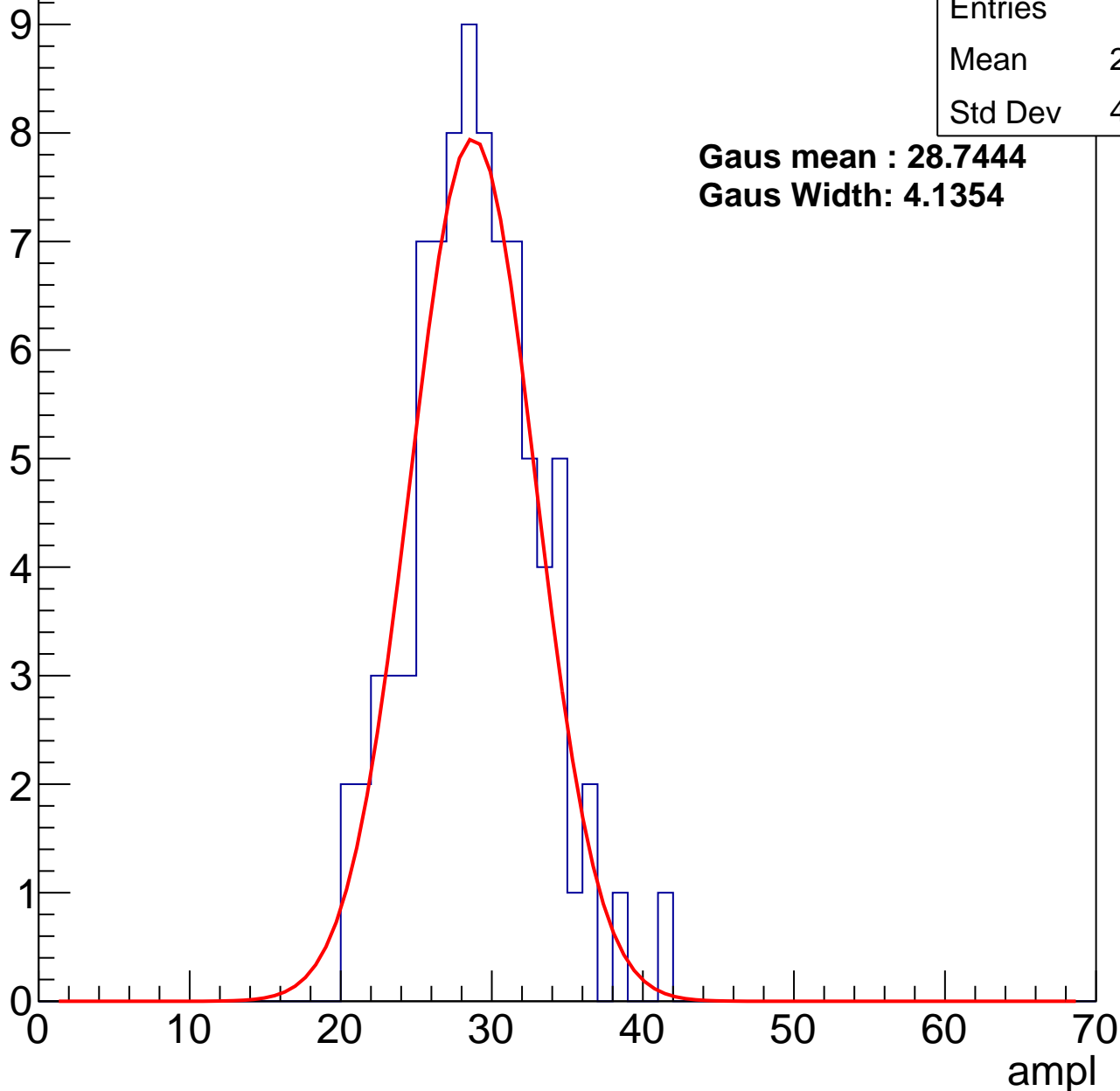
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	85
Mean	28.48
Std Dev	4.118

Gaus mean : 28.7444

Gaus Width: 4.1354



B1L100S, U1-ch69, adc1

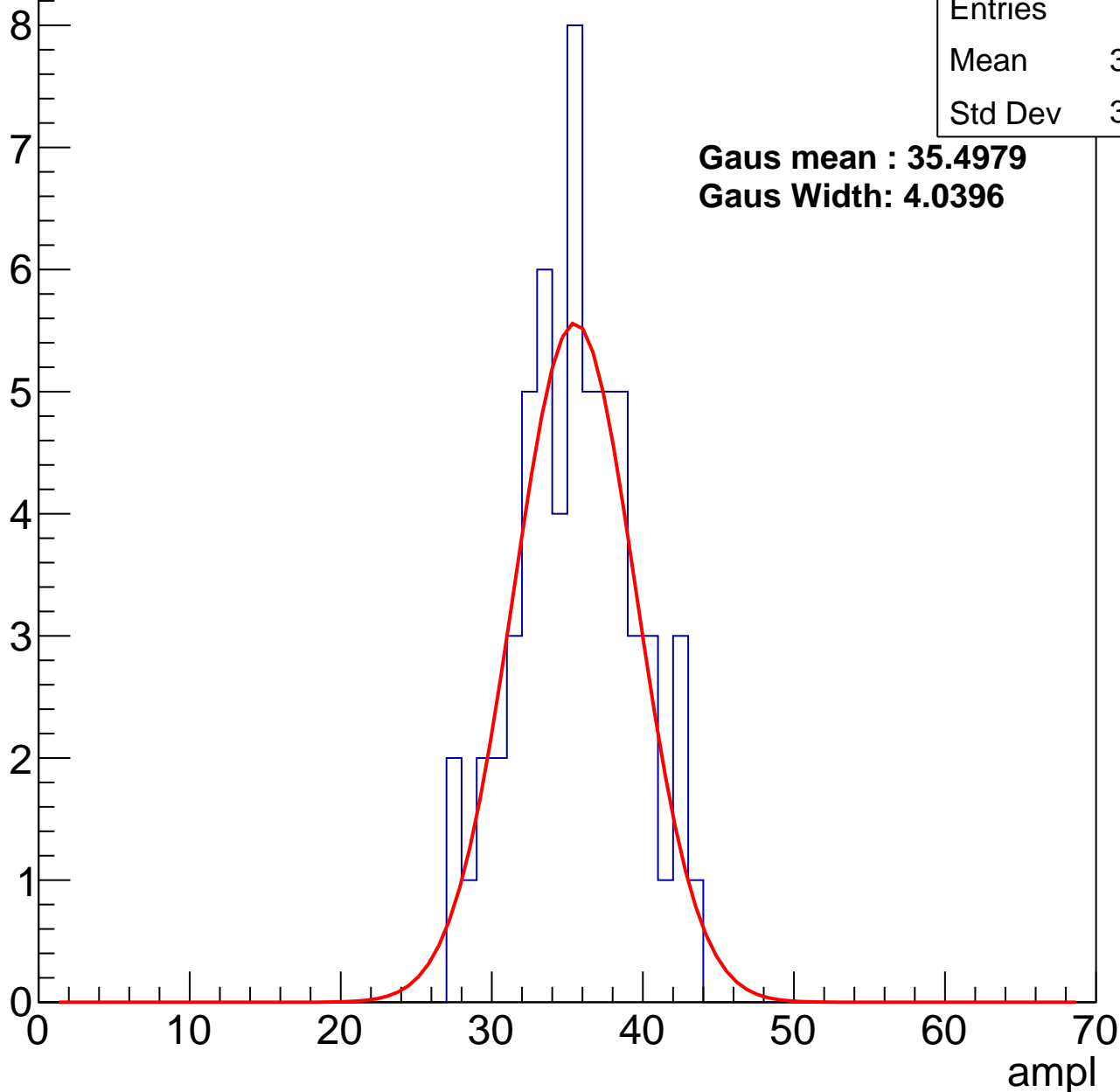
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.07
Std Dev	3.817

Gaus mean : 35.4979

Gaus Width: 4.0396



B1L100S, U1-ch69, adc2

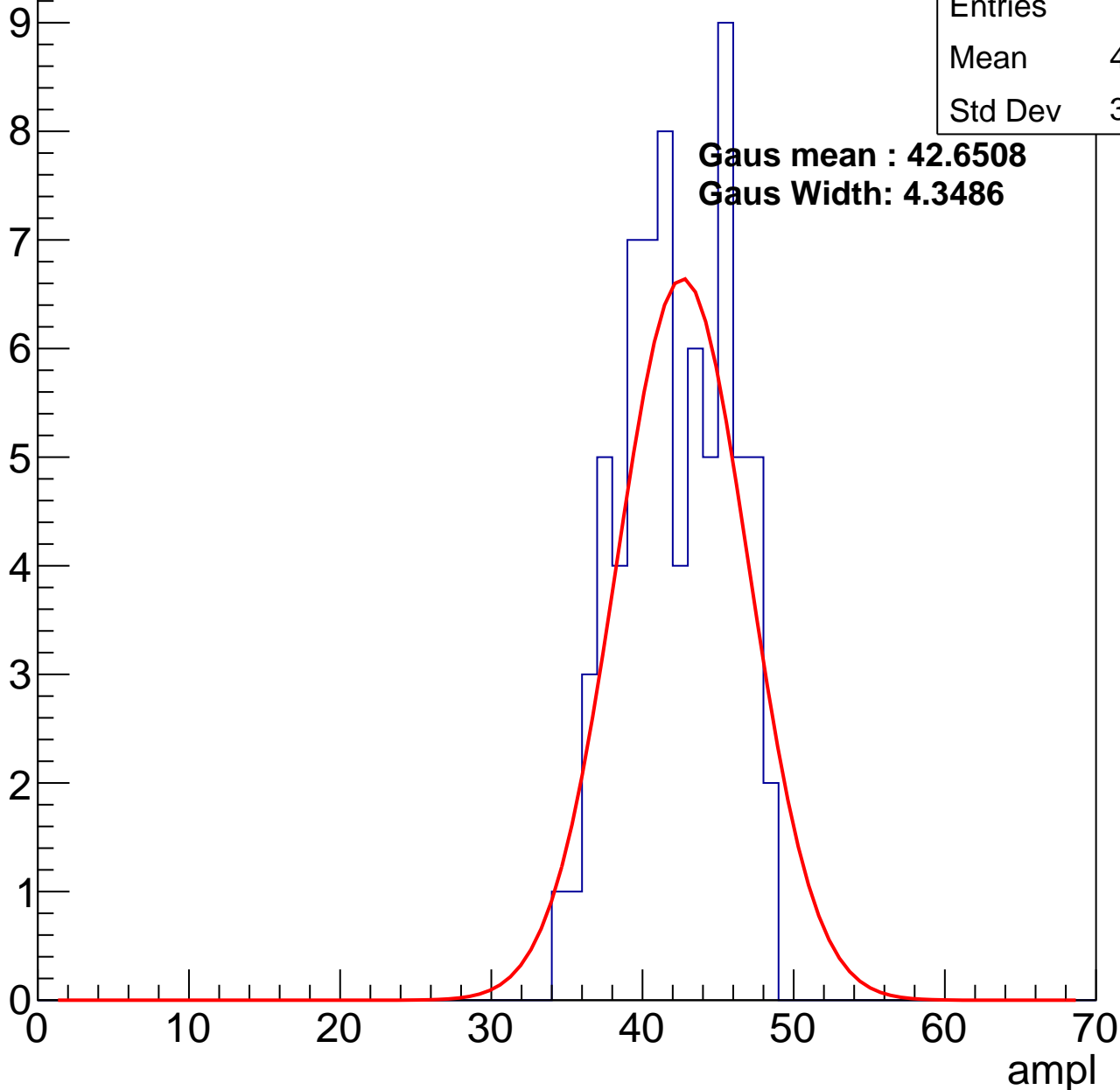
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.76
Std Dev	3.514

Gaus mean : 42.6508

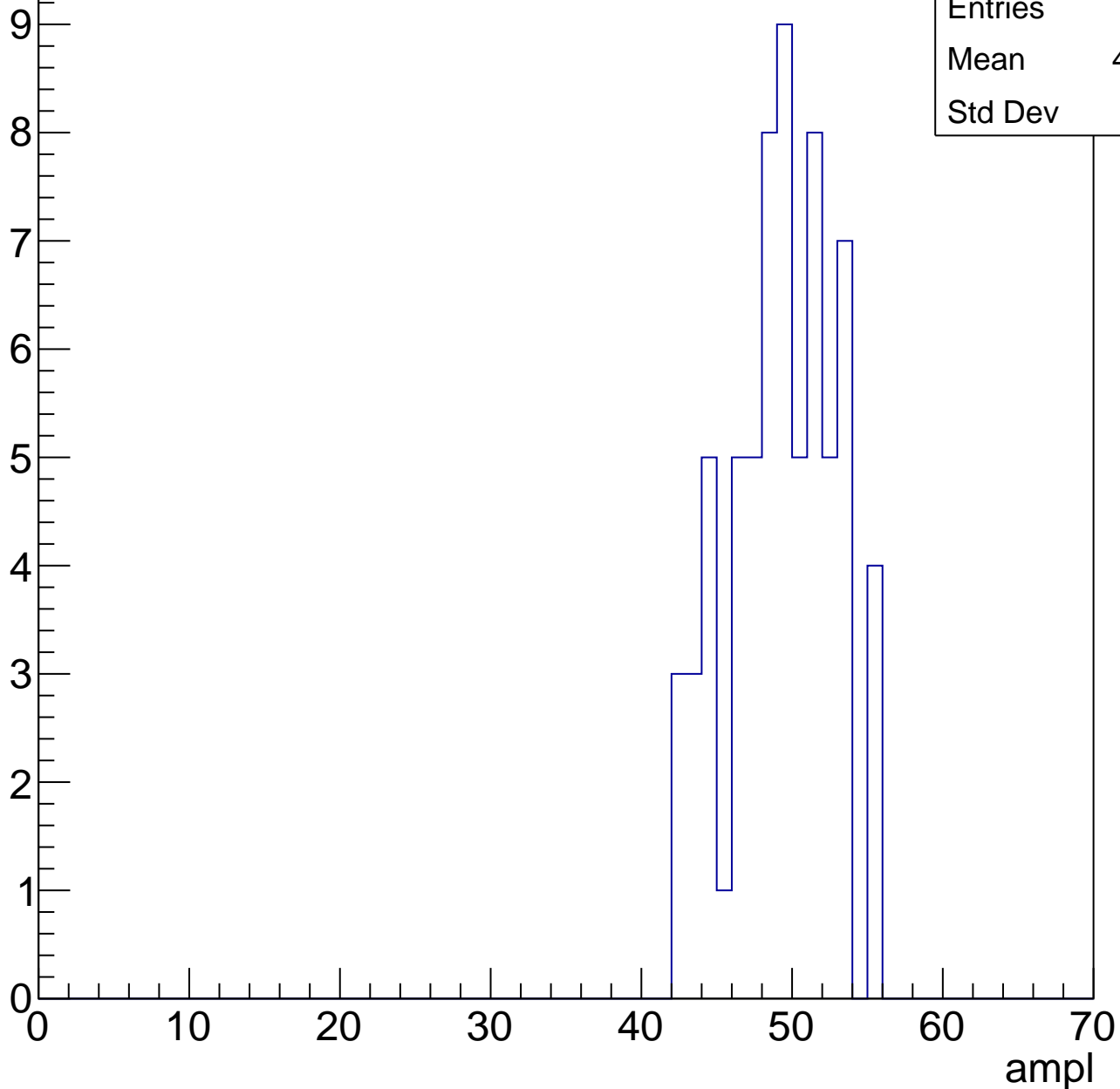
Gaus Width: 4.3486



B1L100S, U1-ch69, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

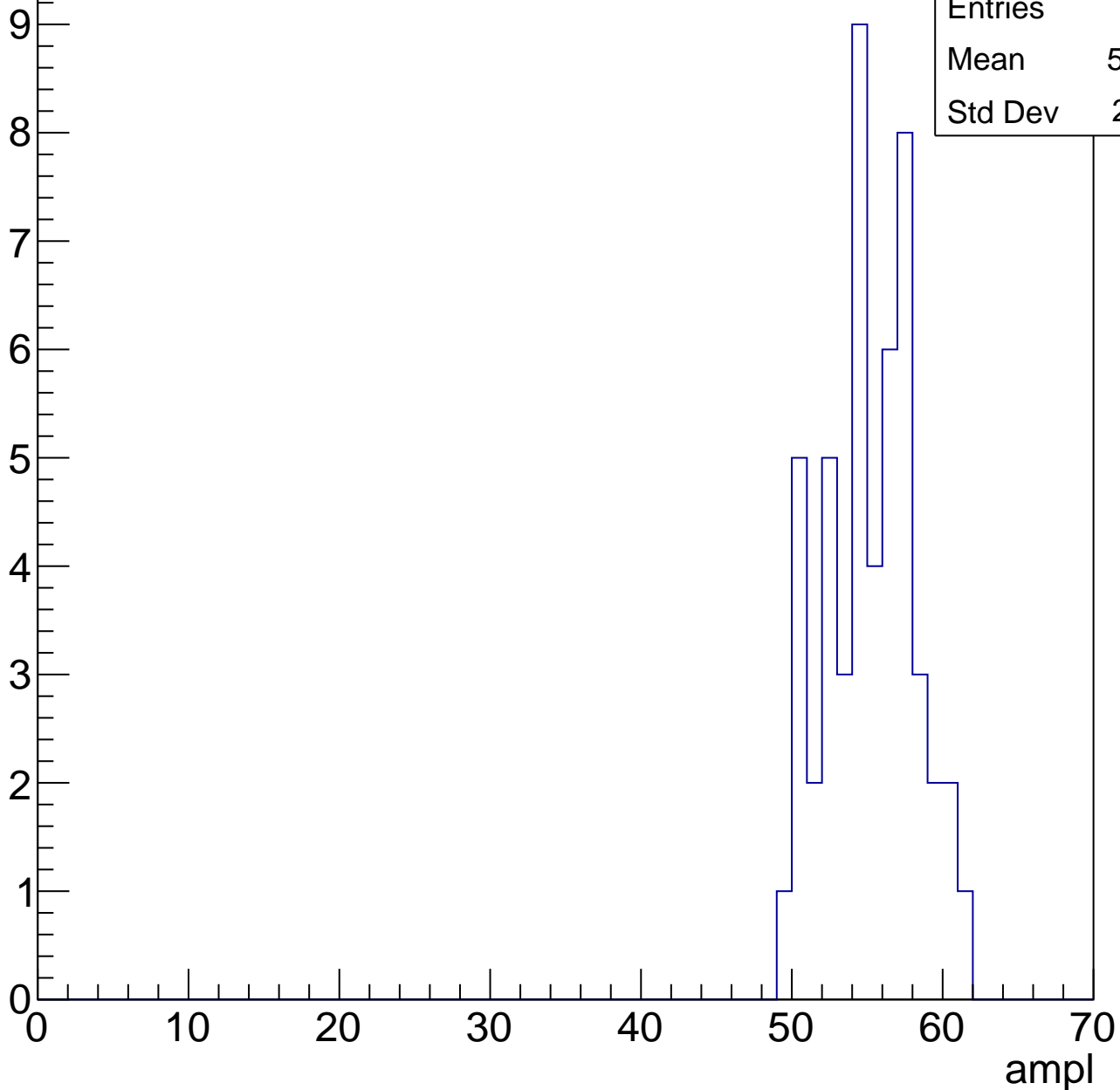


B1L100S, U1-ch69, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	54.73
Std Dev	2.931

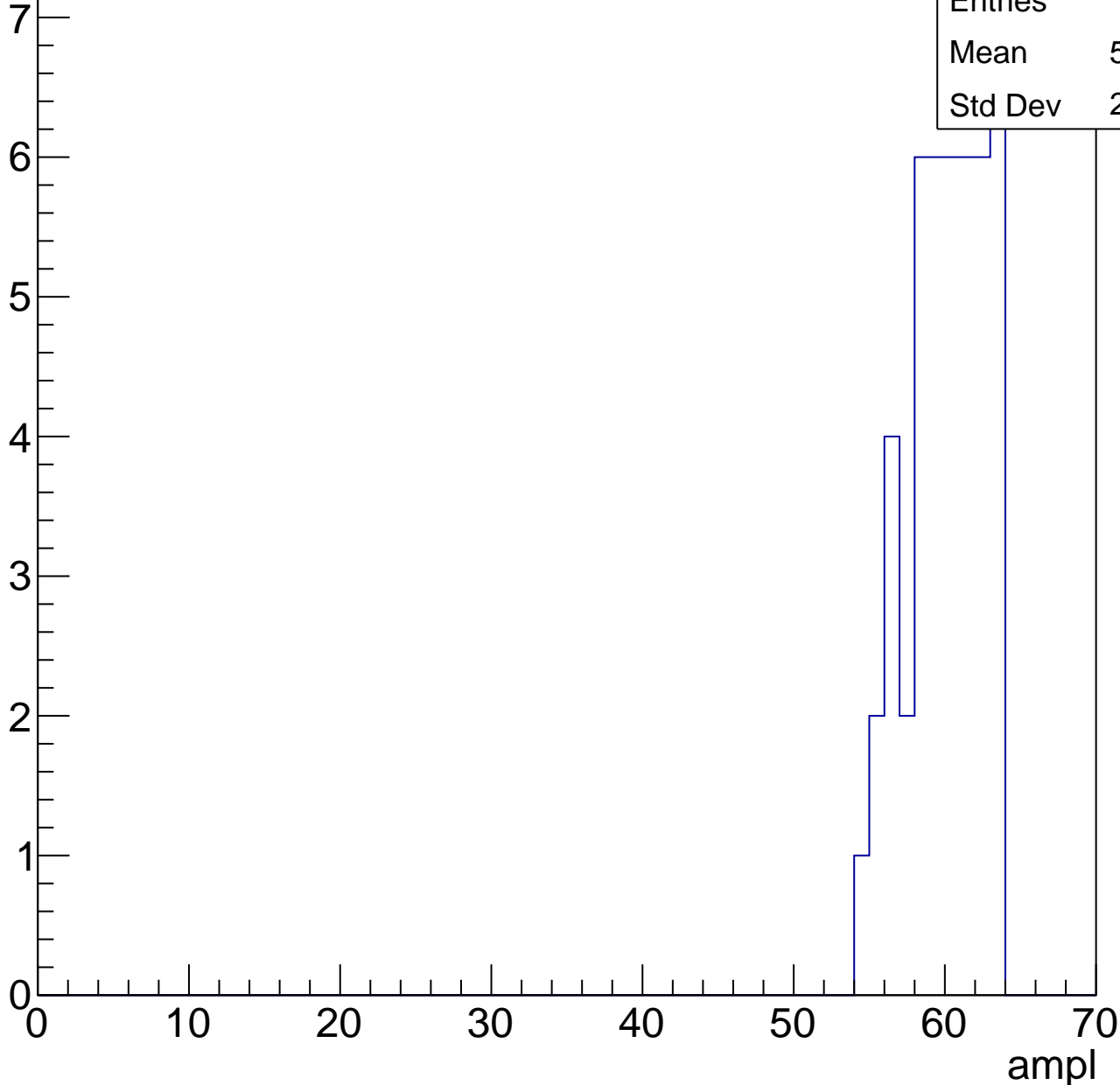


B1L100S, U1-ch69, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

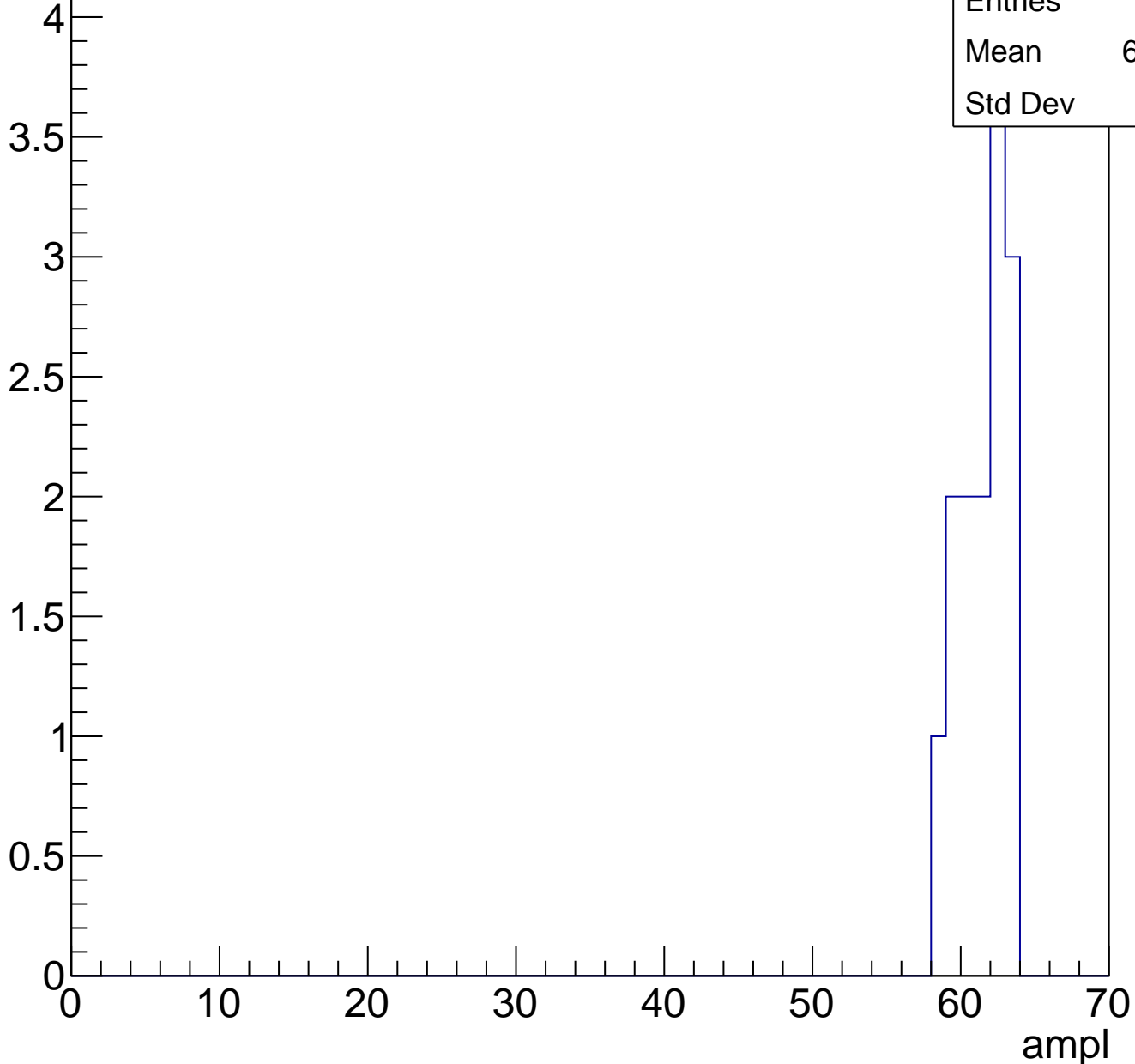
Entries	46
Mean	59.63
Std Dev	2.488



B1L100S, U1-ch69, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	14
Mean	61.07
Std Dev	1.58

B1L100S, U1-ch69, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch70, adc0

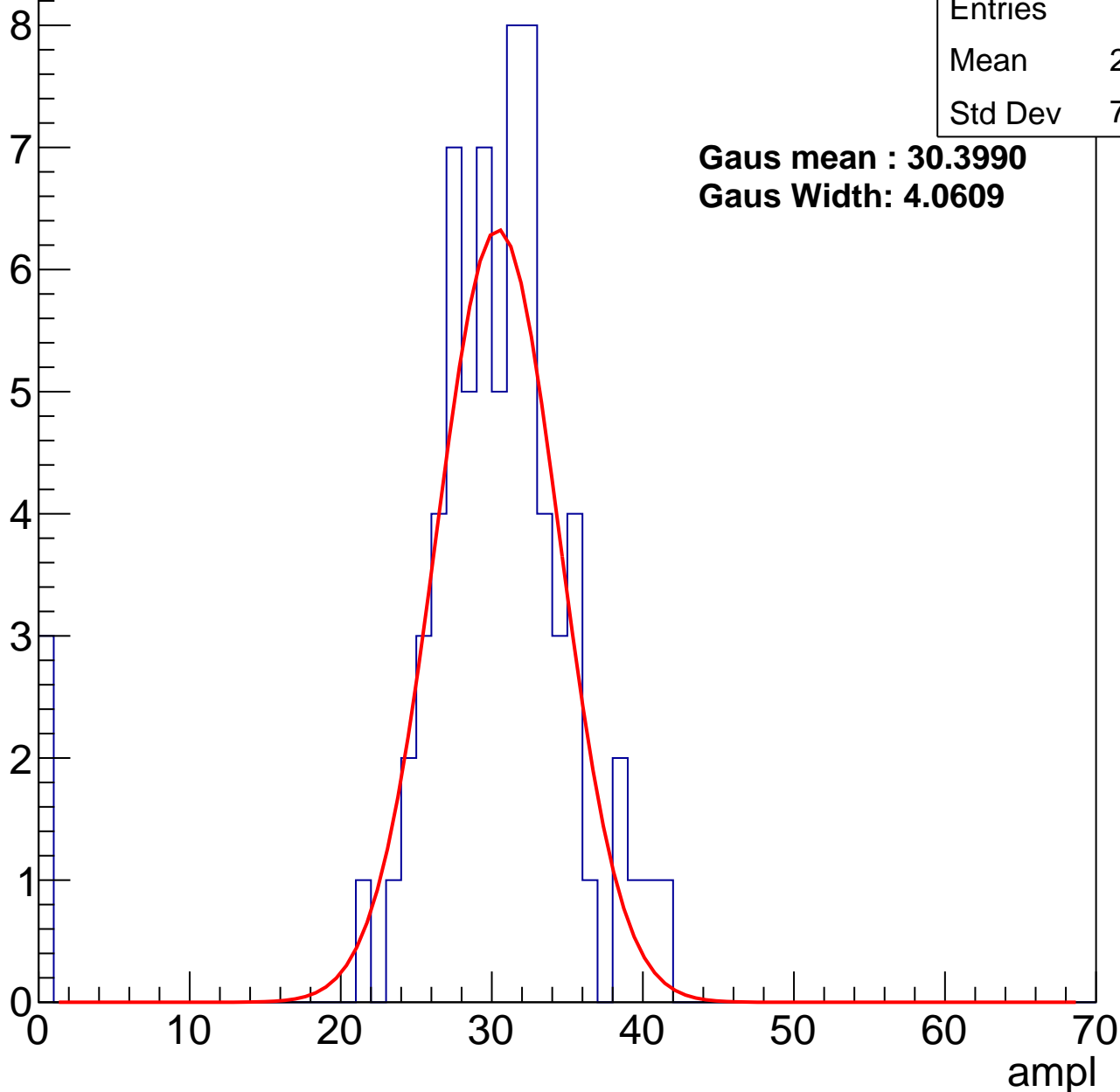
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	29.06
Std Dev	7.275

Gaus mean : 30.3990

Gaus Width: 4.0609



B1L100S, U1-ch70, adc1

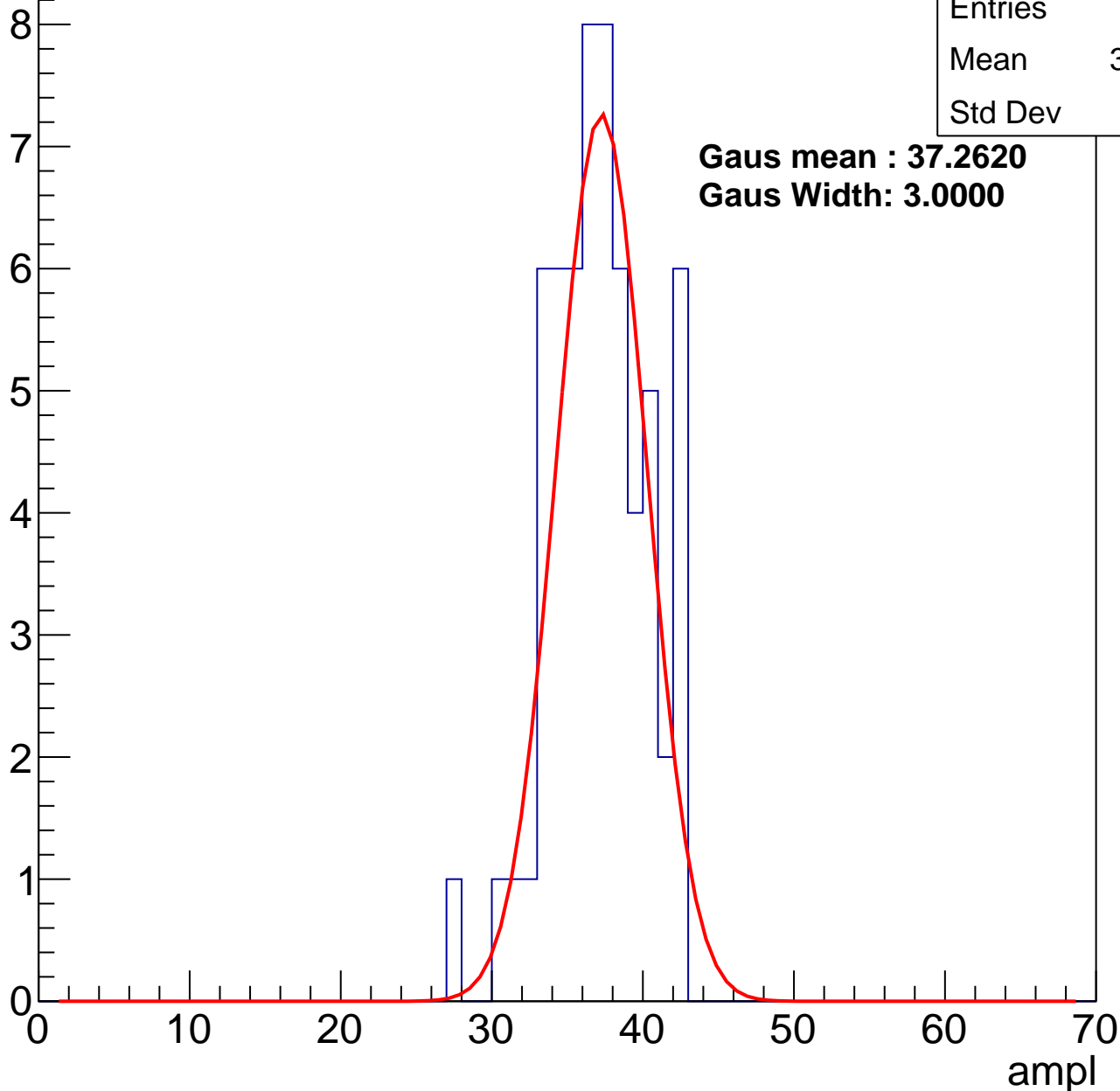
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	36.62
Std Dev	3.22

Gaus mean : 37.2620

Gaus Width: 3.0000



B1L100S, U1-ch70, adc2

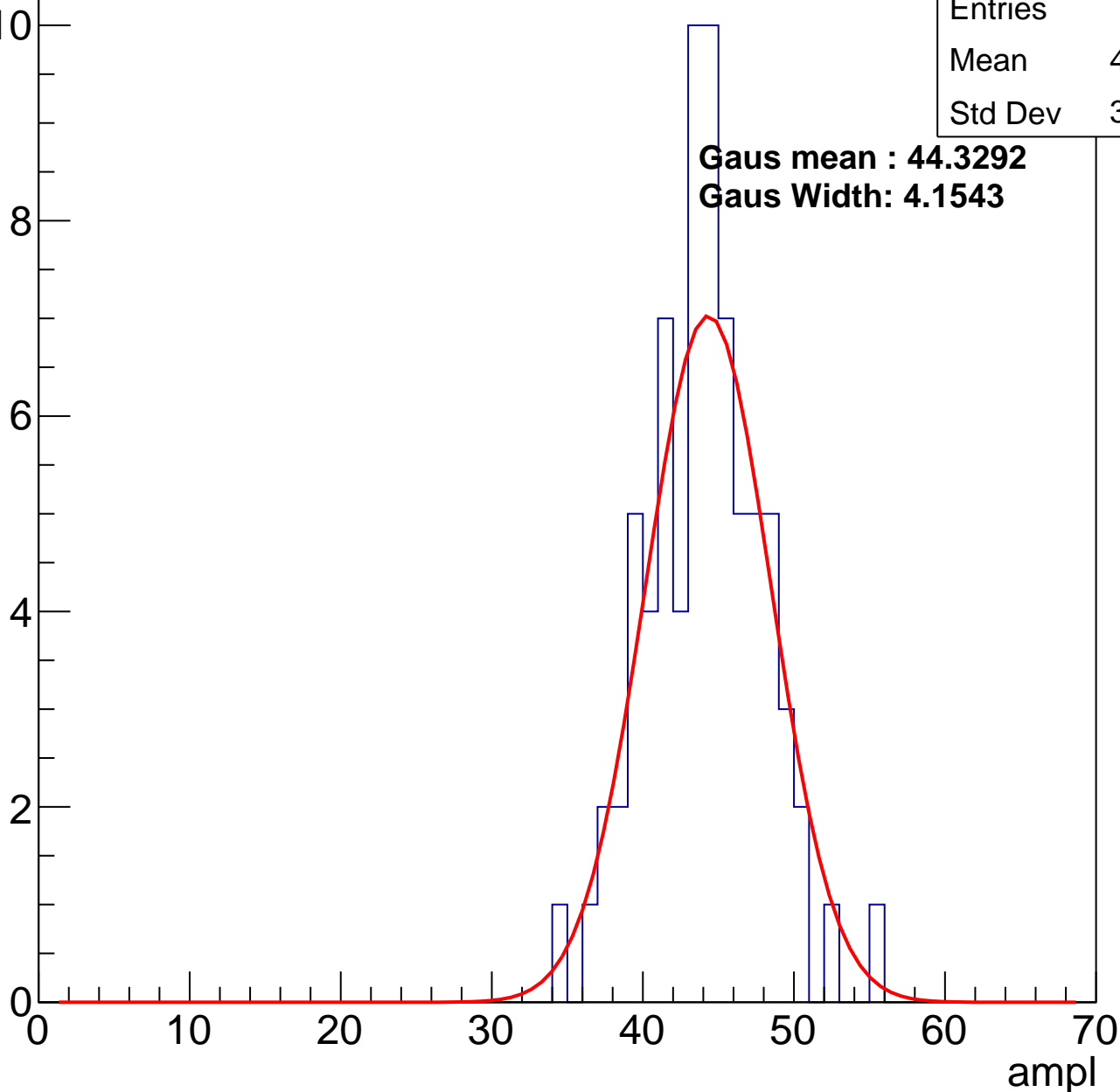
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	43.65
Std Dev	3.807

Gaus mean : 44.3292

Gaus Width: 4.1543

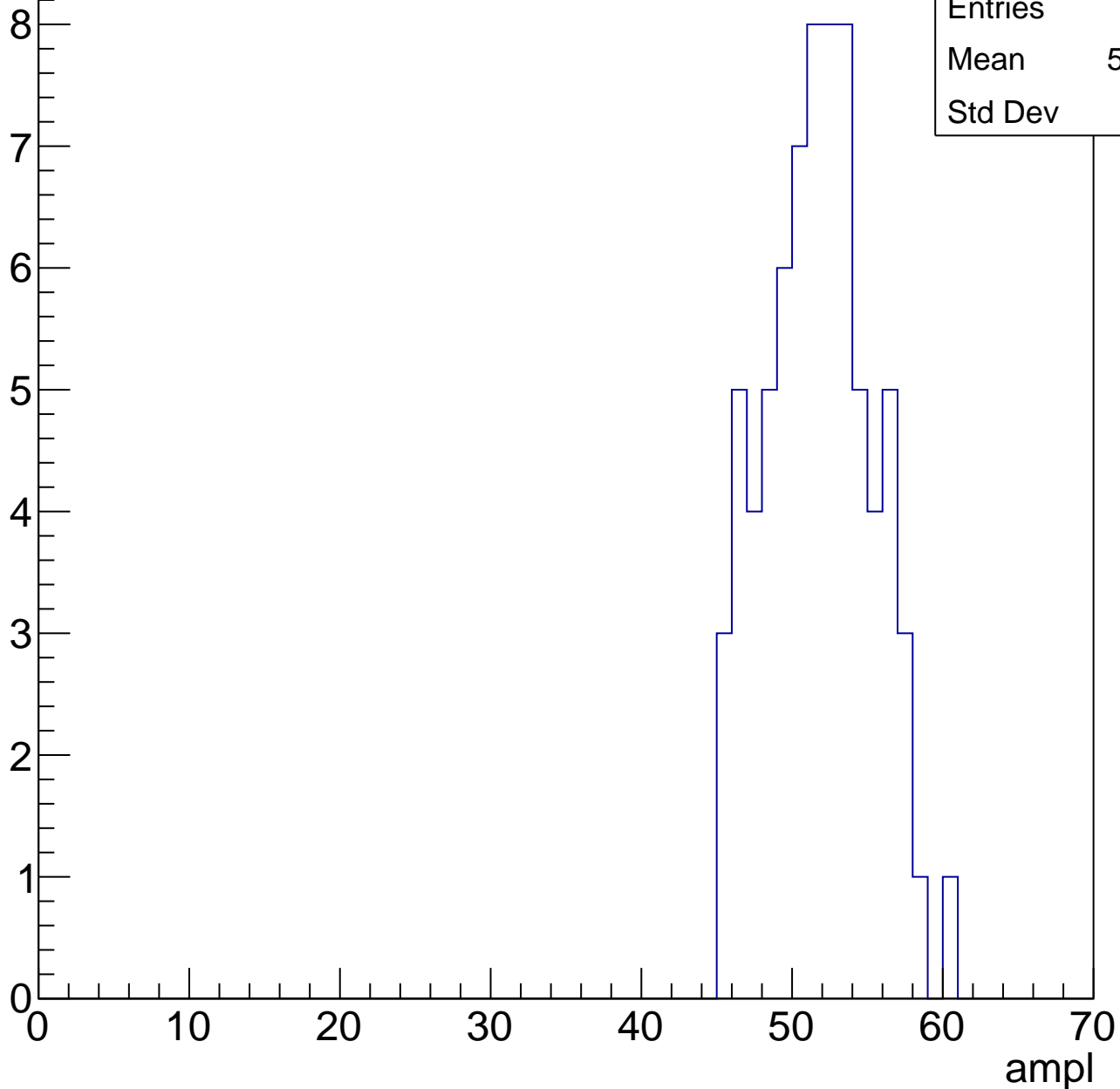


B1L100S, U1-ch70, adc3

calib_packv5_042523_0143.root, FC#4, port A2

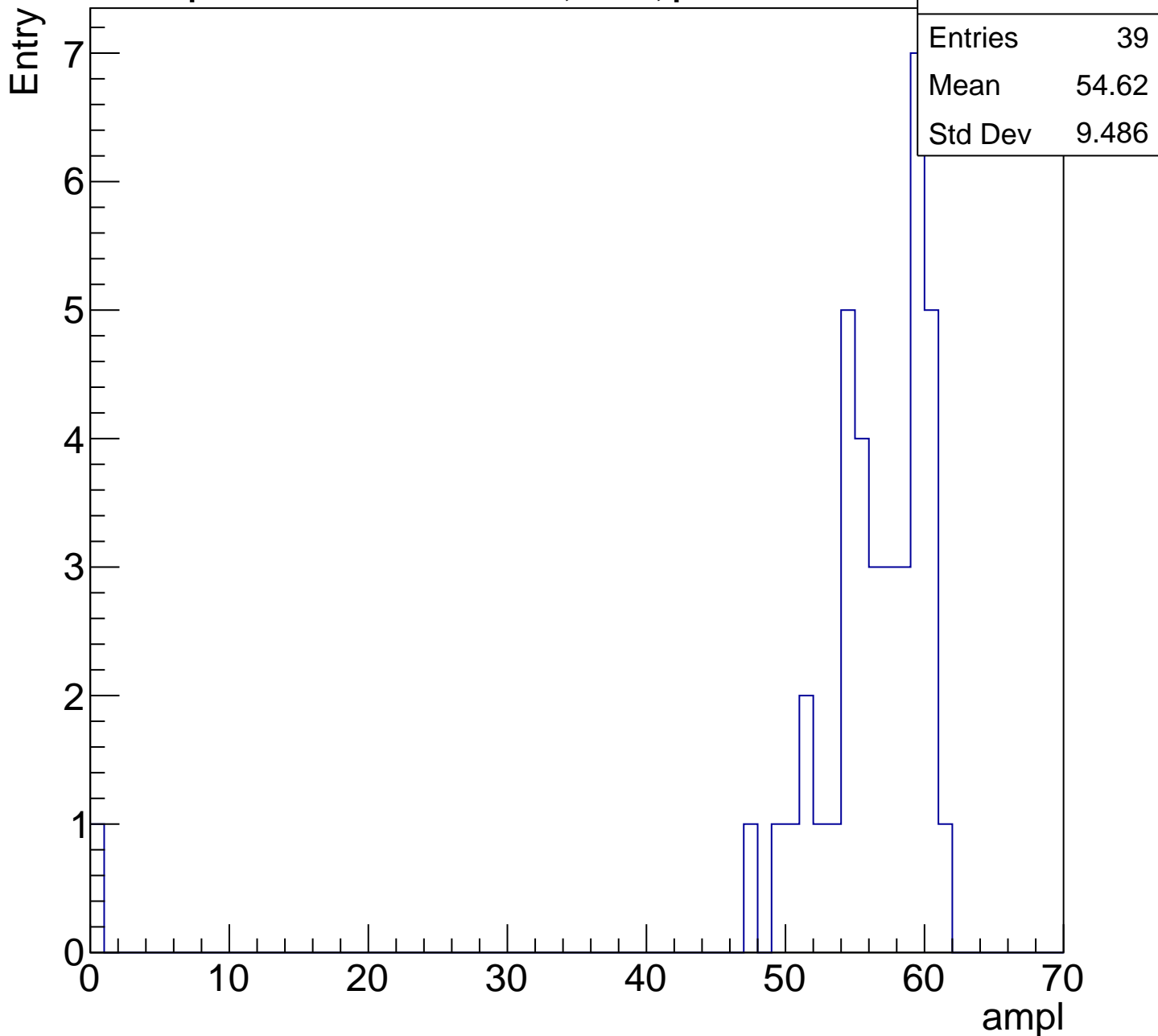
Entry

Entries	73
Mean	51.29
Std Dev	3.47



B1L100S, U1-ch70, adc4

calib_packv5_042523_0143.root, FC#4, port A2

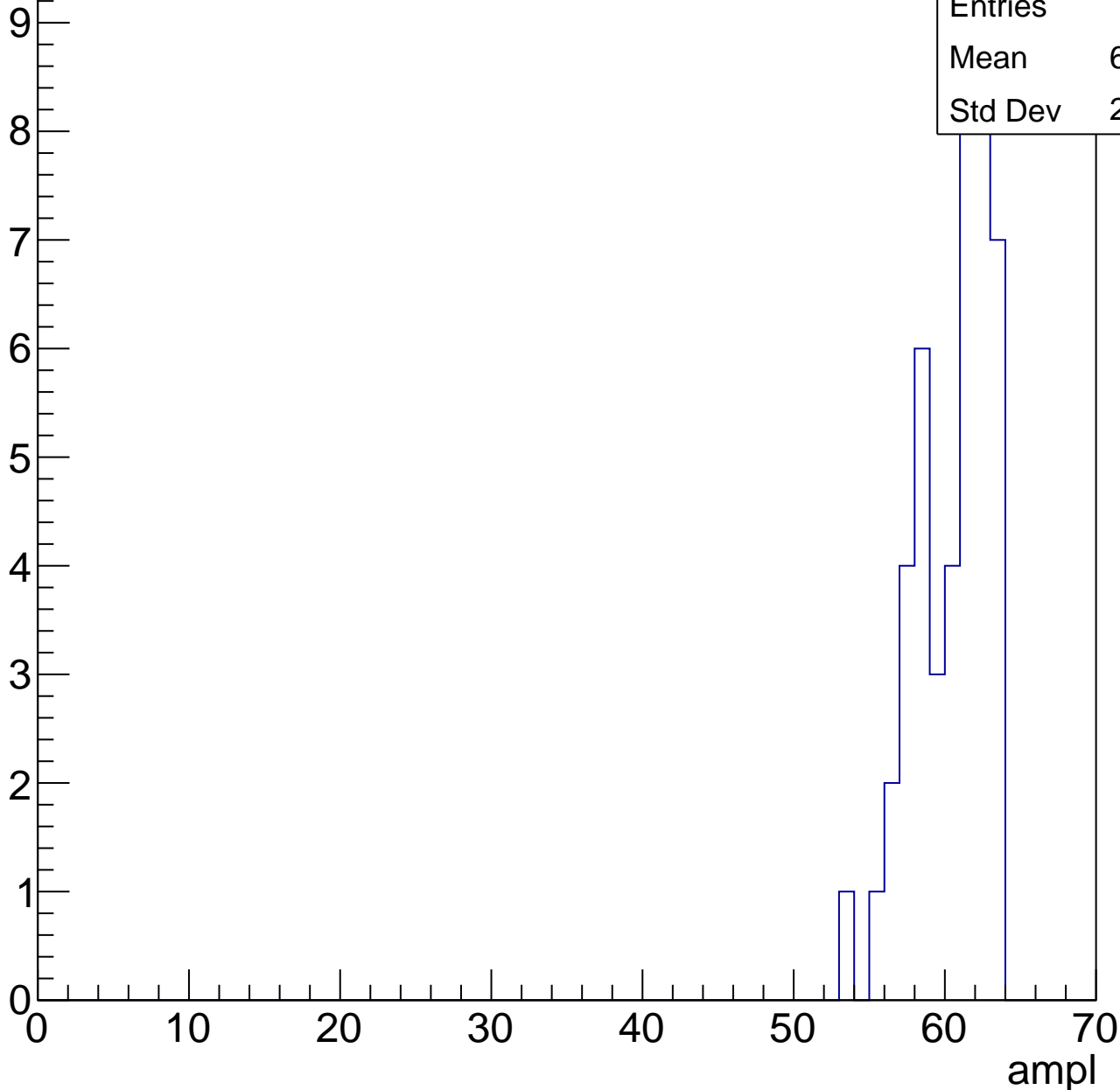


B1L100S, U1-ch70, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	60.02
Std Dev	2.454



B1L100S, U1-ch70, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch70, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch71, adc0

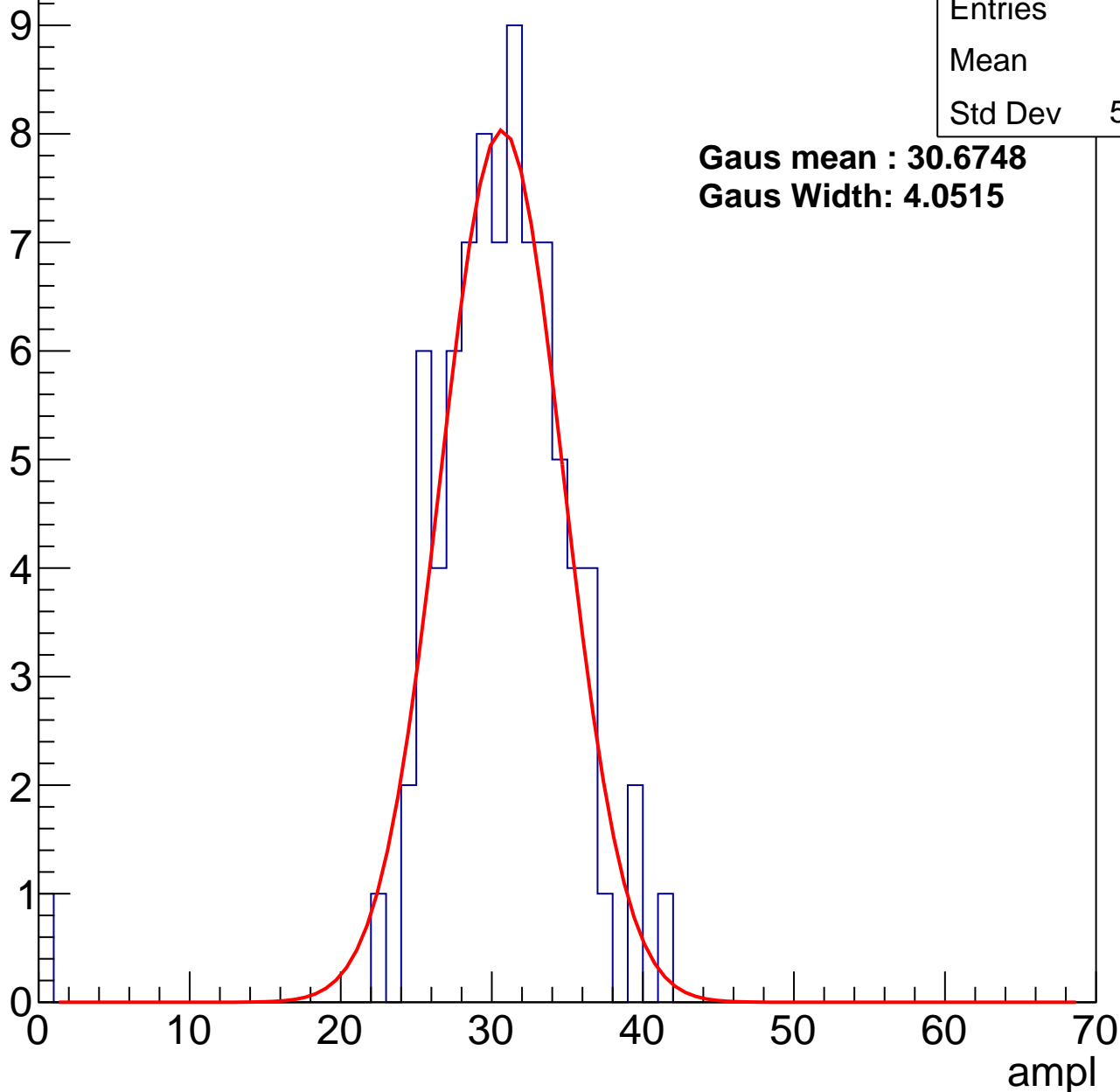
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	82
Mean	30.1
Std Dev	5.055

Gaus mean : 30.6748

Gaus Width: 4.0515



B1L100S, U1-ch71, adc1

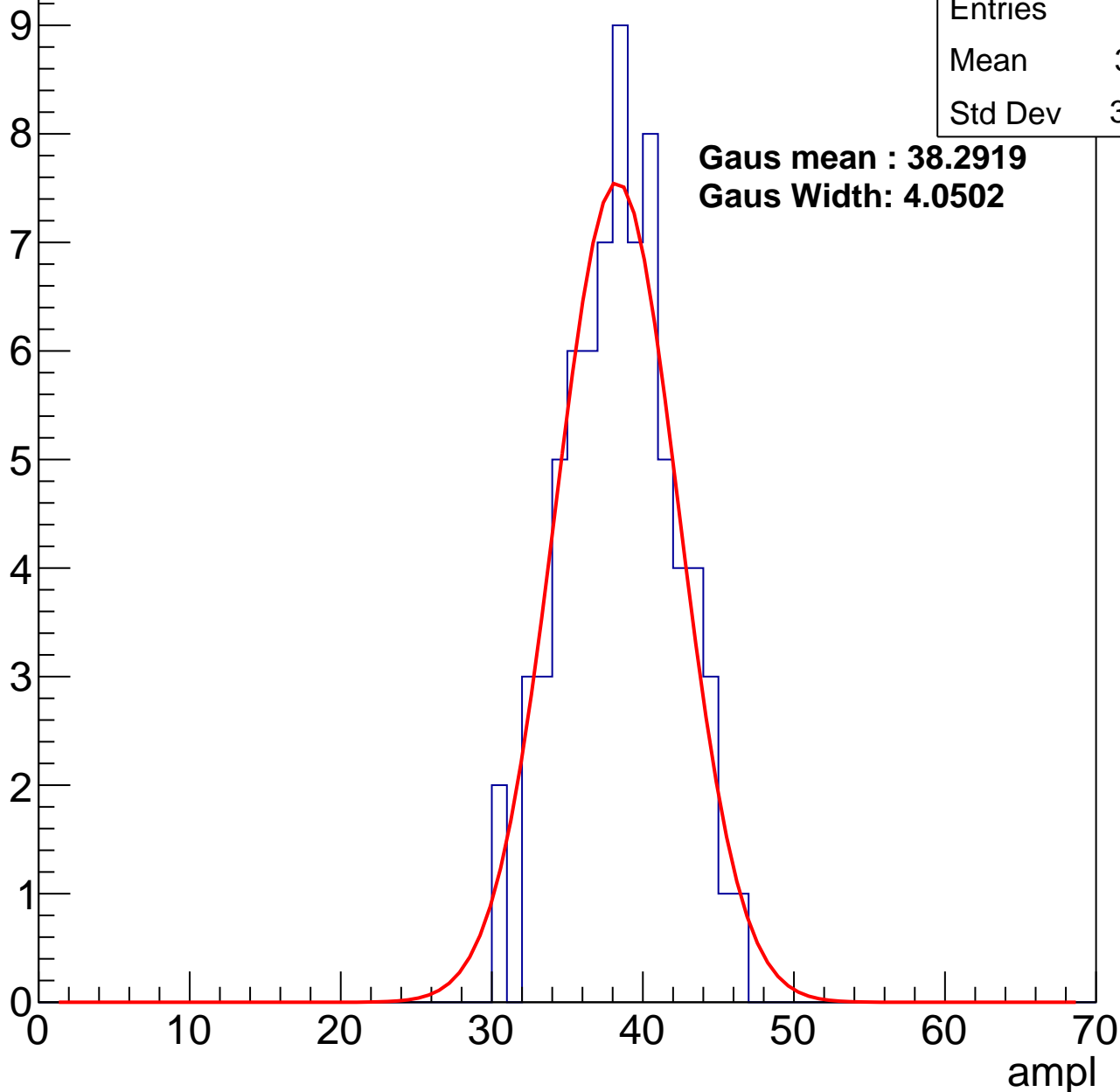
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	38.01
Std Dev	3.574

Gaus mean : 38.2919

Gaus Width: 4.0502



B1L100S, U1-ch71, adc2

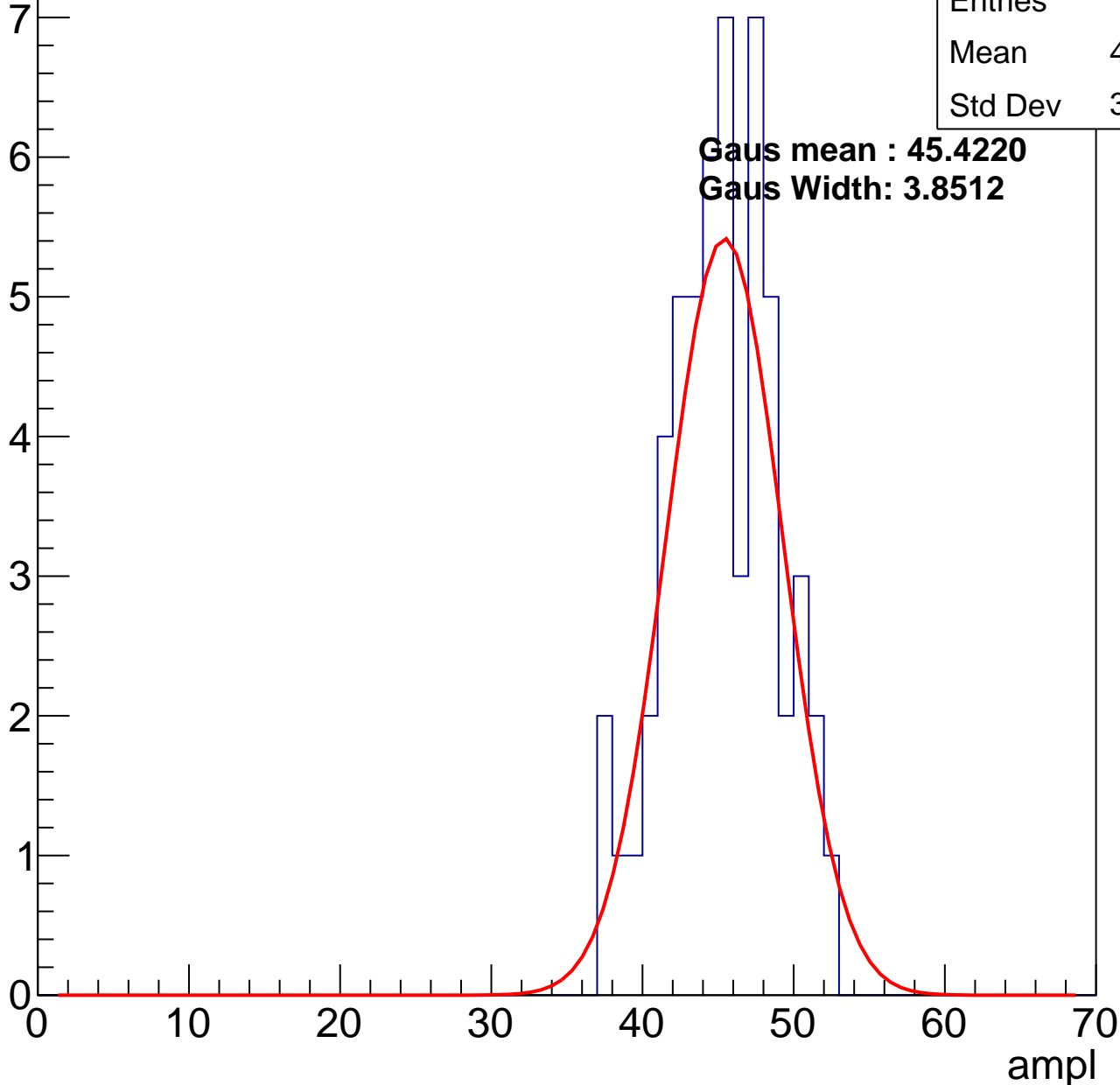
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	44.79
Std Dev	3.534

Gaus mean : 45.4220

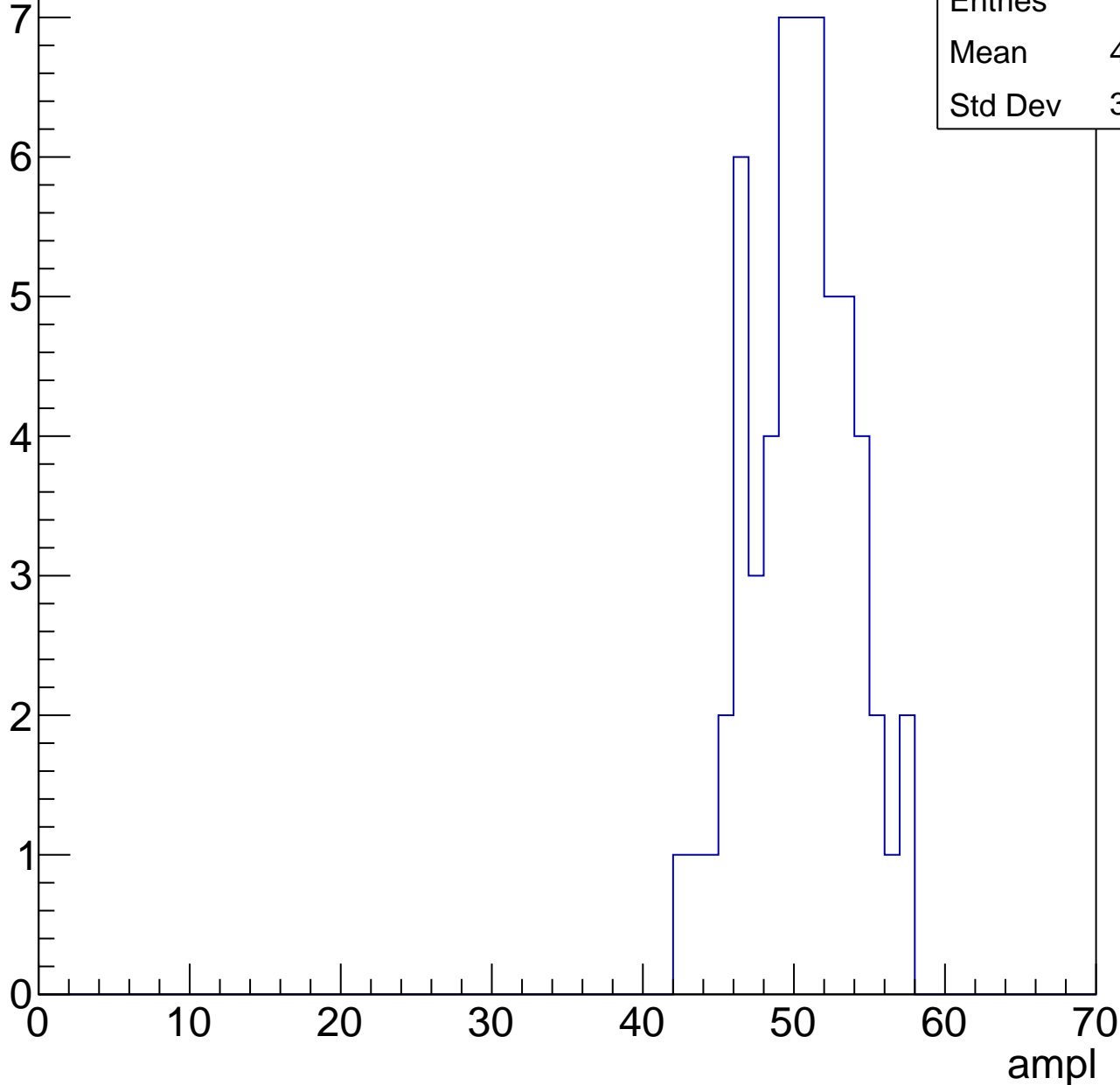
Gaus Width: 3.8512



B1L100S, U1-ch71, adc3

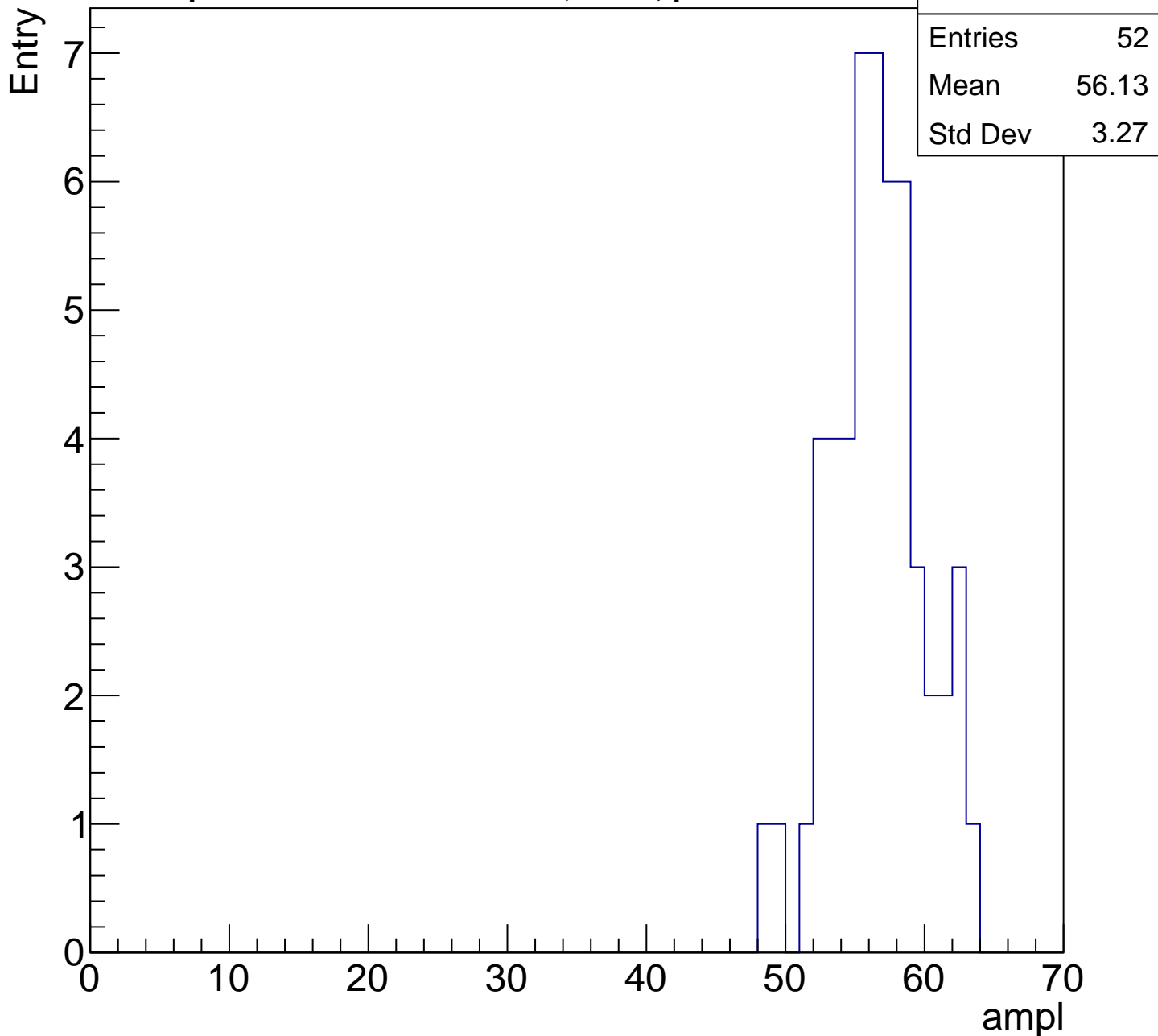
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch71, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch71, adc5

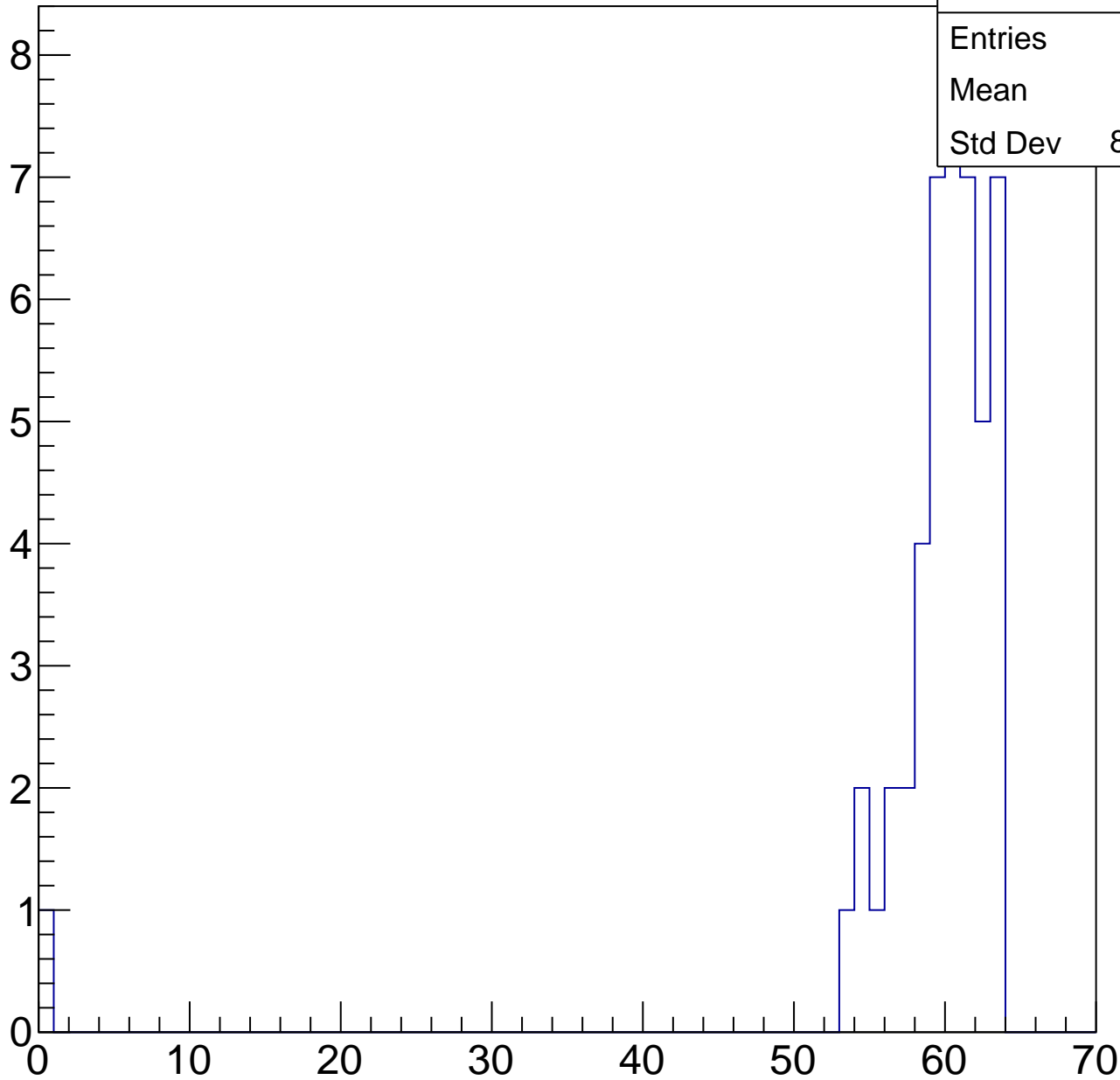
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.4
Std Dev	8.979

ampl



B1L100S, U1-ch71, adc6

calib_packv5_042523_0143.root, FC#4, port A2

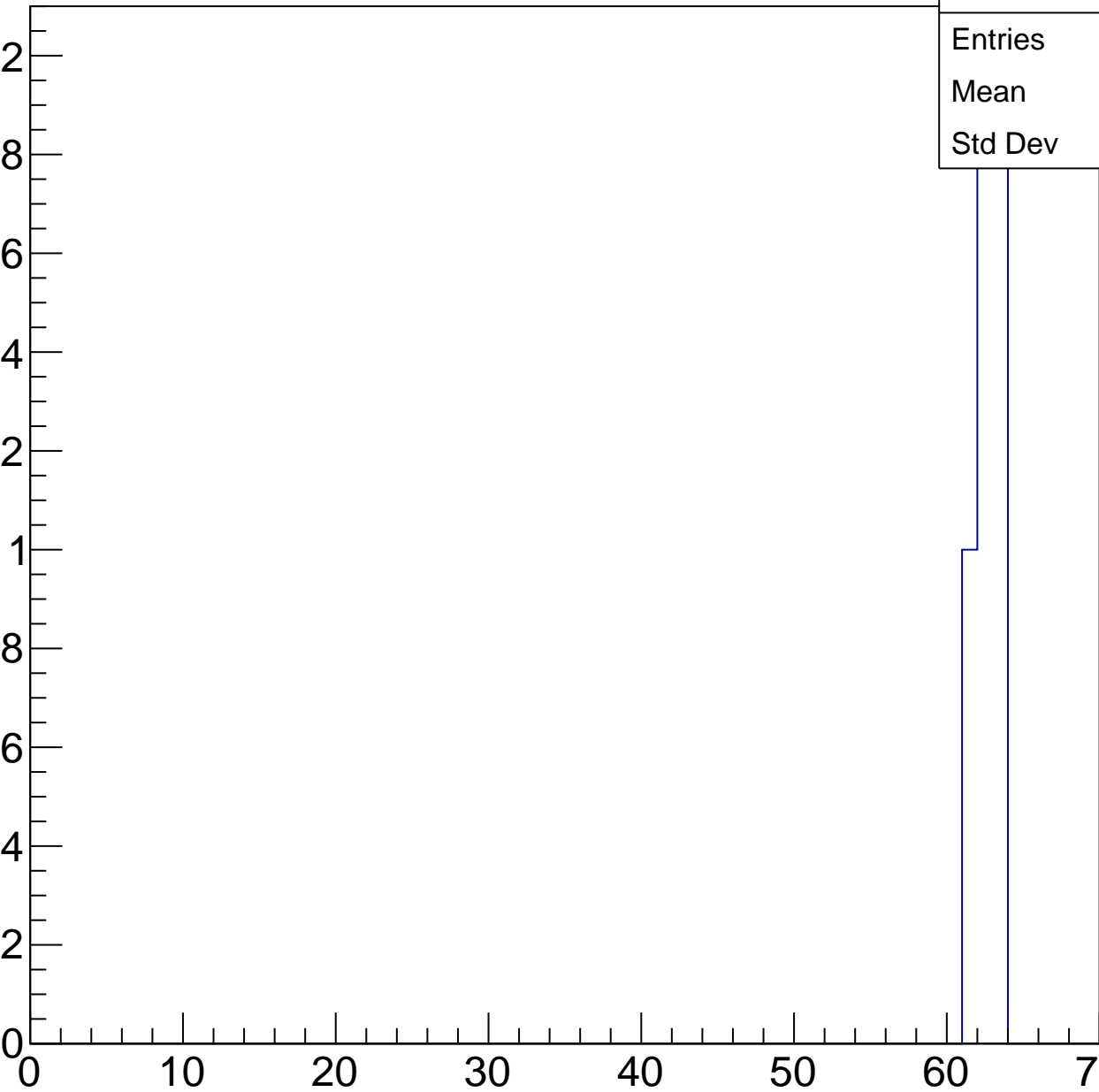
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	62.2
Std Dev	0.7483

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch71, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch72, adc0

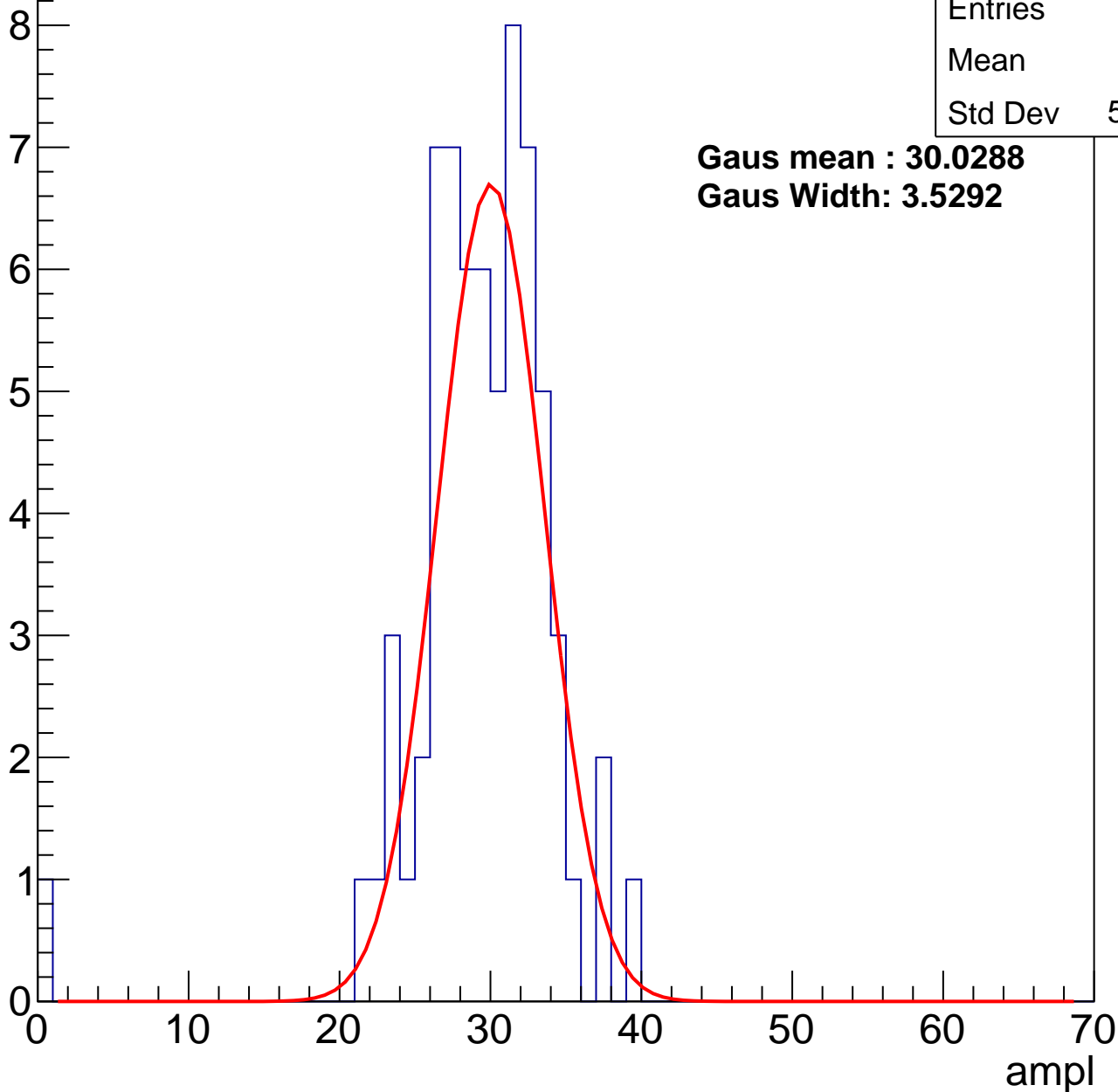
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.9
Std Dev	5.088

Gaus mean : 30.0288

Gaus Width: 3.5292



B1L100S, U1-ch72, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	35.8
Std Dev	3.543

Gaus mean : 36.2823

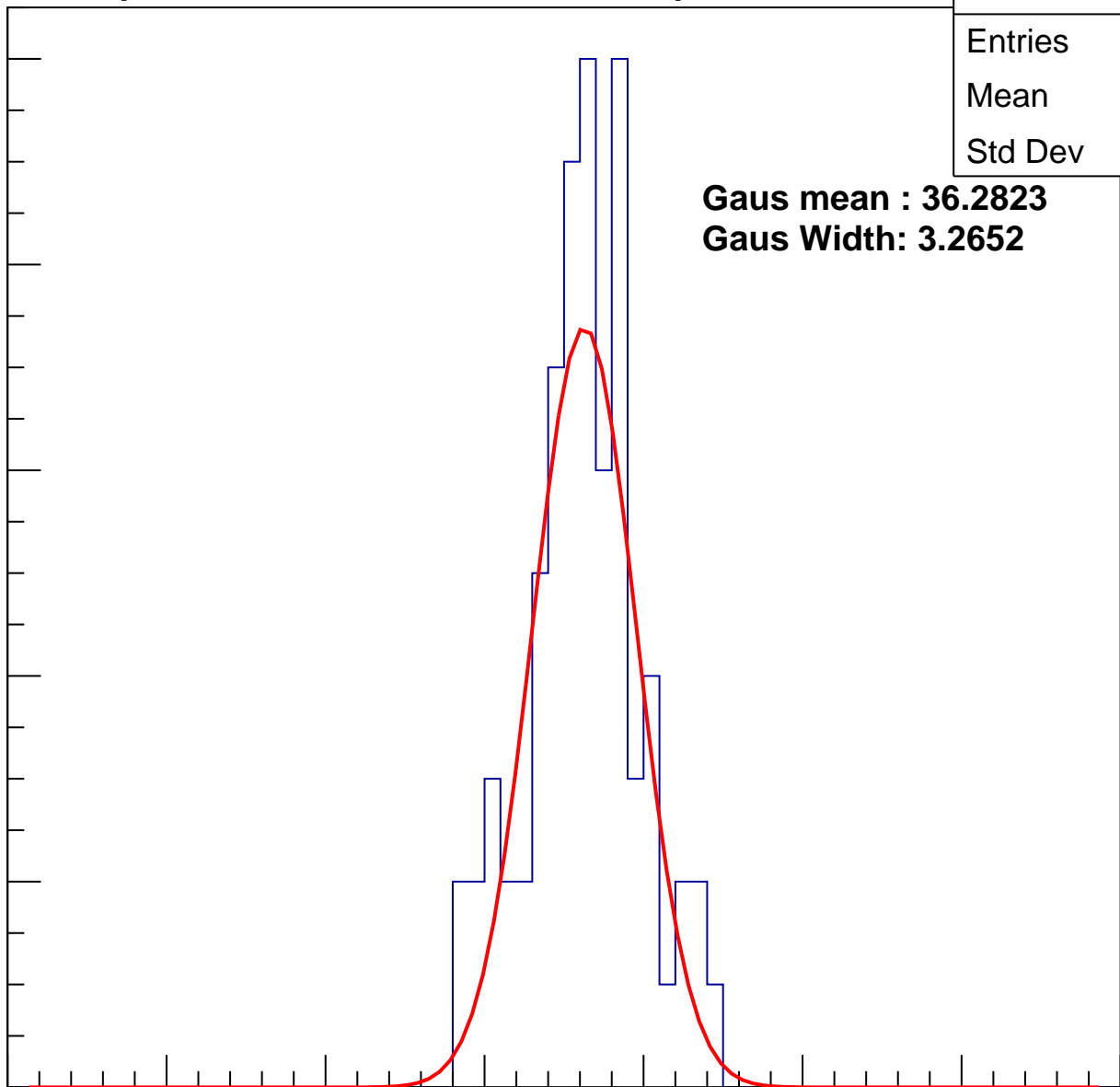
Gaus Width: 3.2652

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch72, adc2

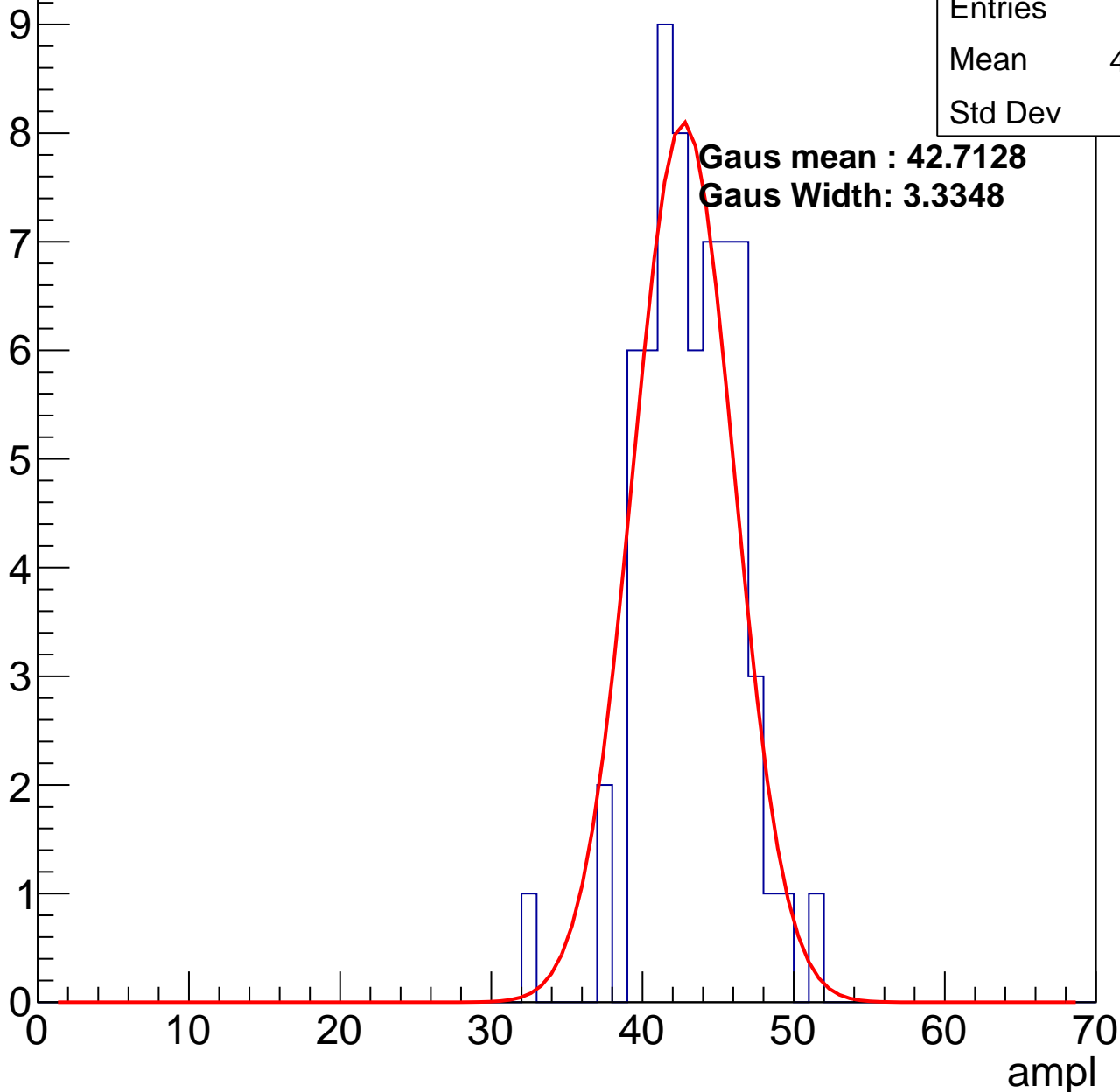
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	42.72
Std Dev	3.17

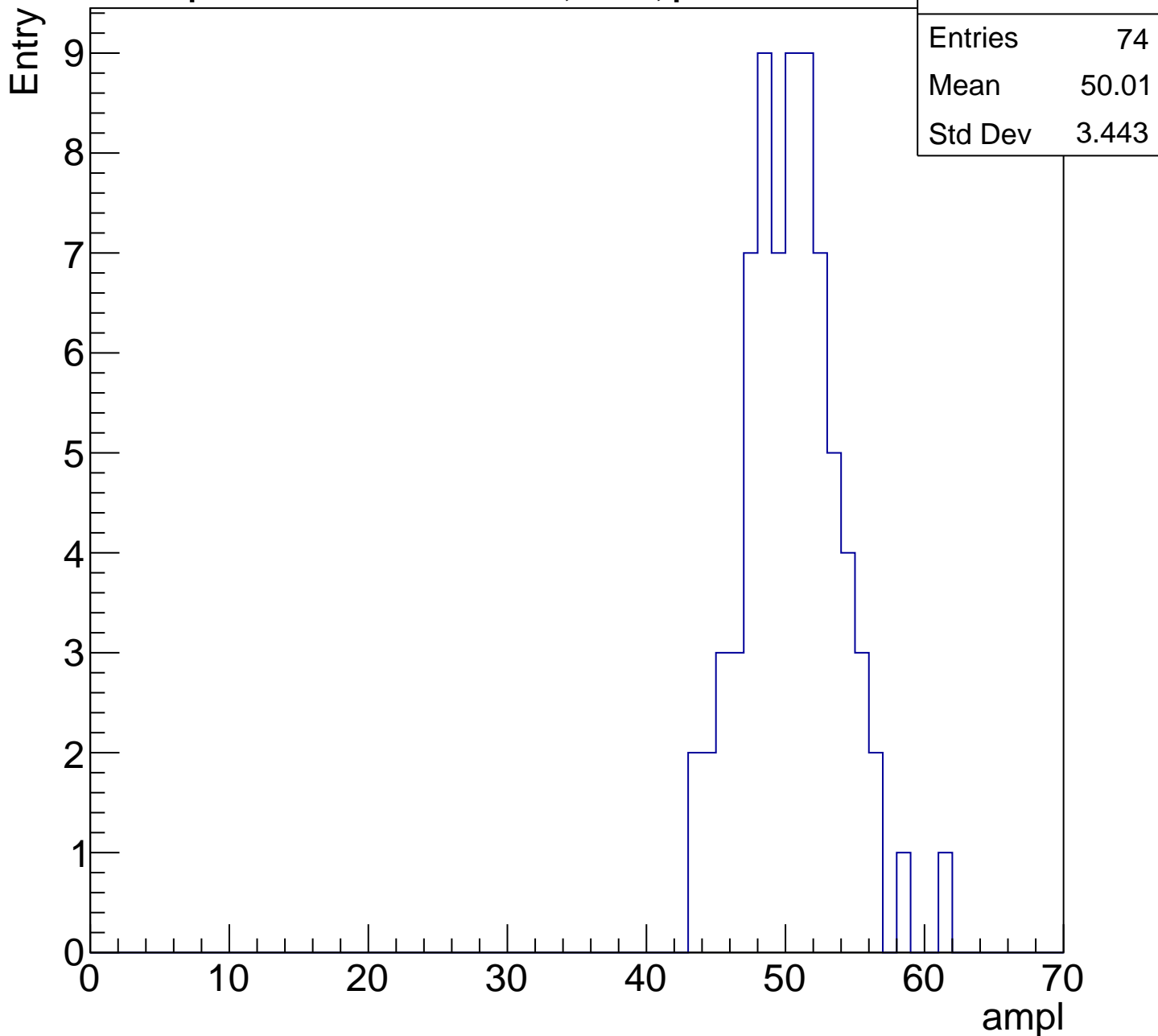
Gaus mean : 42.7128

Gaus Width: 3.3348



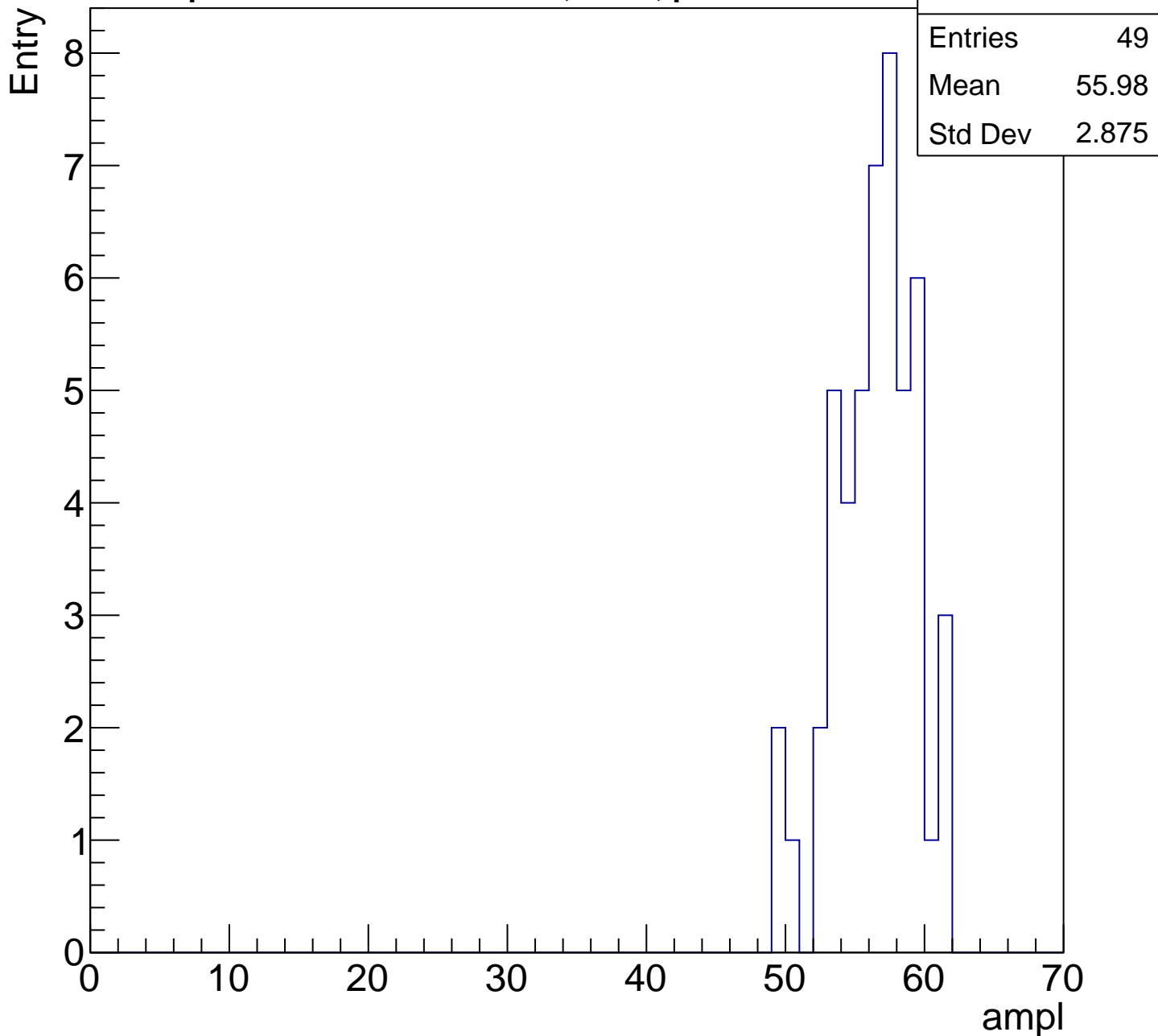
B1L100S, U1-ch72, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch72, adc4

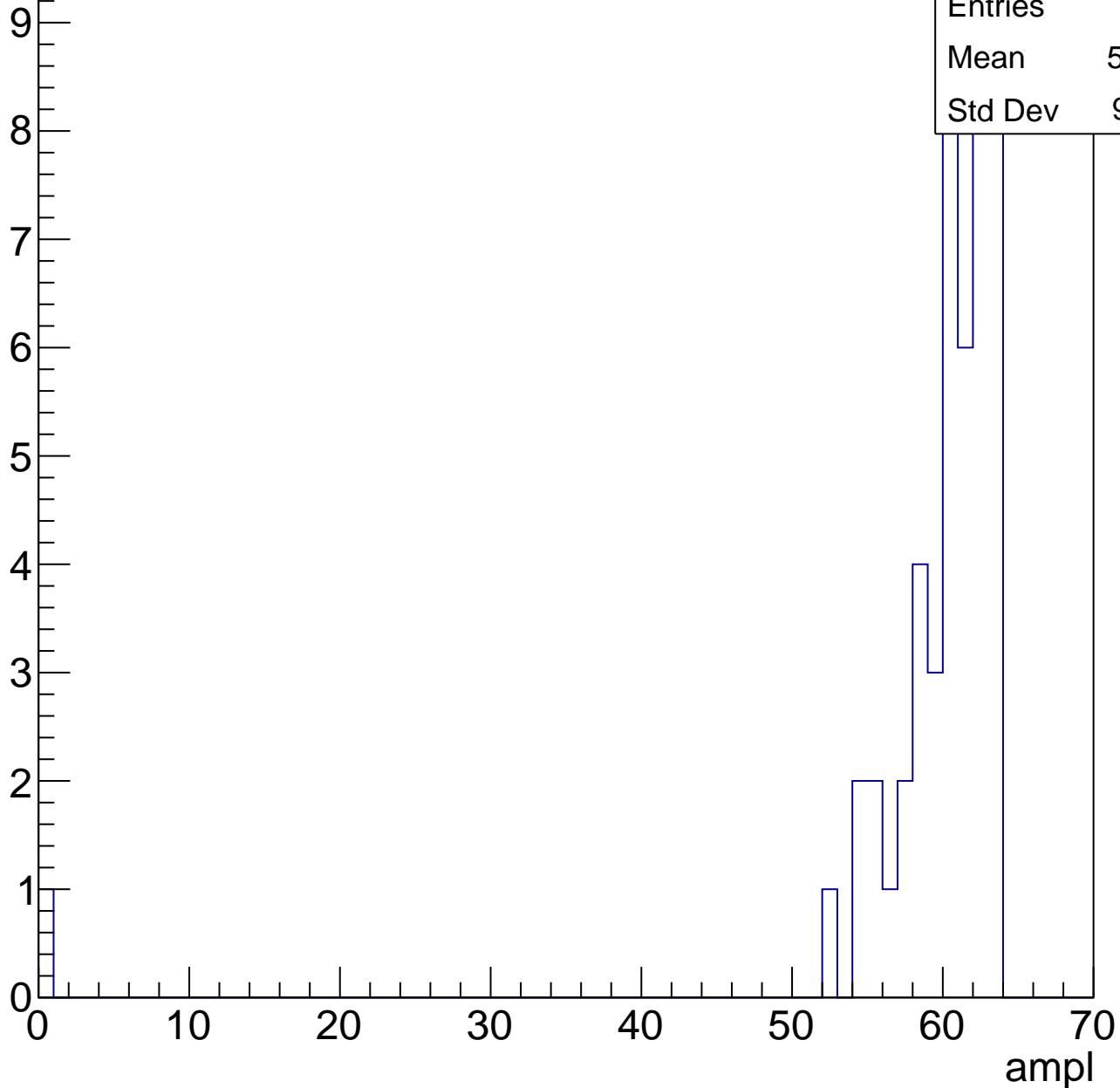
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch72, adc5

calib_packv5_042523_0143.root, FC#4, port A2

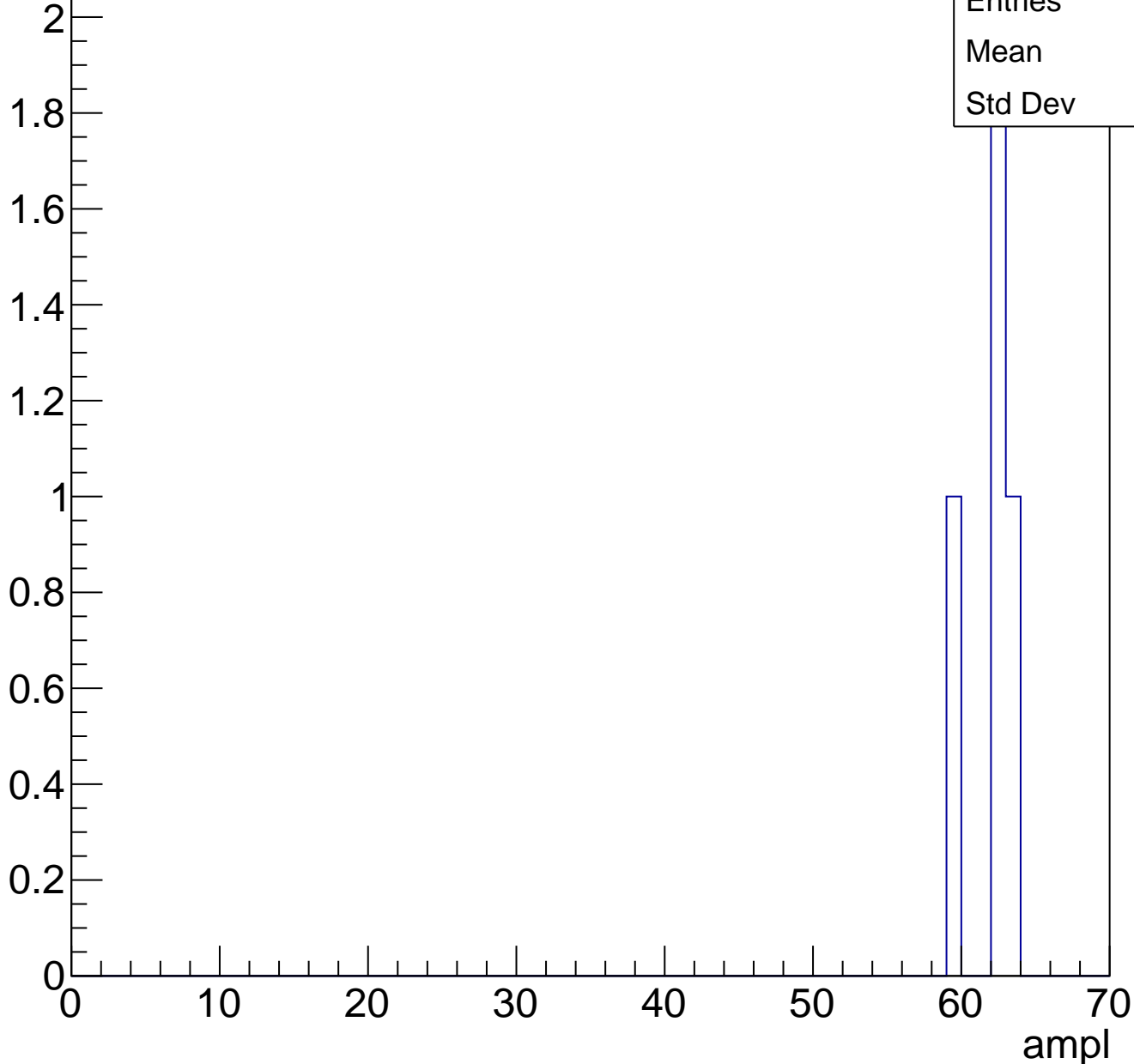
Entry



B1L100S, U1-ch72, adc6

calib_packv5_042523_0143.root, FC#4, port A2

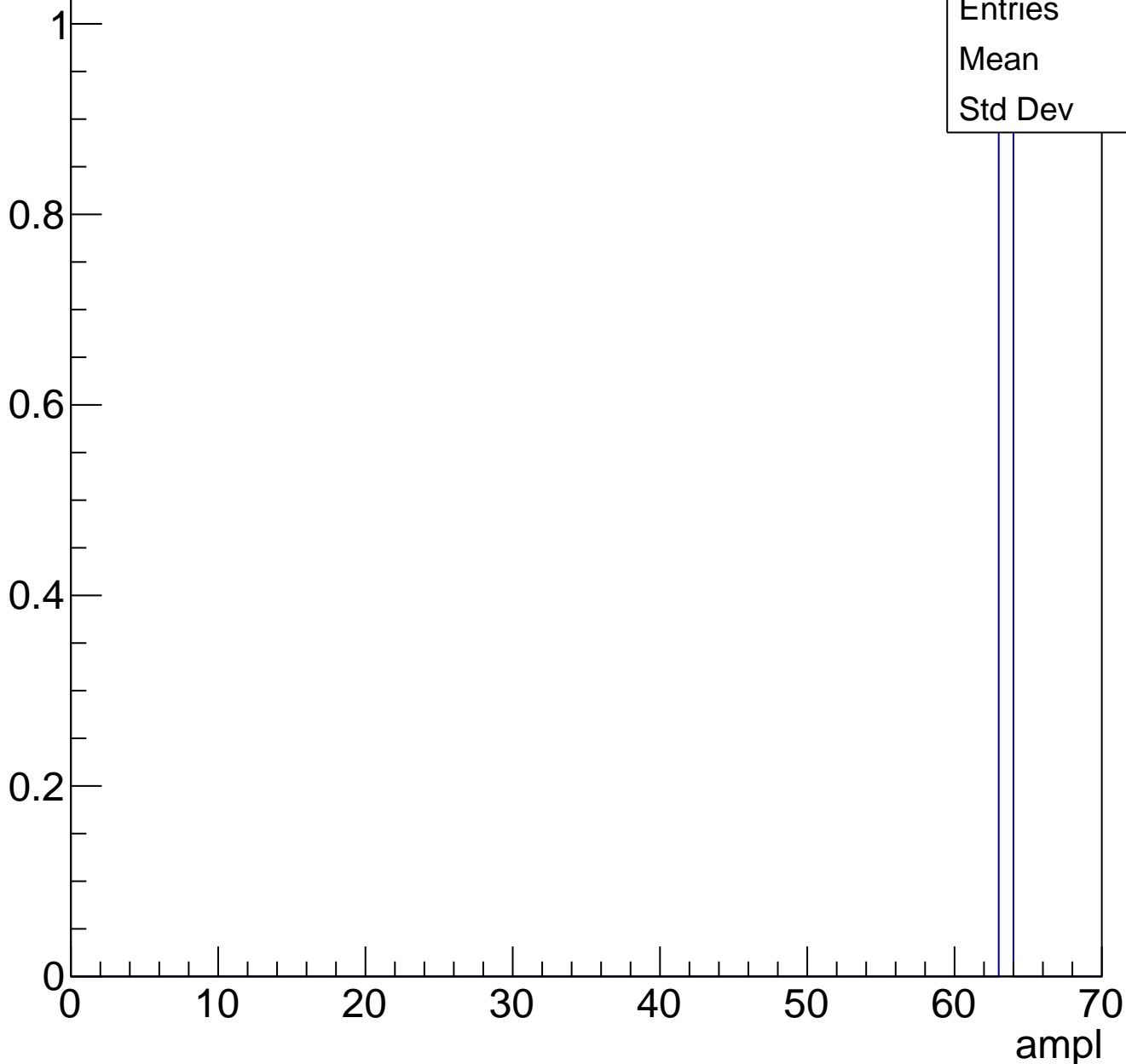
Entry



B1L100S, U1-ch72, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch73, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	92
Mean	28.42
Std Dev	4.877

Gaus mean : 29.2931

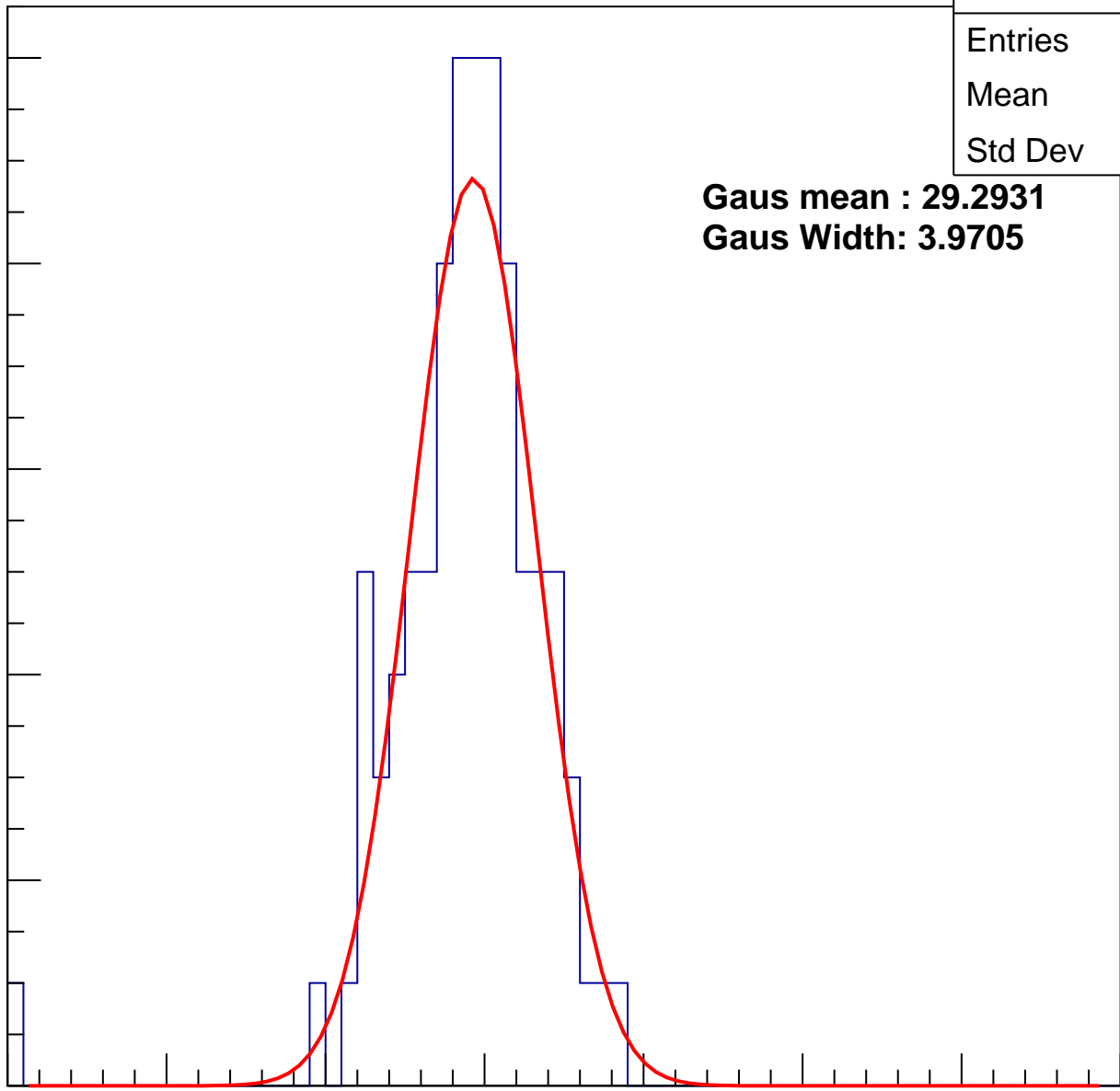
Gaus Width: 3.9705

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch73, adc1

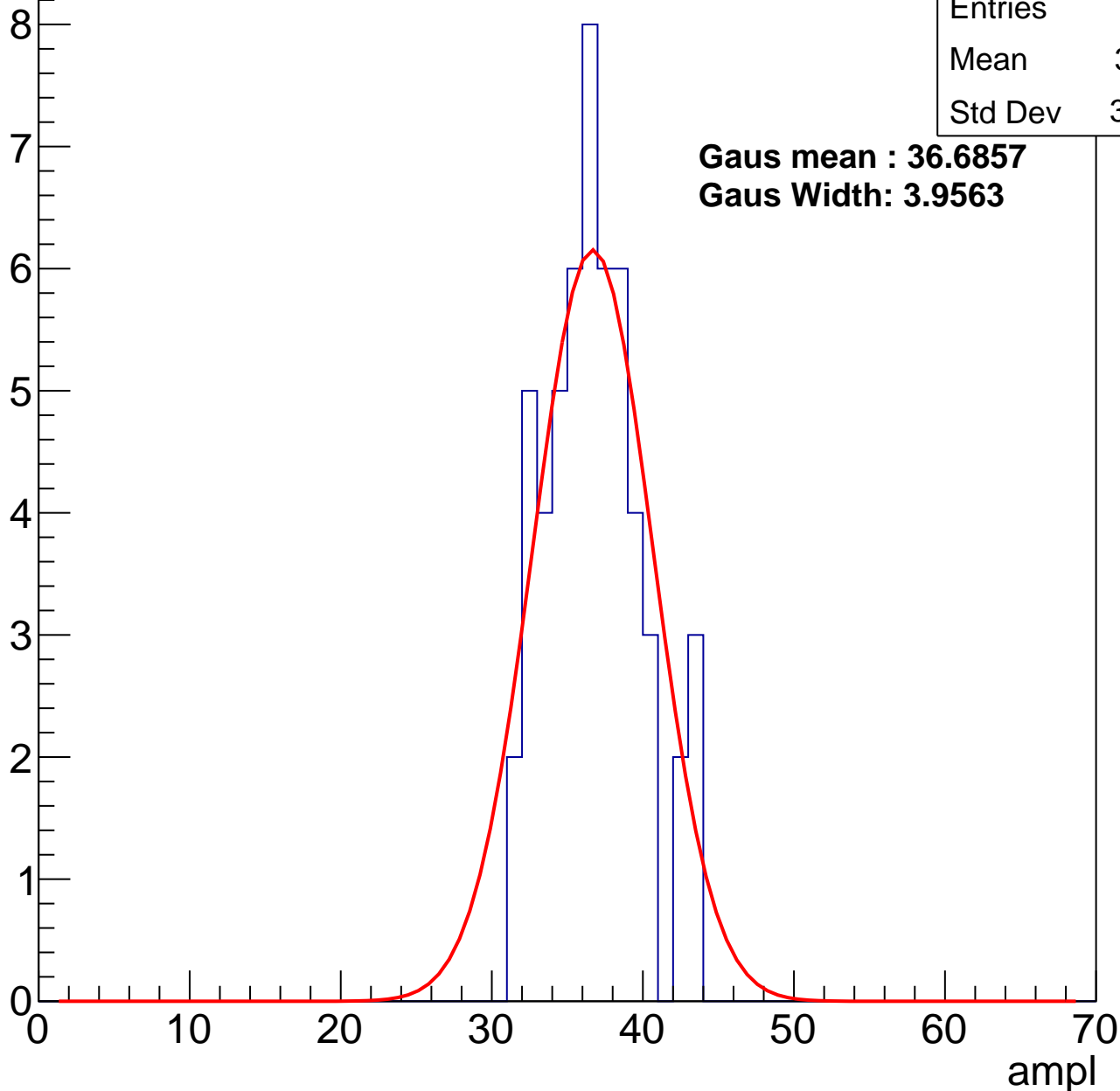
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	36.31
Std Dev	3.102

Gaus mean : 36.6857

Gaus Width: 3.9563



B1L100S, U1-ch73, adc2

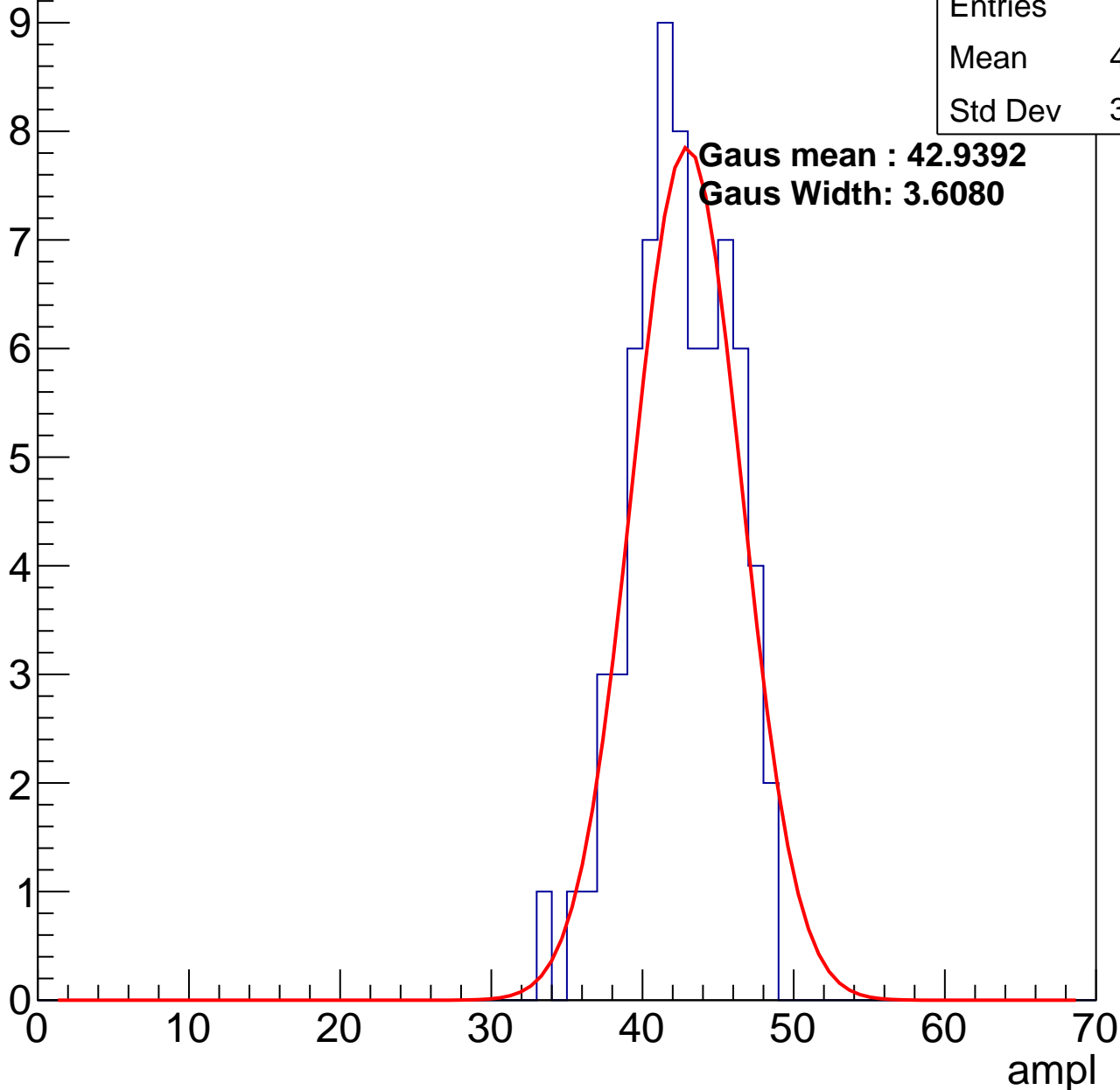
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.07
Std Dev	3.253

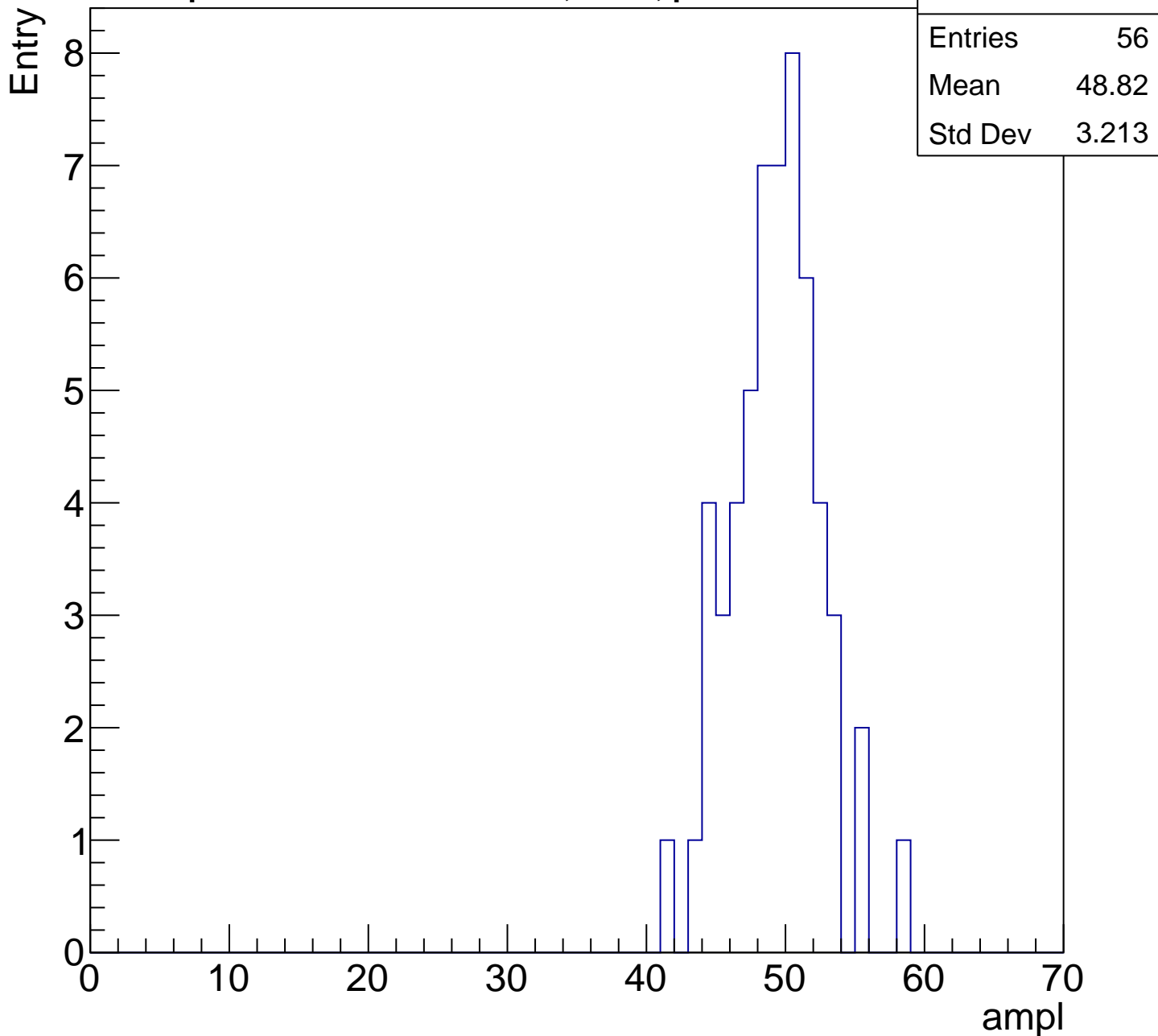
Gaus mean : 42.9392

Gaus Width: 3.6080



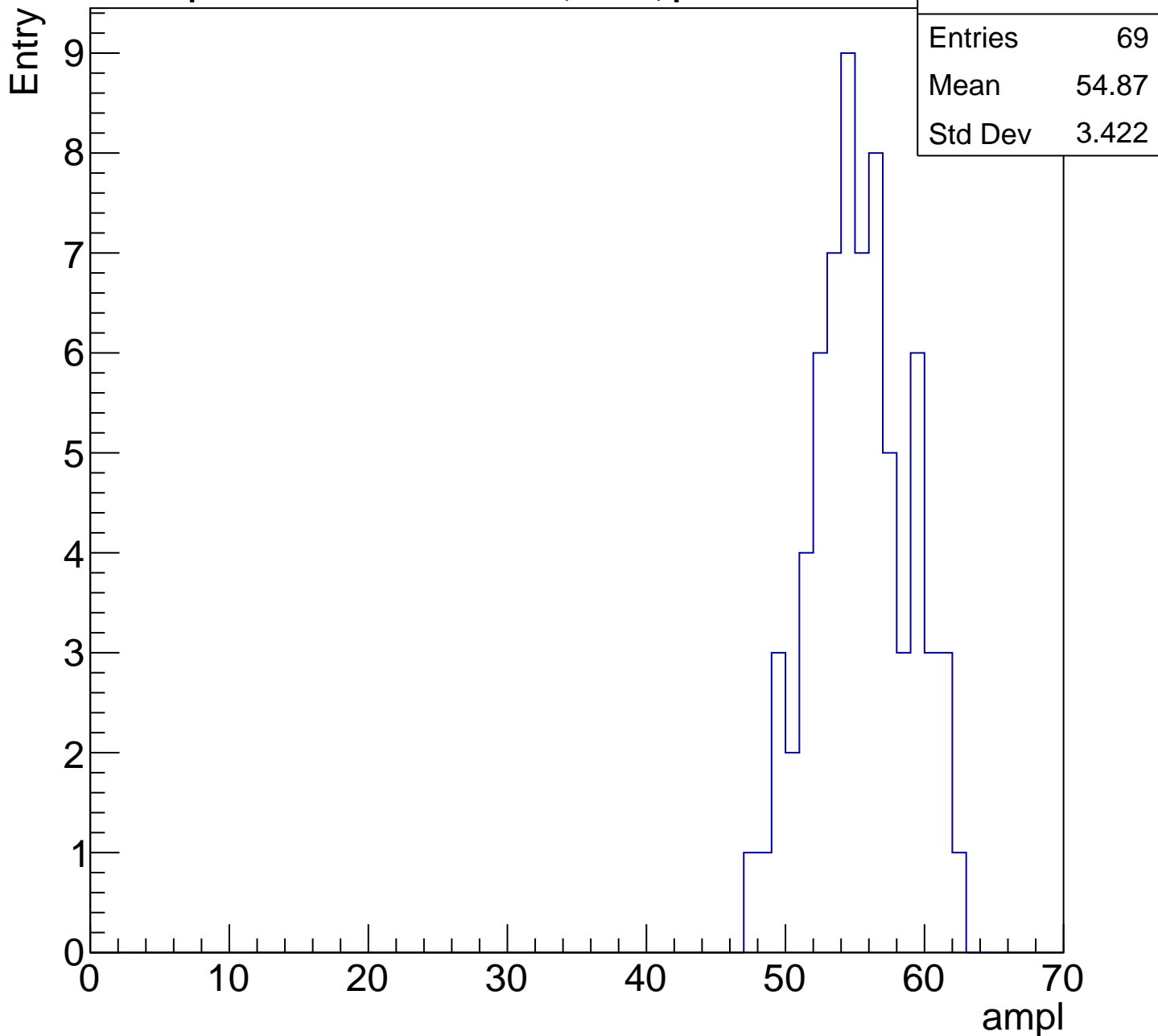
B1L100S, U1-ch73, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch73, adc4

calib_packv5_042523_0143.root, FC#4, port A2

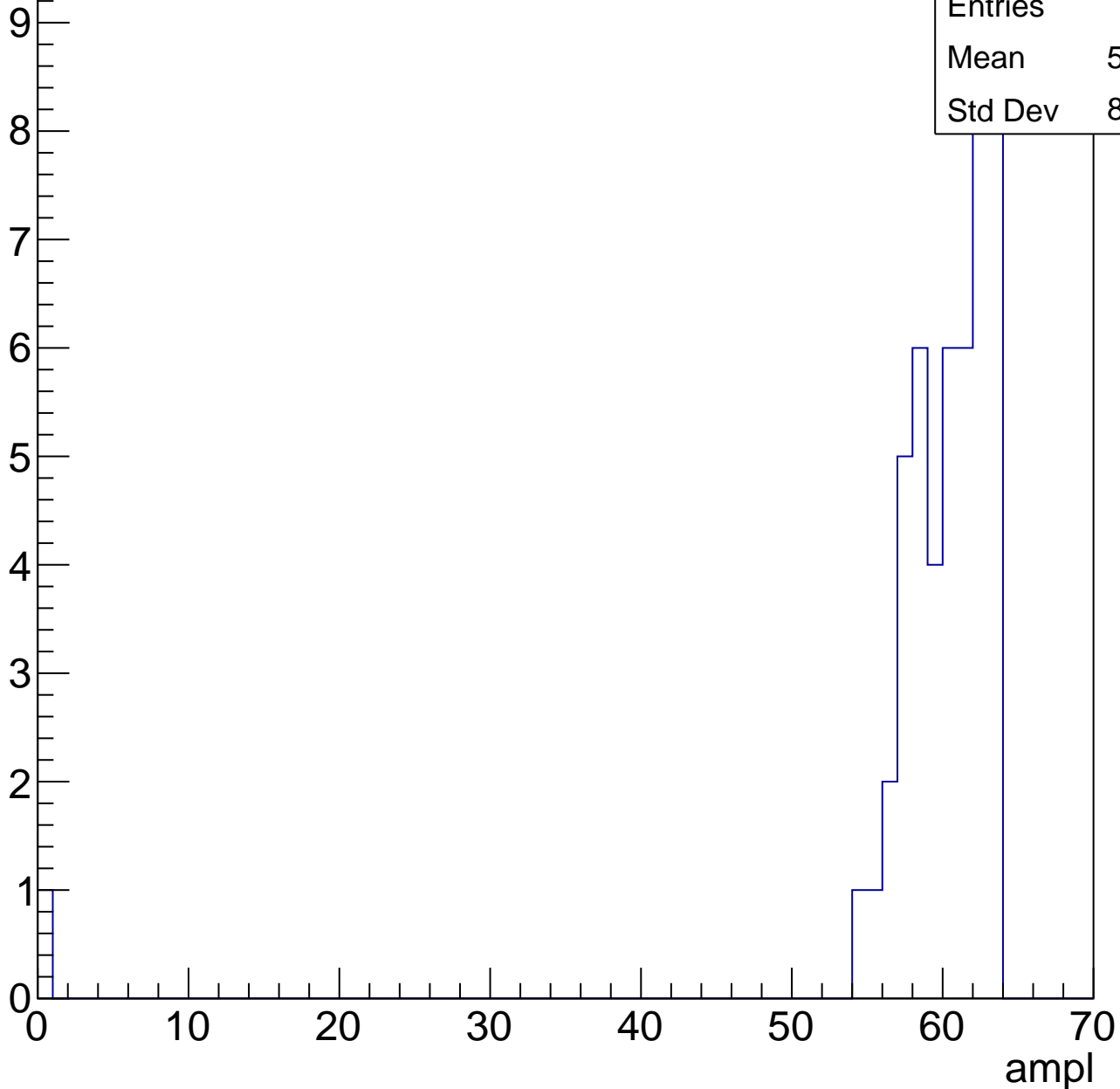


B1L100S, U1-ch73, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

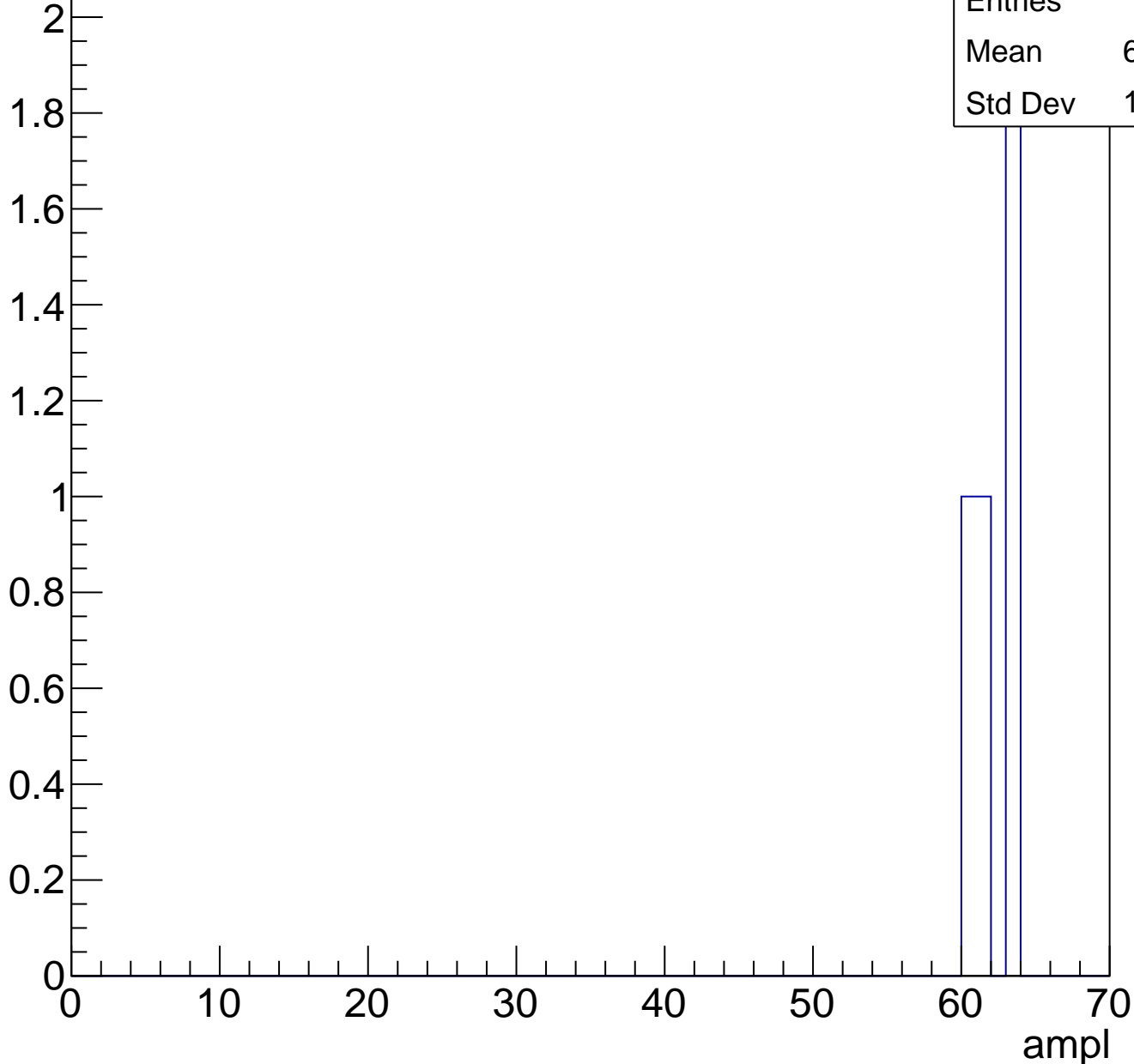
Entries	49
Mean	58.73
Std Dev	8.808



B1L100S, U1-ch73, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch73, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch74, adc0

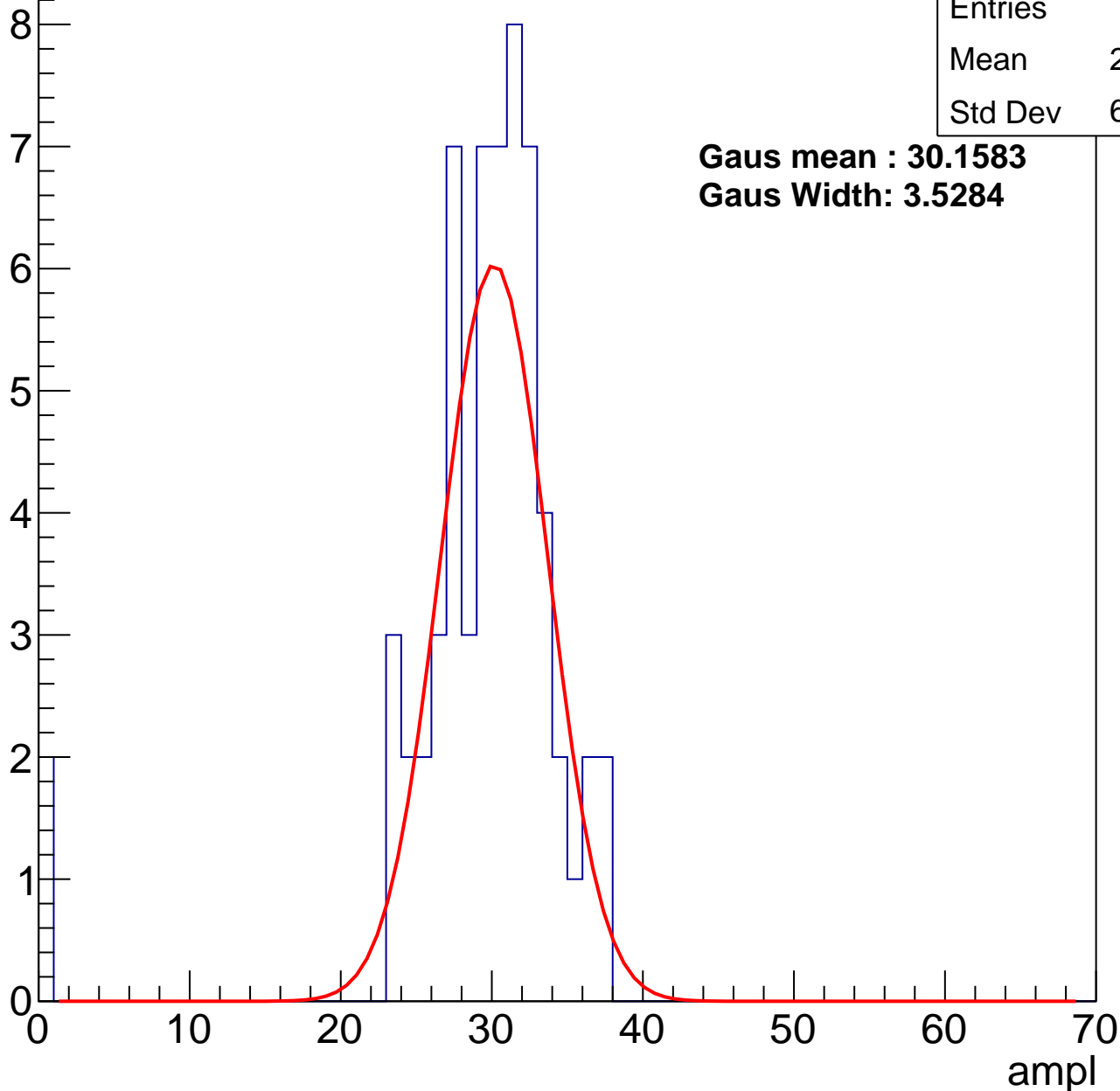
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.77
Std Dev	6.228

Gaus mean : 30.1583

Gaus Width: 3.5284



B1L100S, U1-ch74, adc1

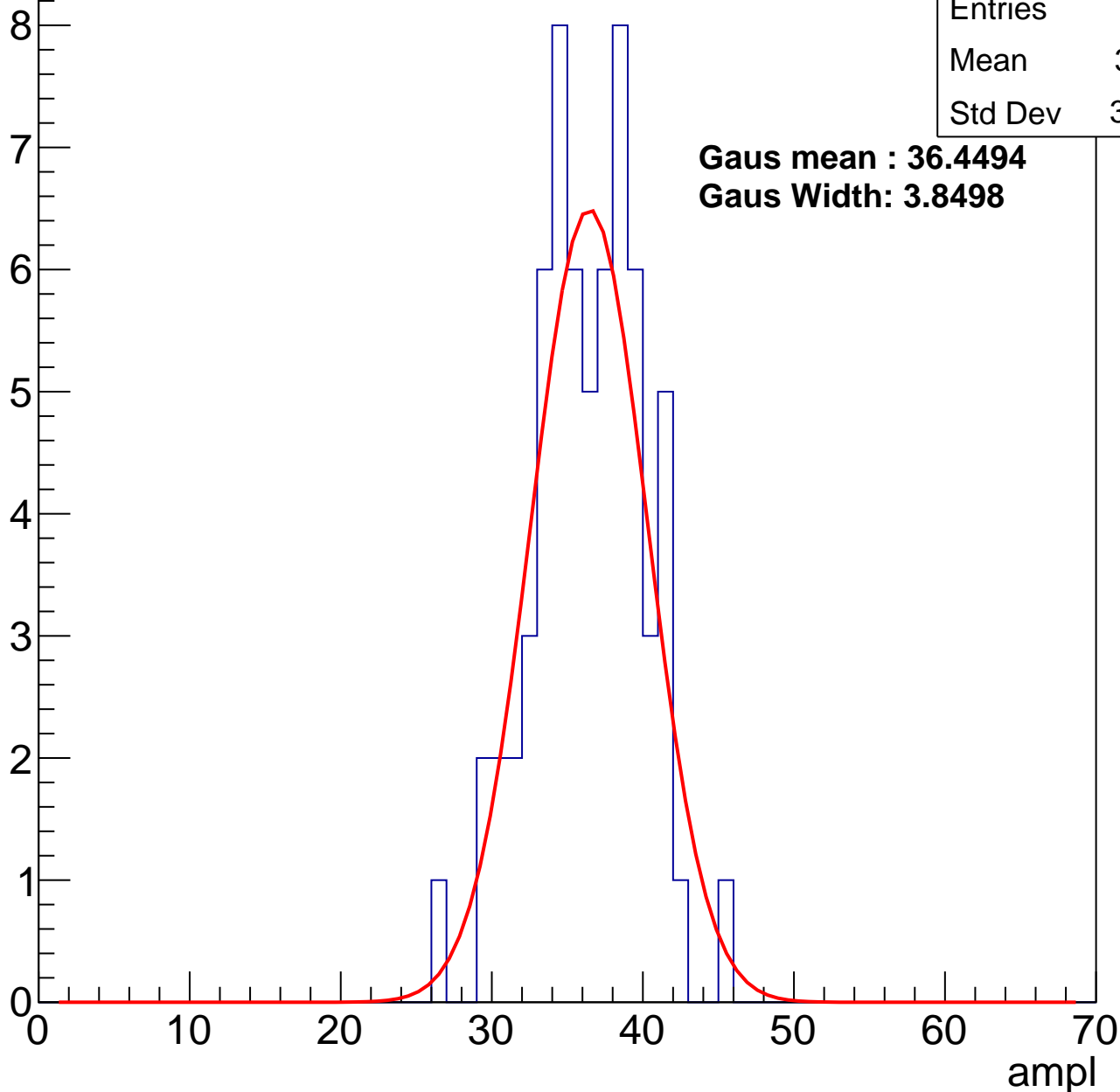
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.91
Std Dev	3.598

Gaus mean : 36.4494

Gaus Width: 3.8498



B1L100S, U1-ch74, adc2

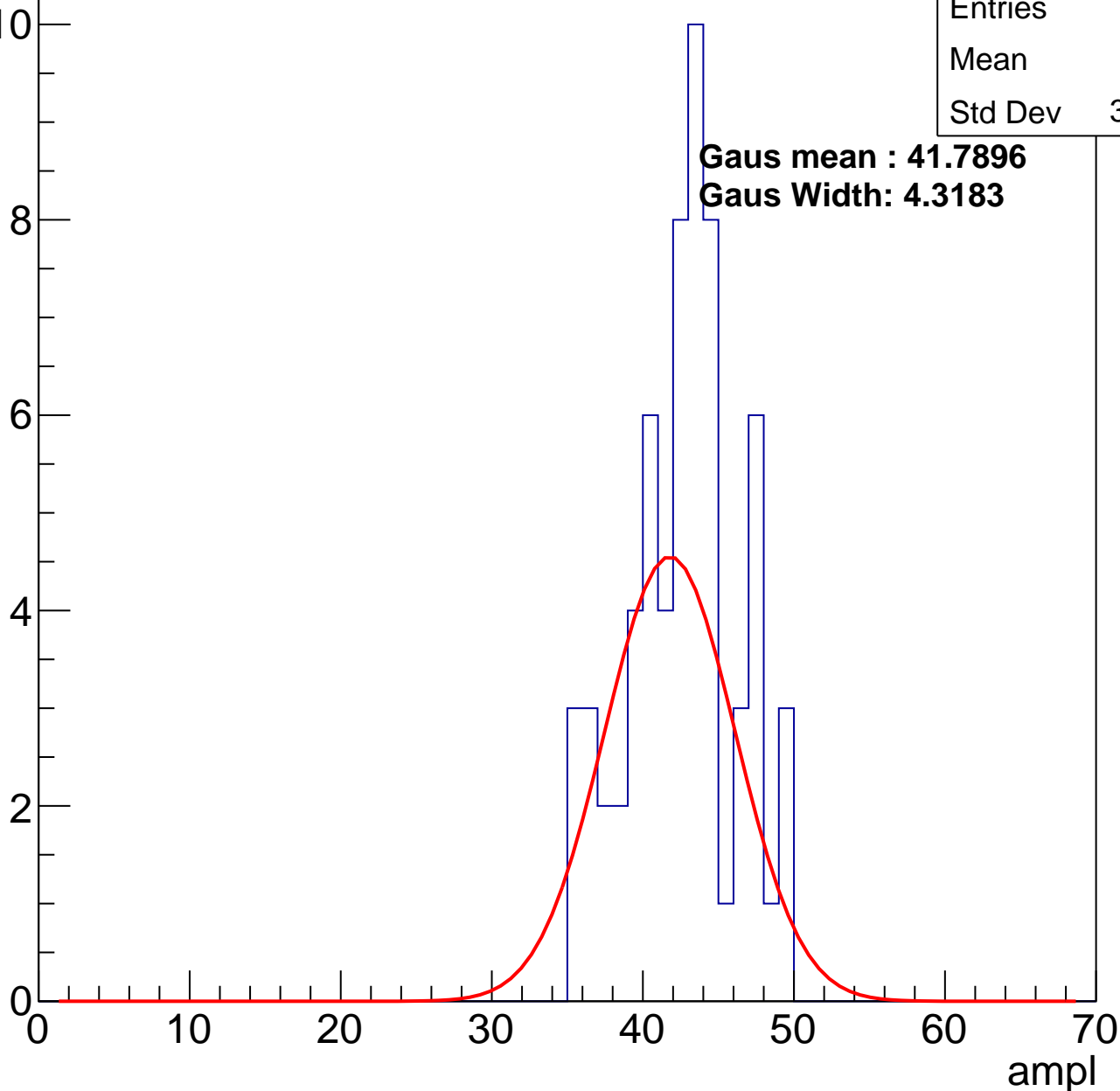
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	42.2
Std Dev	3.602

Gaus mean : 41.7896

Gaus Width: 4.3183

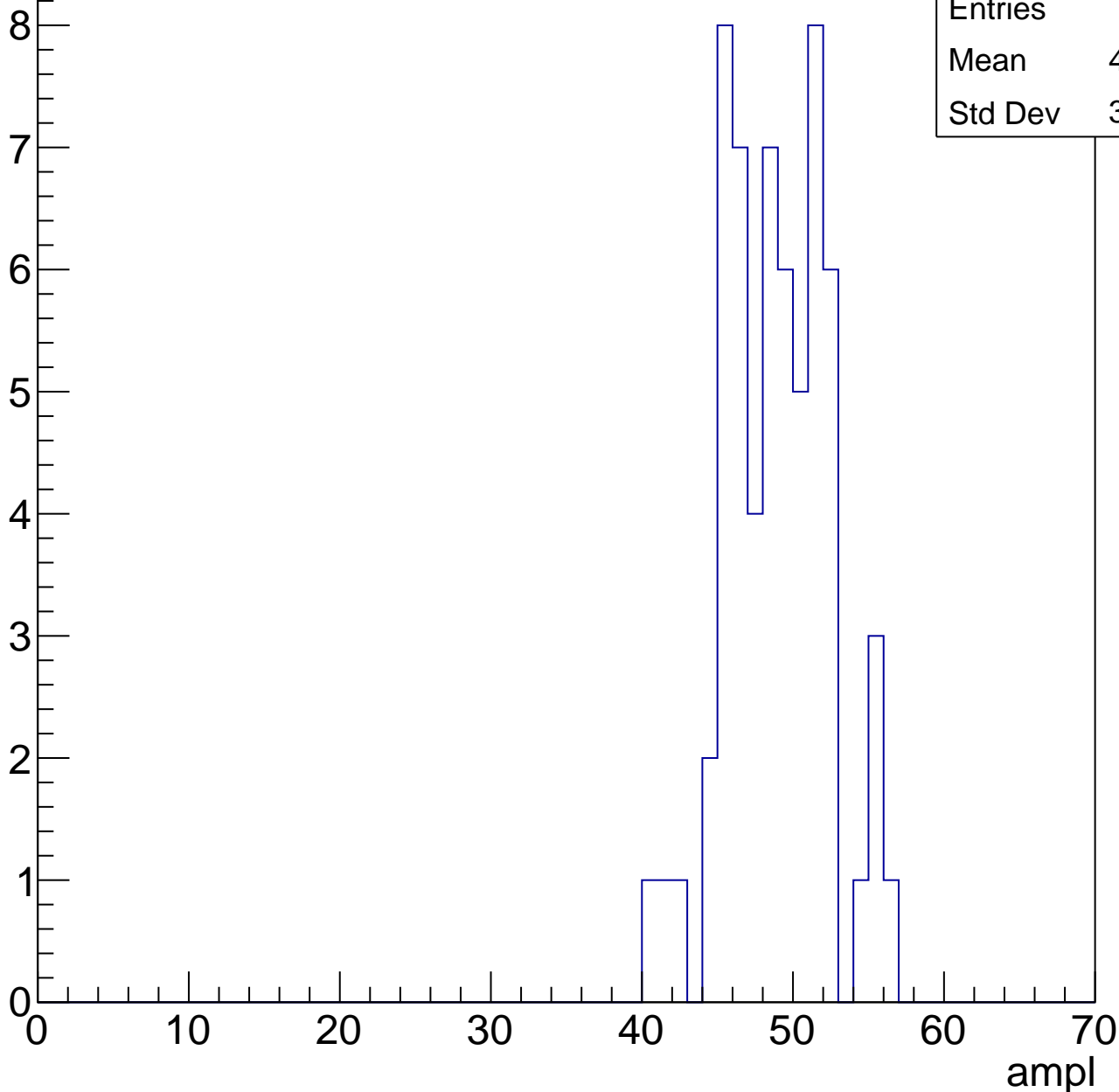


B1L100S, U1-ch74, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	48.46
Std Dev	3.419



B1L100S, U1-ch74, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	56
Mean	54.07
Std Dev	2.846

Entry

10

8

6

4

2

0

0

10

20

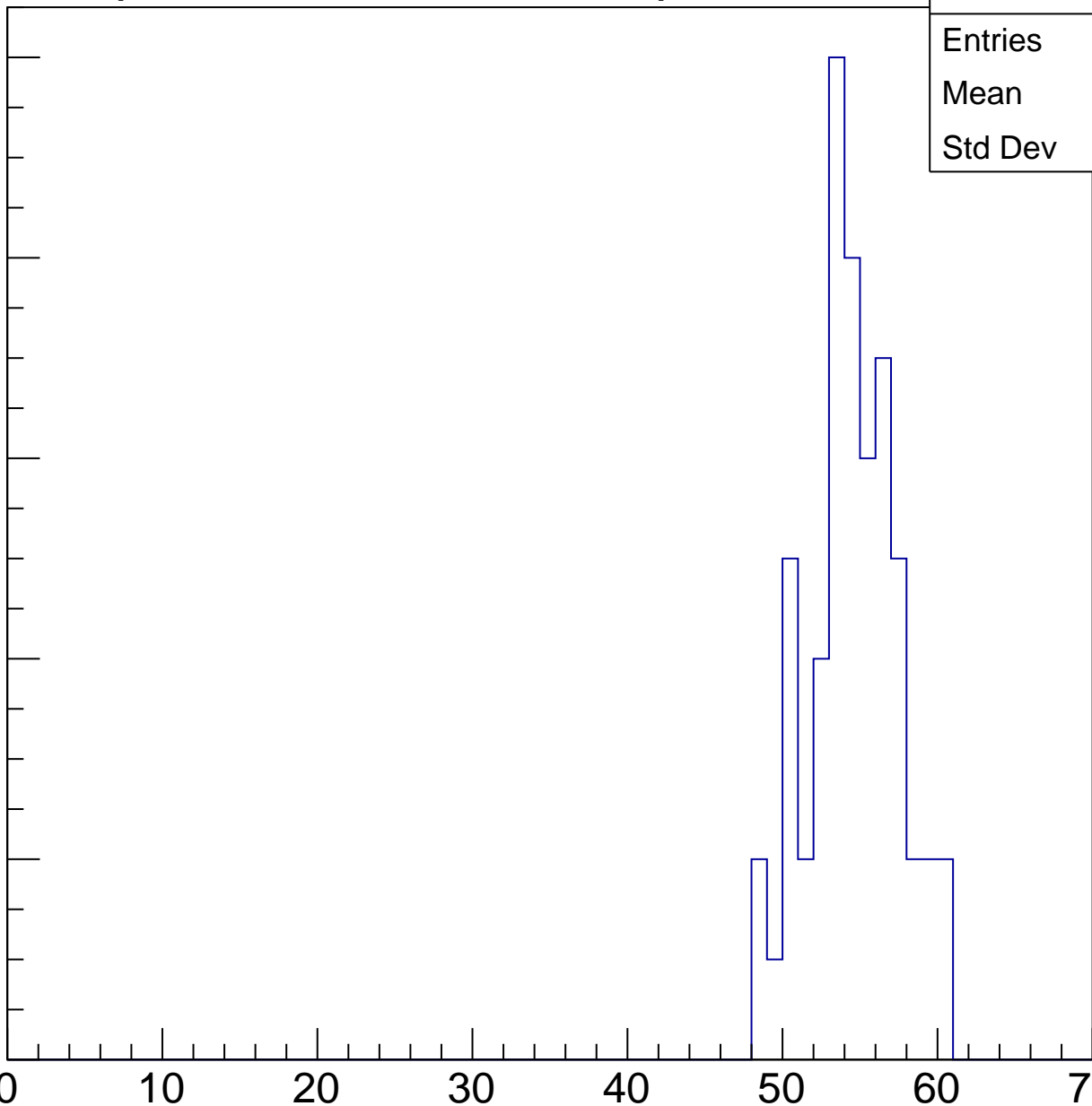
30

40

50

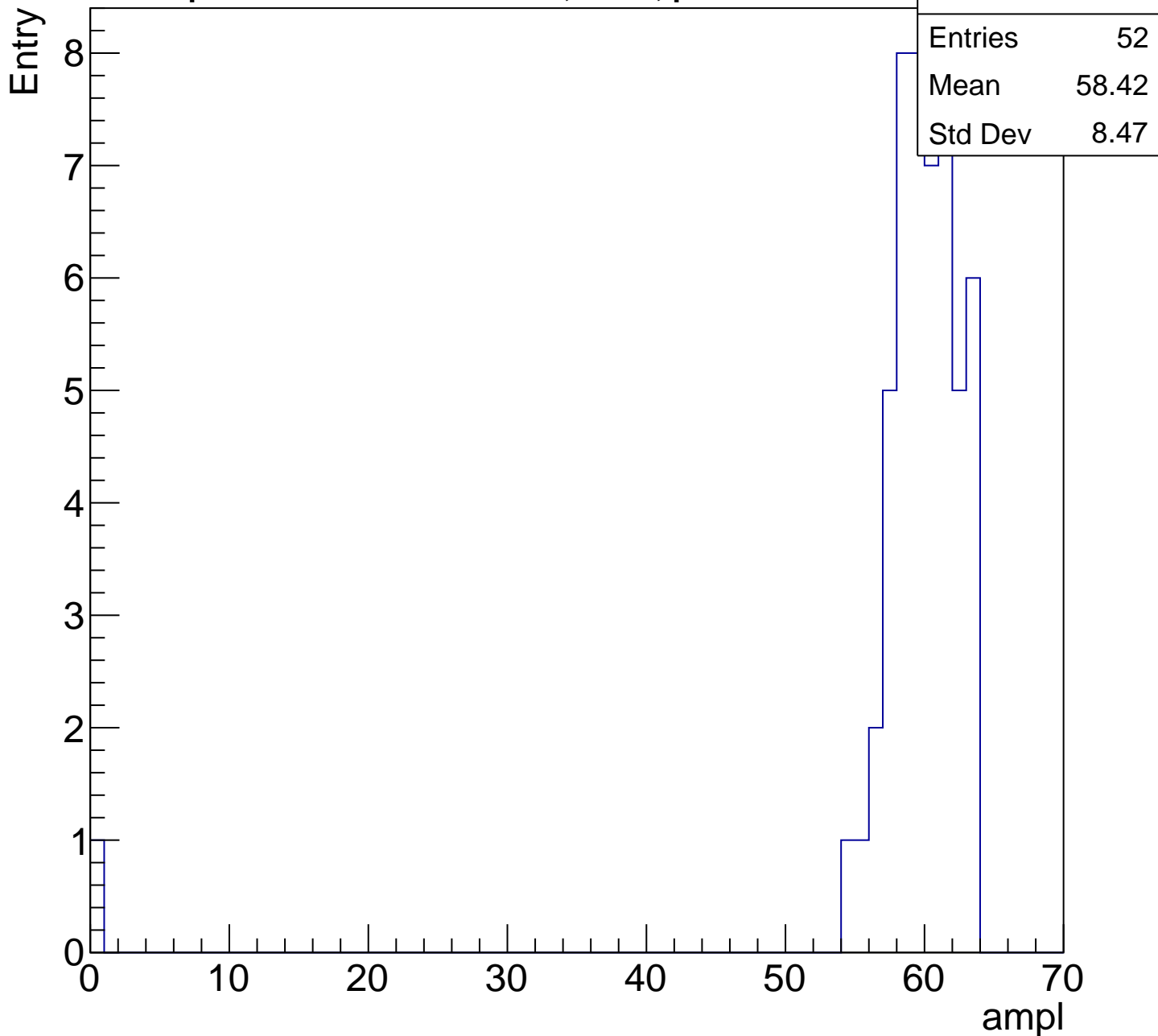
60

ampl



B1L100S, U1-ch74, adc5

calib_packv5_042523_0143.root, FC#4, port A2

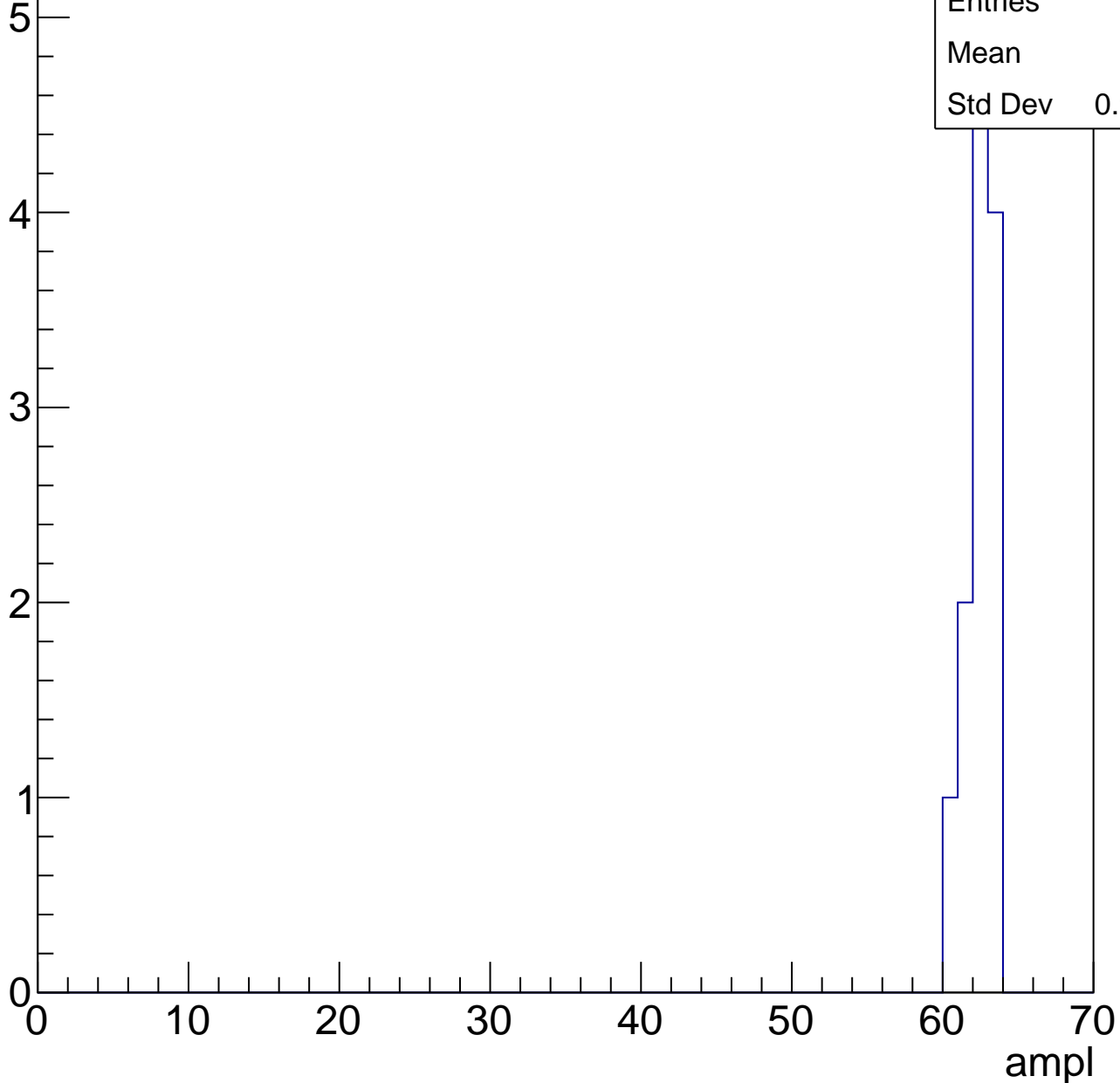


B1L100S, U1-ch74, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	62
Std Dev	0.9129



B1L100S, U1-ch74, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

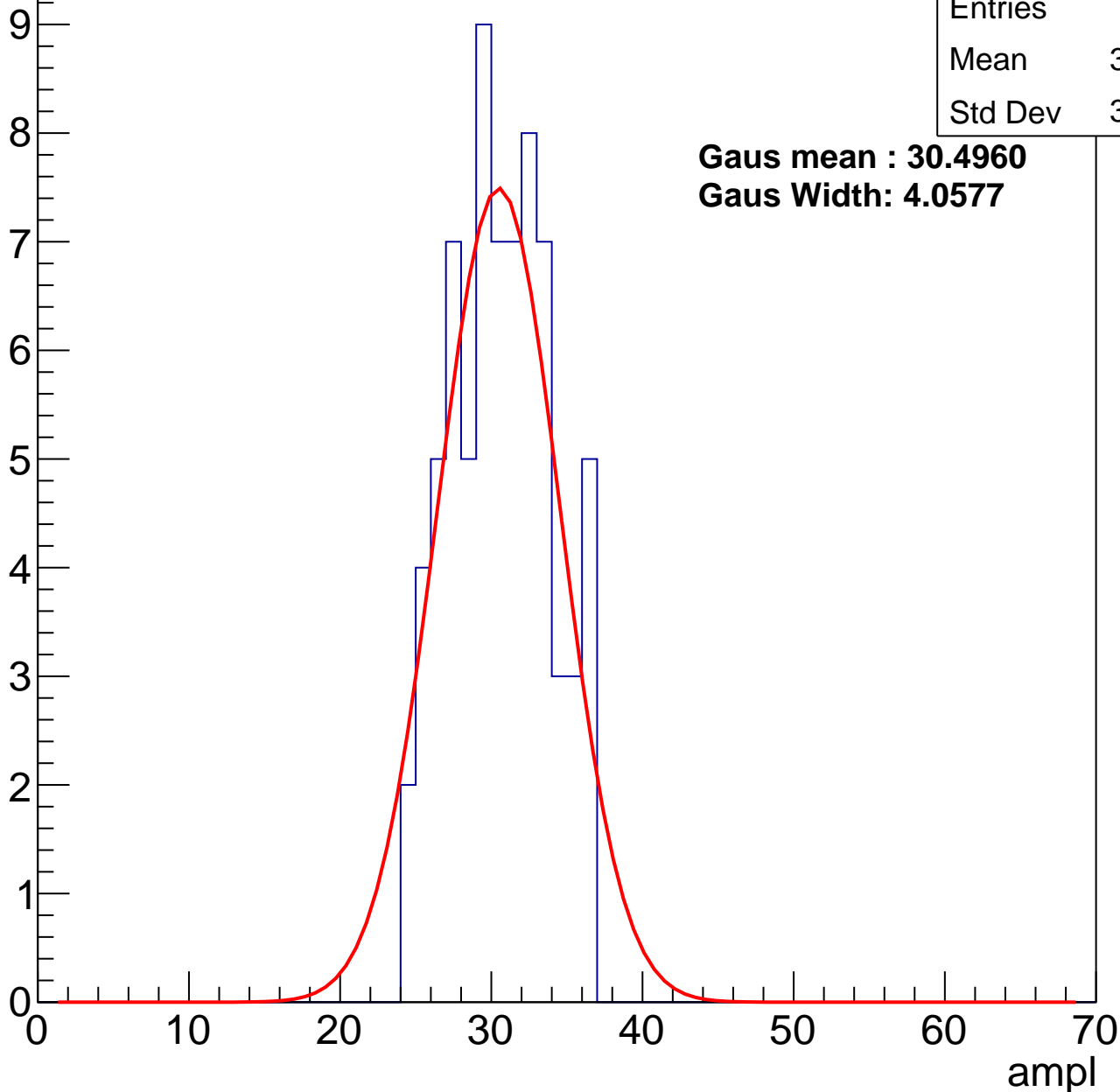


Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch75, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch75, adc1

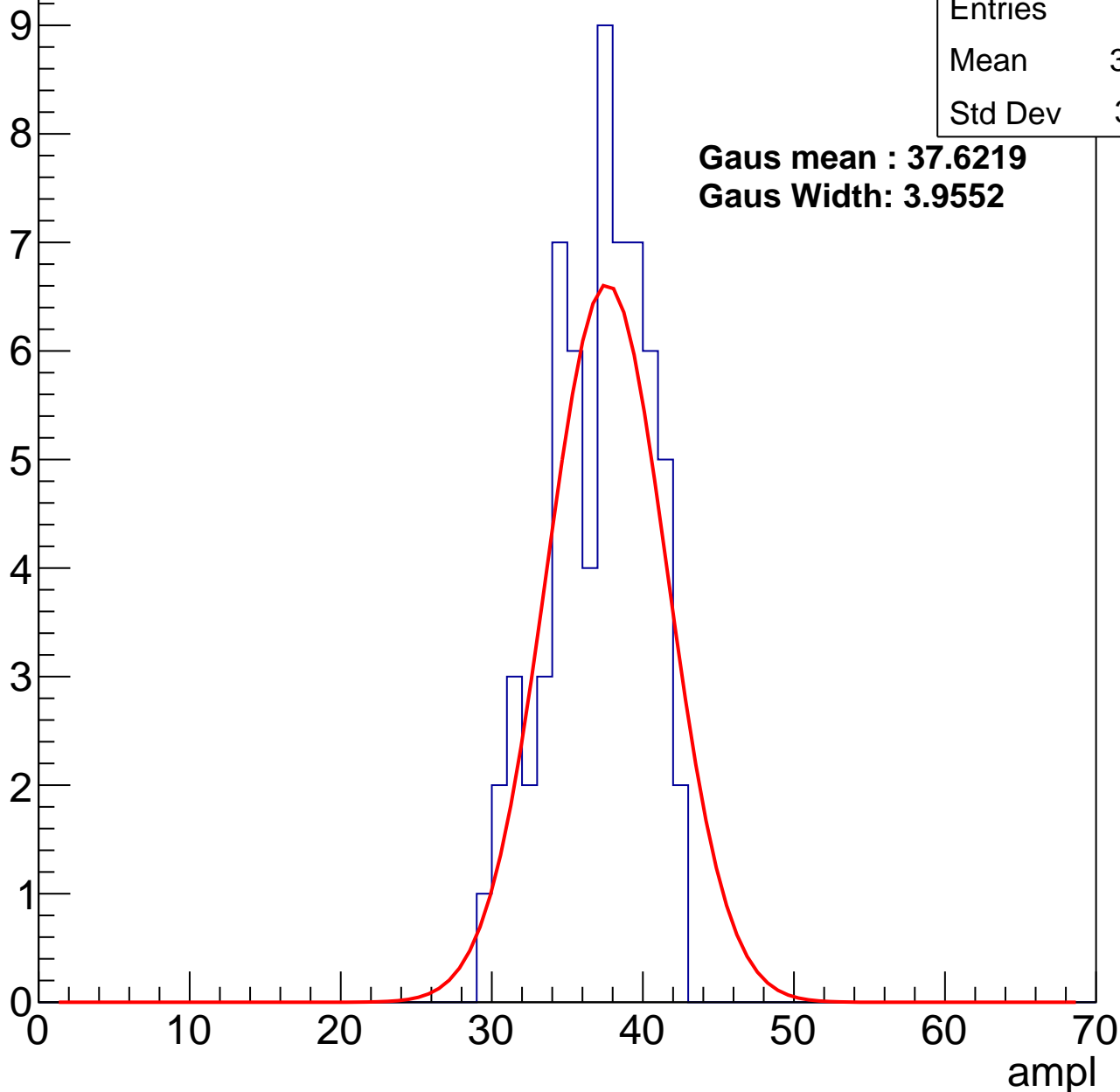
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.53
Std Dev	3.221

Gaus mean : 37.6219

Gaus Width: 3.9552



B1L100S, U1-ch75, adc2

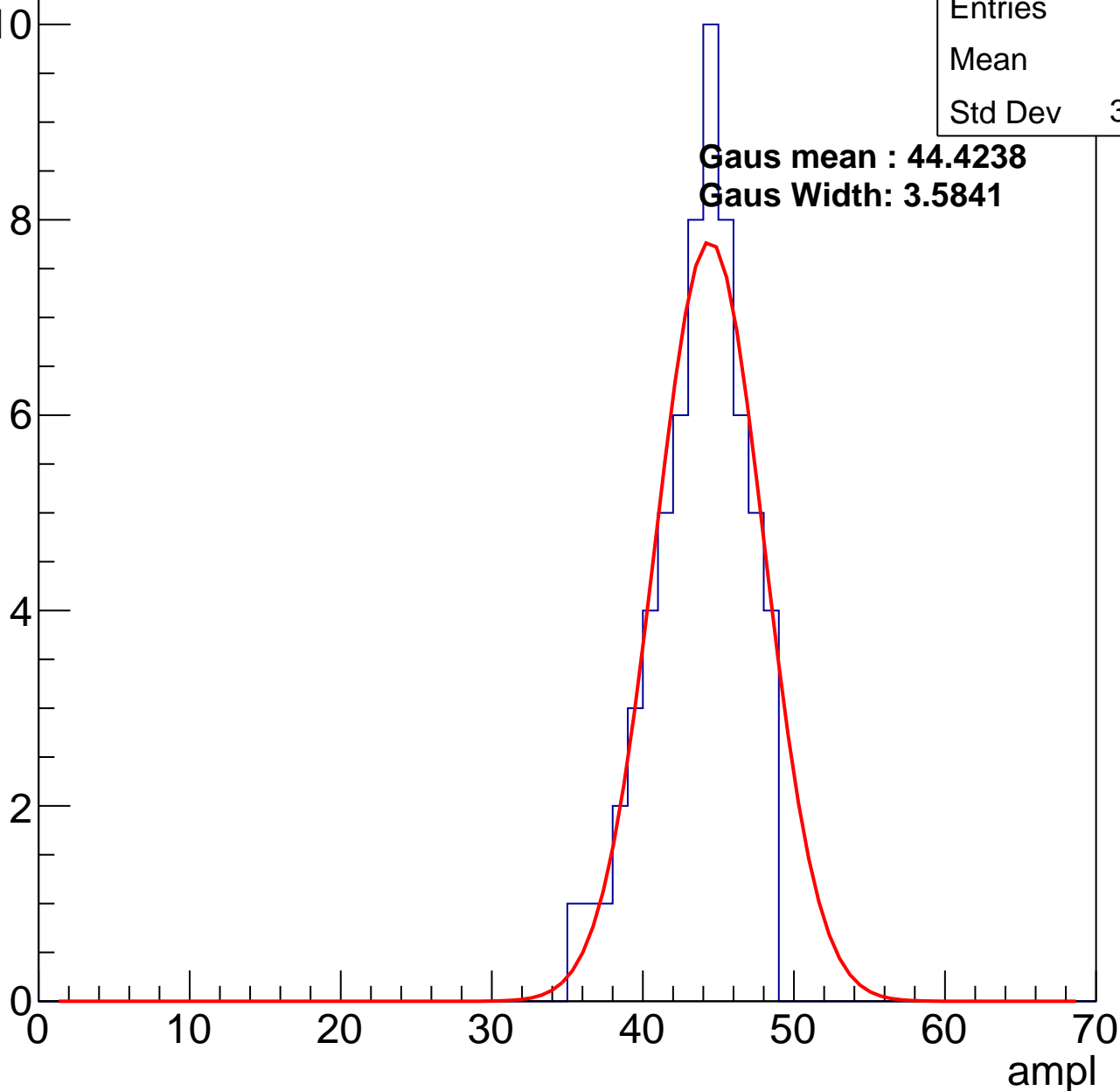
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	43.2
Std Dev	3.017

Gaus mean : 44.4238

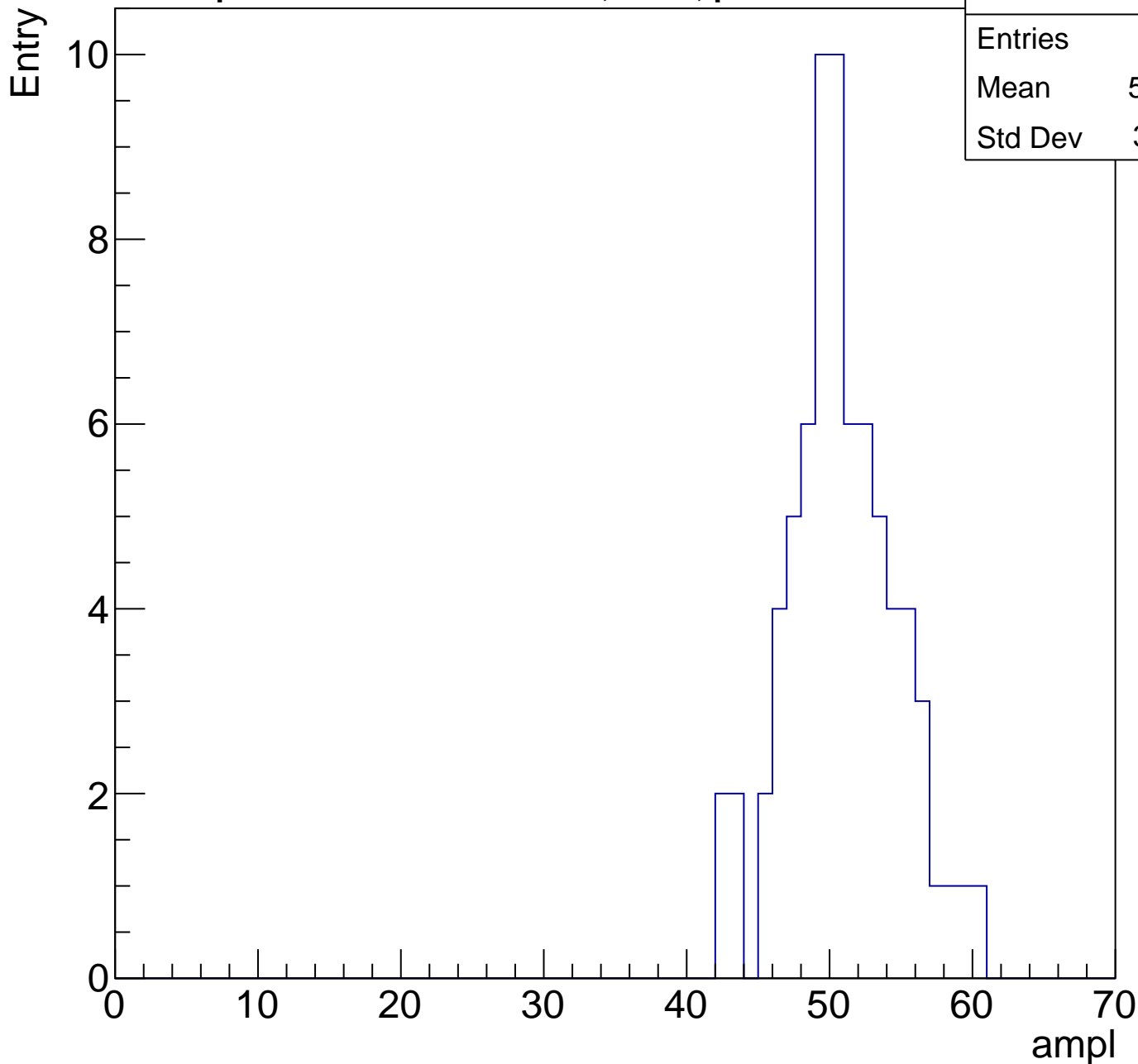
Gaus Width: 3.5841



B1L100S, U1-ch75, adc3

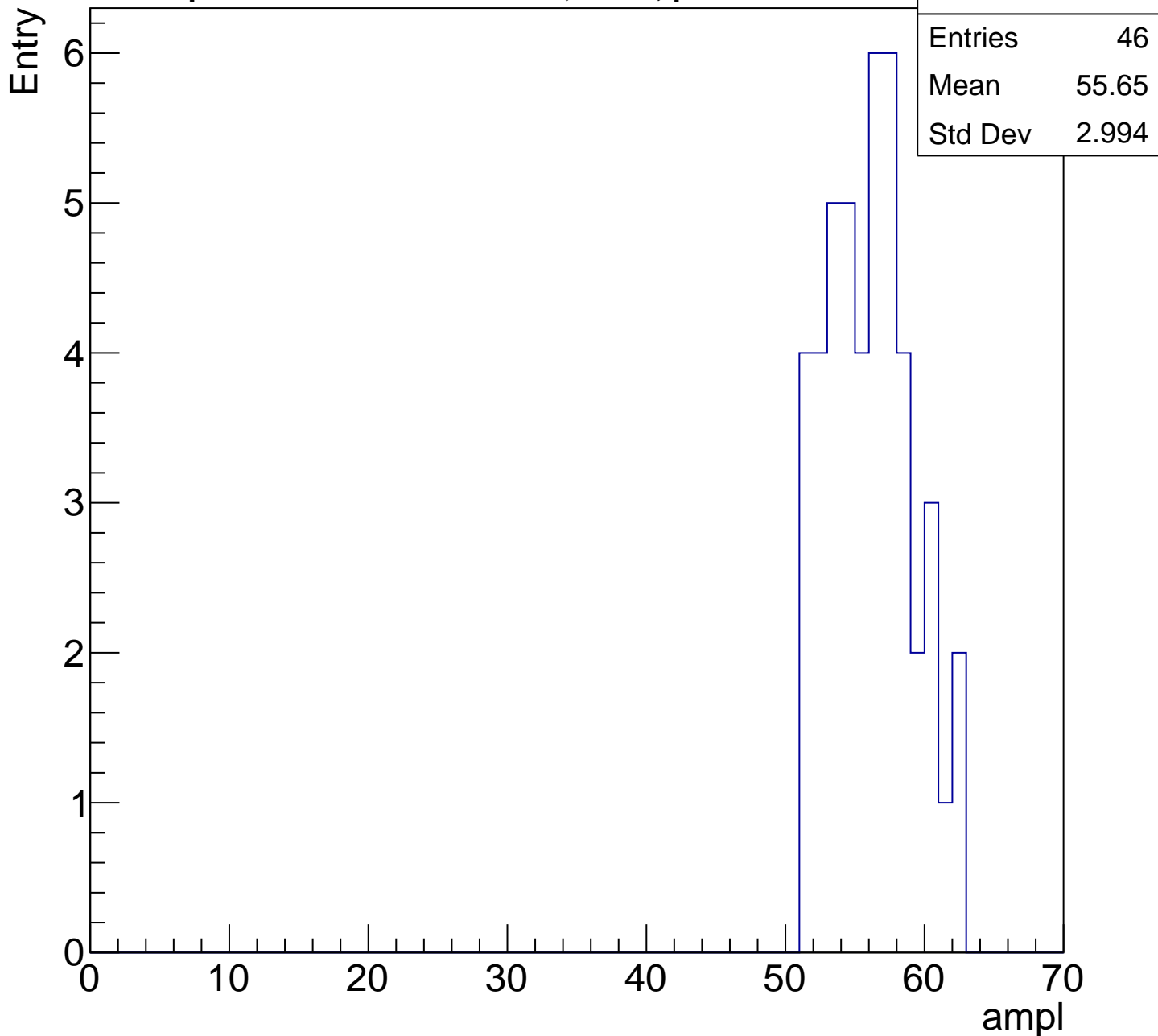
calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	50.38
Std Dev	3.791



B1L100S, U1-ch75, adc4

calib_packv5_042523_0143.root, FC#4, port A2

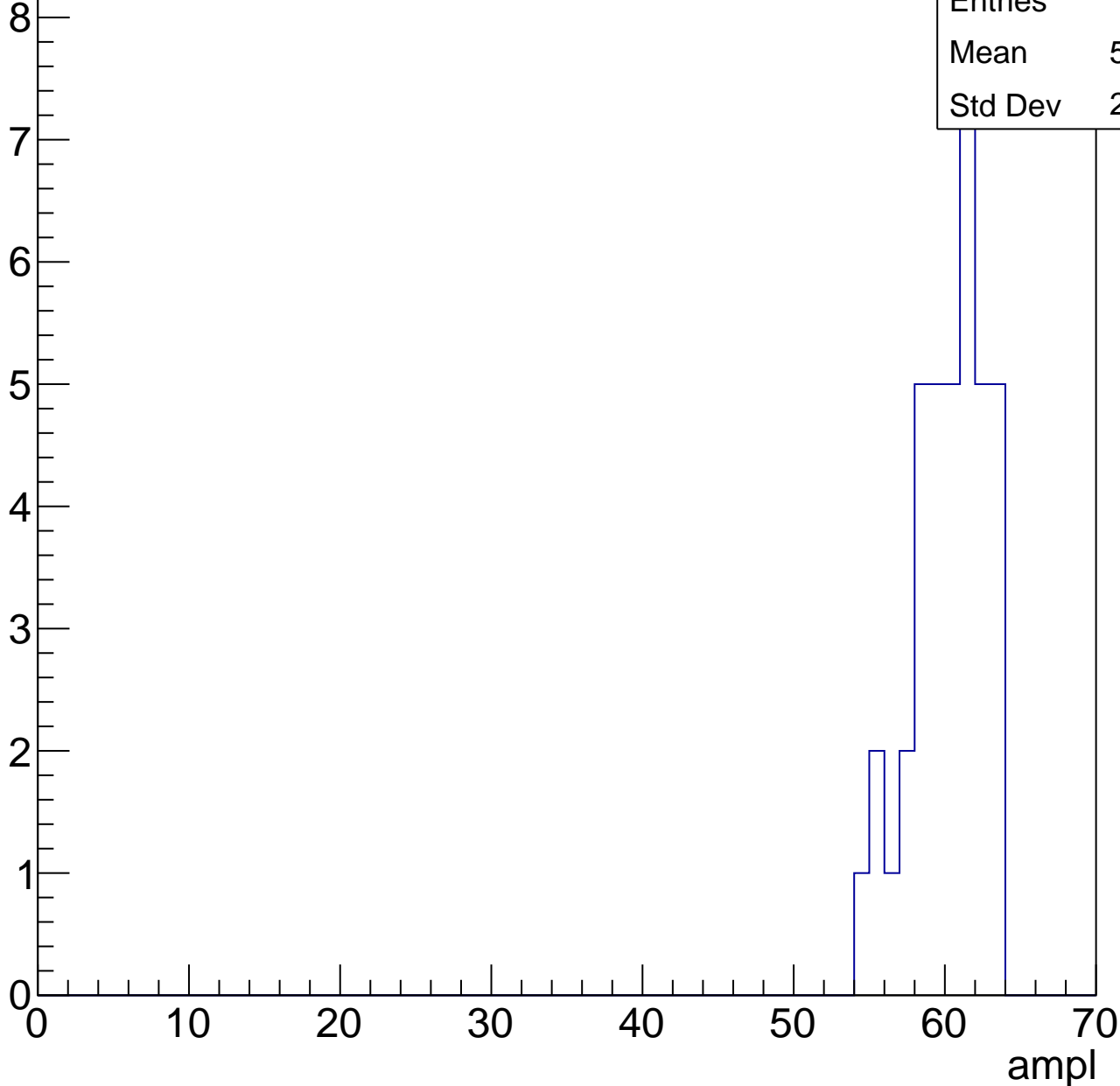


B1L100S, U1-ch75, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	59.79
Std Dev	2.355

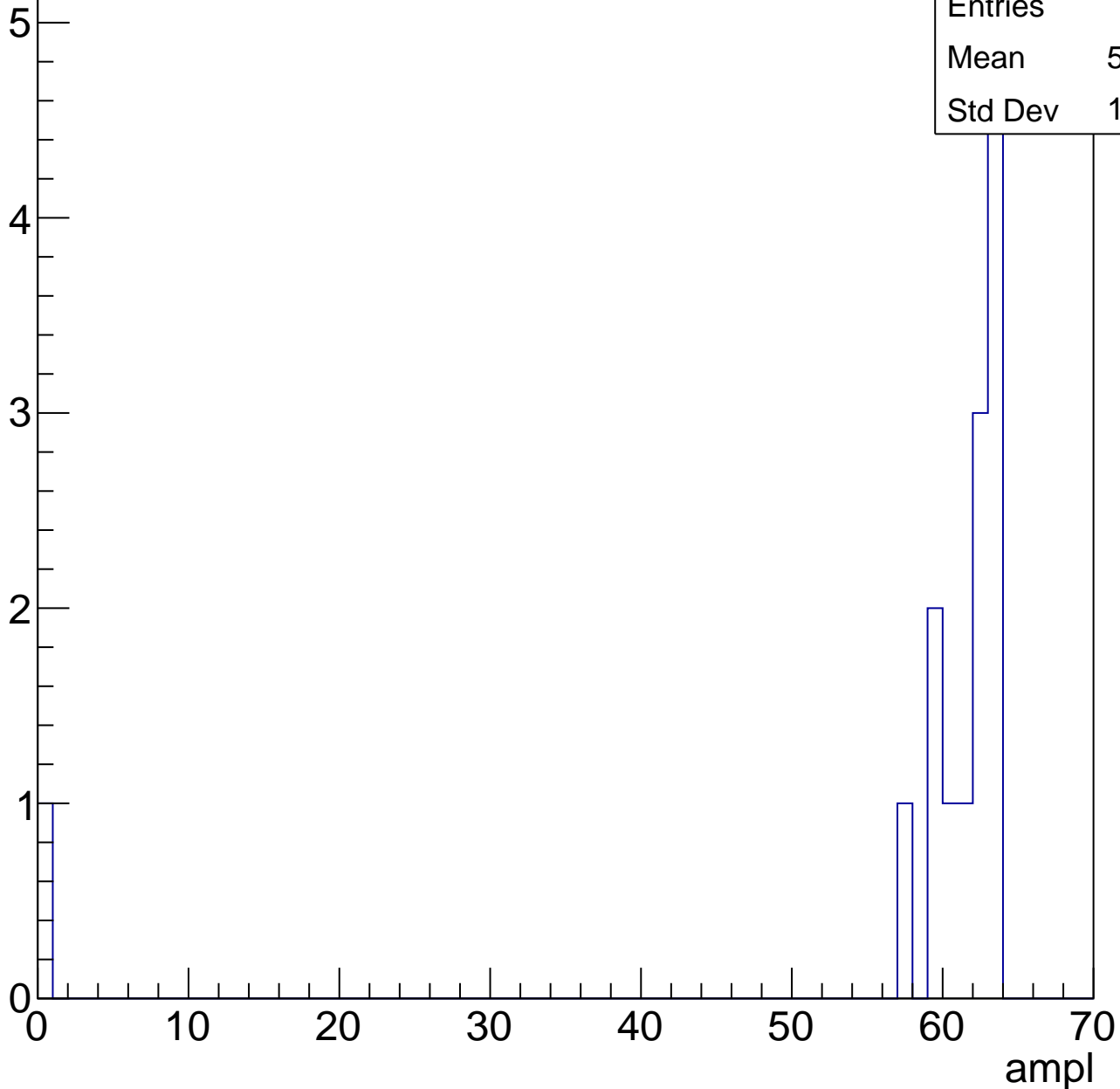


B1L100S, U1-ch75, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	56.93
Std Dev	15.89



B1L100S, U1-ch75, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch76, adc0

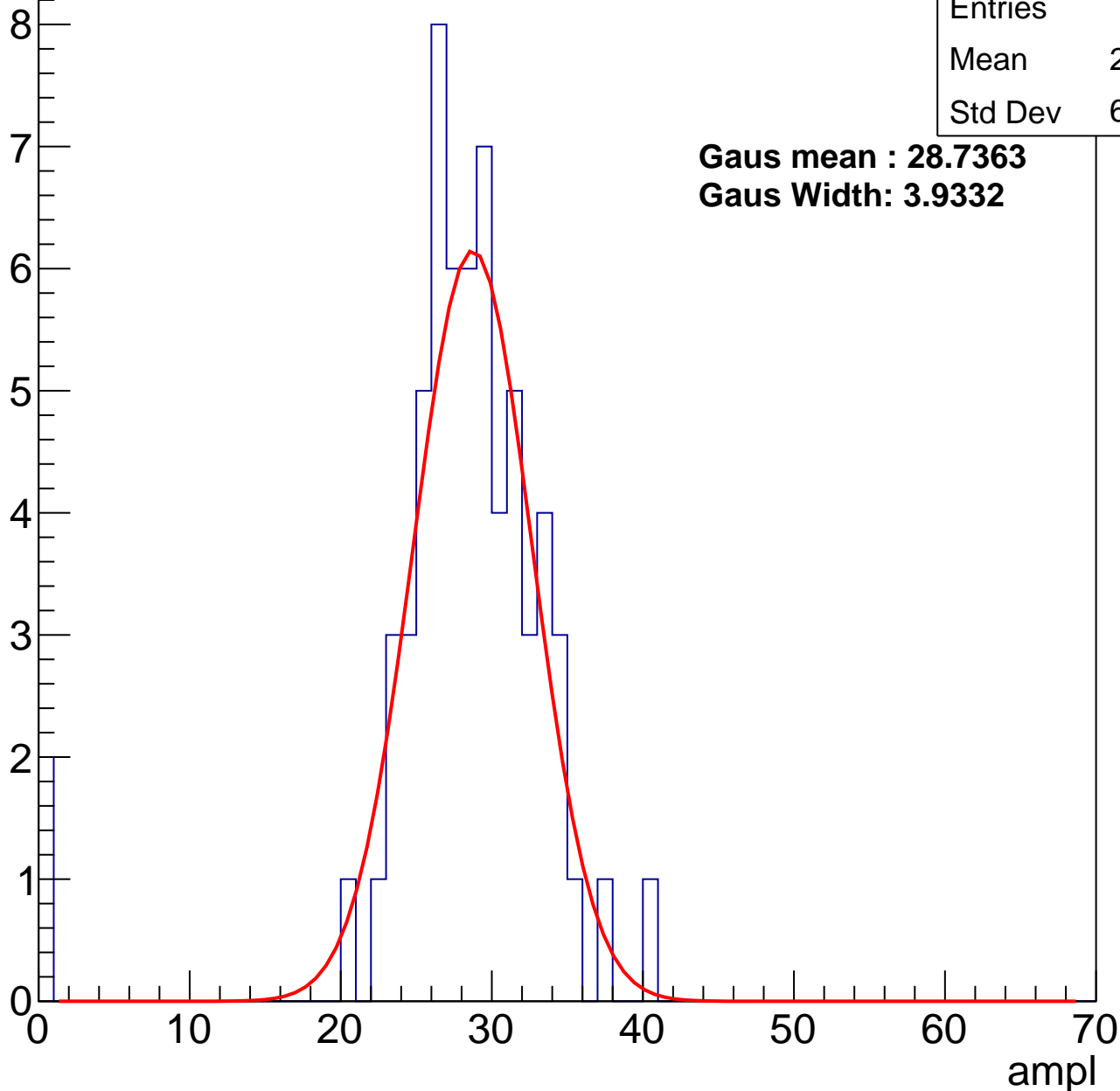
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	27.59
Std Dev	6.202

Gaus mean : 28.7363

Gaus Width: 3.9332



B1L100S, U1-ch76, adc1

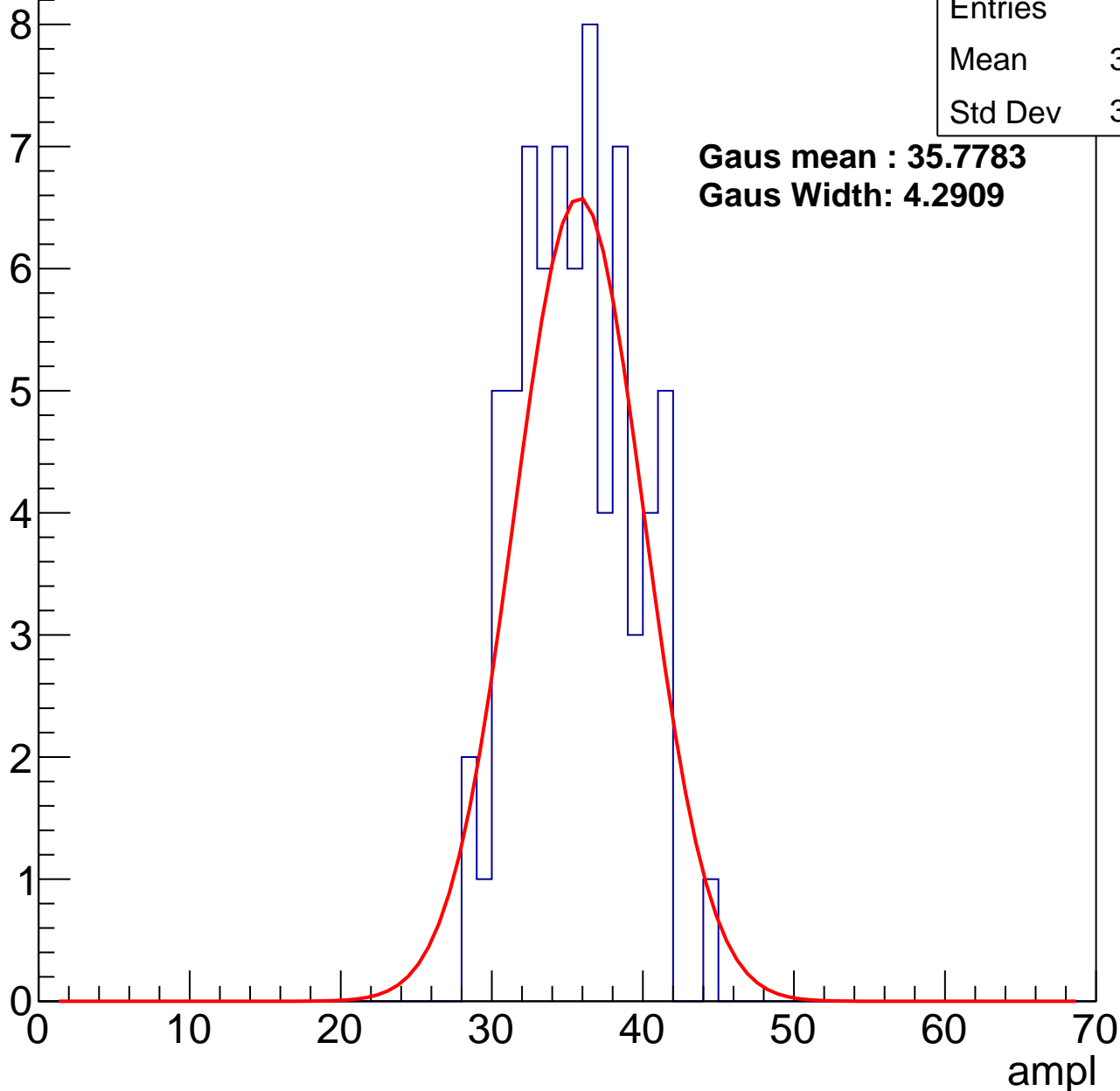
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	35.04
Std Dev	3.617

Gaus mean : 35.7783

Gaus Width: 4.2909



B1L100S, U1-ch76, adc2

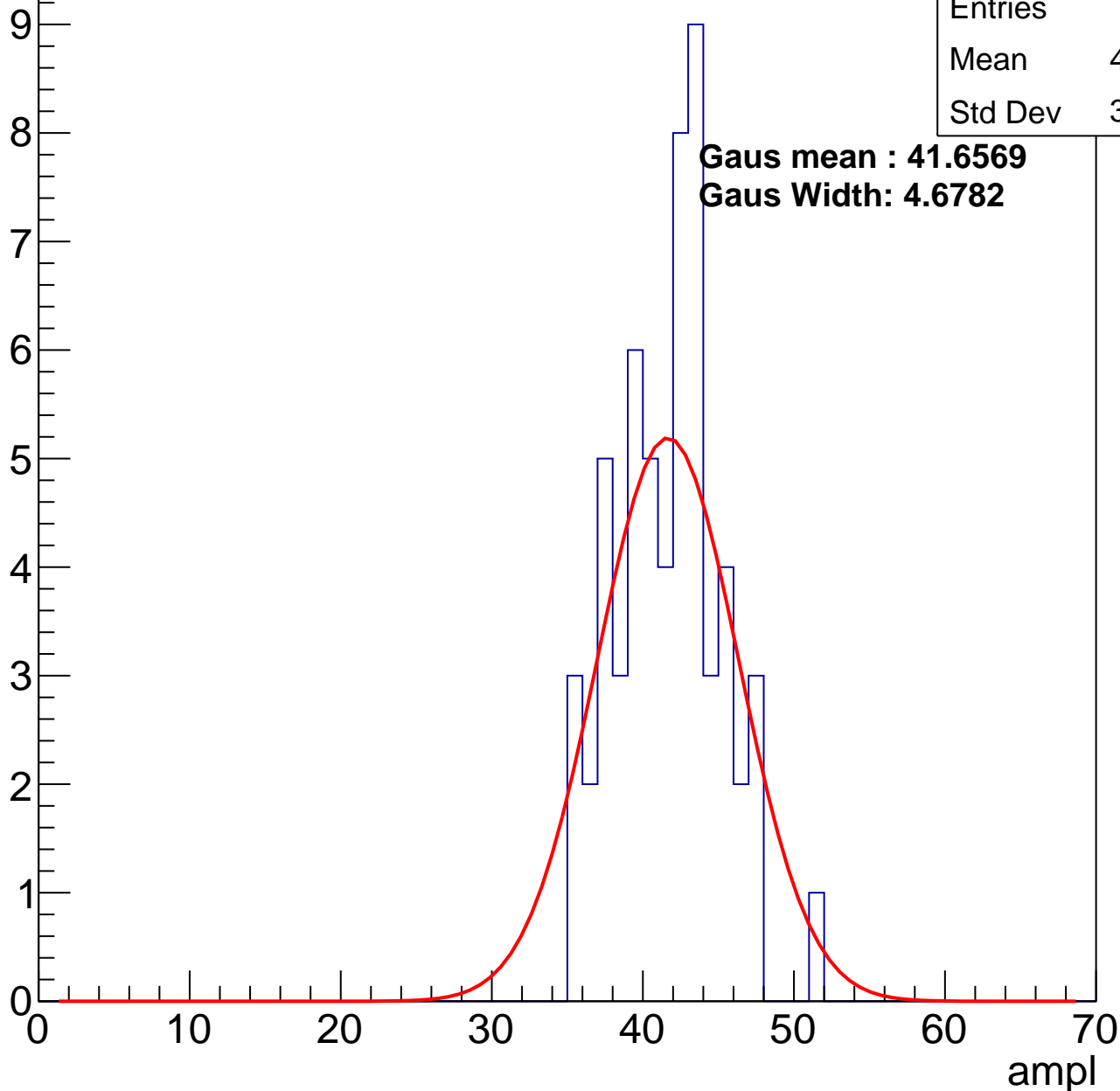
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	41.26
Std Dev	3.432

Gaus mean : 41.6569

Gaus Width: 4.6782

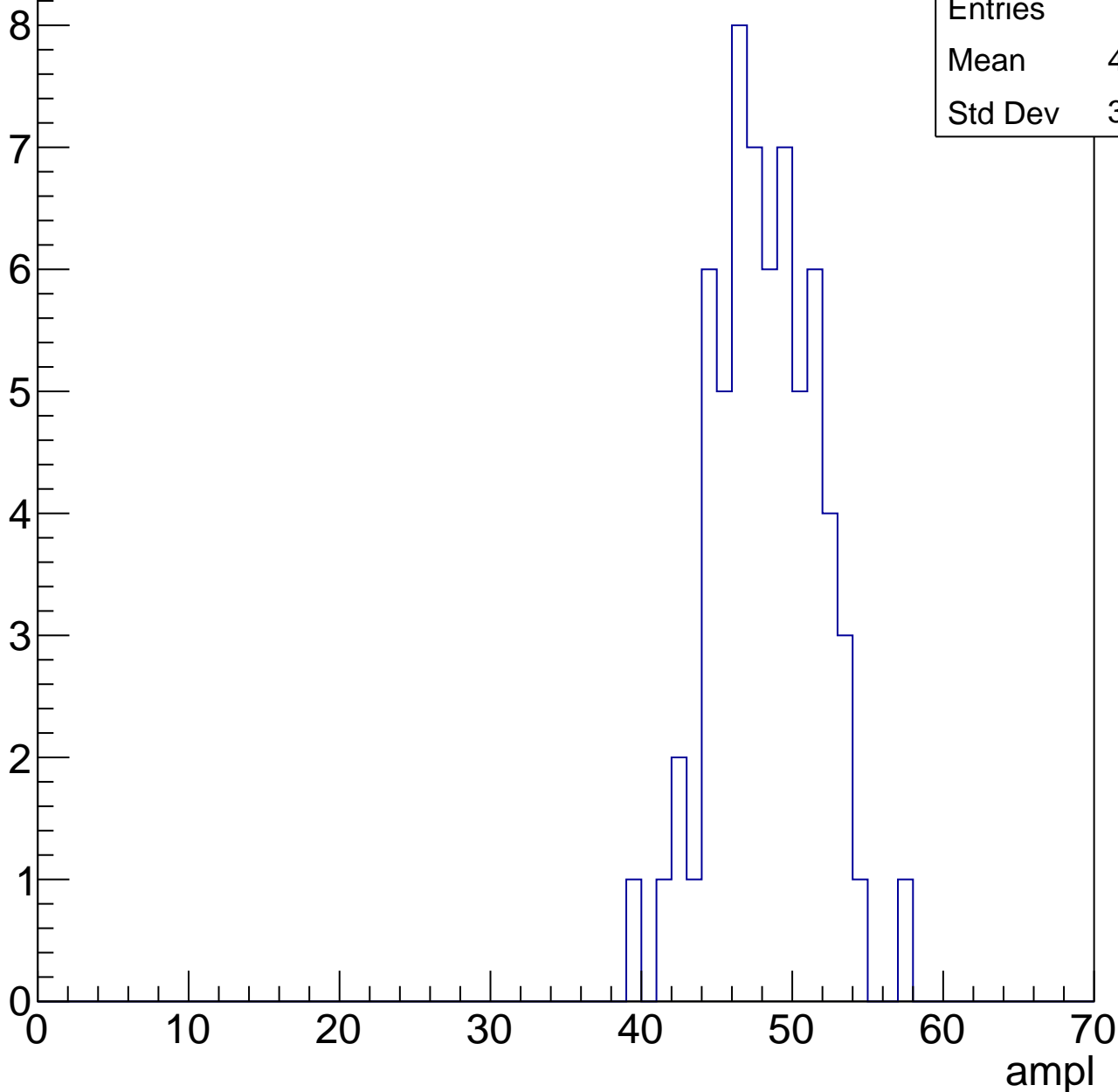


B1L100S, U1-ch76, adc3

calib_packv5_042523_0143.root, FC#4, port A2

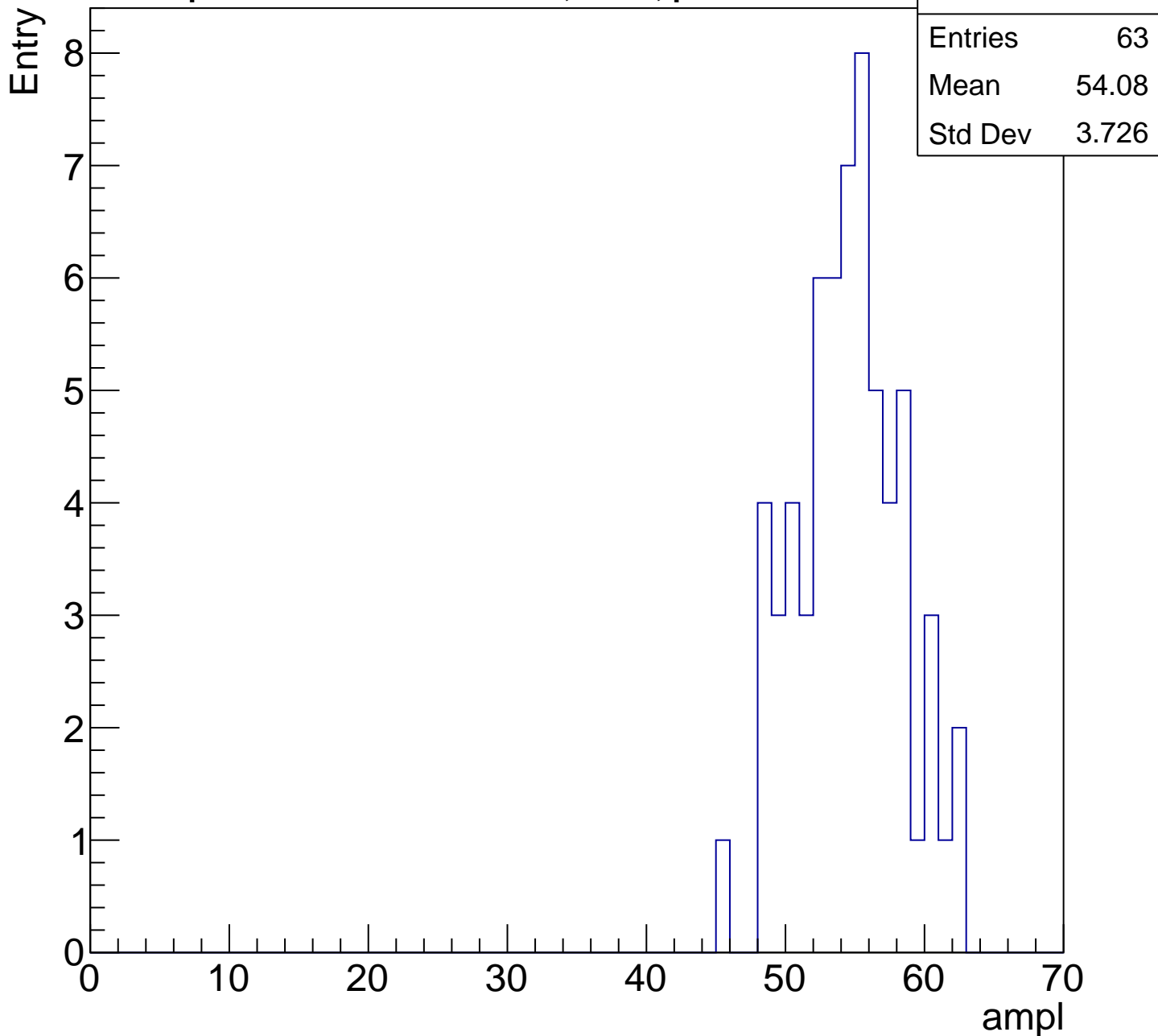
Entry

Entries	64
Mean	47.78
Std Dev	3.403



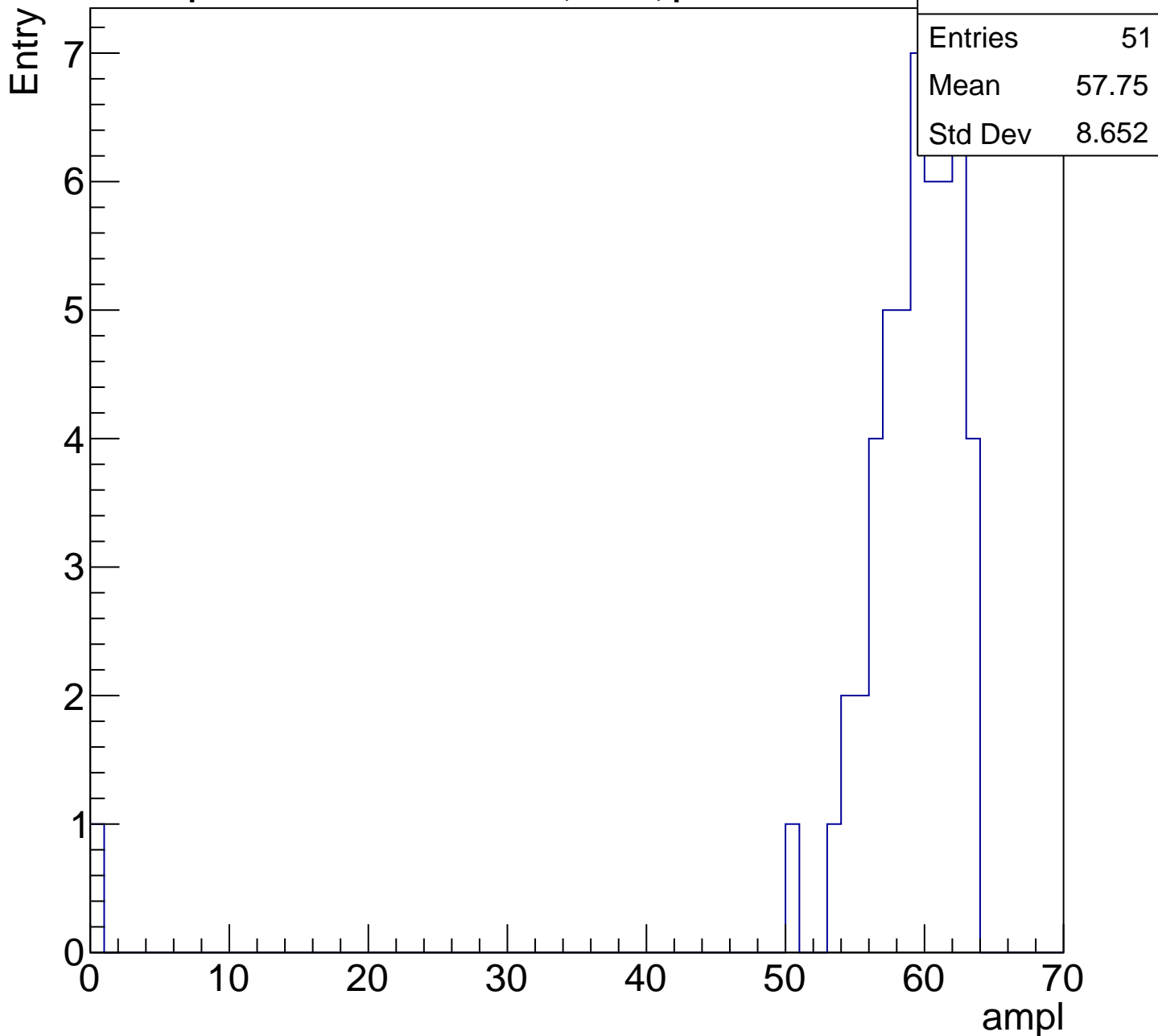
B1L100S, U1-ch76, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch76, adc5

calib_packv5_042523_0143.root, FC#4, port A2

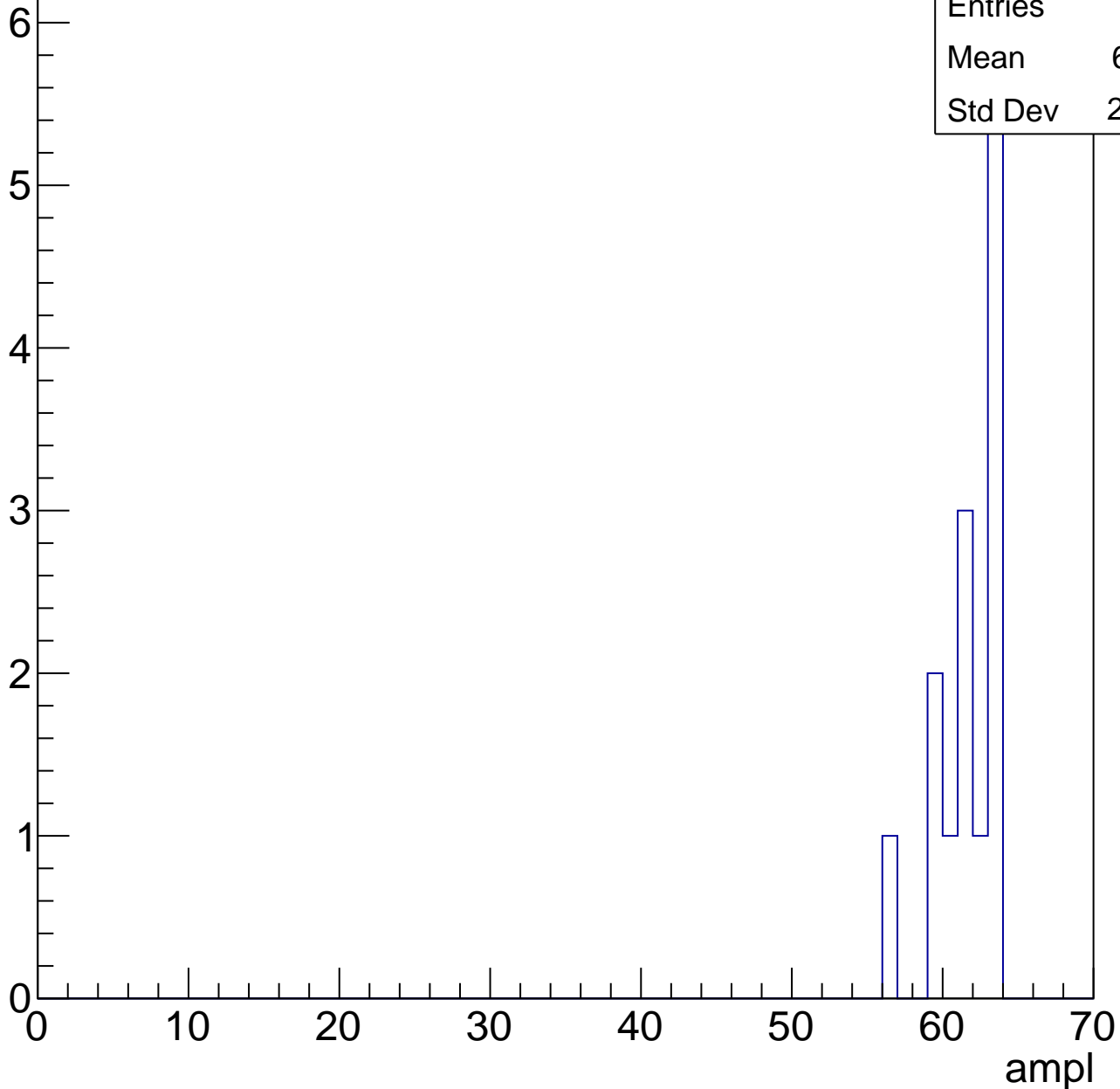


B1L100S, U1-ch76, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	14
Mean	61.21
Std Dev	2.042



B1L100S, U1-ch76, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch77, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	27.47
Std Dev	4.572

Gaus mean : 28.3930

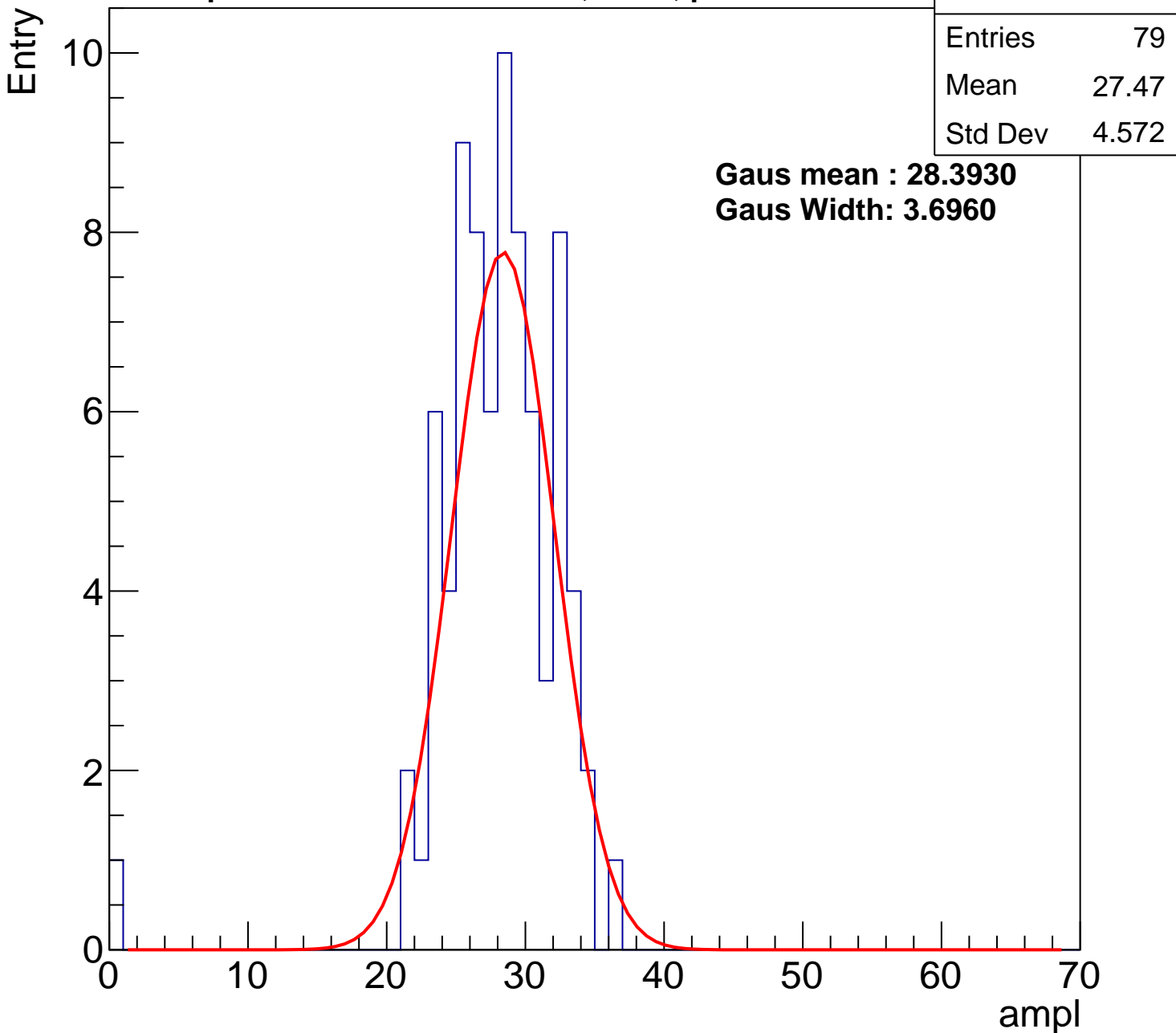
Gaus Width: 3.6960

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch77, adc1

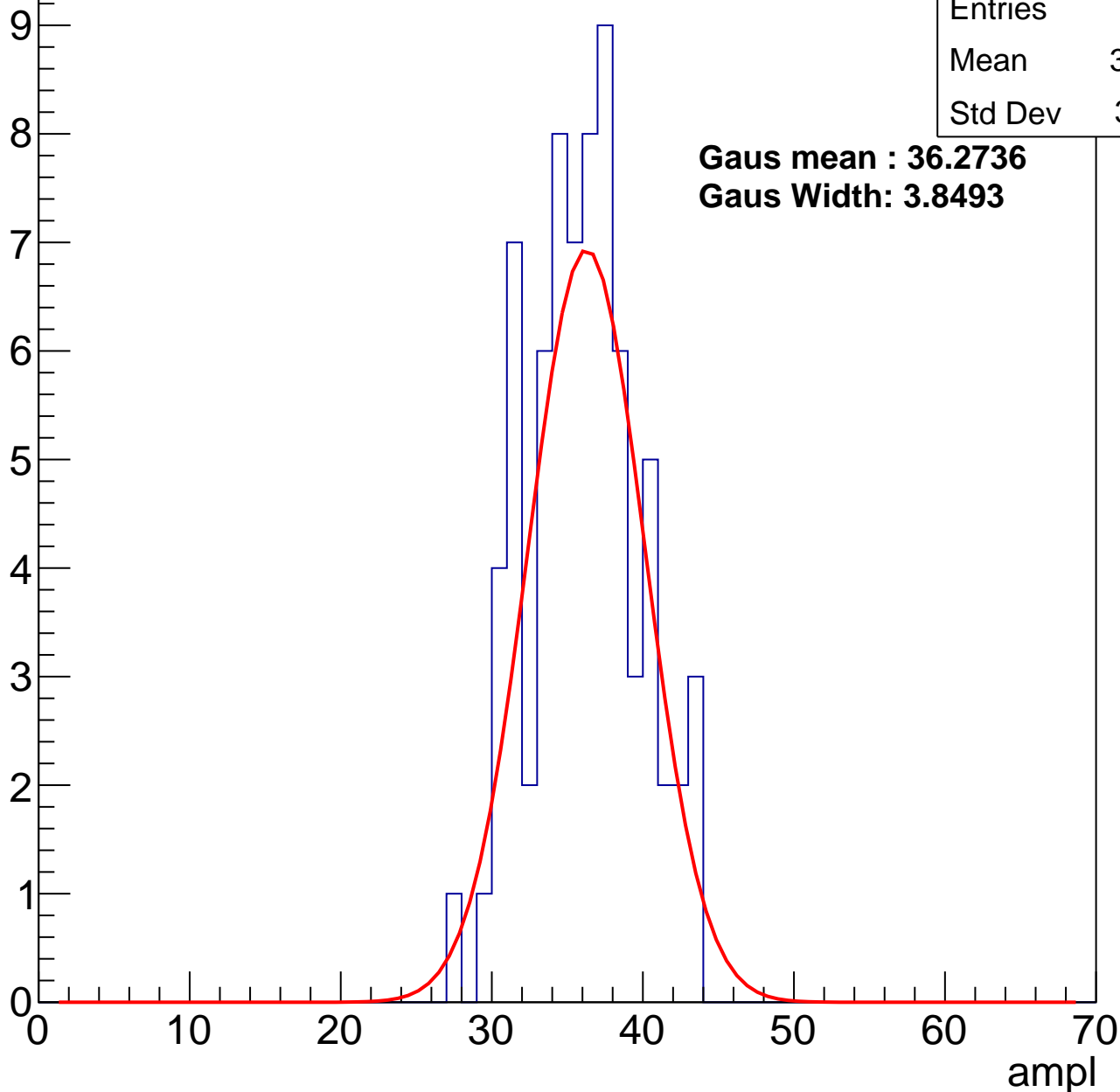
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.58
Std Dev	3.621

Gaus mean : 36.2736

Gaus Width: 3.8493



B1L100S, U1-ch77, adc2

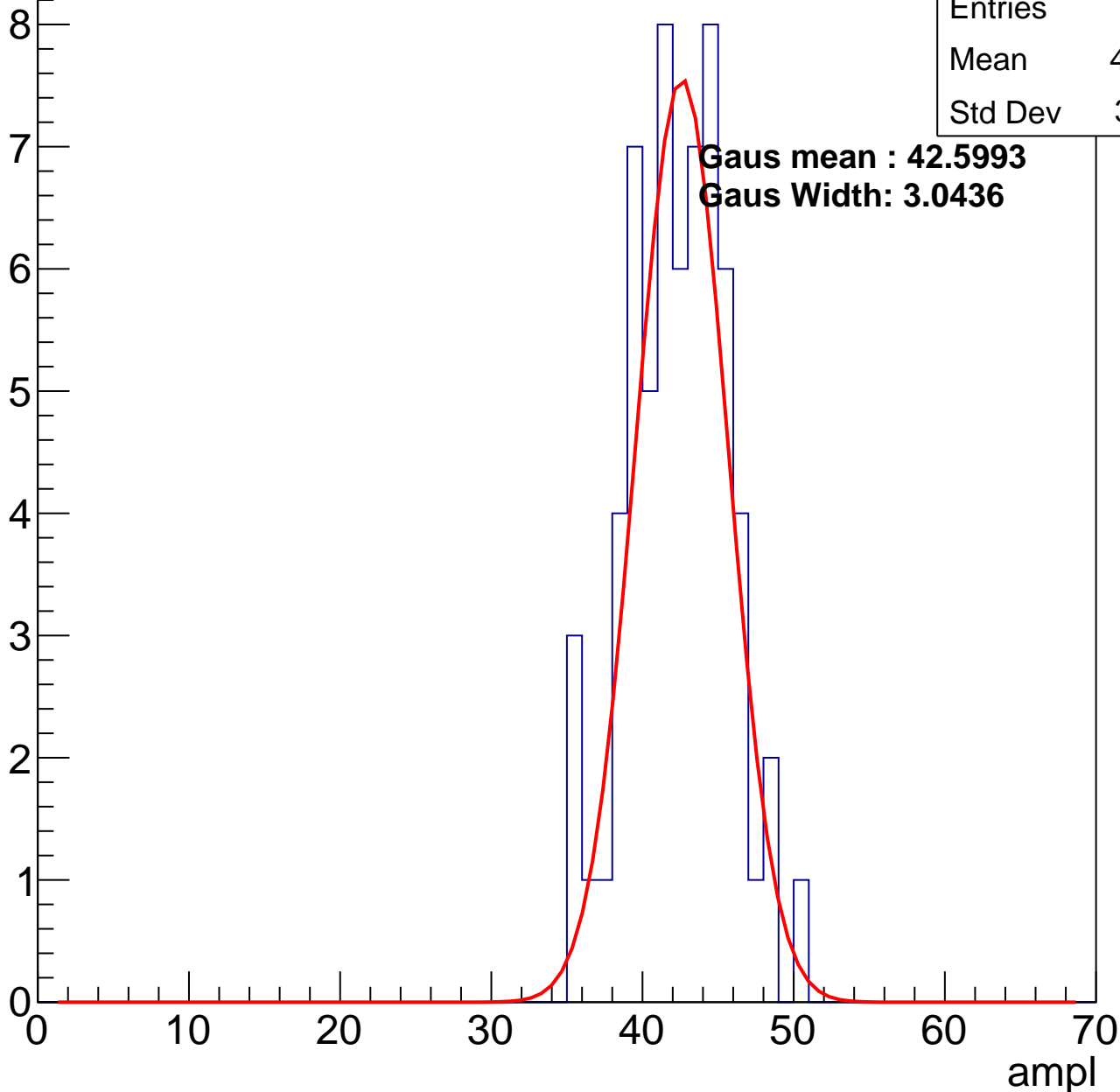
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.92
Std Dev	3.261

Gaus mean : 42.5993

Gaus Width: 3.0436



B1L100S, U1-ch77, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	49.64
Std Dev	3.553

Entry

10

8

6

4

2

0

0

10

20

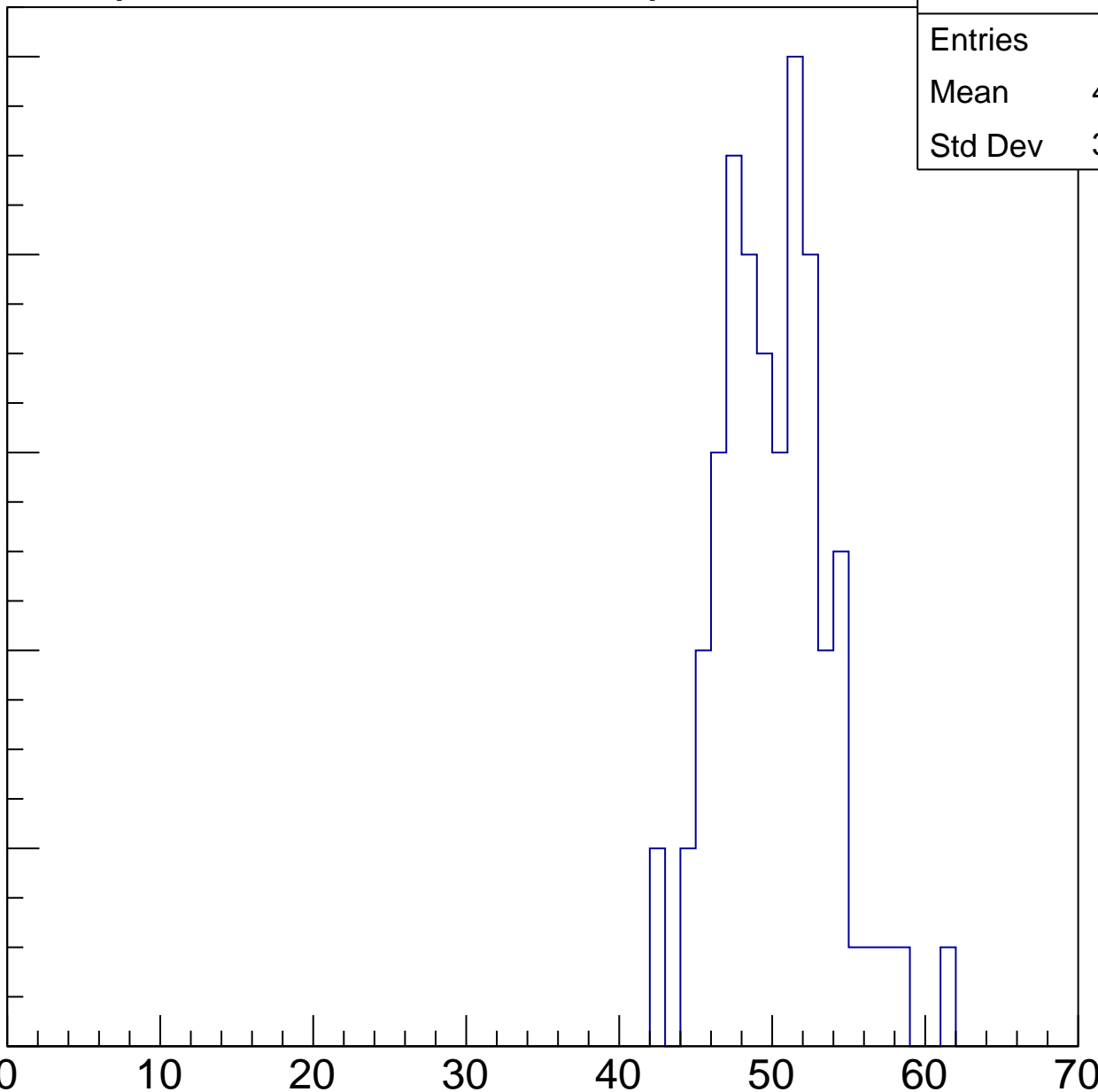
30

40

50

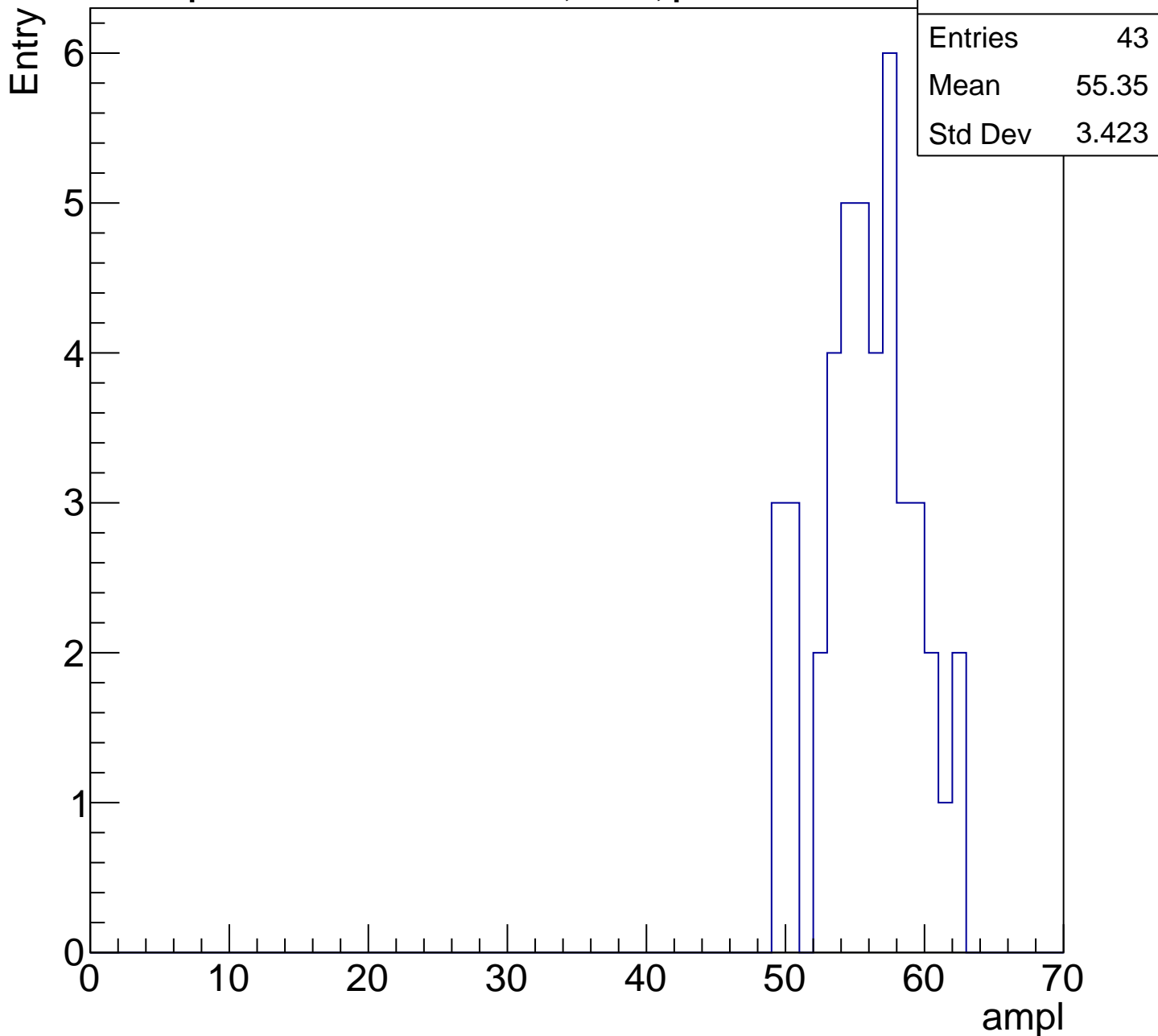
60

ampl



B1L100S, U1-ch77, adc4

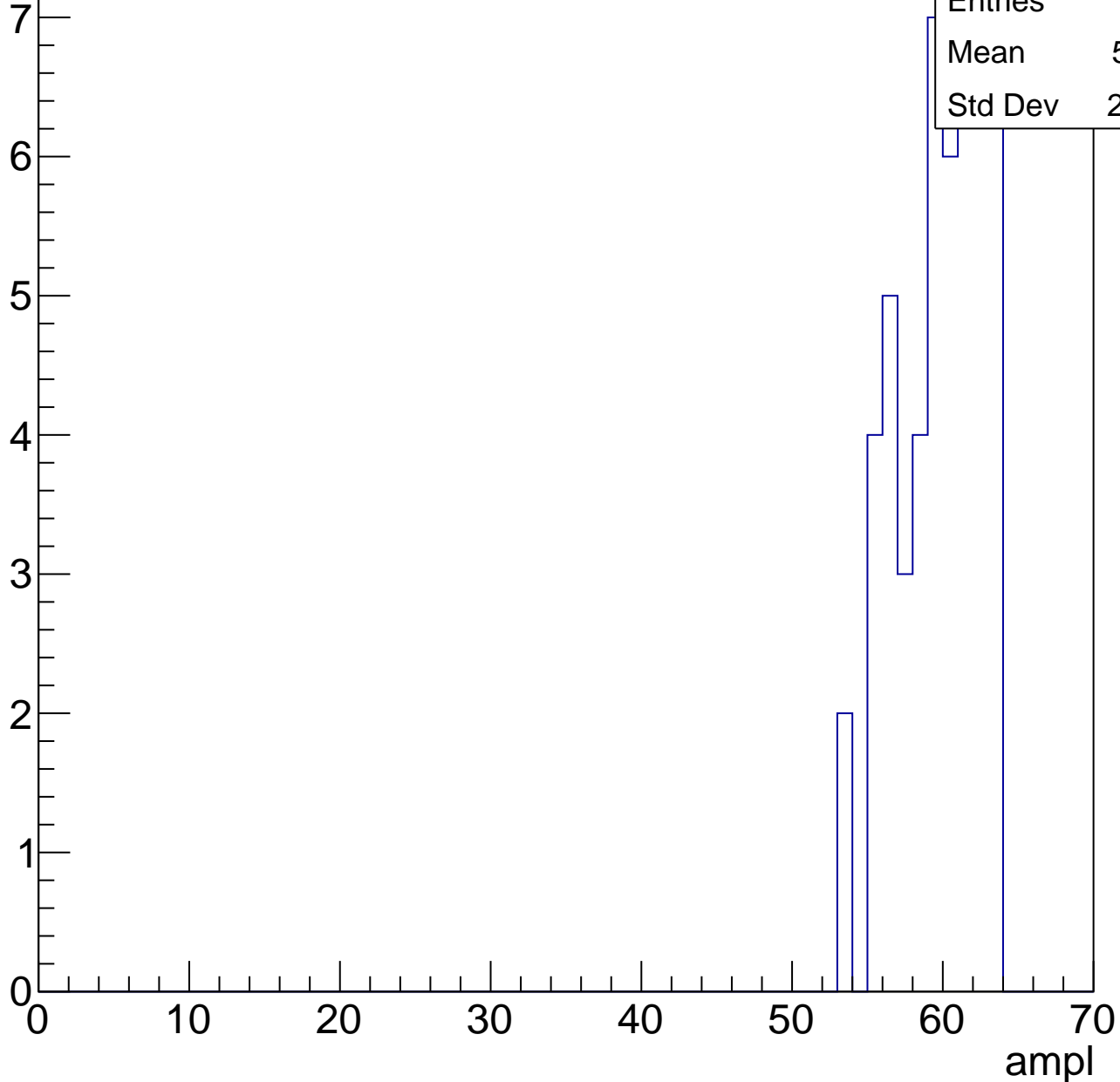
calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch77, adc5

calib_packv5_042523_0143.root, FC#4, port A2

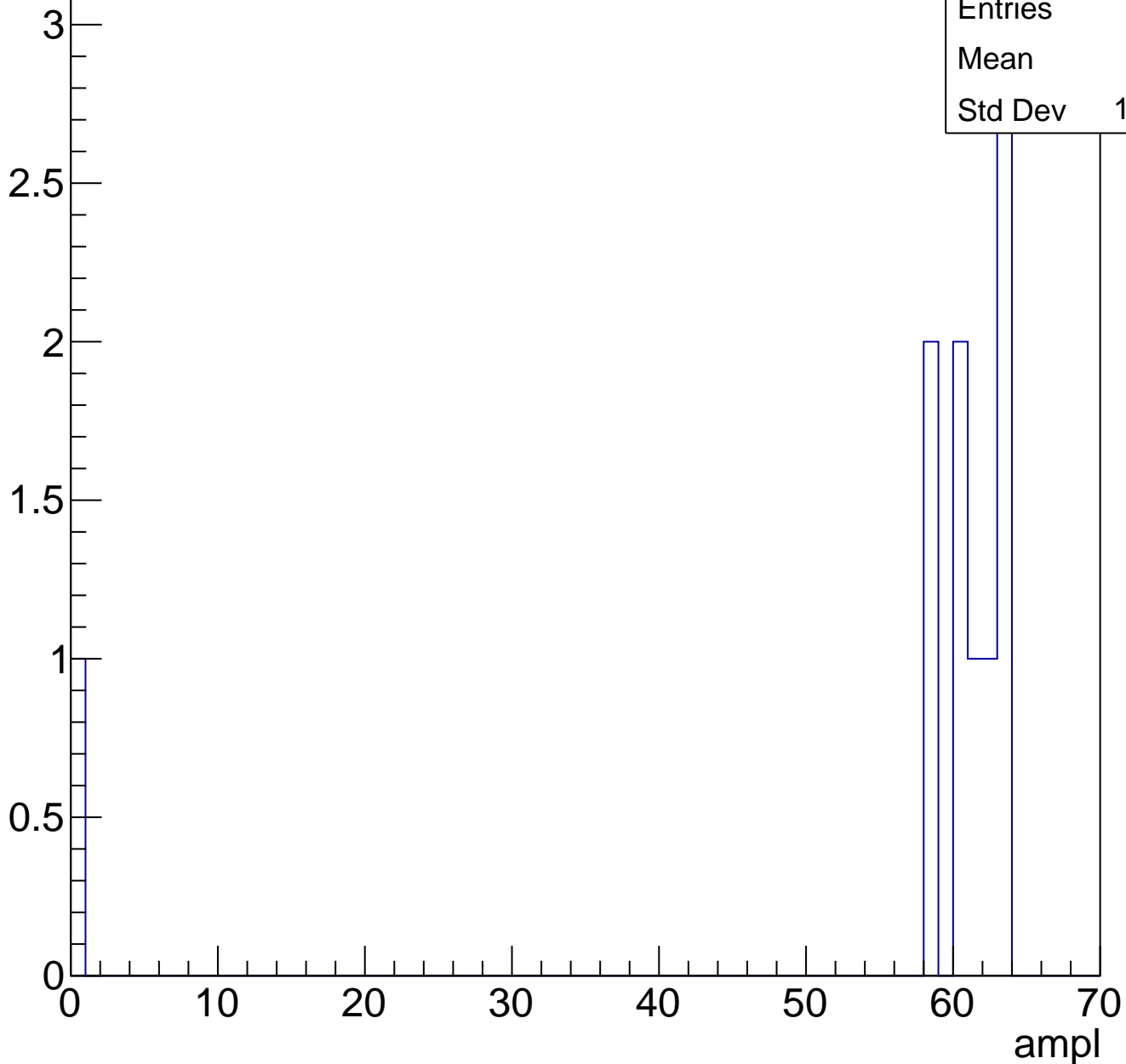
Entry



B1L100S, U1-ch77, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch77, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch78, adc0

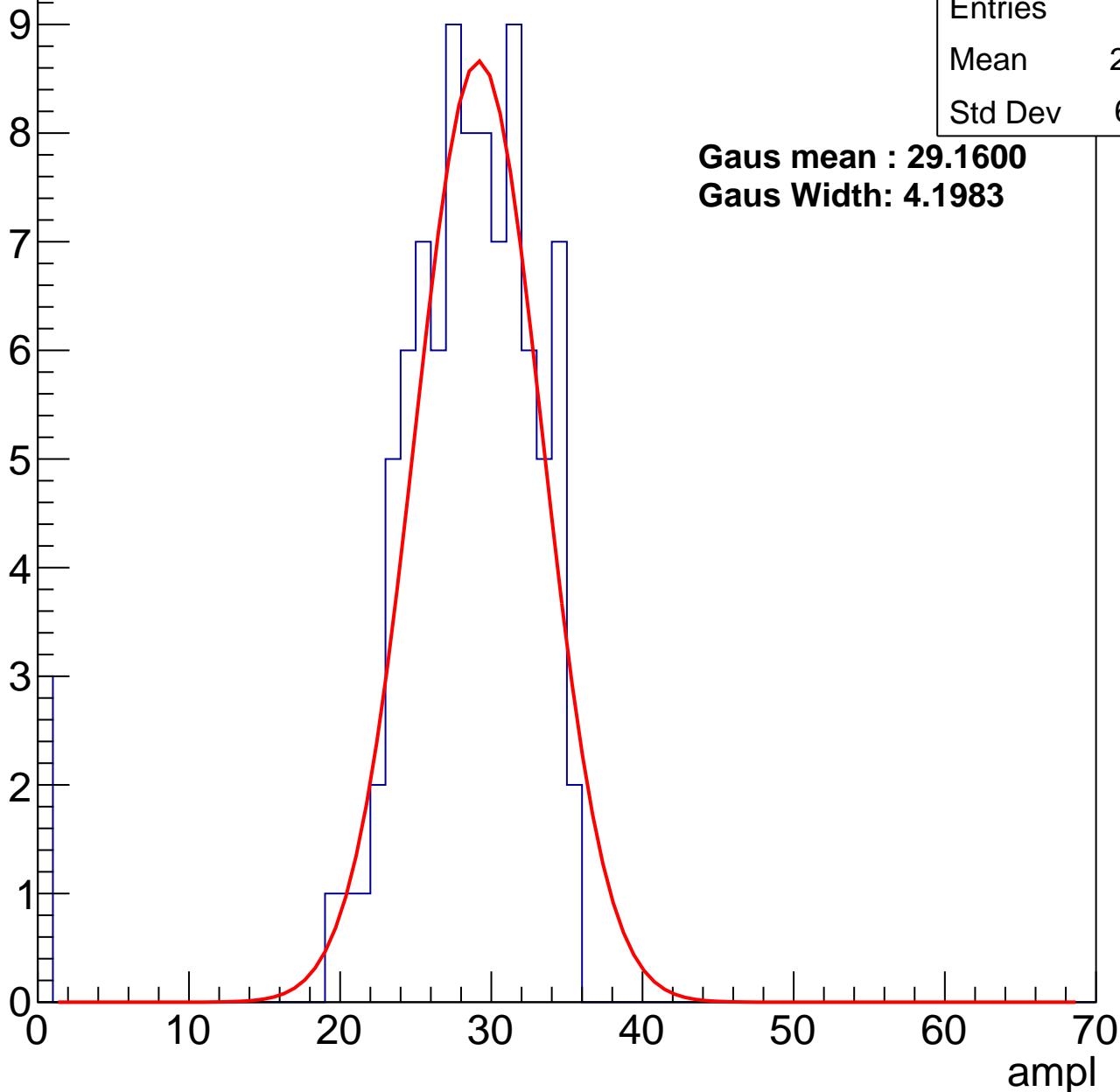
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	93
Mean	27.39
Std Dev	6.211

Gaus mean : 29.1600

Gaus Width: 4.1983



B1L100S, U1-ch78, adc1

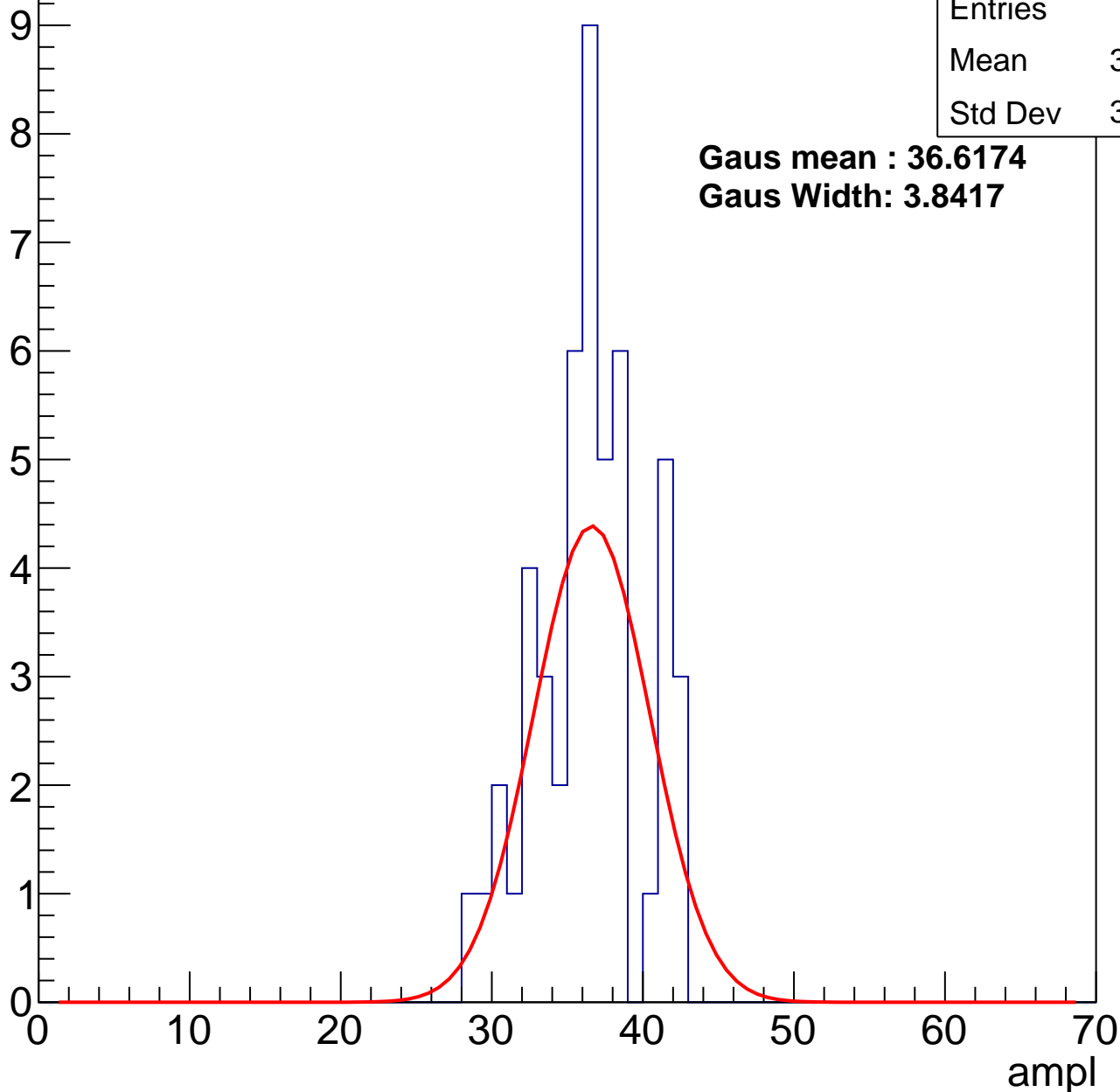
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	35.94
Std Dev	3.478

Gaus mean : 36.6174

Gaus Width: 3.8417



B1L100S, U1-ch78, adc2

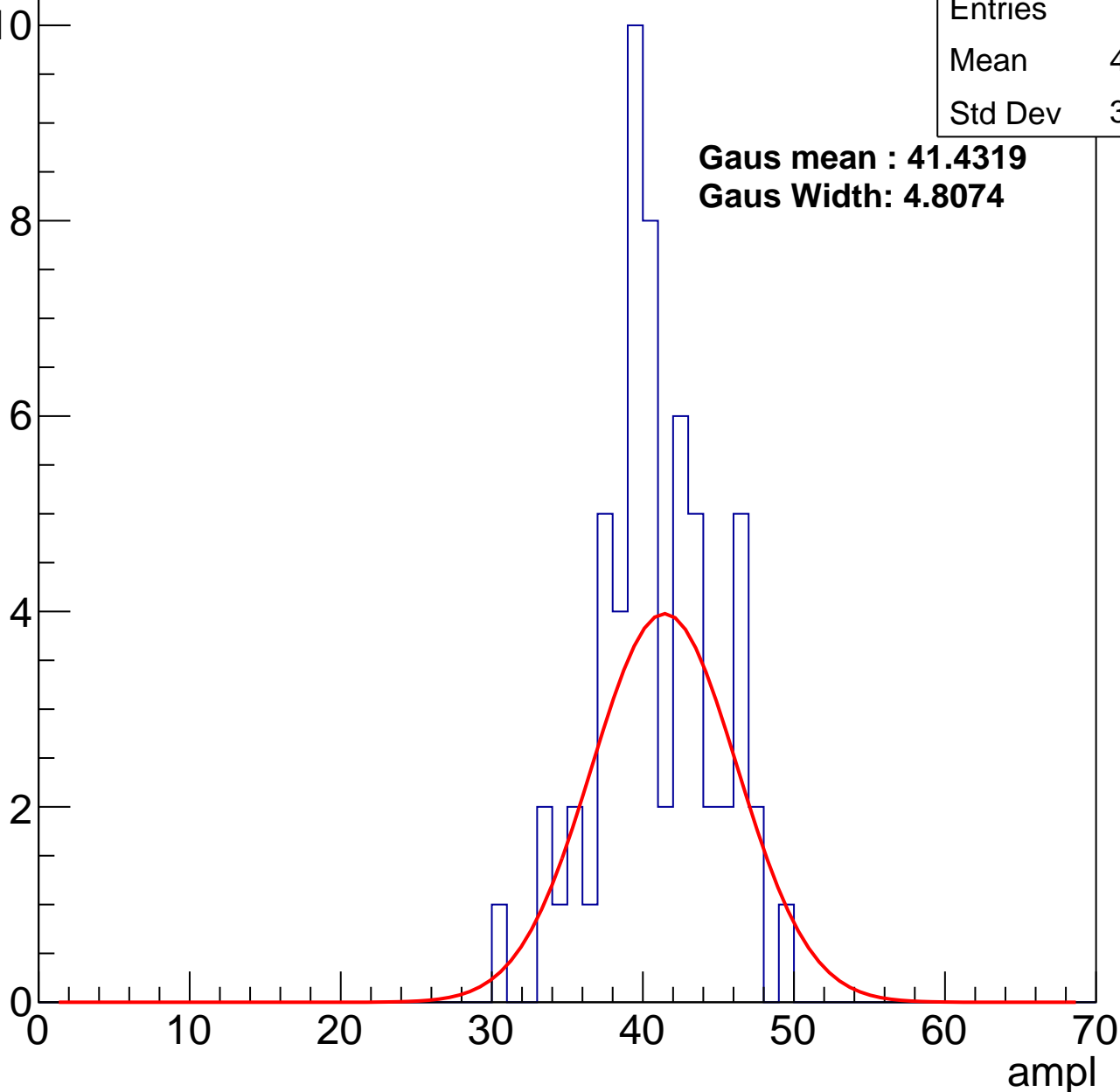
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	40.39
Std Dev	3.849

Gaus mean : 41.4319

Gaus Width: 4.8074



B1L100S, U1-ch78, adc3

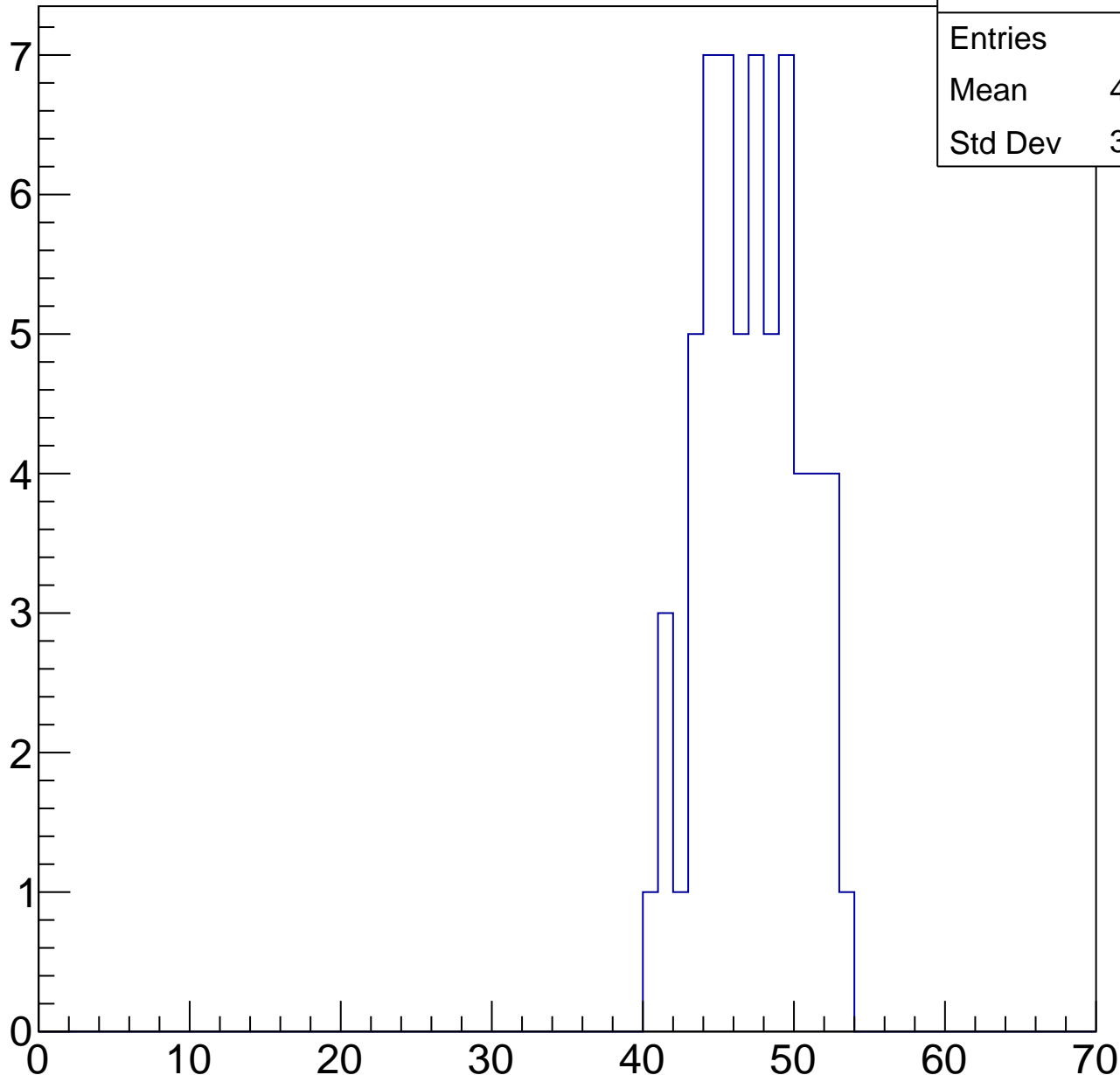
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	61
Mean	46.72
Std Dev	3.194

ampl

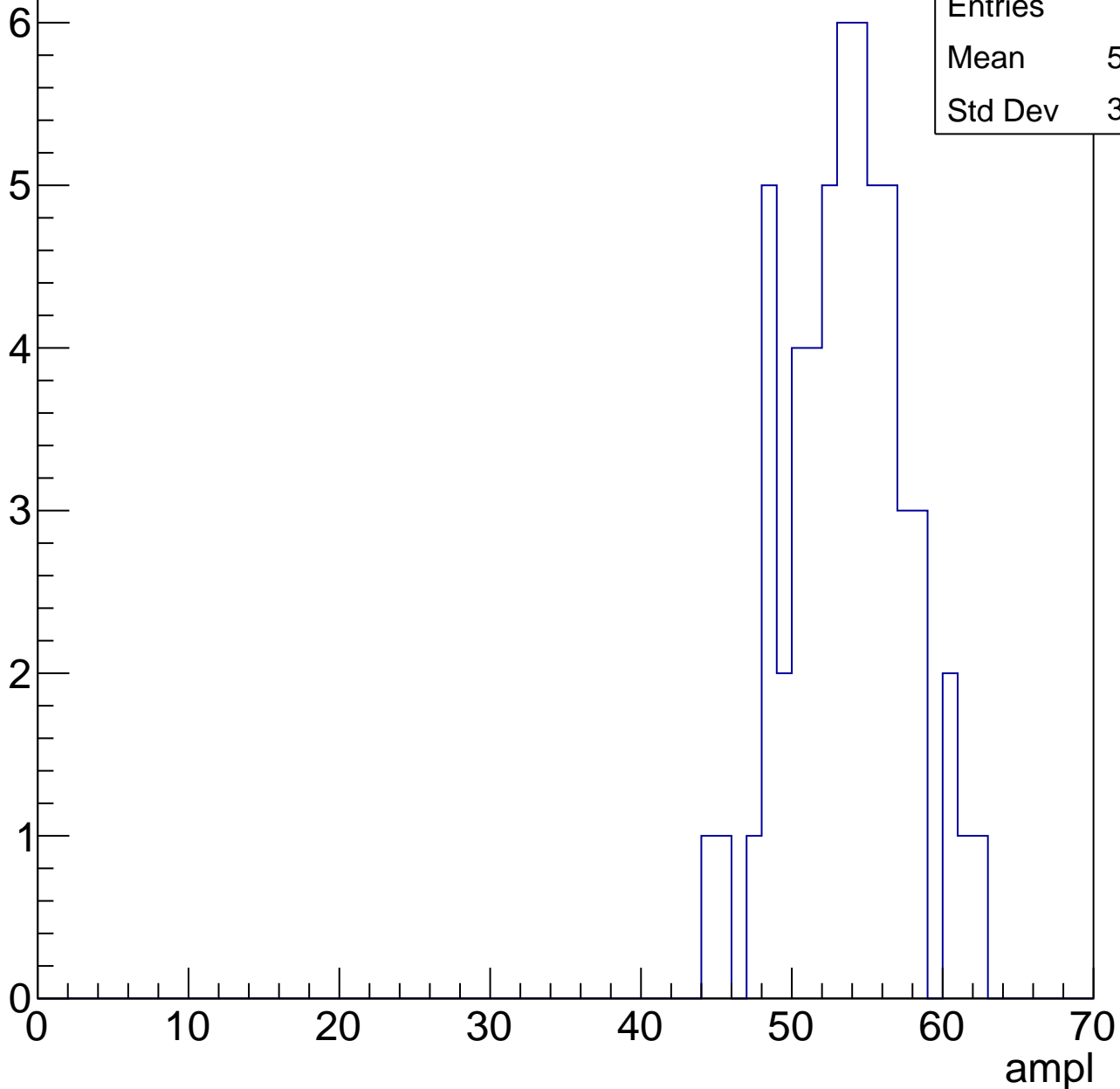


B1L100S, U1-ch78, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

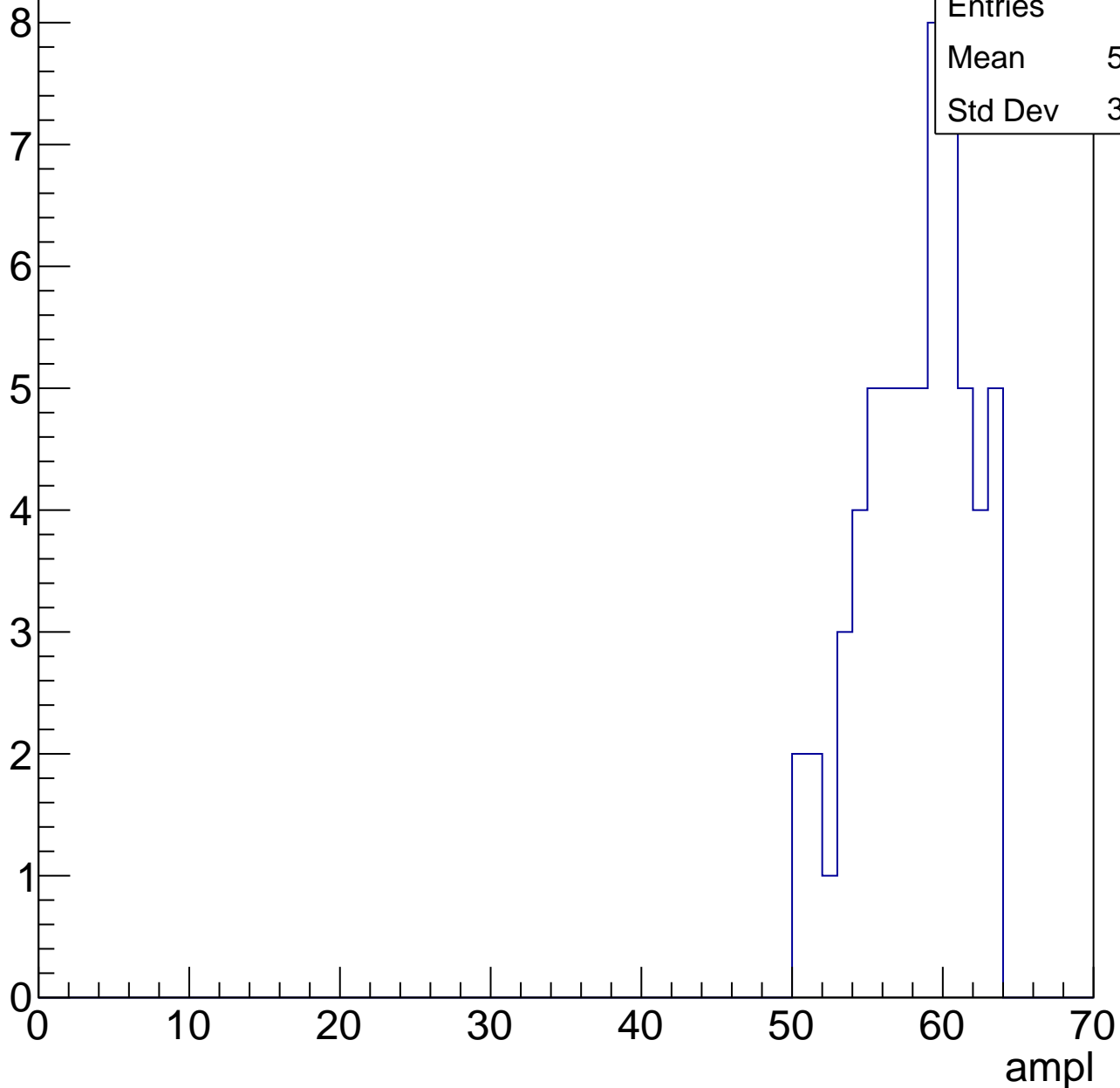
Entries	55
Mean	53.15
Std Dev	3.887



B1L100S, U1-ch78, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



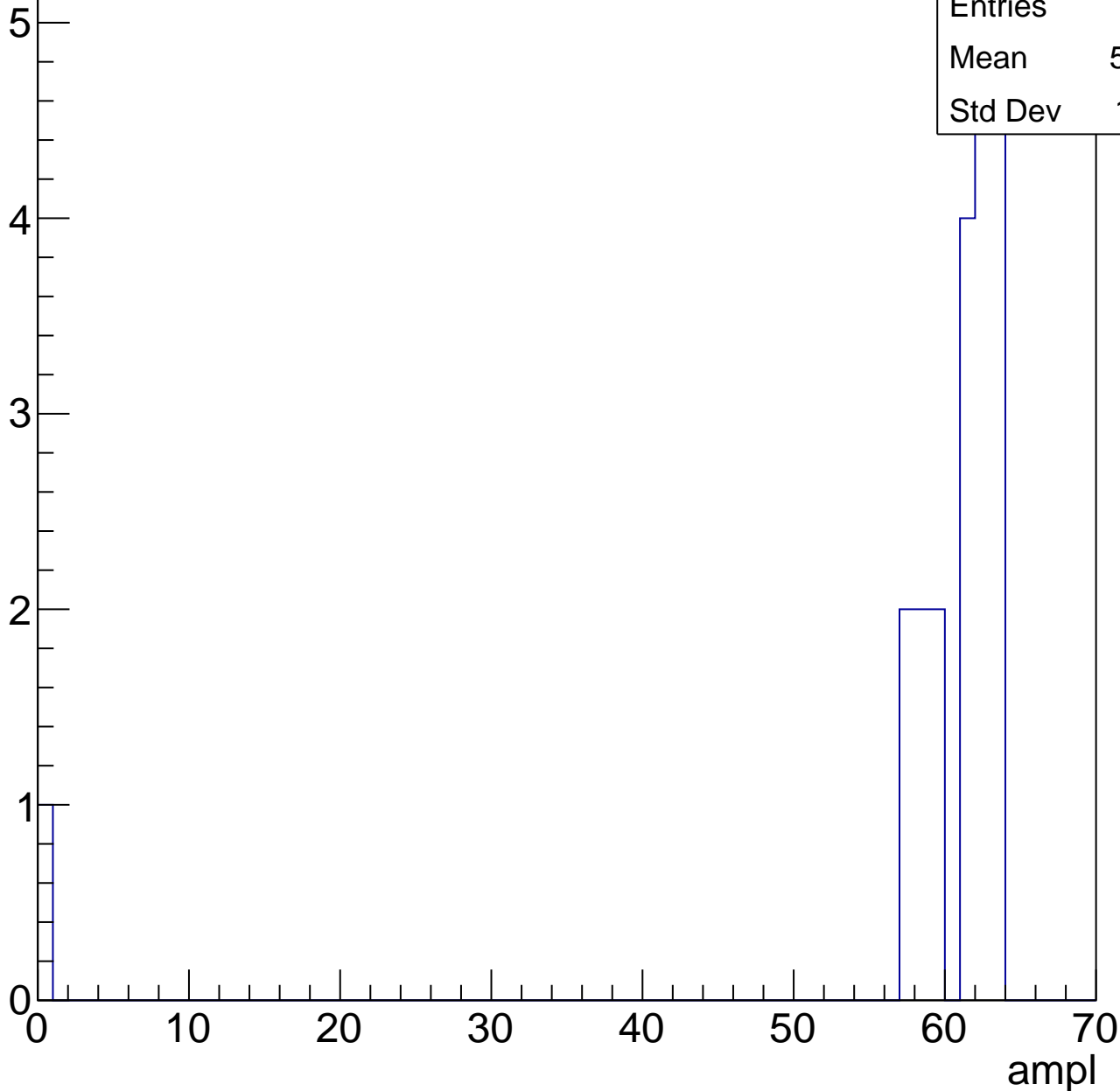
Entries	62
Mean	57.73
Std Dev	3.456

B1L100S, U1-ch78, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	57.95
Std Dev	13.11



B1L100S, U1-ch78, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch79, adc0

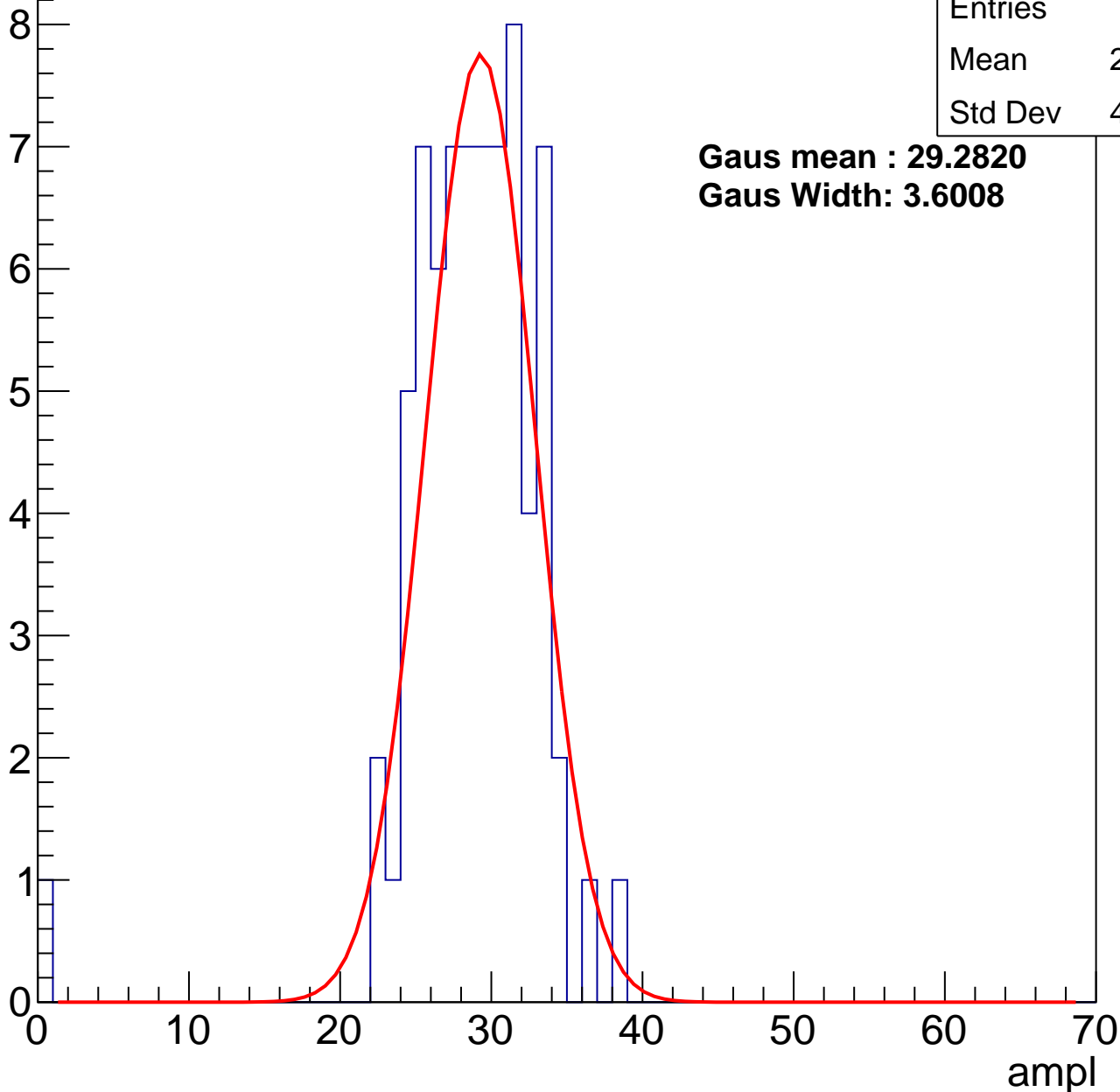
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.29
Std Dev	4.727

Gaus mean : 29.2820

Gaus Width: 3.6008



B1L100S, U1-ch79, adc1

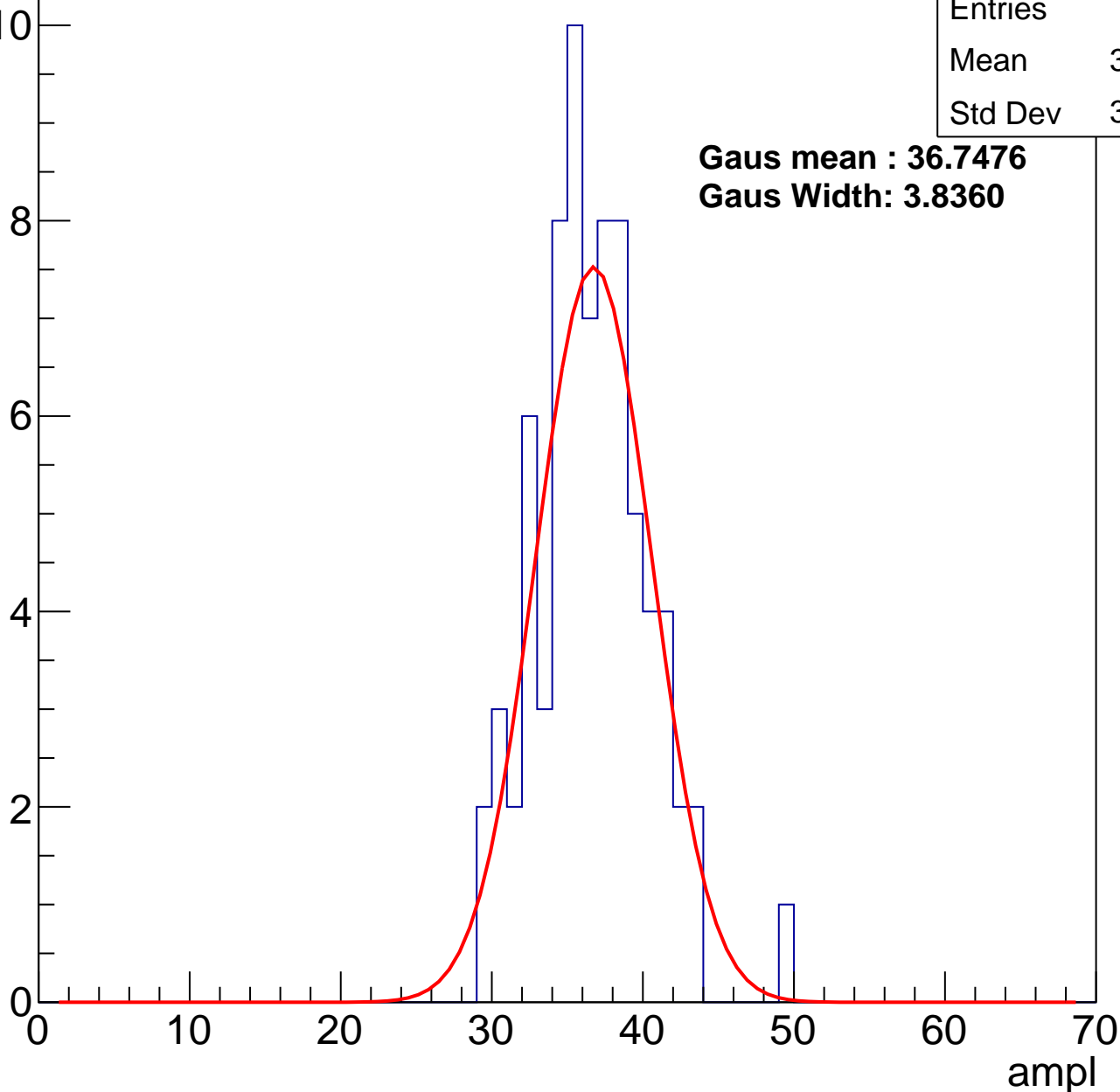
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.17
Std Dev	3.664

Gaus mean : 36.7476

Gaus Width: 3.8360



B1L100S, U1-ch79, adc2

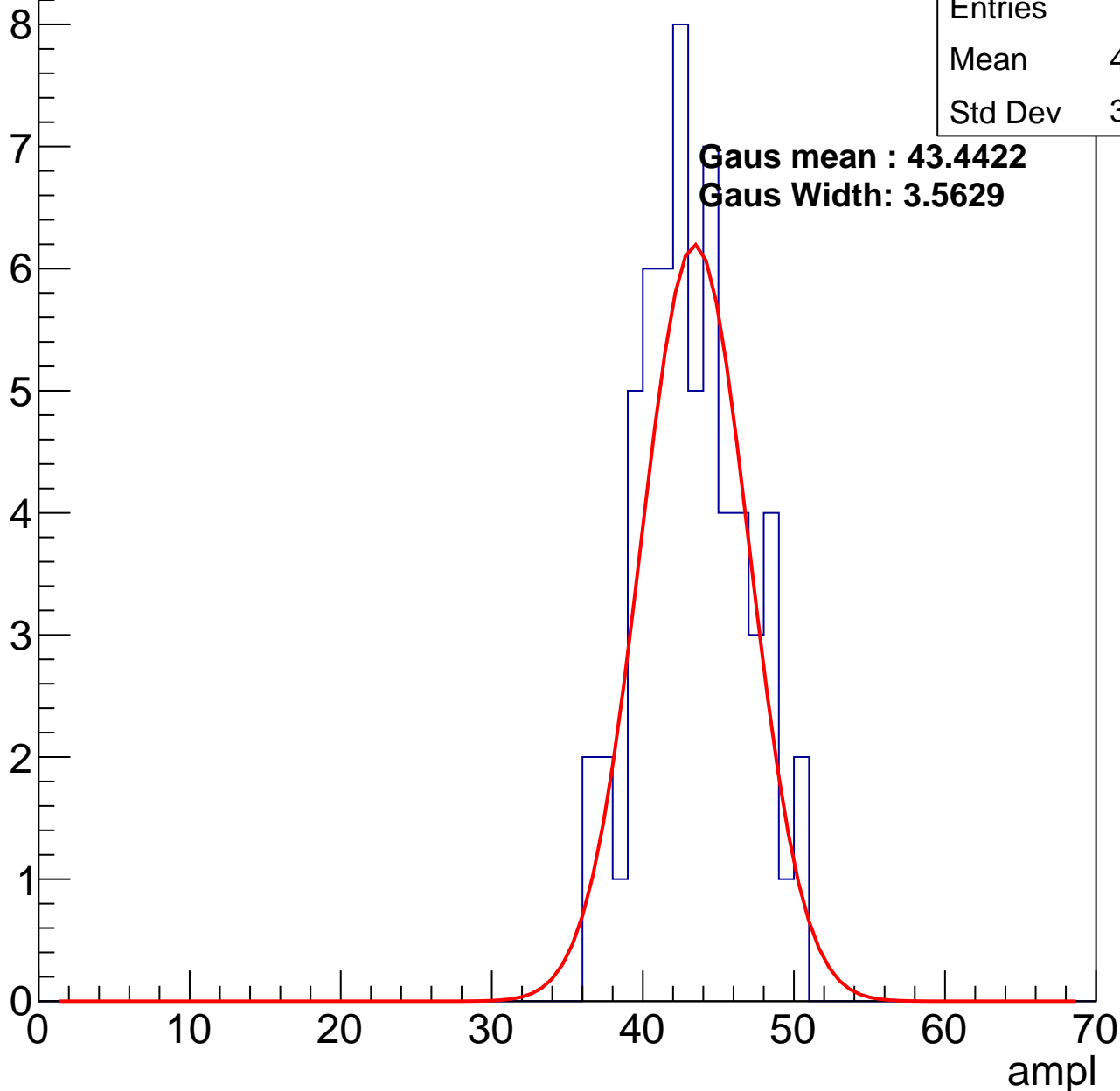
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	42.83
Std Dev	3.416

Gaus mean : 43.4422

Gaus Width: 3.5629

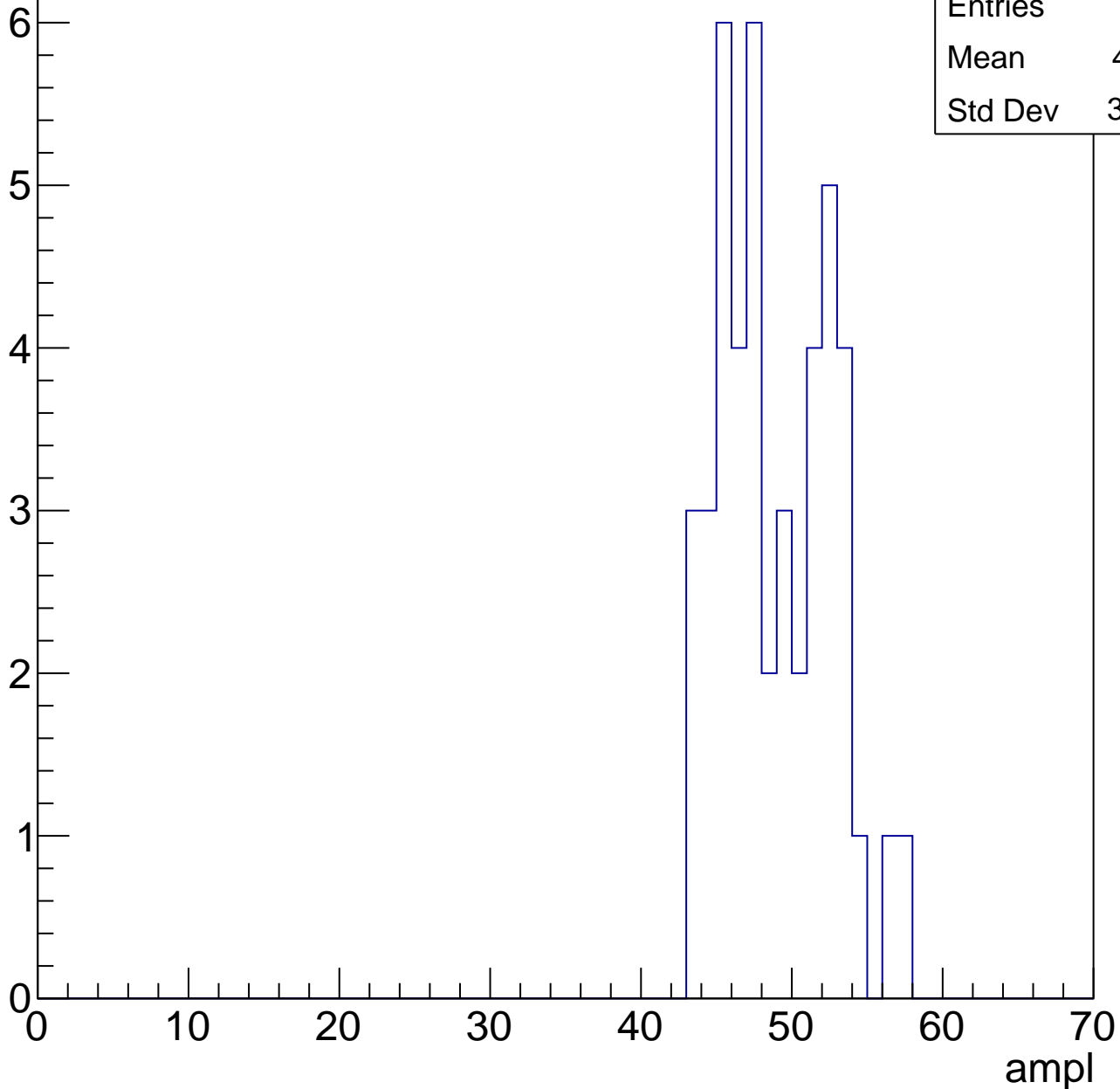


B1L100S, U1-ch79, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	48.51
Std Dev	3.637

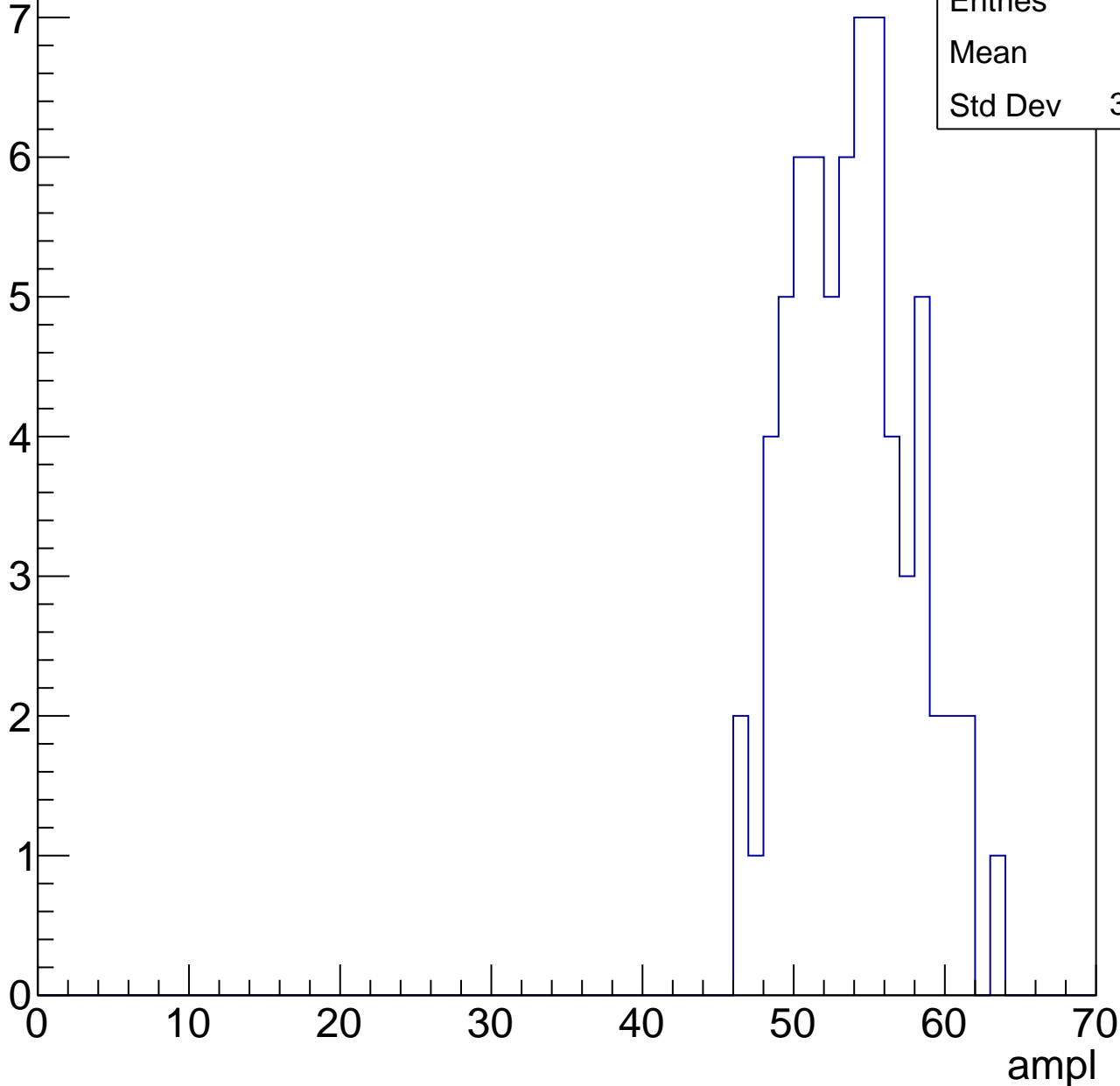


B1L100S, U1-ch79, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	53.4
Std Dev	3.889

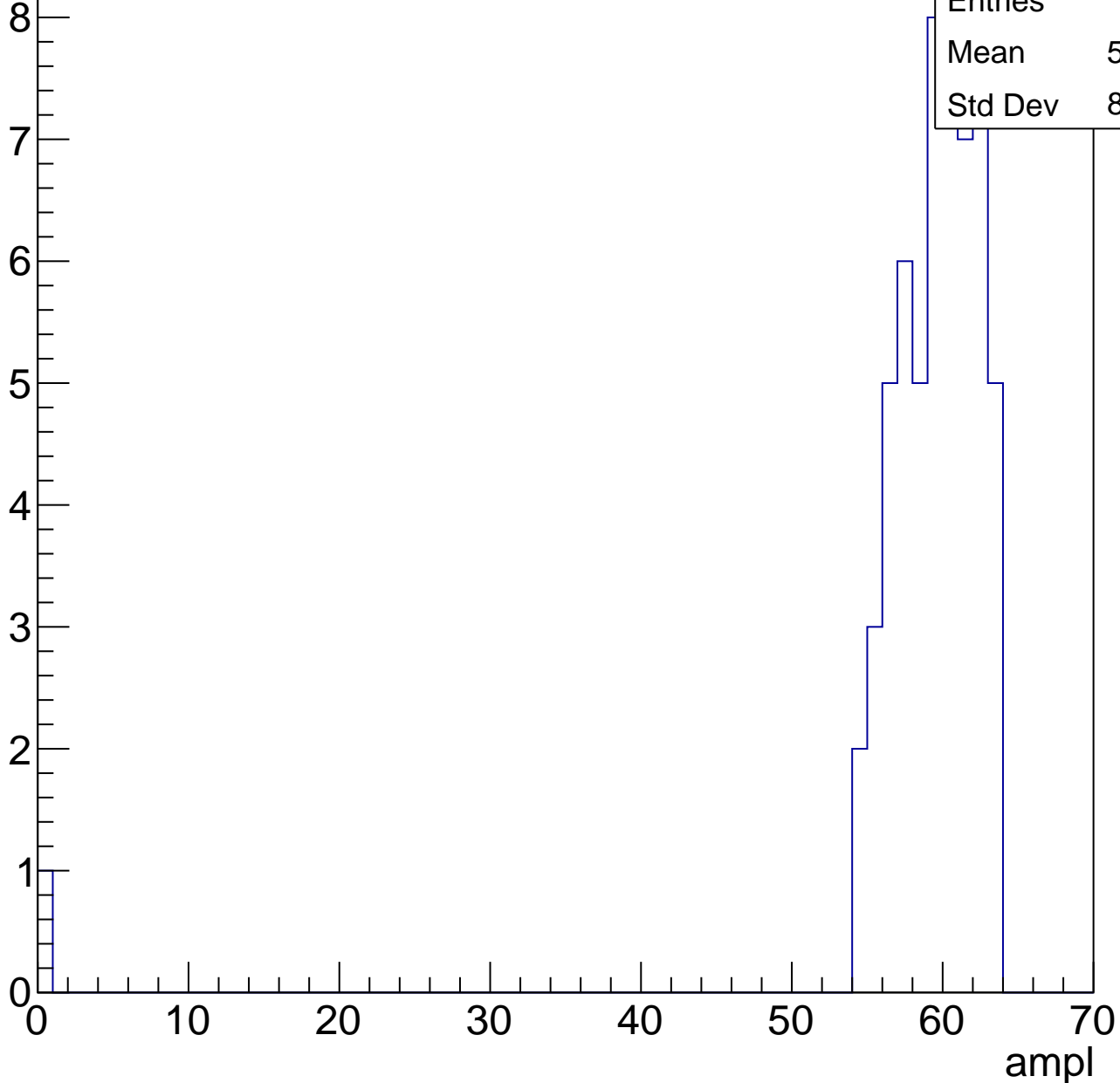


B1L100S, U1-ch79, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

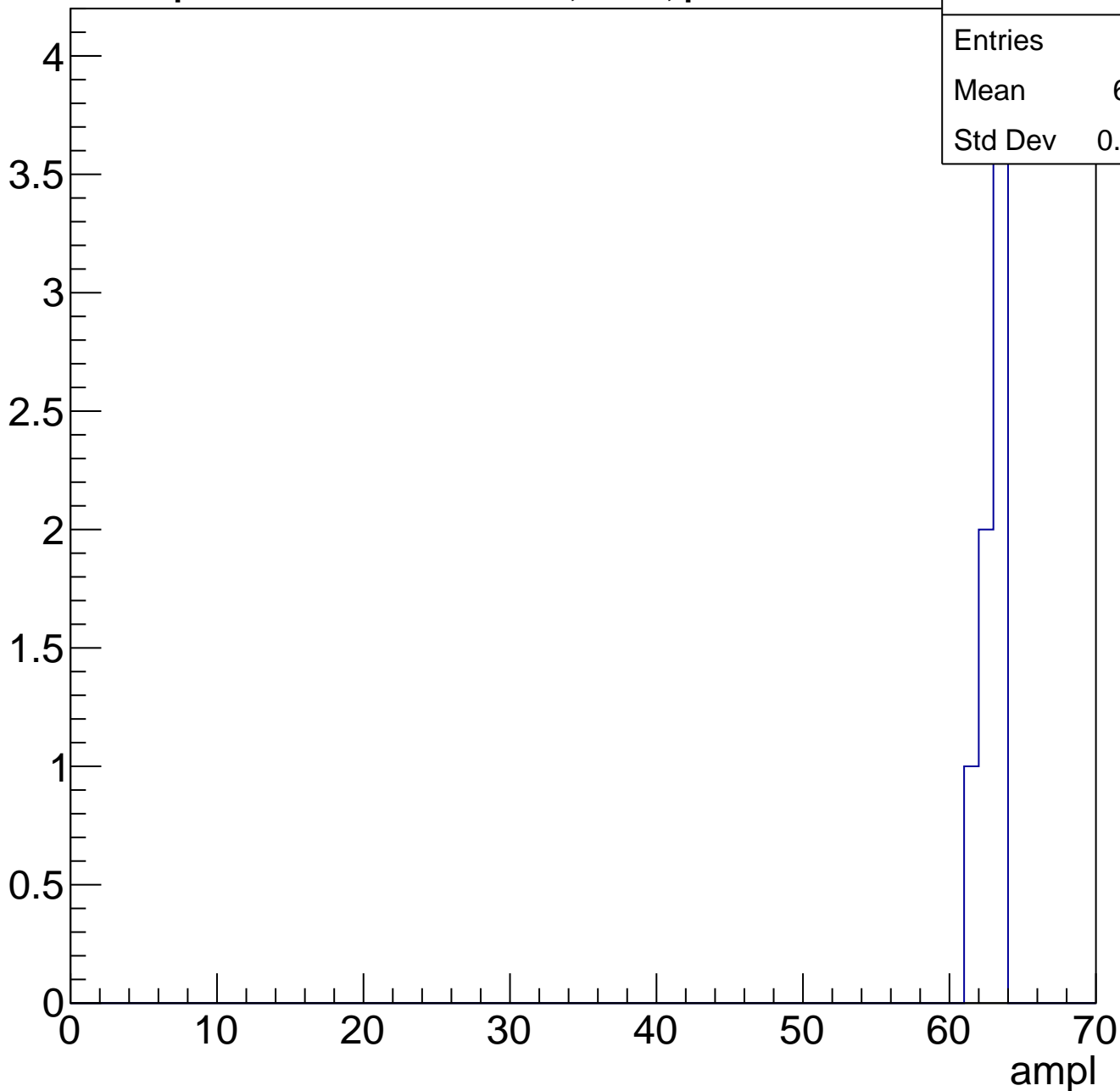
Entries	58
Mean	58.19
Std Dev	8.097



B1L100S, U1-ch79, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch79, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch80, adc0

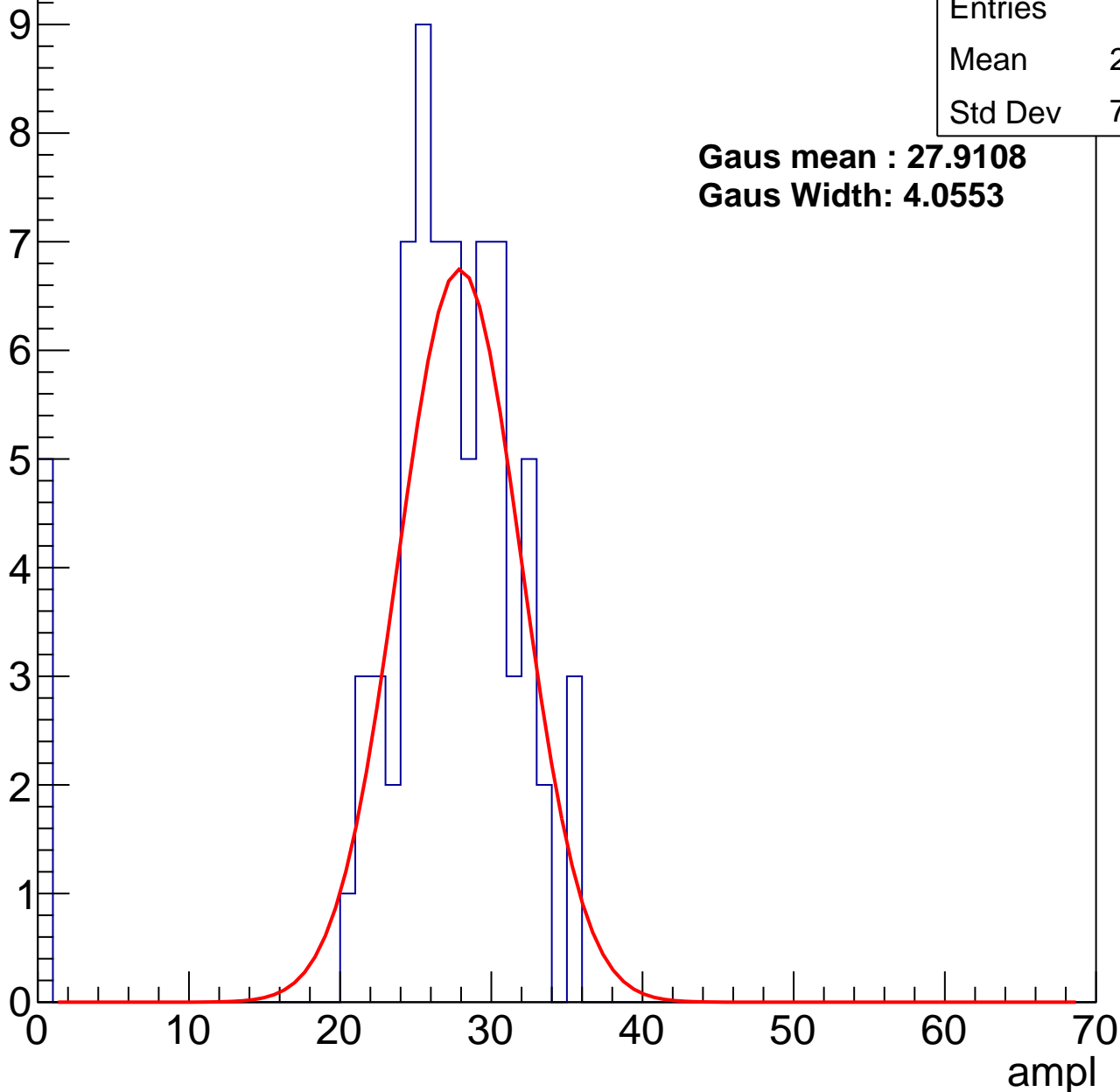
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	76
Mean	25.47
Std Dev	7.584

Gaus mean : 27.9108

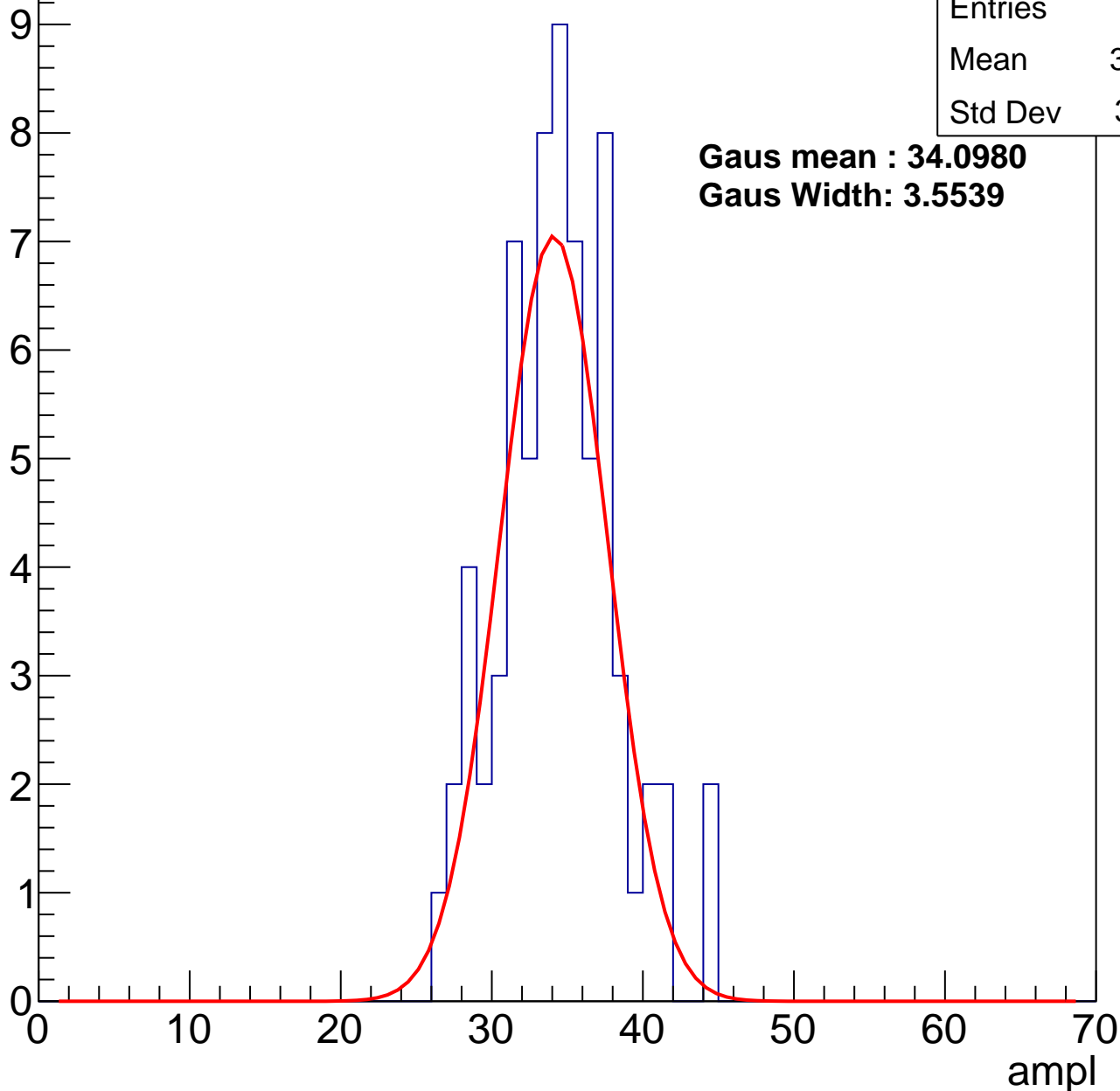
Gaus Width: 4.0553



B1L100S, U1-ch80, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	71
Mean	33.96
Std Dev	3.821

Gaus mean : 34.0980

Gaus Width: 3.5539

B1L100S, U1-ch80, adc2

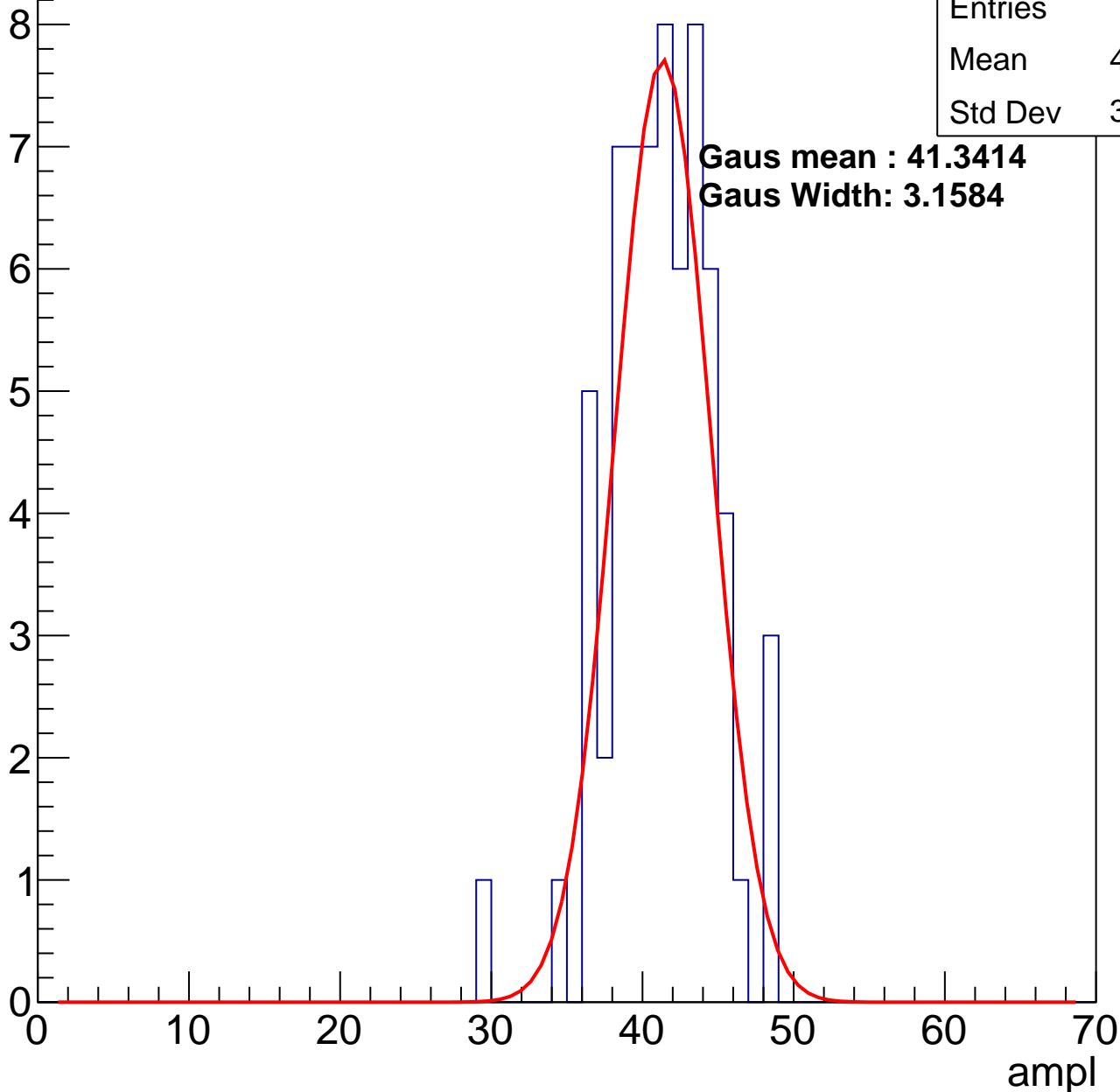
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	40.82
Std Dev	3.424

Gaus mean : 41.3414

Gaus Width: 3.1584



B1L100S, U1-ch80, adc3

calib_packv5_042523_0143.root, FC#4, port A2

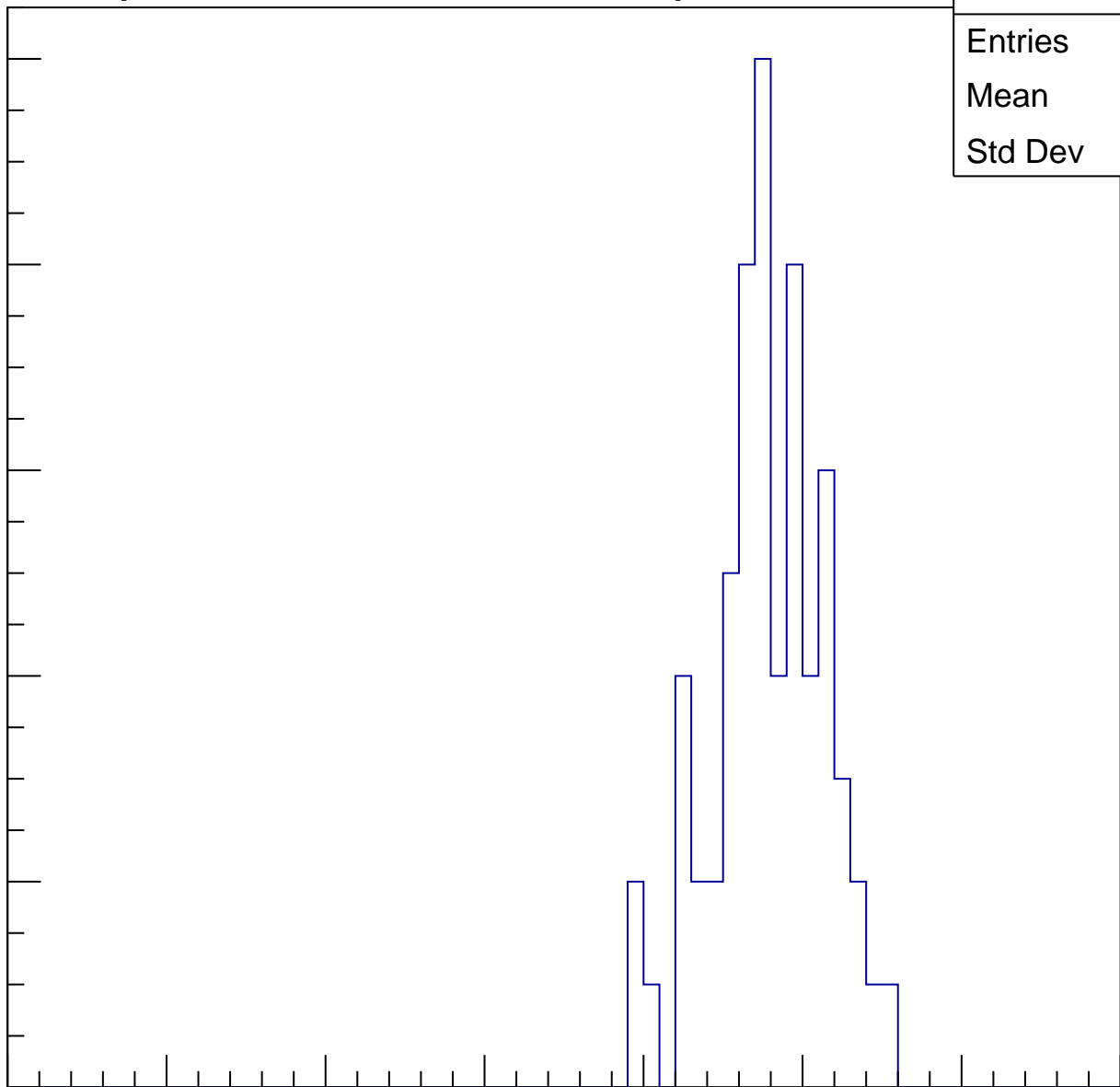
Entries	63
Mean	47.37
Std Dev	3.506

Entry

10
8
6
4
2
0

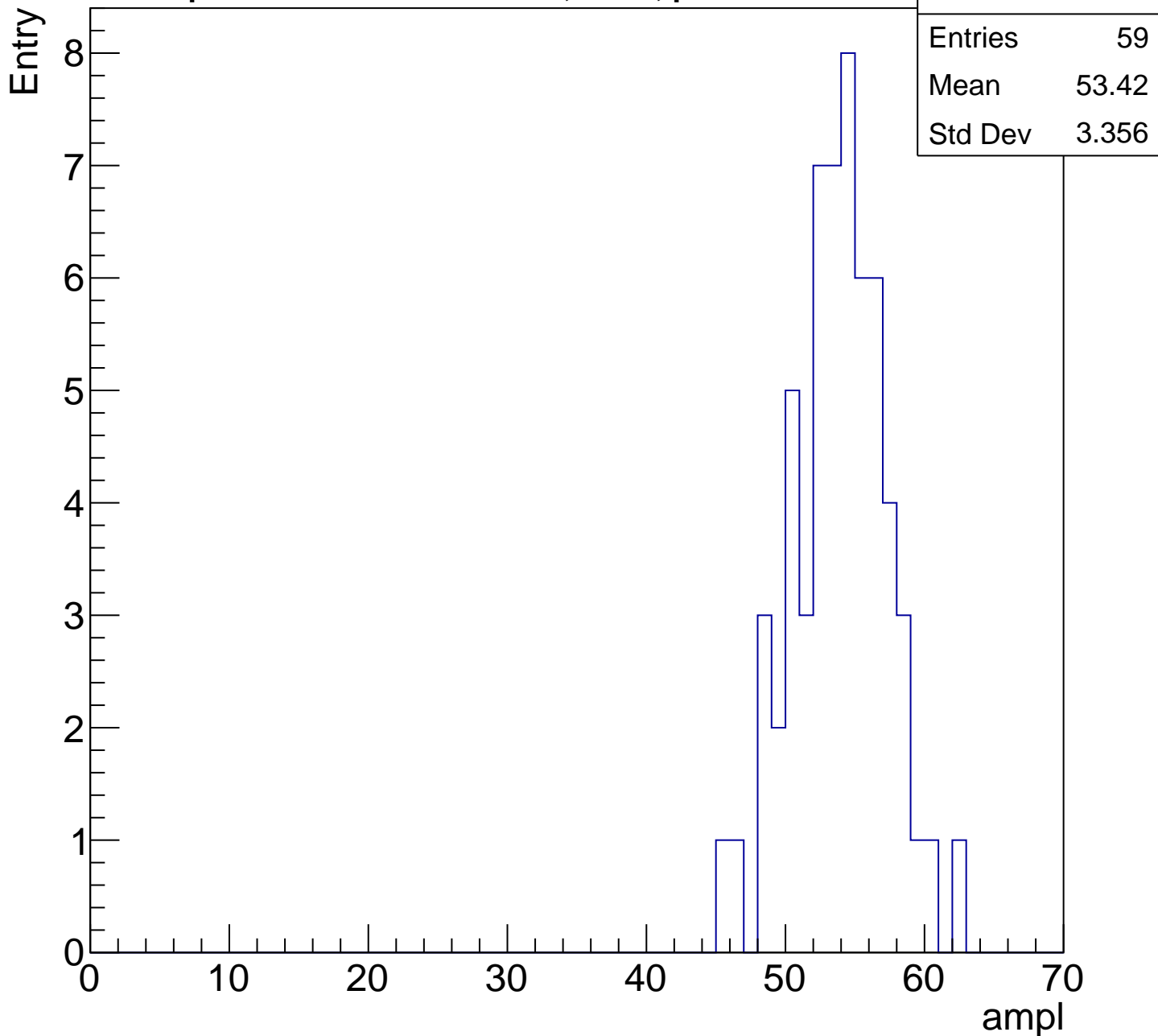
0 10 20 30 40 50 60 70

ampl



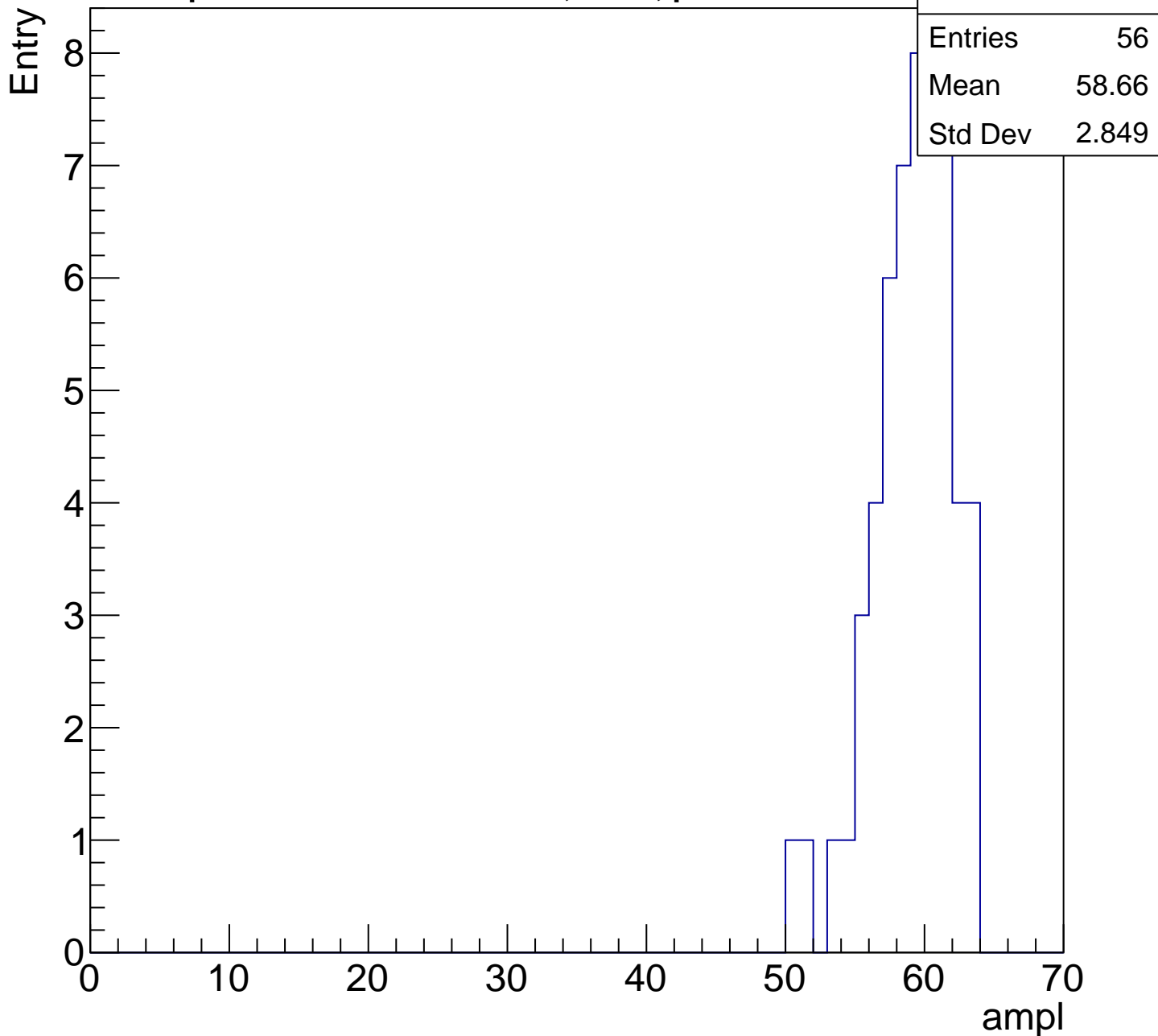
B1L100S, U1-ch80, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch80, adc5

calib_packv5_042523_0143.root, FC#4, port A2

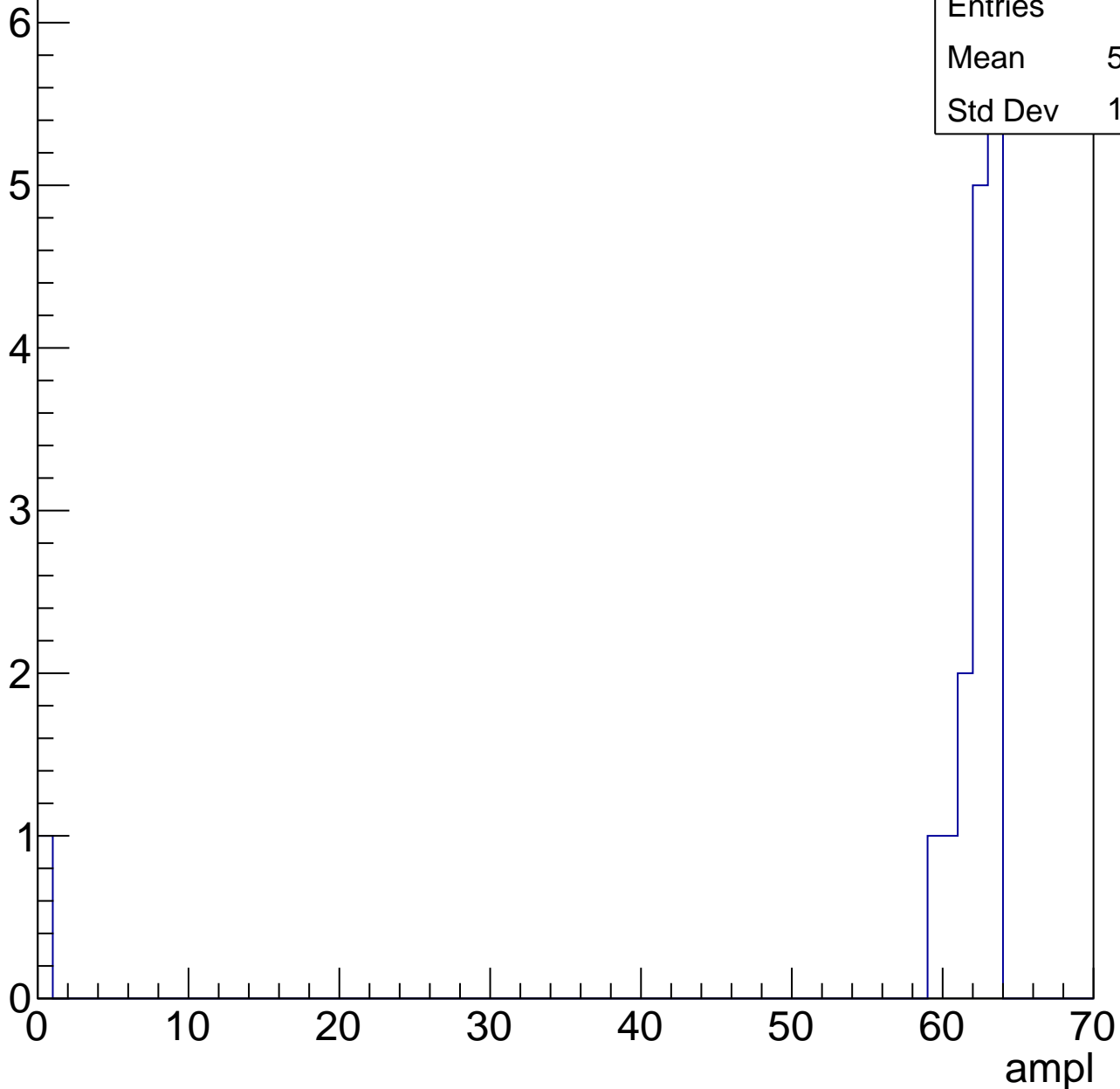


B1L100S, U1-ch80, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	58.06
Std Dev	15.04



B1L100S, U1-ch80, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch81, adc0

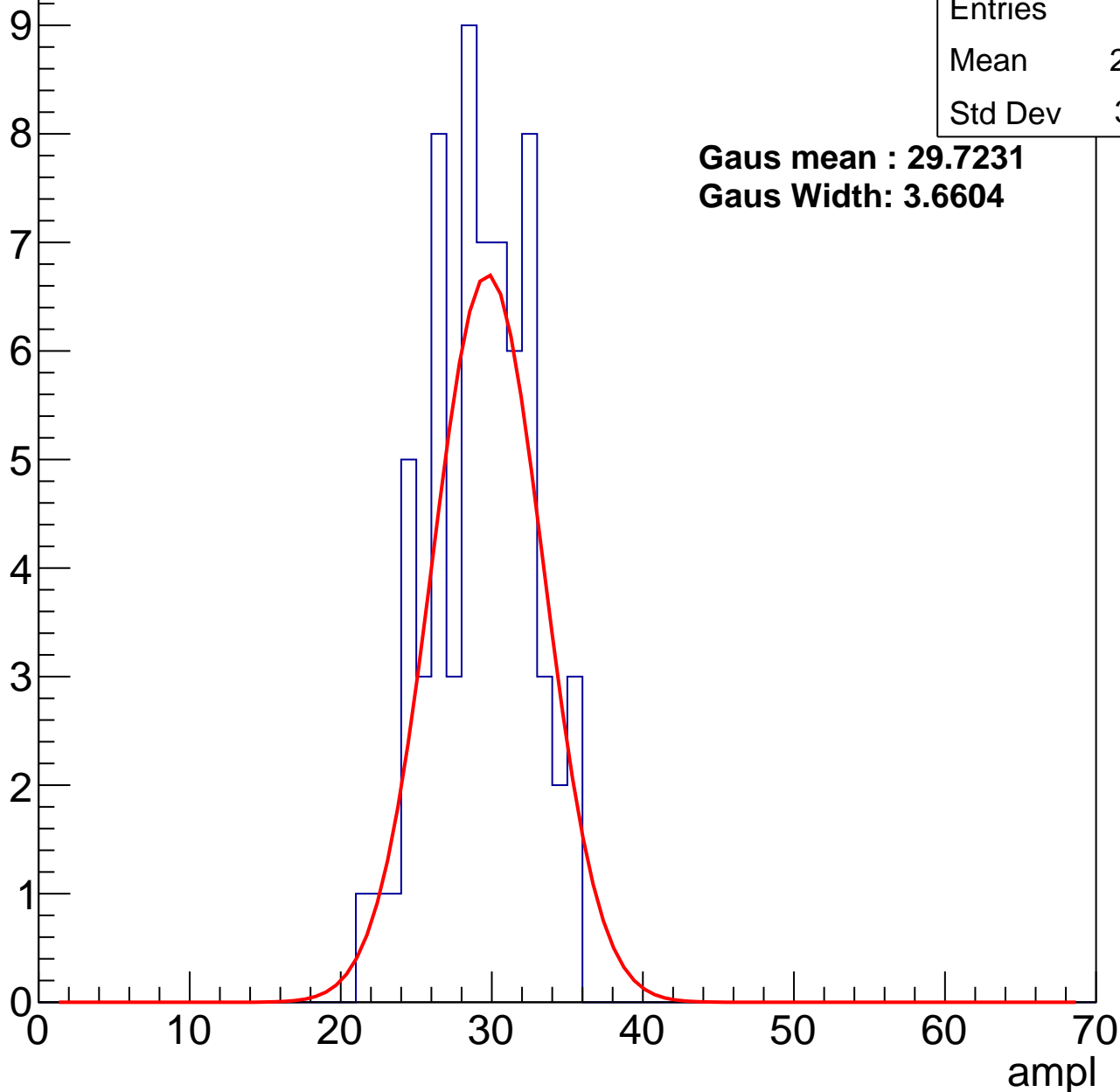
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	28.79
Std Dev	3.281

Gaus mean : 29.7231

Gaus Width: 3.6604



B1L100S, U1-ch81, adc1

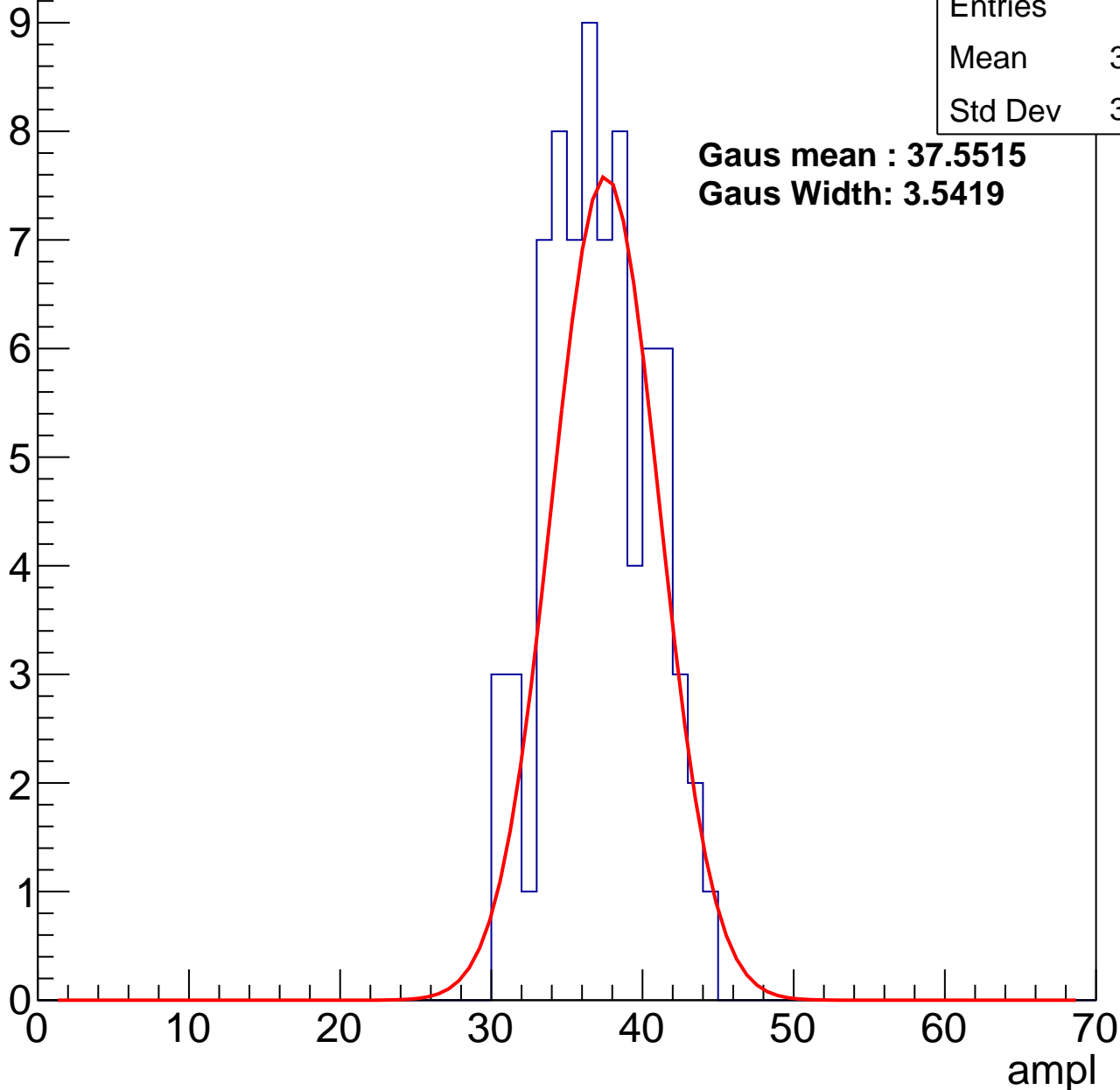
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.64
Std Dev	3.389

Gaus mean : 37.5515

Gaus Width: 3.5419



B1L100S, U1-ch81, adc2

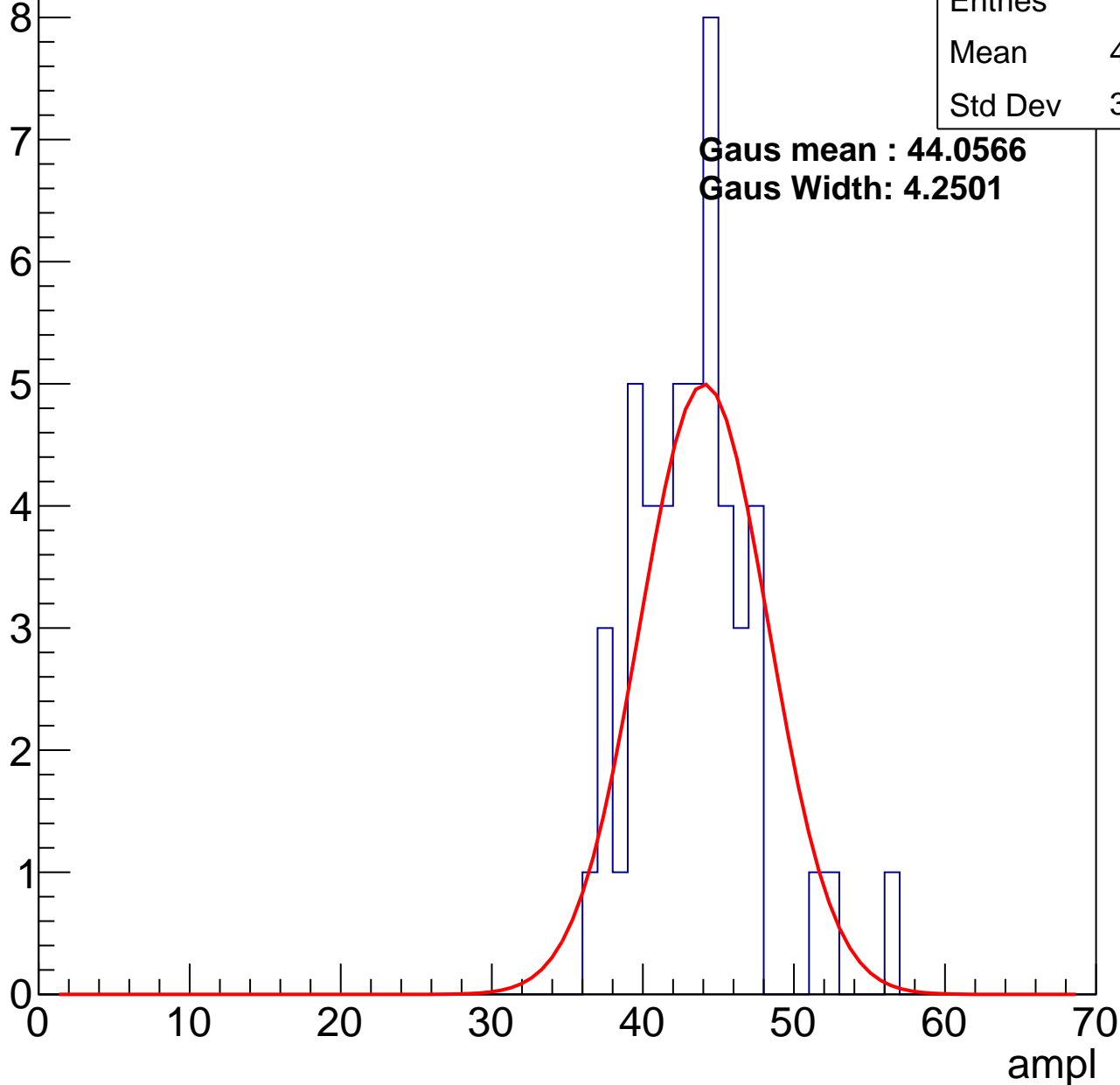
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	42.92
Std Dev	3.867

Gaus mean : 44.0566

Gaus Width: 4.2501

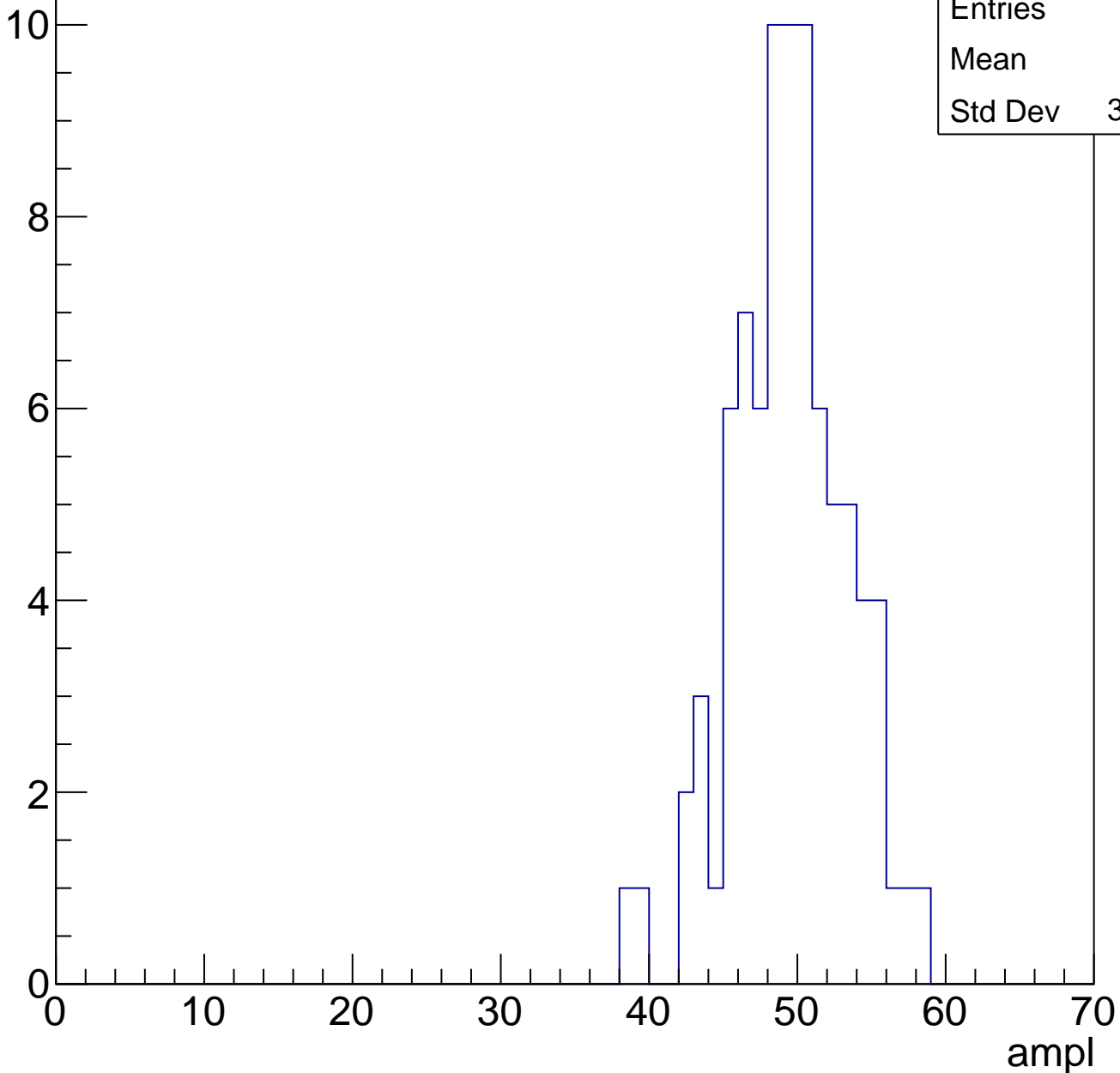


B1L100S, U1-ch81, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	49
Std Dev	3.845

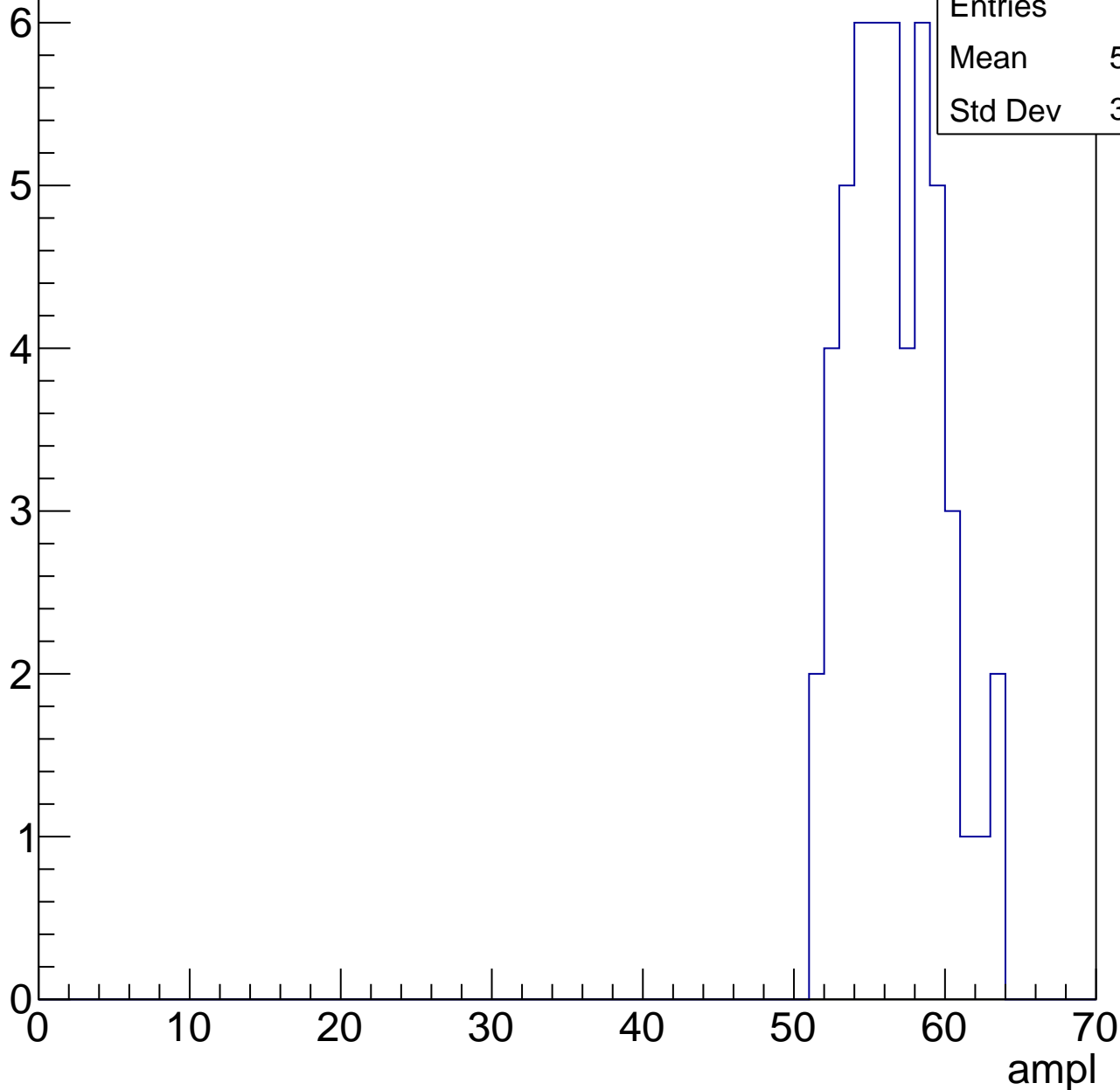
Entry



B1L100S, U1-ch81, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



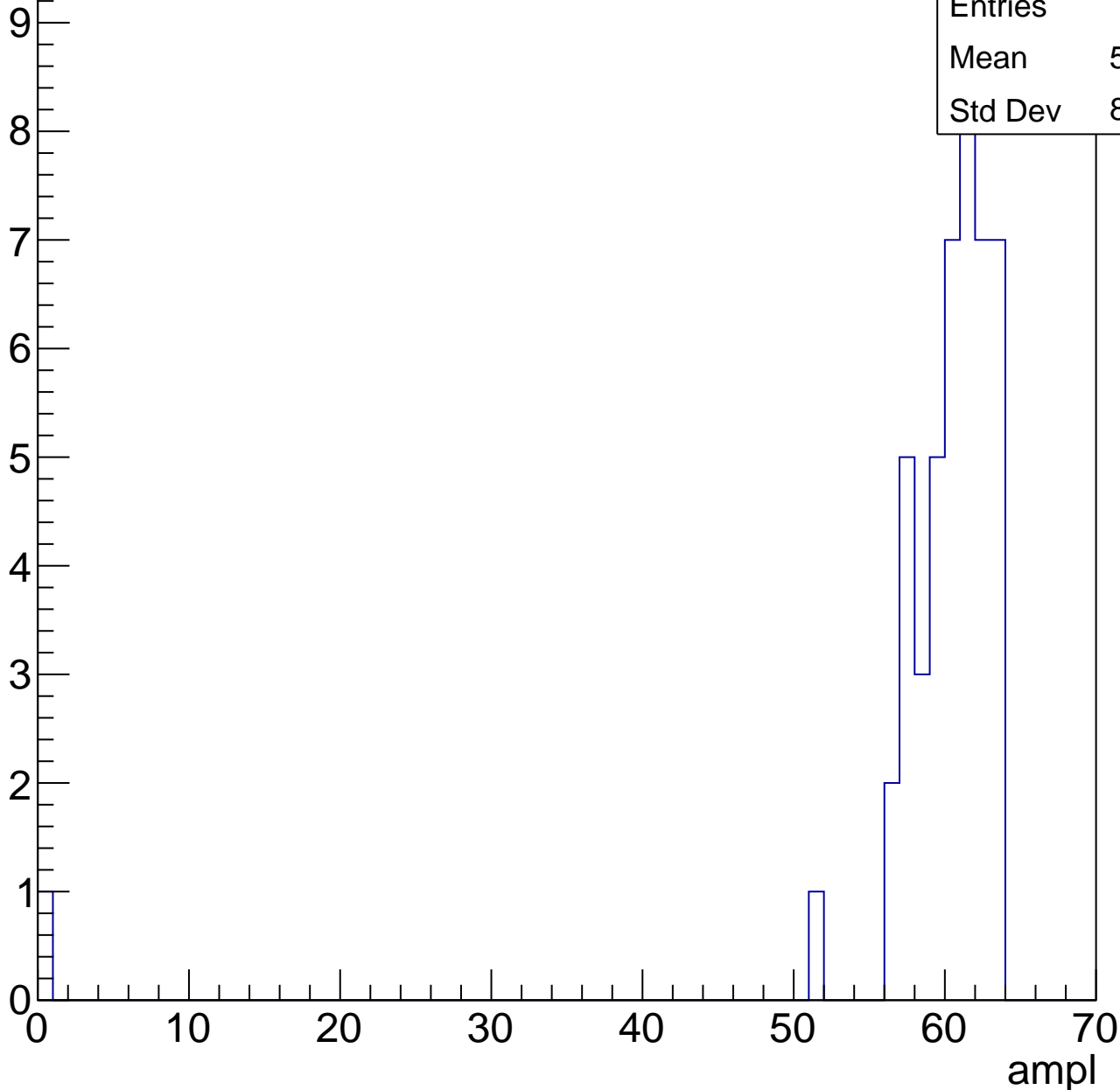
Entries	51
Mean	56.18
Std Dev	3.027

B1L100S, U1-ch81, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	58.74
Std Dev	8.993



B1L100S, U1-ch81, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch81, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch82, adc0

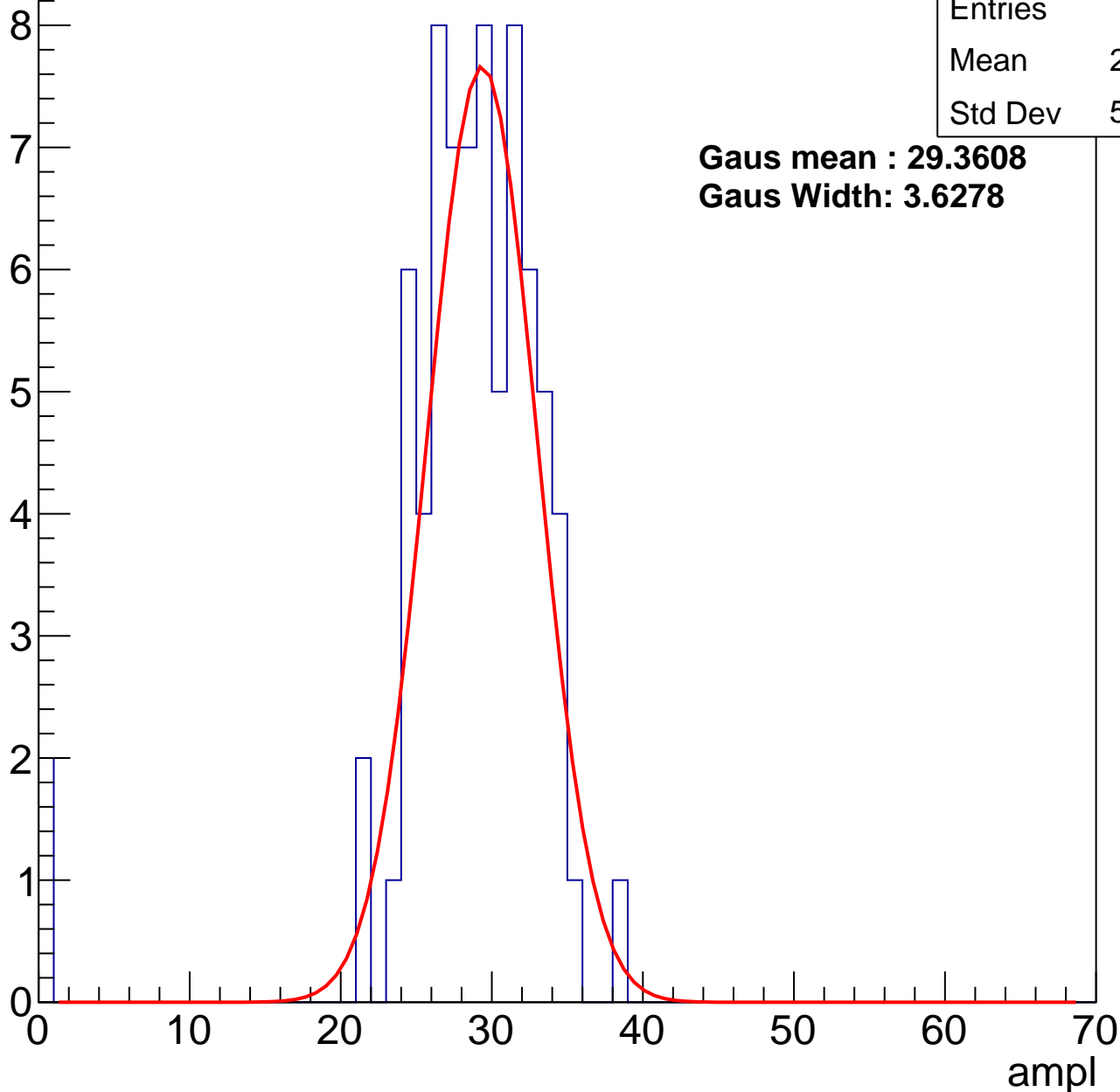
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	27.97
Std Dev	5.746

Gaus mean : 29.3608

Gaus Width: 3.6278



B1L100S, U1-ch82, adc1

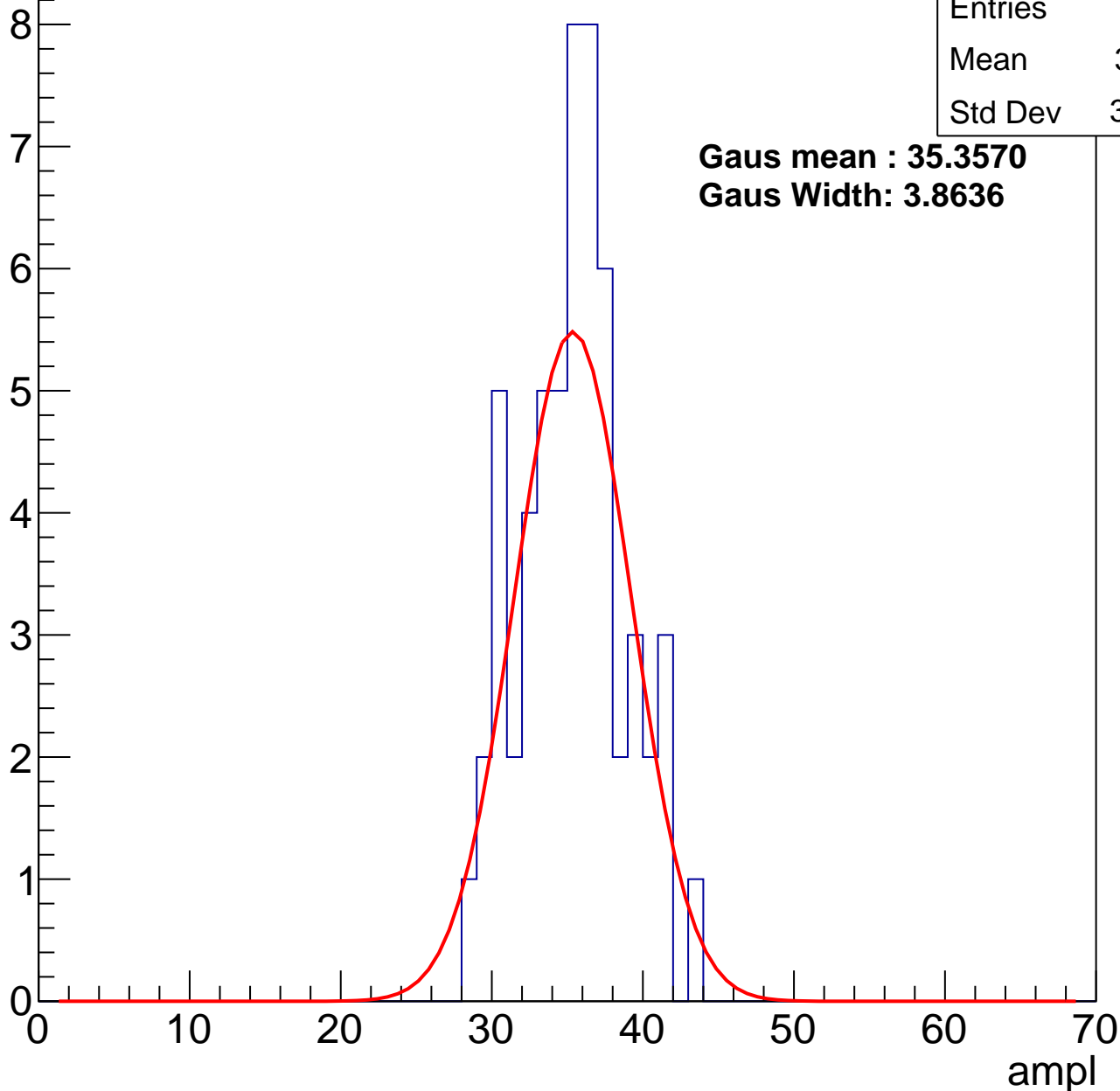
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	34.91
Std Dev	3.399

Gaus mean : 35.3570

Gaus Width: 3.8636



B1L100S, U1-ch82, adc2

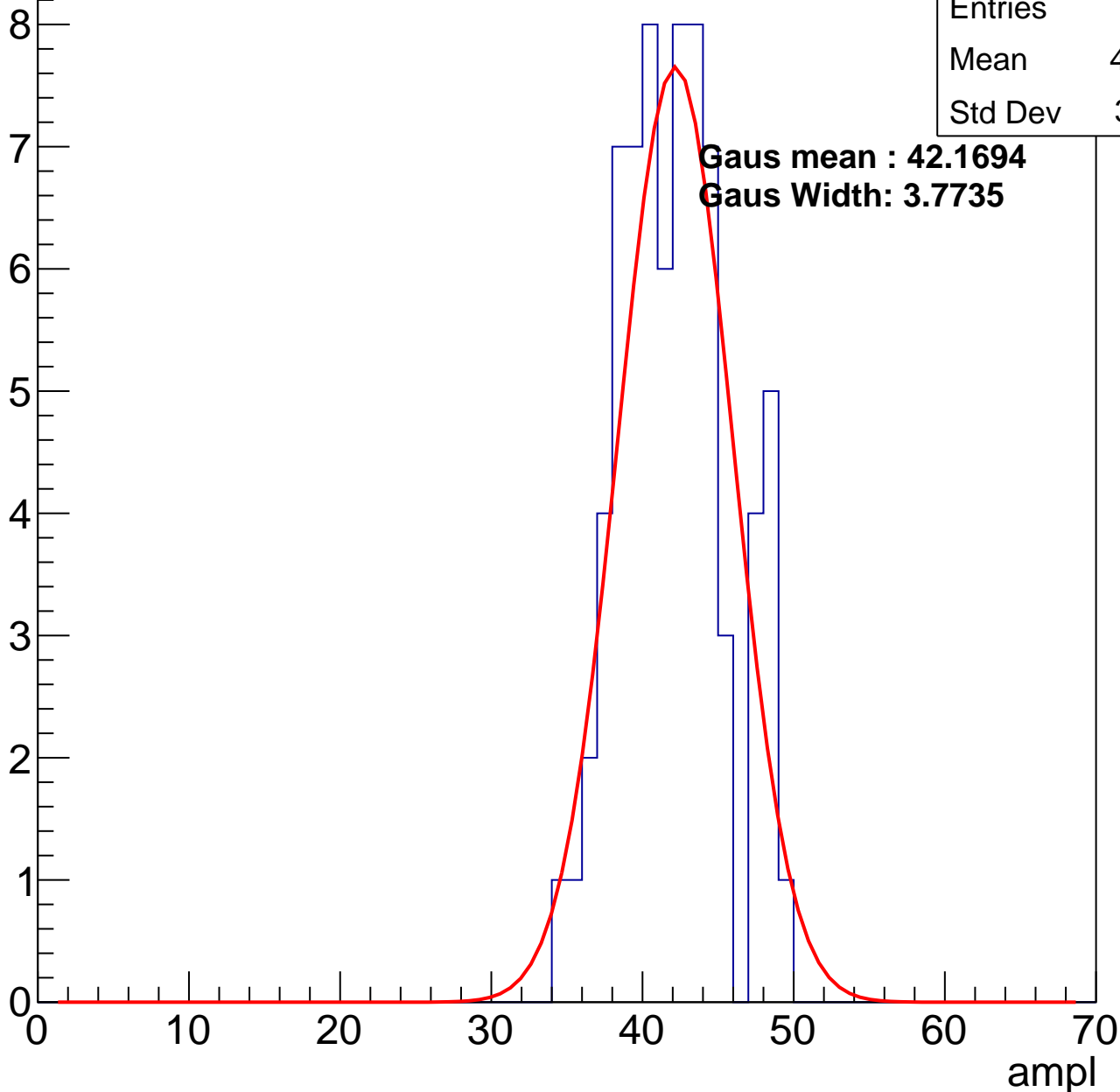
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.58
Std Dev	3.491

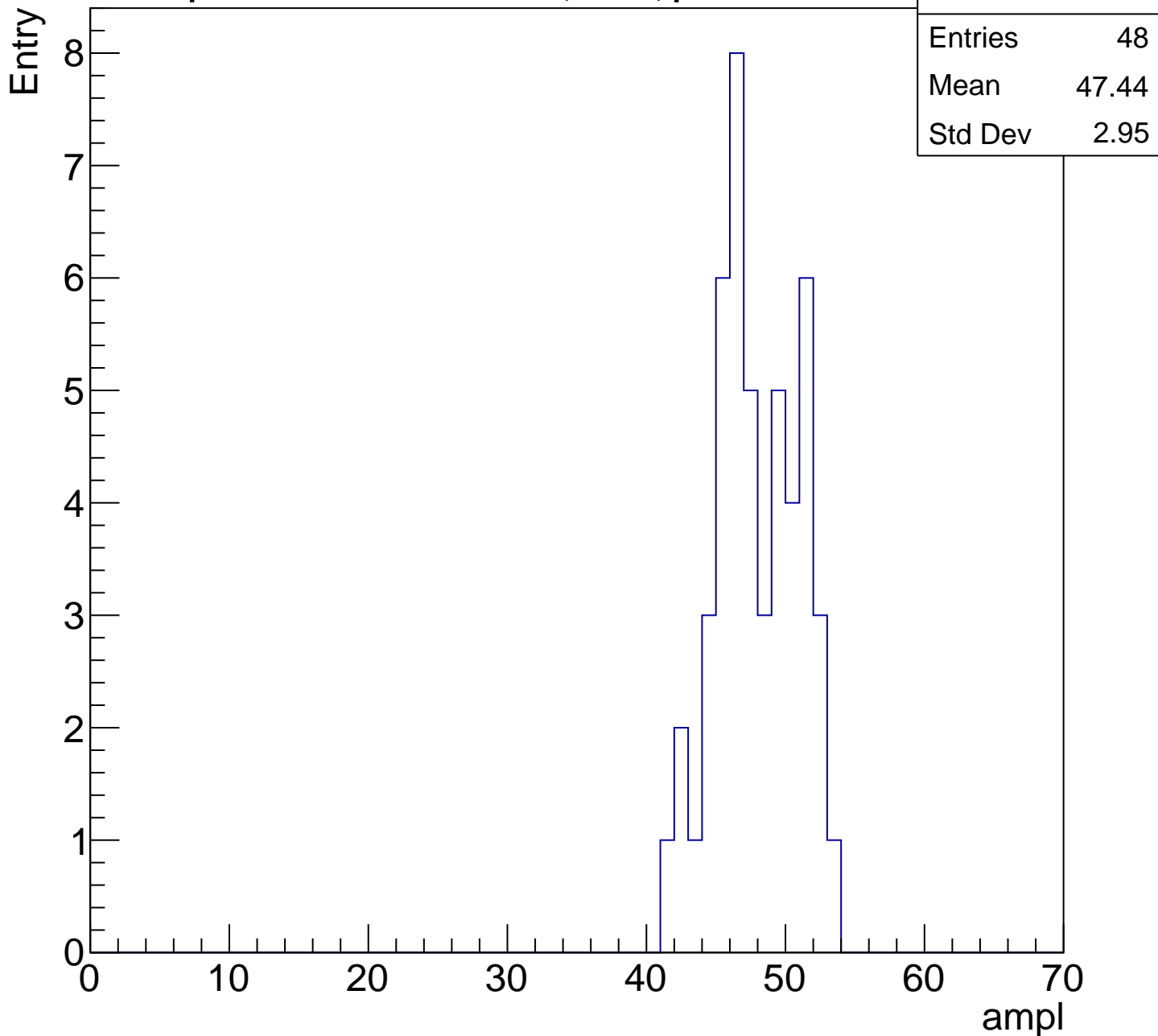
Gaus mean : 42.1694

Gaus Width: 3.7735



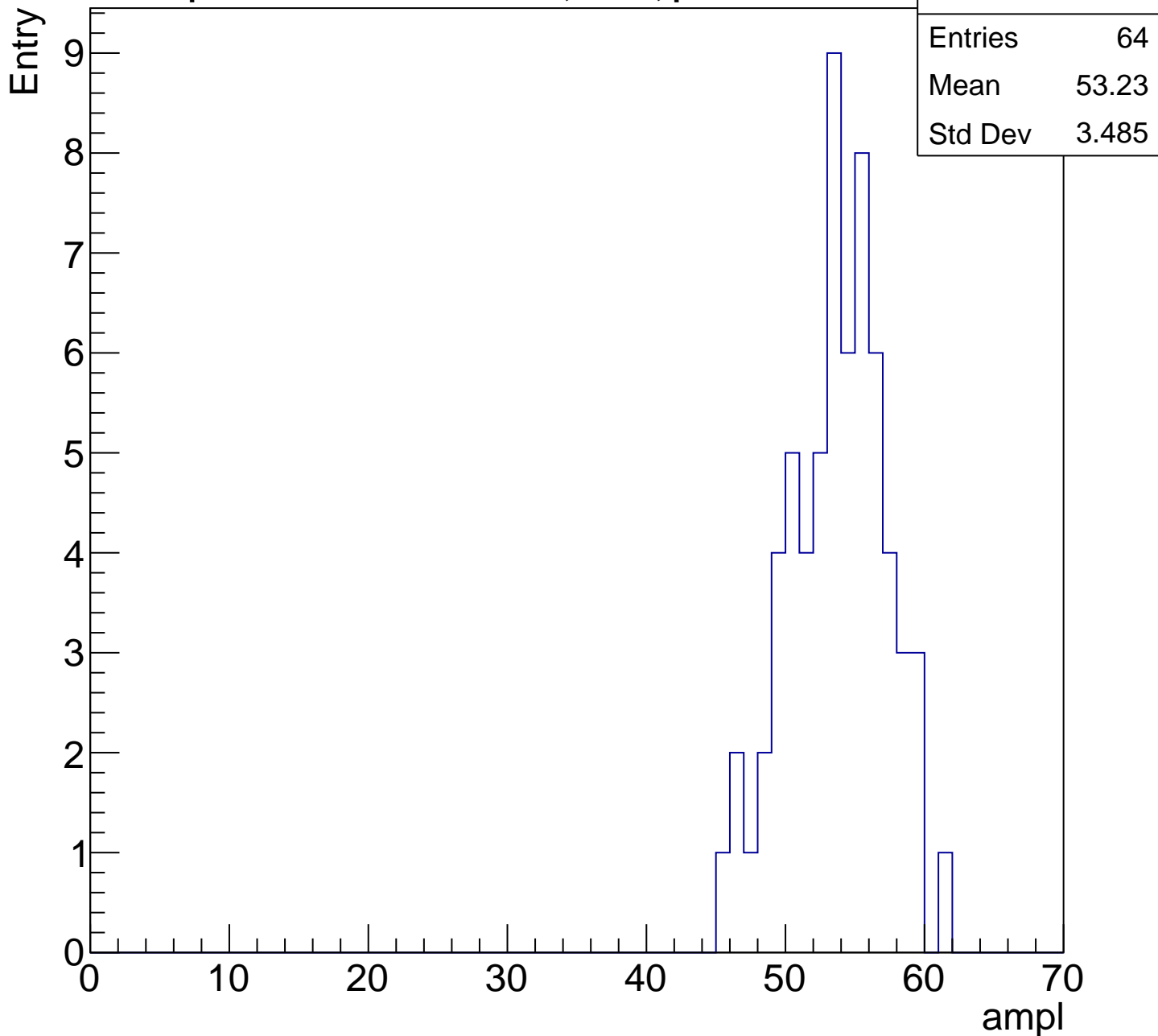
B1L100S, U1-ch82, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch82, adc4

calib_packv5_042523_0143.root, FC#4, port A2

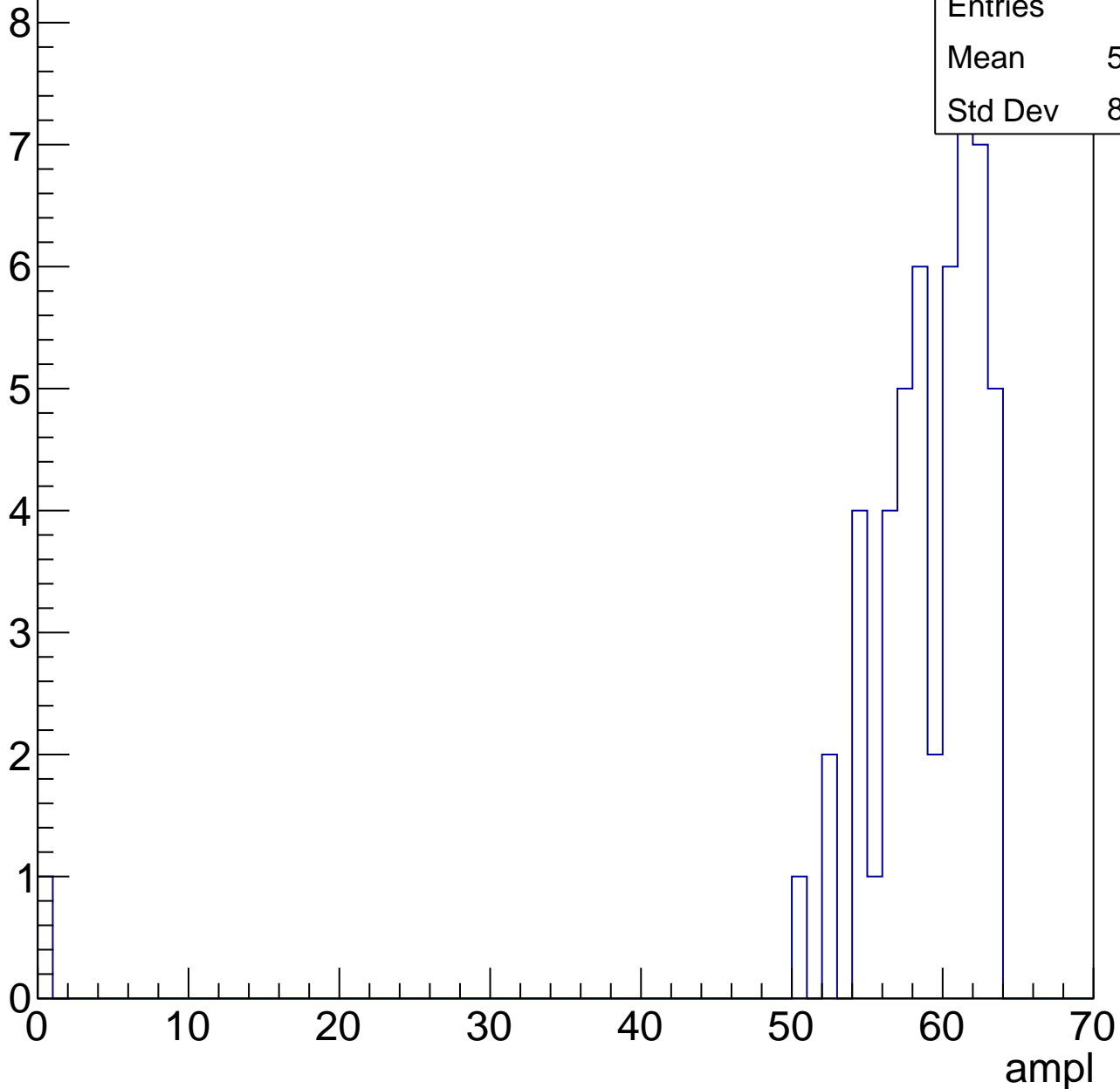


B1L100S, U1-ch82, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	52
Mean	57.63
Std Dev	8.685



B1L100S, U1-ch82, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

4
3.5
3
2.5
2
1.5
1
0.5
0

Entries	19
Mean	60.26
Std Dev	2.173

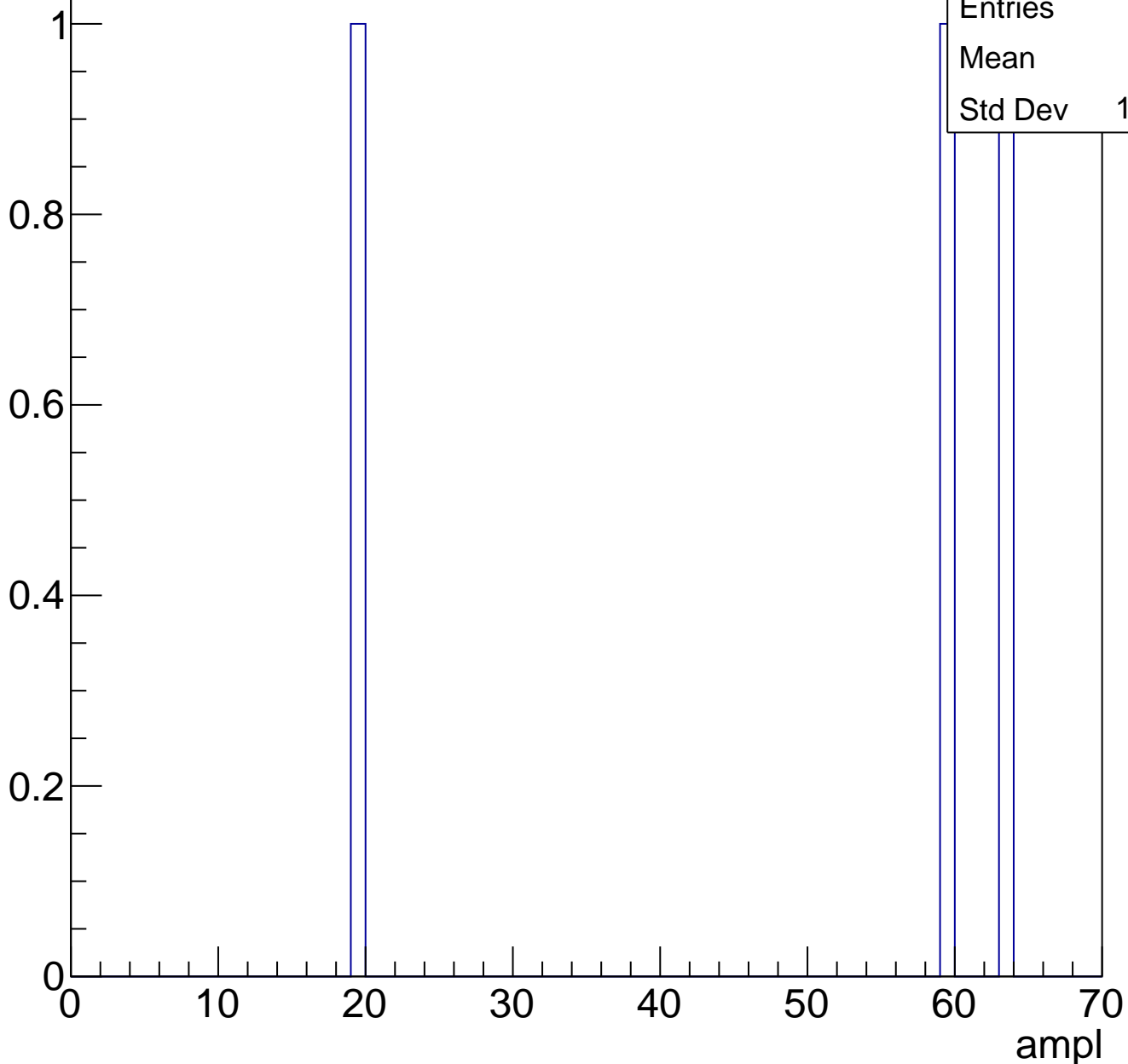
ampl

0 10 20 30 40 50 60 70

B1L100S, U1-ch82, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



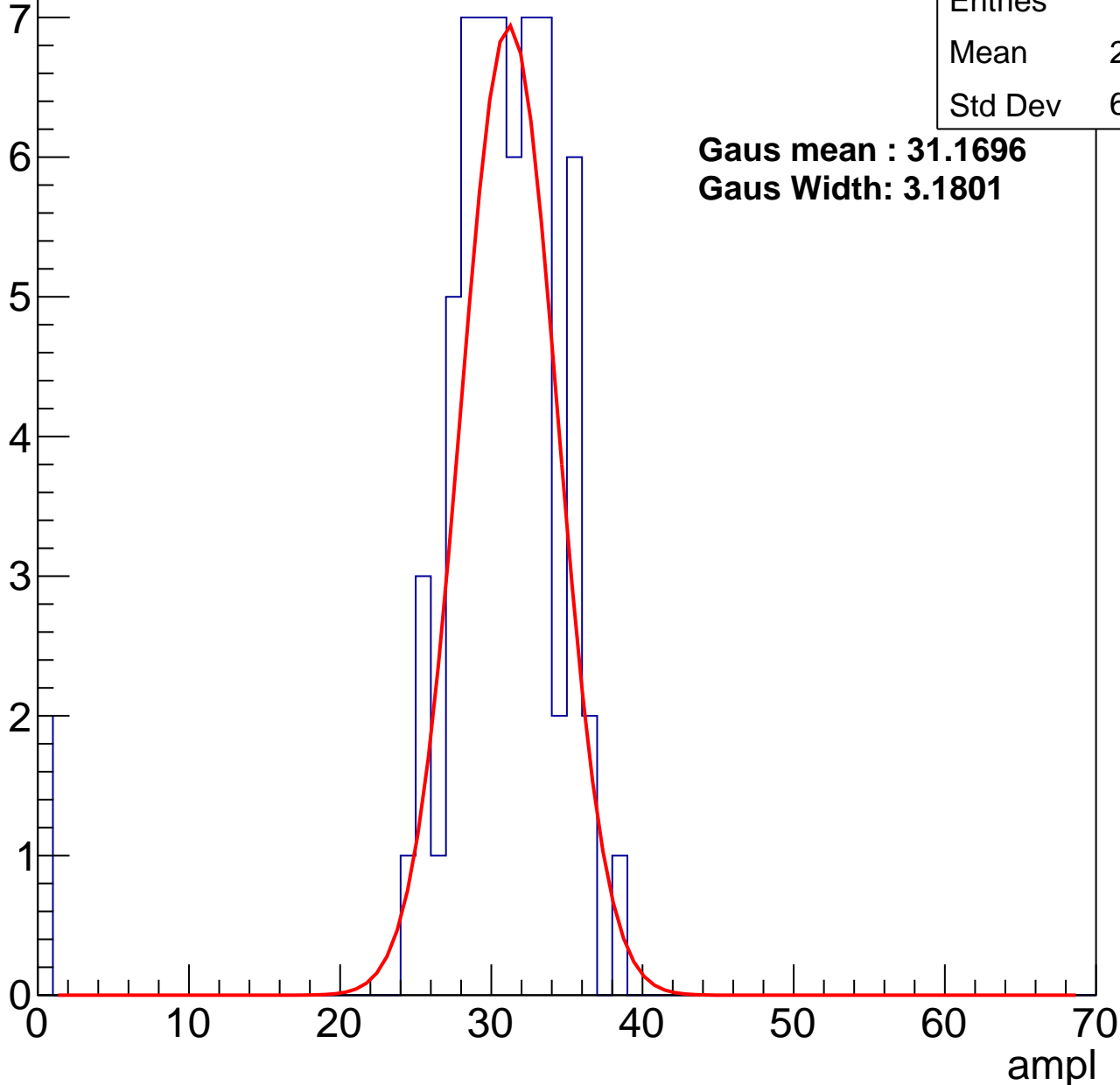
B1L100S, U1-ch83, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	29.66
Std Dev	6.142

Gaus mean : 31.1696
Gaus Width: 3.1801



B1L100S, U1-ch83, adc1

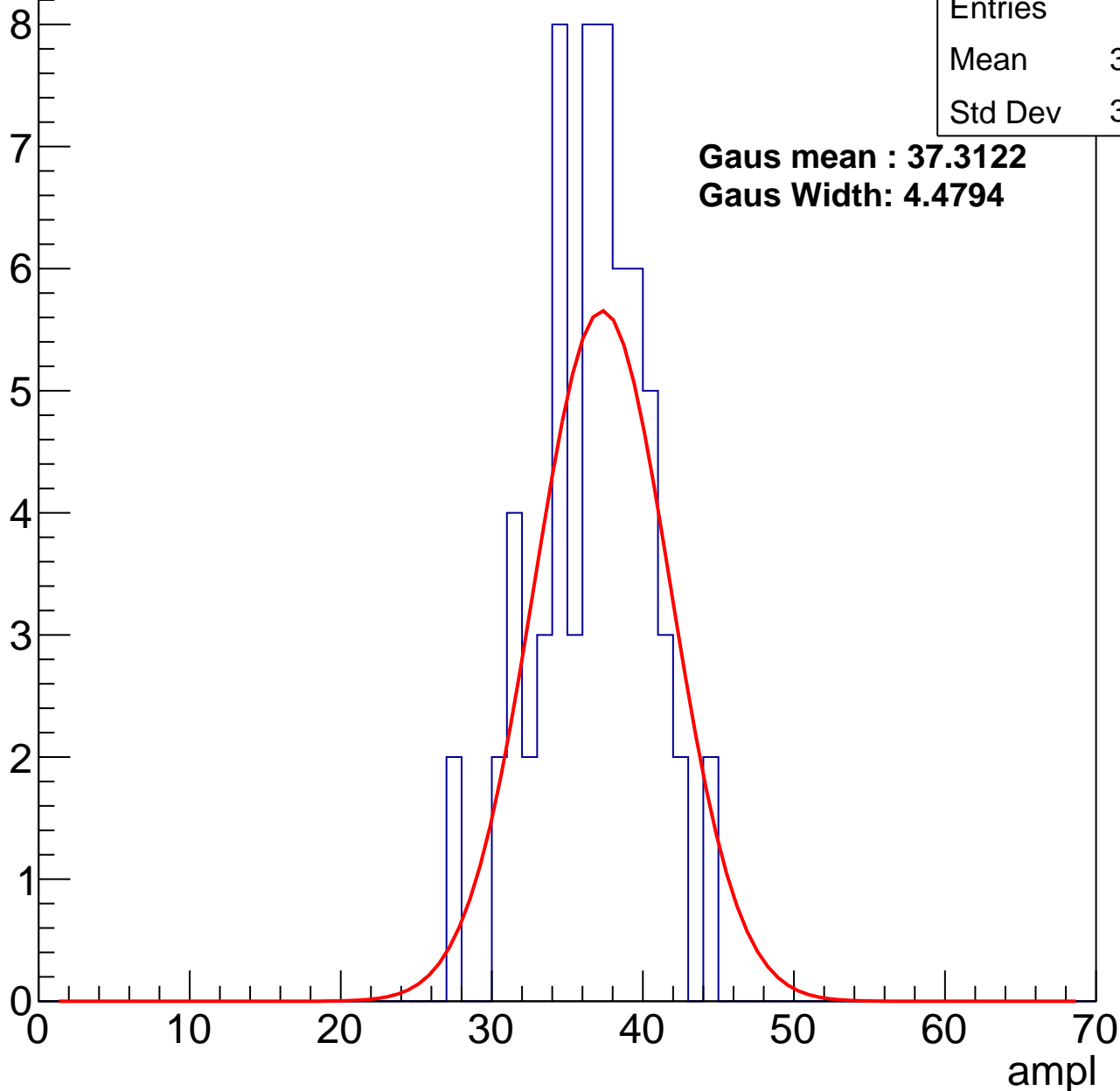
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	36.23
Std Dev	3.677

Gaus mean : 37.3122

Gaus Width: 4.4794



B1L100S, U1-ch83, adc2

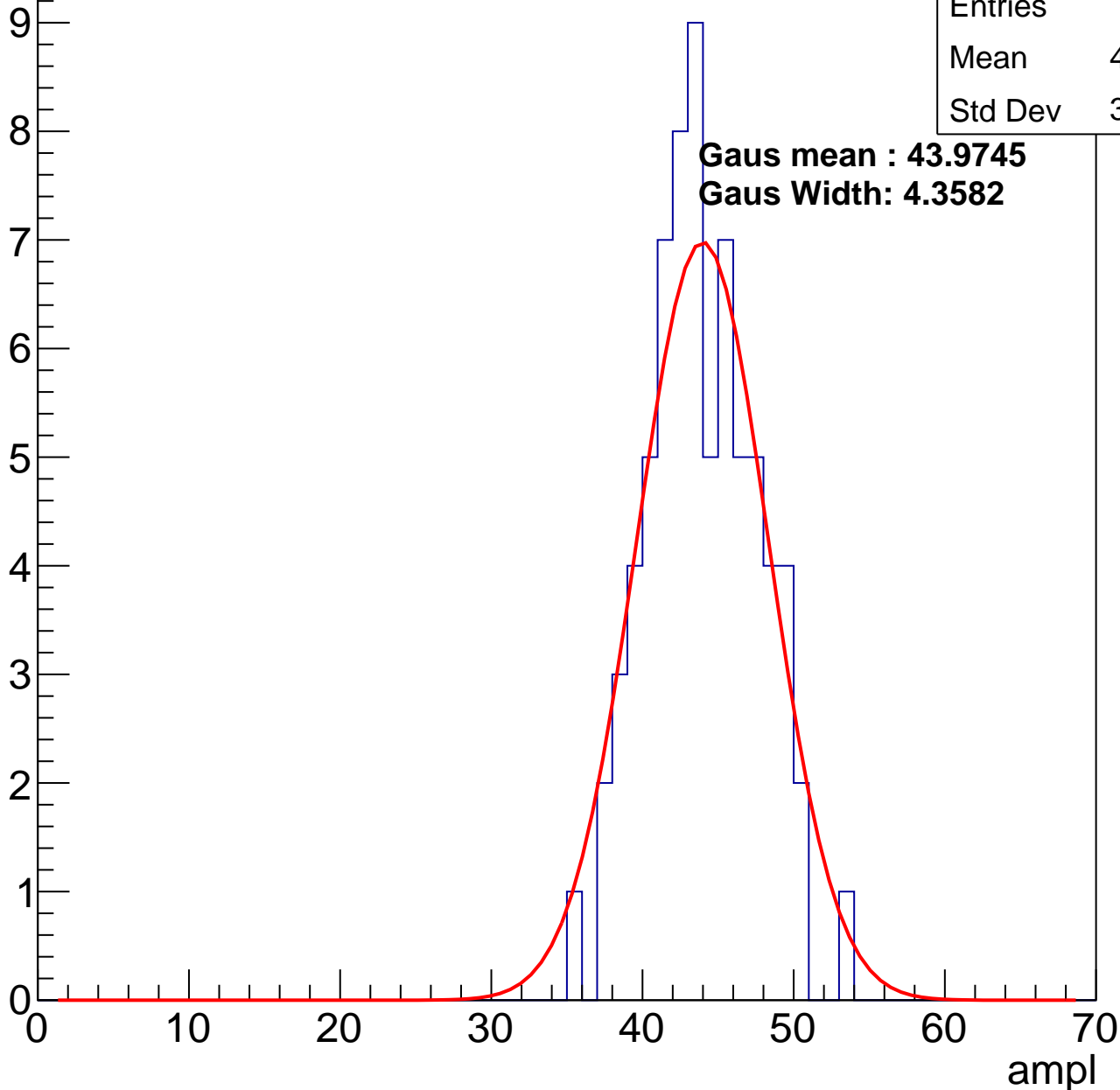
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.47
Std Dev	3.613

Gaus mean : 43.9745

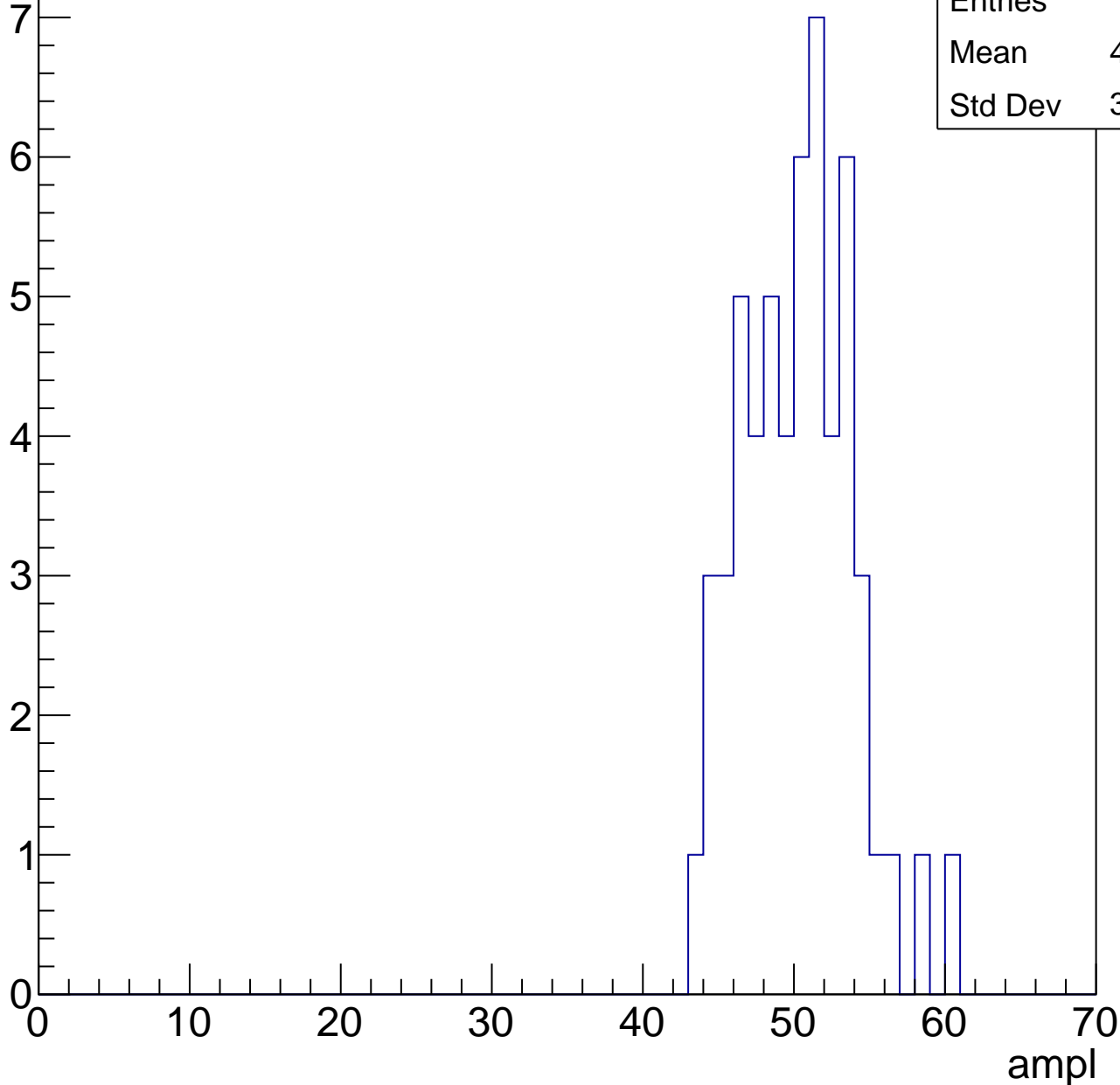
Gaus Width: 4.3582



B1L100S, U1-ch83, adc3

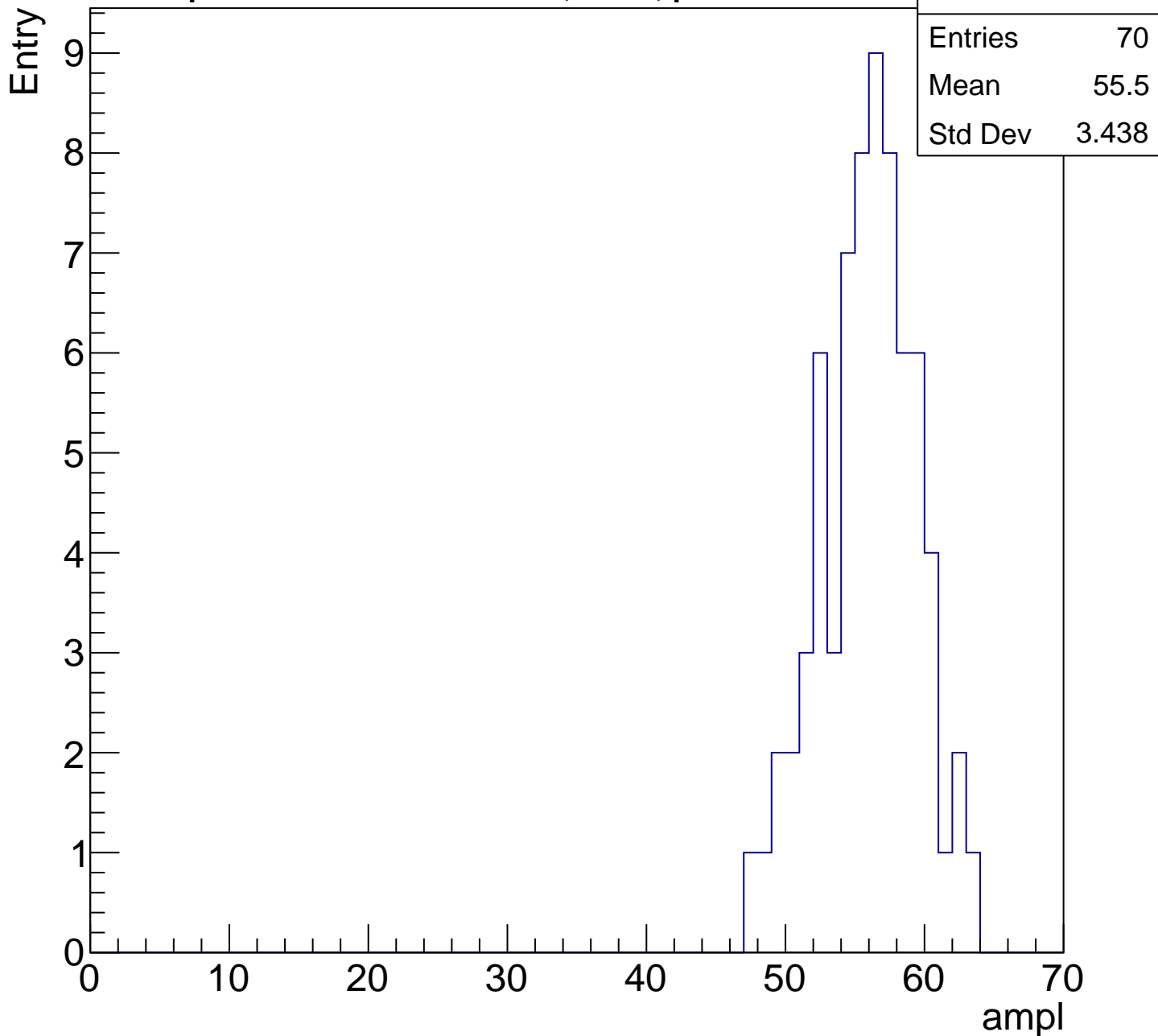
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch83, adc4

calib_packv5_042523_0143.root, FC#4, port A2

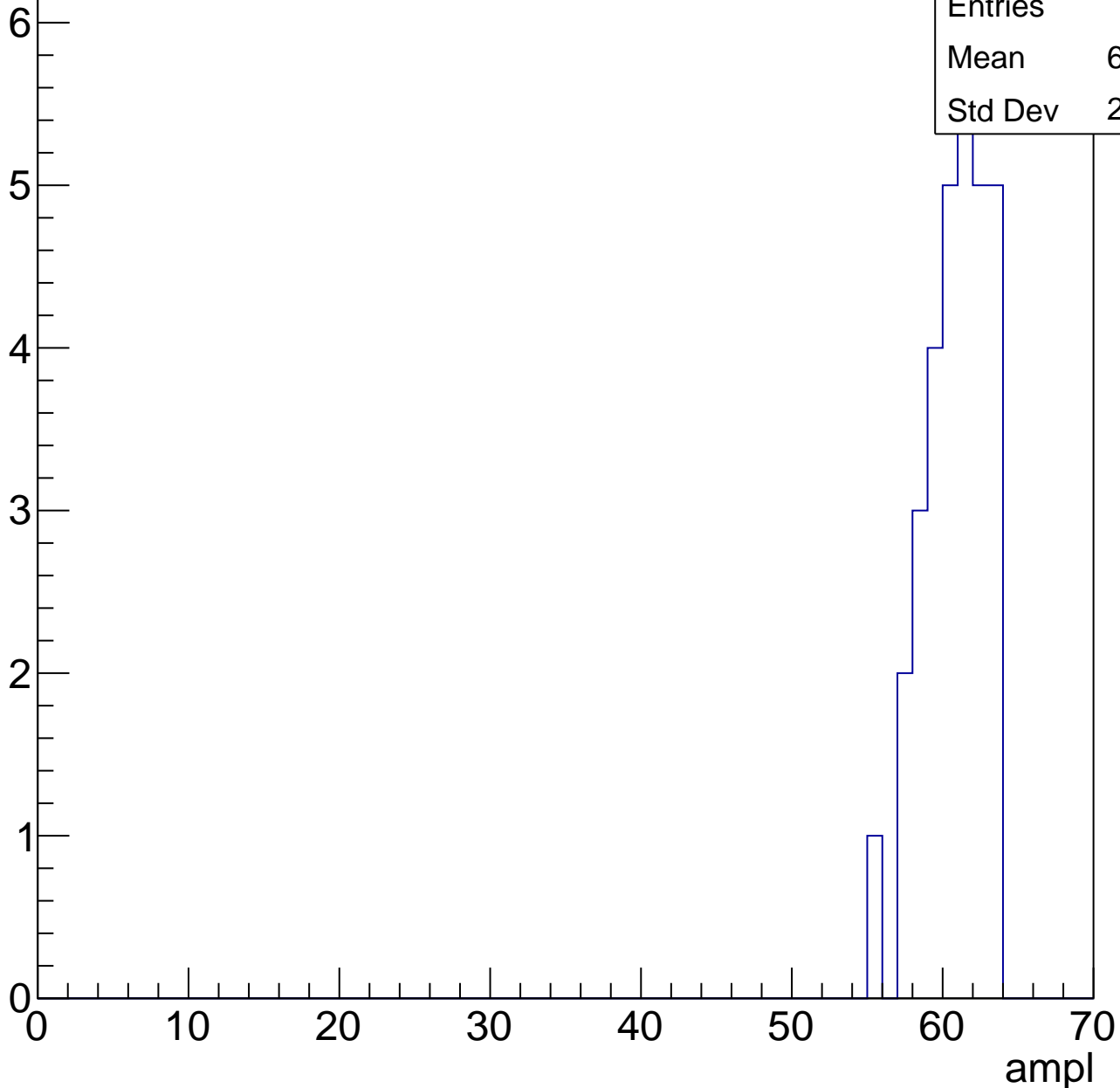


B1L100S, U1-ch83, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

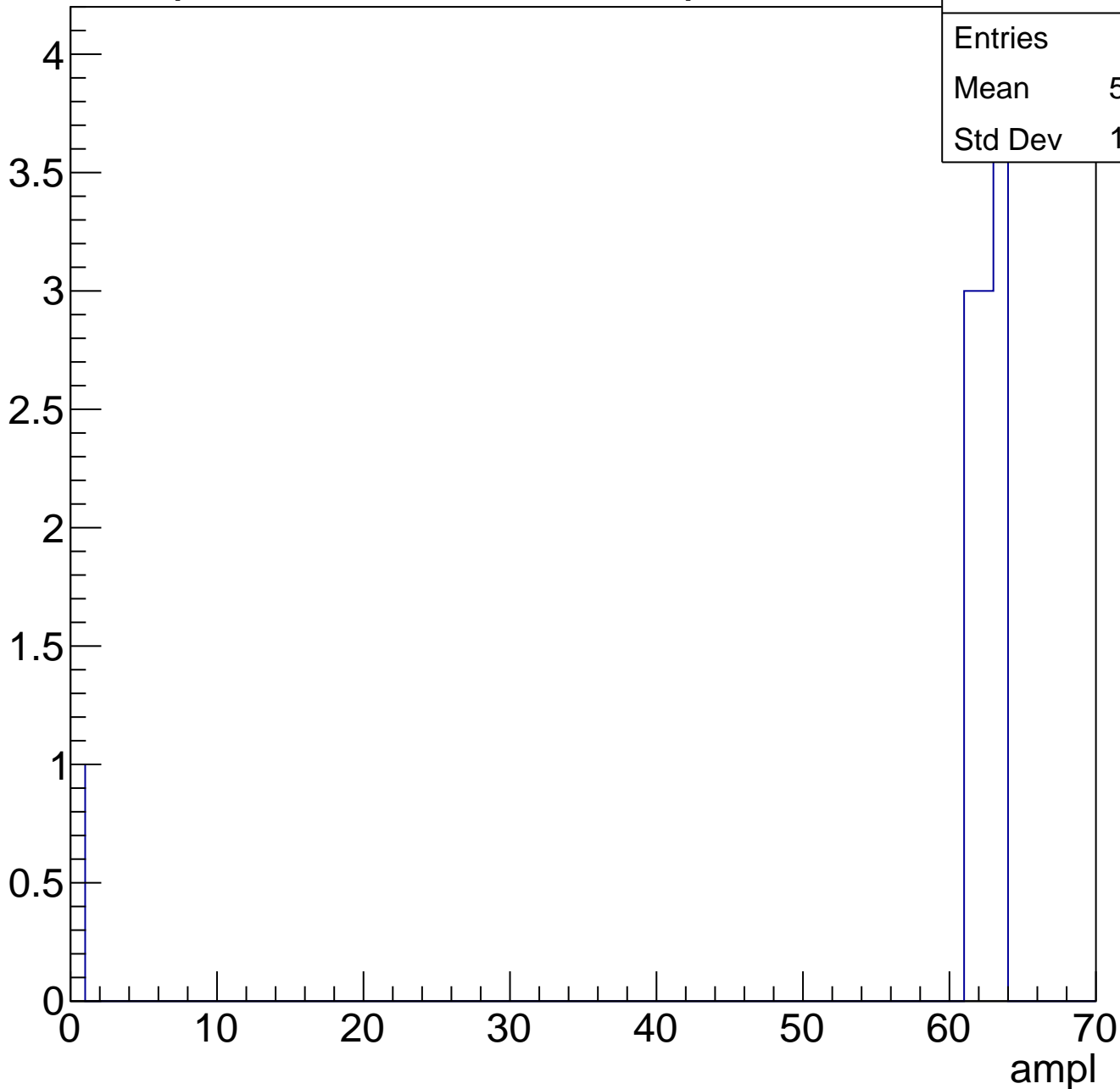
Entries	31
Mean	60.32
Std Dev	2.022



B1L100S, U1-ch83, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch83, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch84, adc0

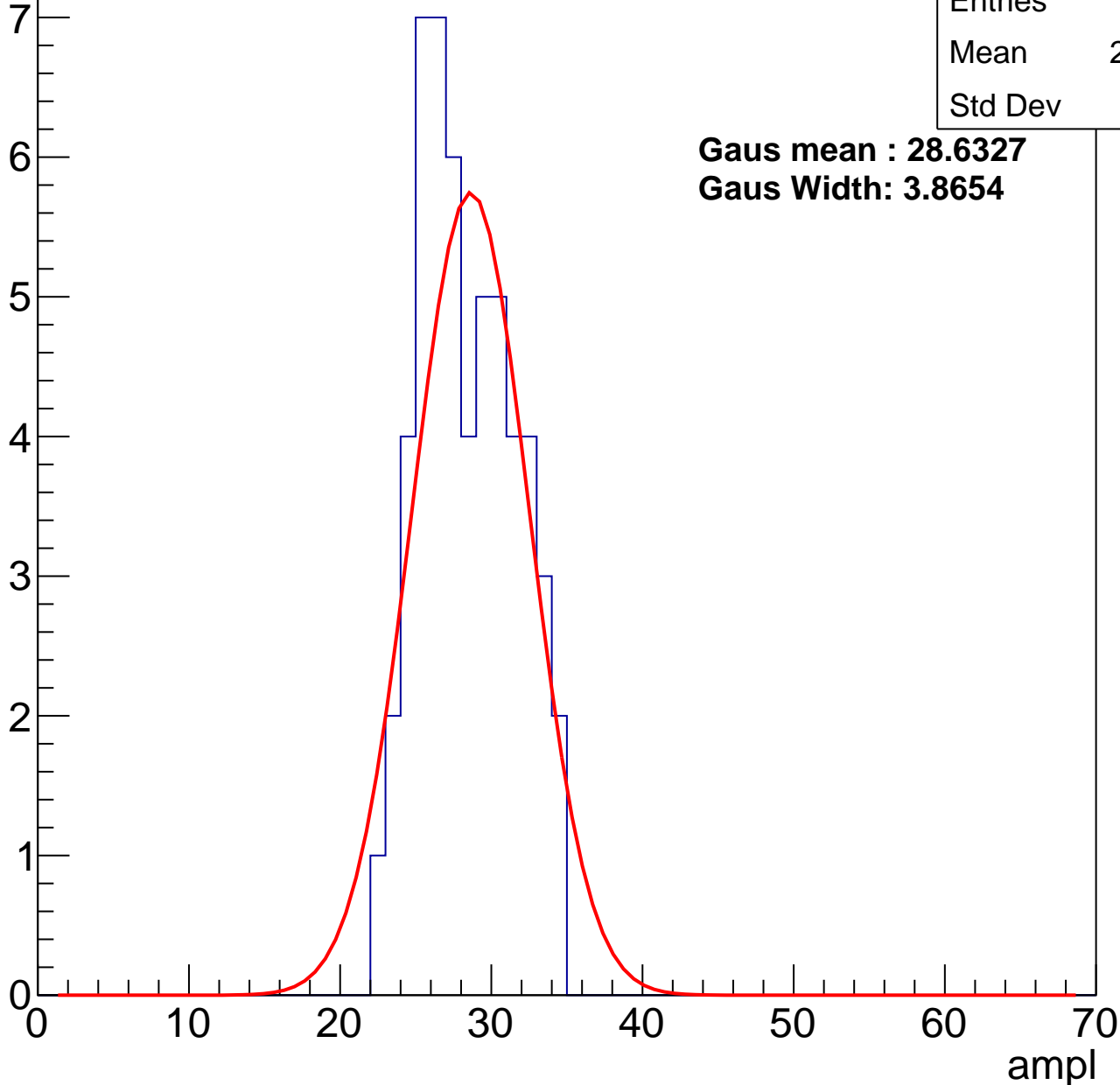
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	27.94
Std Dev	3.1

Gaus mean : 28.6327

Gaus Width: 3.8654



B1L100S, U1-ch84, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	82
Mean	34.87
Std Dev	3.668

Gaus mean : 35.5910

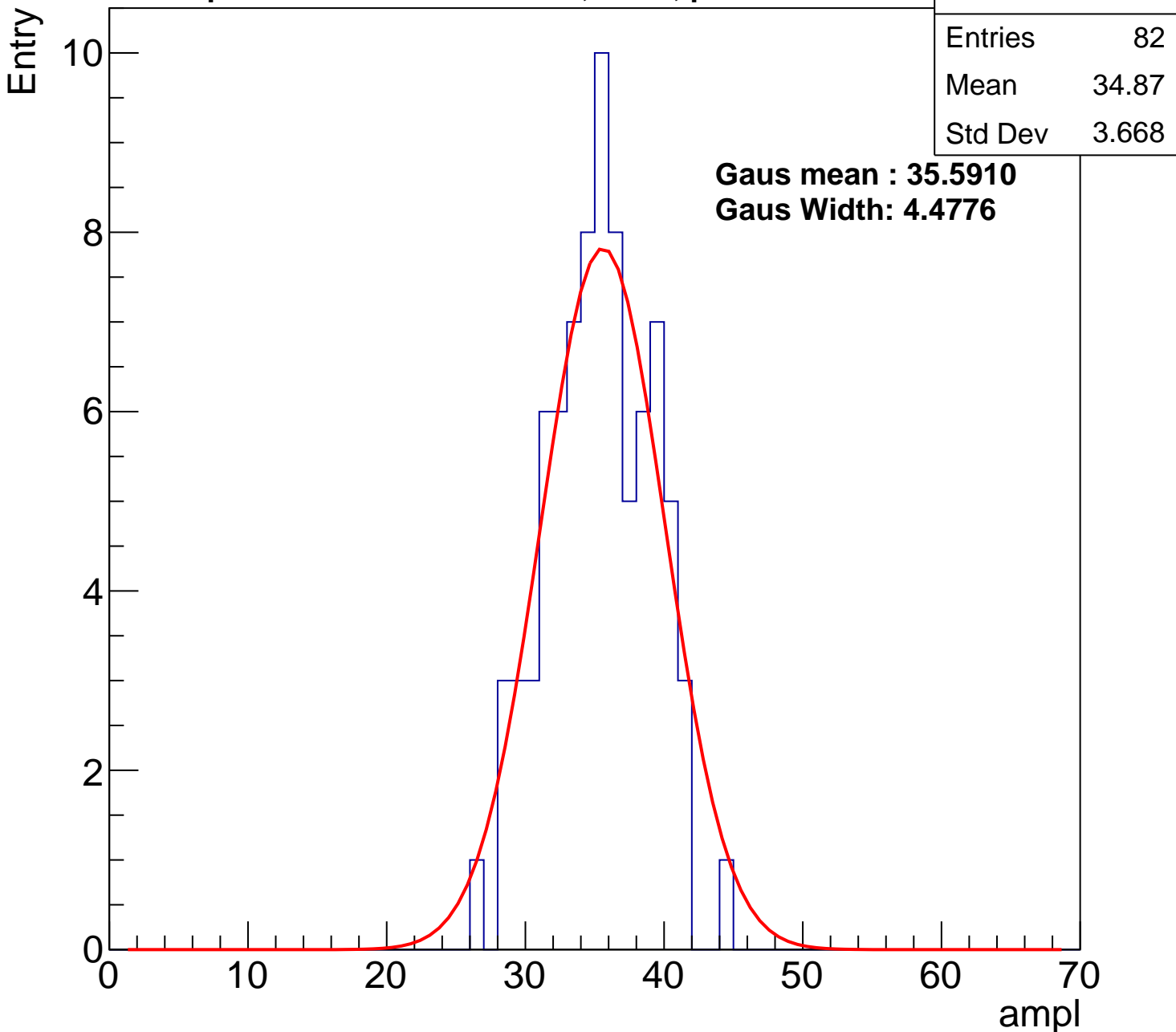
Gaus Width: 4.4776

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch84, adc2

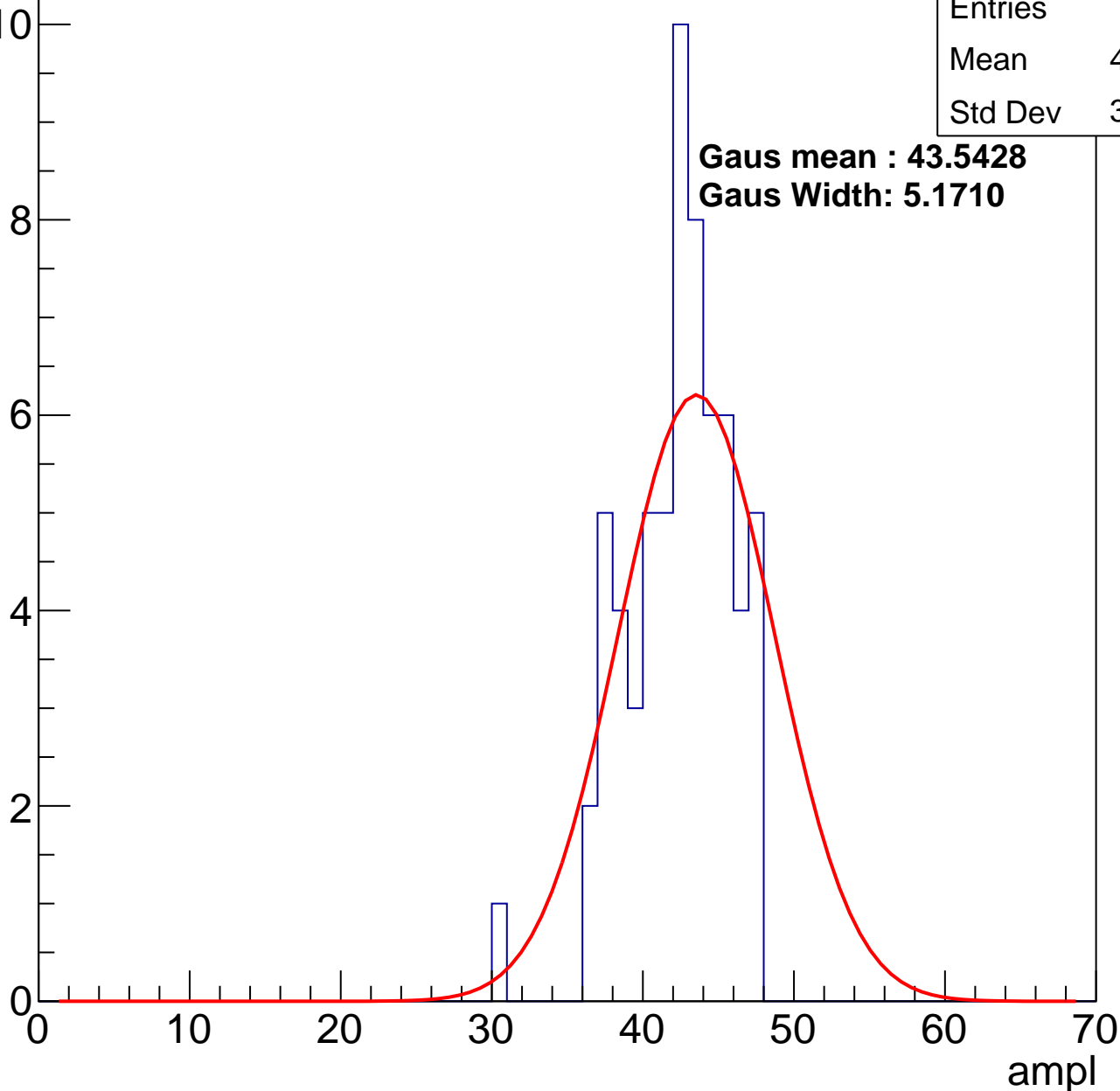
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	41.84
Std Dev	3.378

Gaus mean : 43.5428

Gaus Width: 5.1710

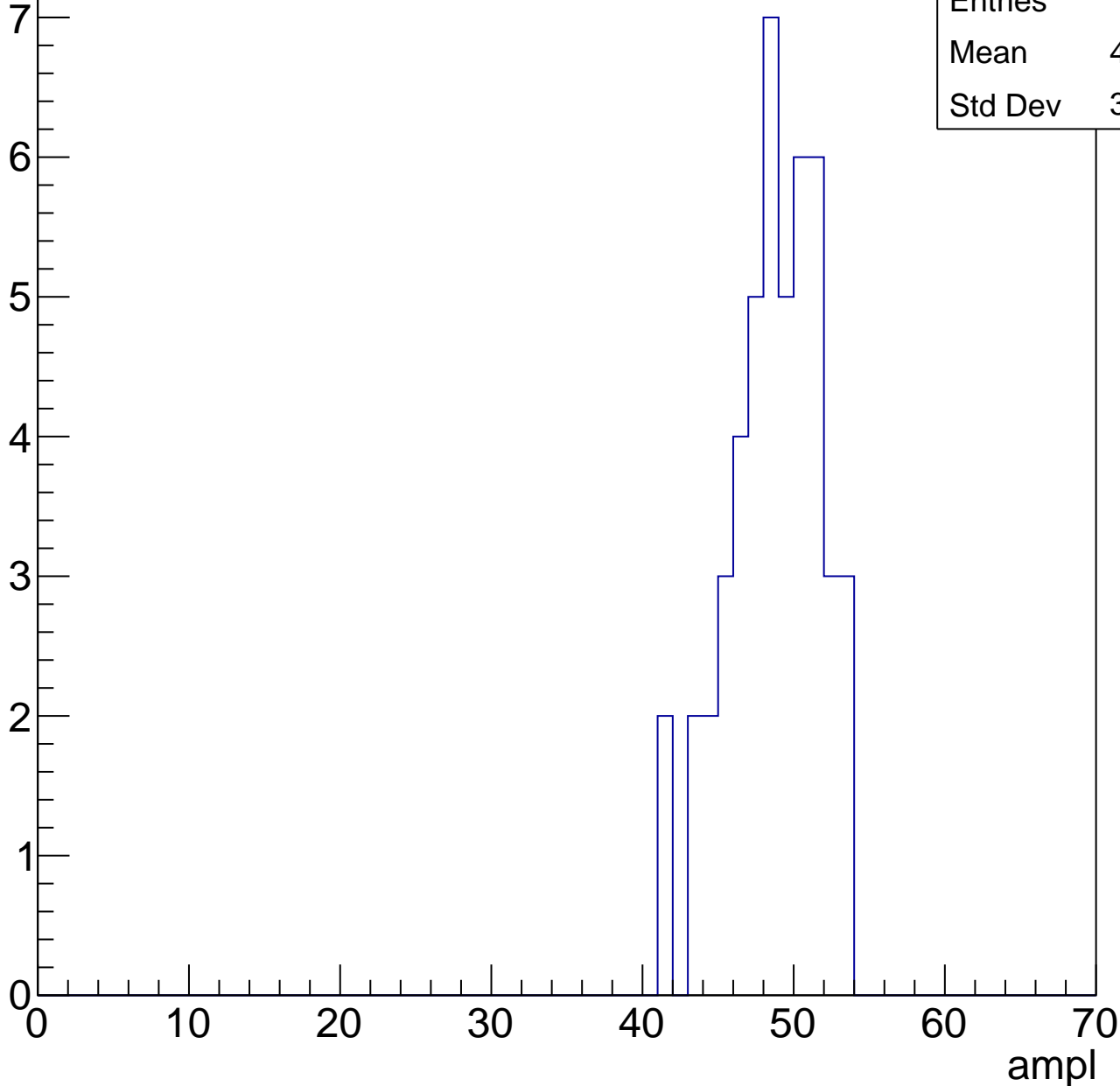


B1L100S, U1-ch84, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

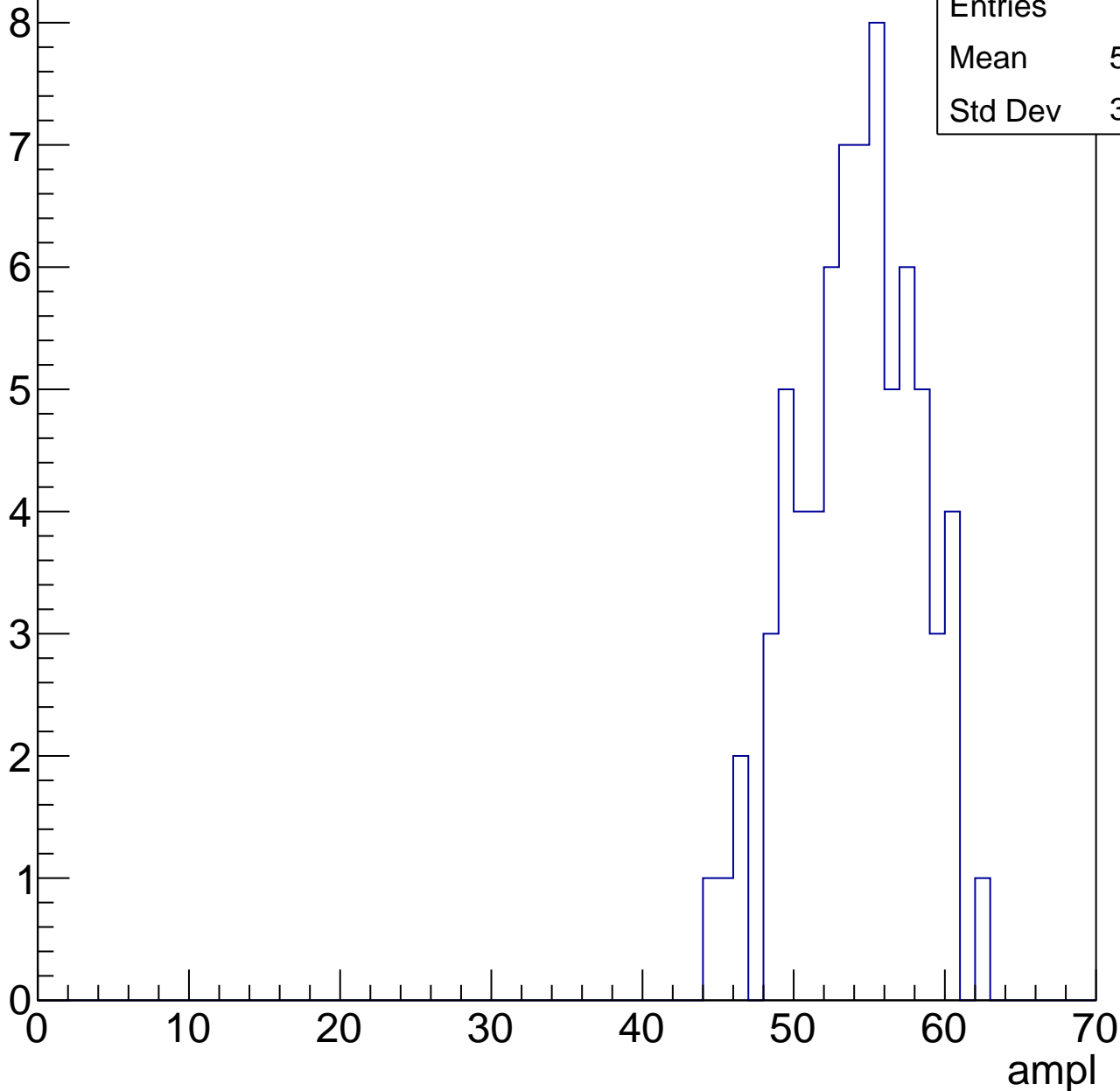
Entries	48
Mean	48.17
Std Dev	3.002



B1L100S, U1-ch84, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch84, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

7

6

5

4

3

2

1

0

Entries	44
Mean	58.91
Std Dev	2.737

ampl

0

10

20

30

40

50

60

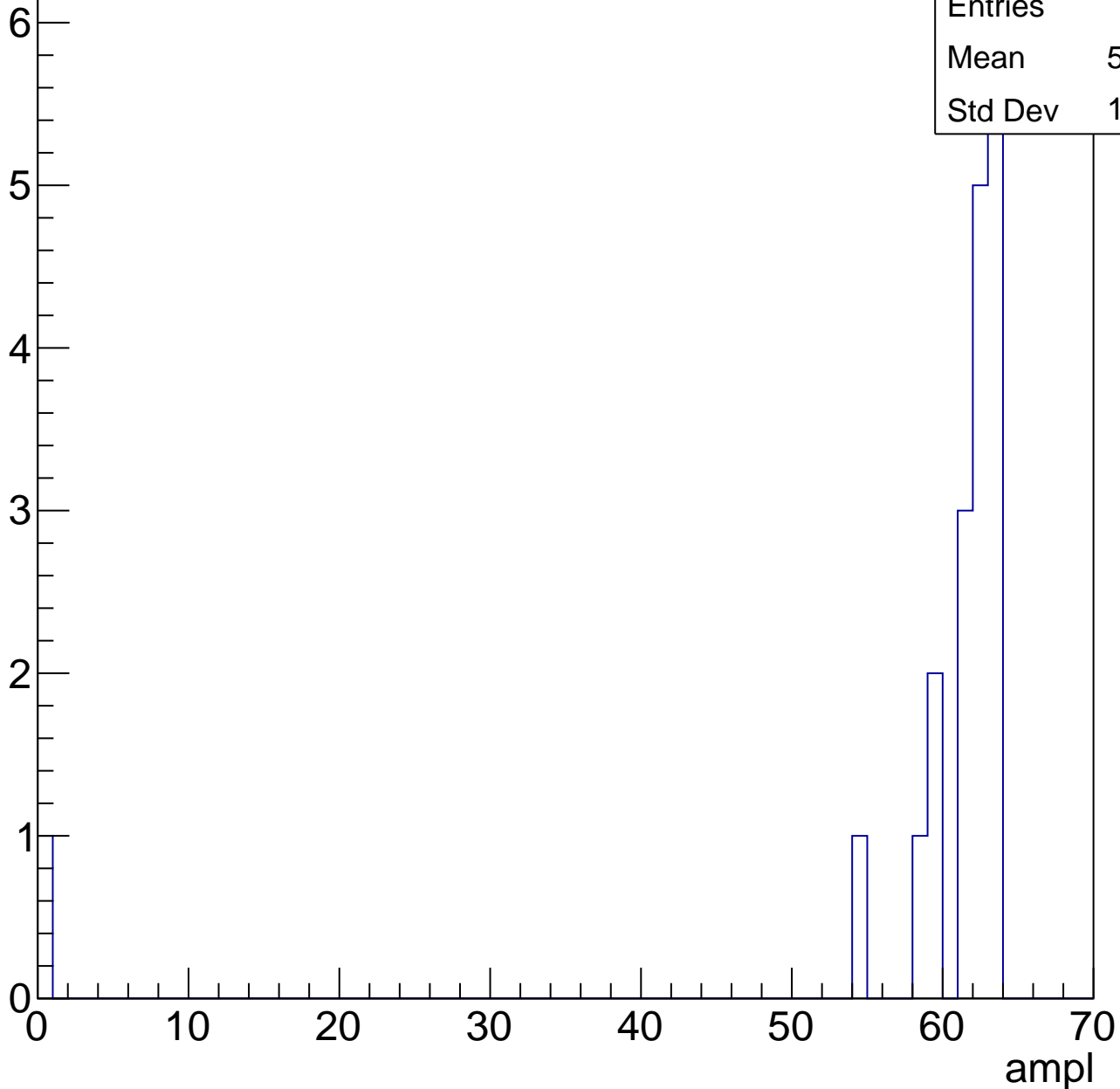
70

B1L100S, U1-ch84, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	57.95
Std Dev	13.84



B1L100S, U1-ch84, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch85, adc0

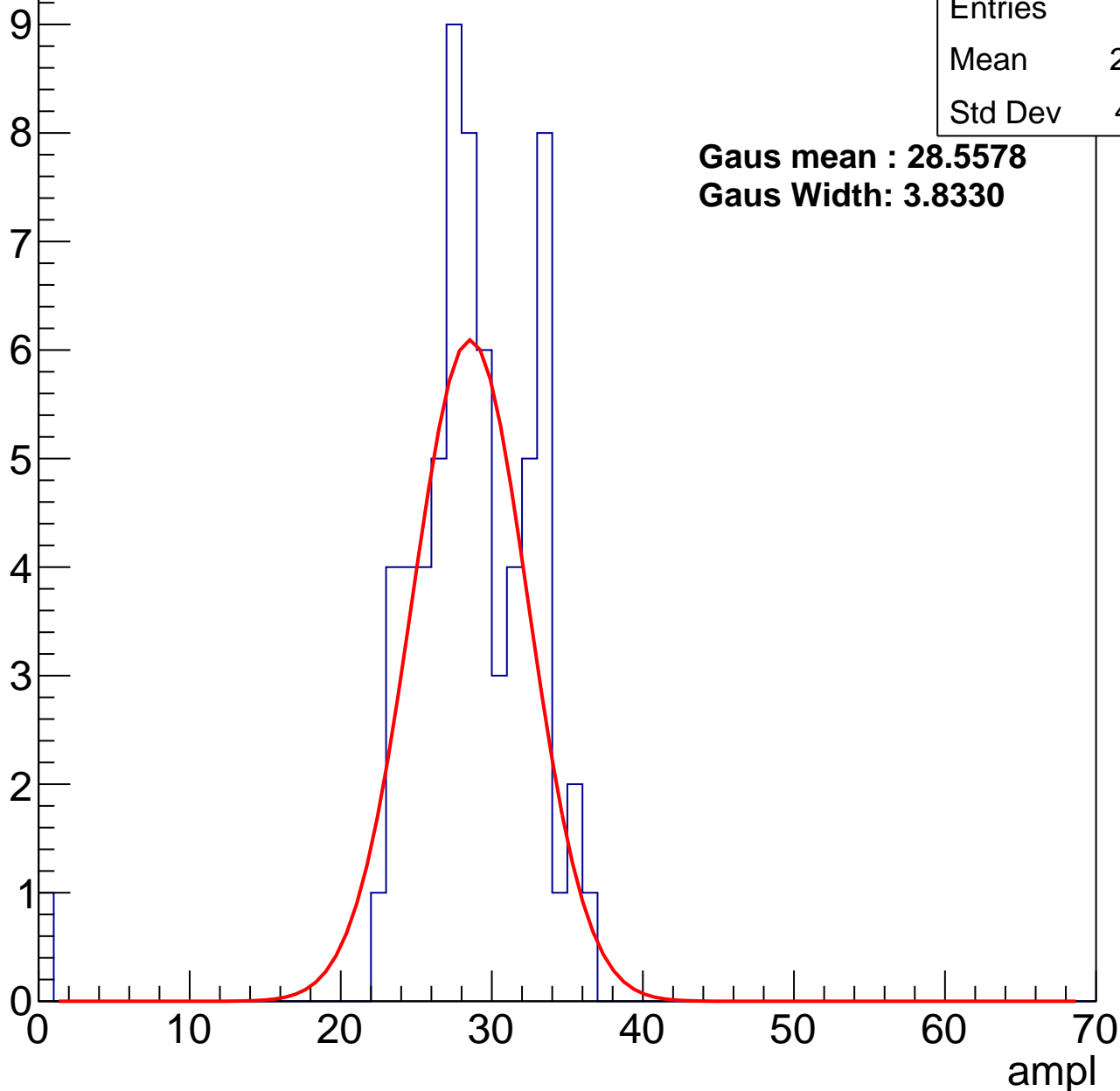
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	28.17
Std Dev	4.891

Gaus mean : 28.5578

Gaus Width: 3.8330



B1L100S, U1-ch85, adc1

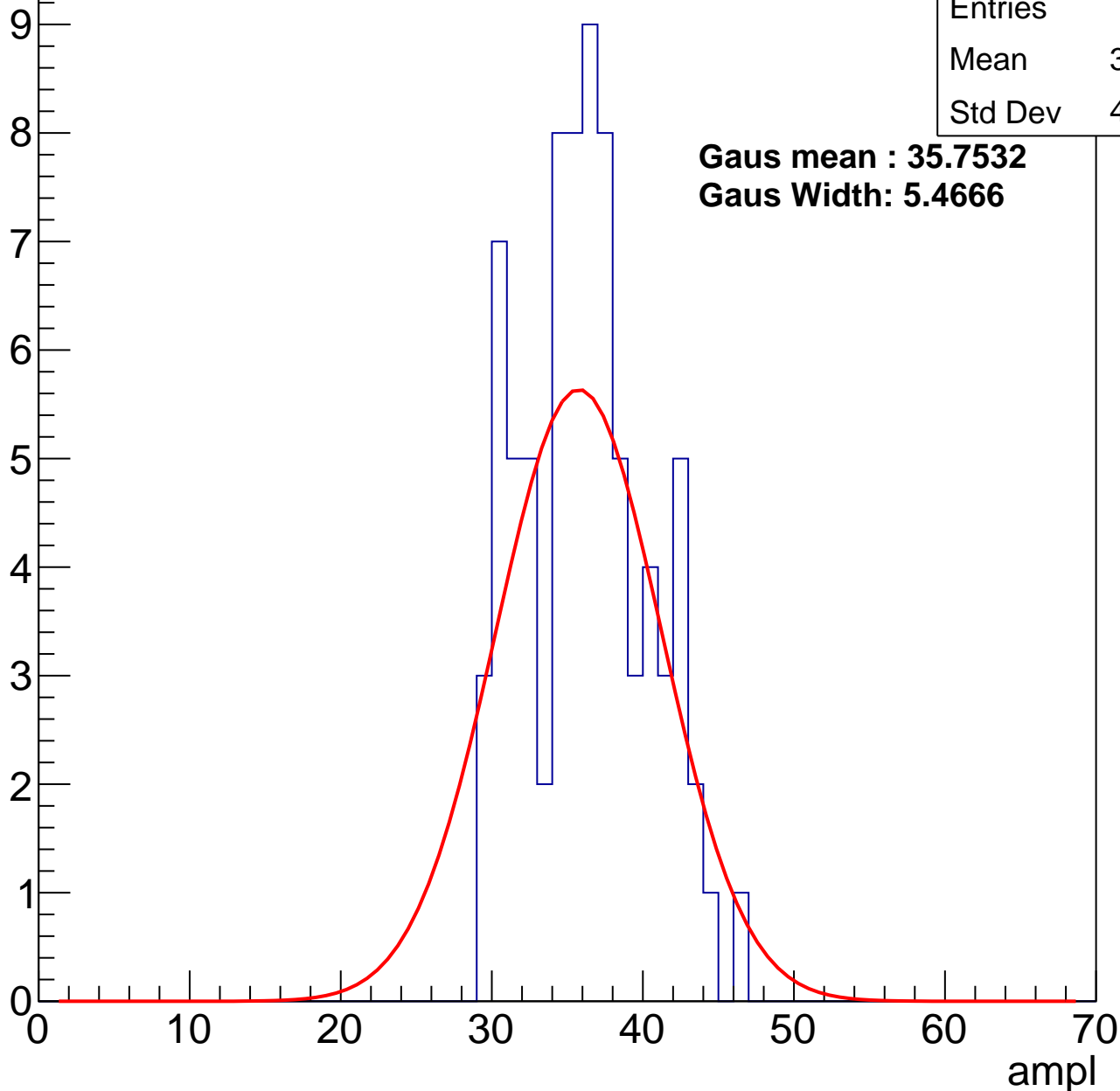
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	35.77
Std Dev	4.047

Gaus mean : 35.7532

Gaus Width: 5.4666



B1L100S, U1-ch85, adc2

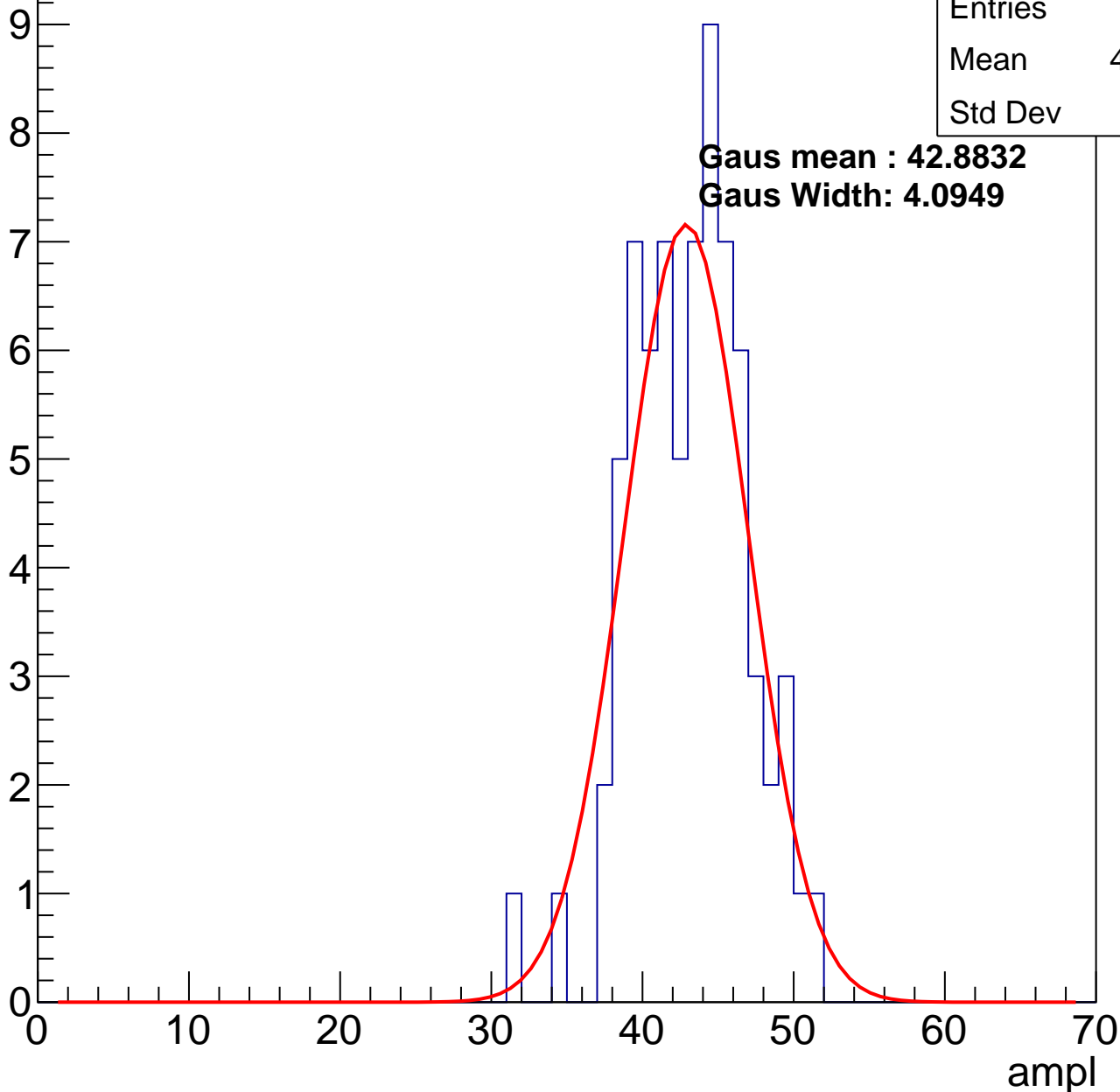
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	42.63
Std Dev	3.74

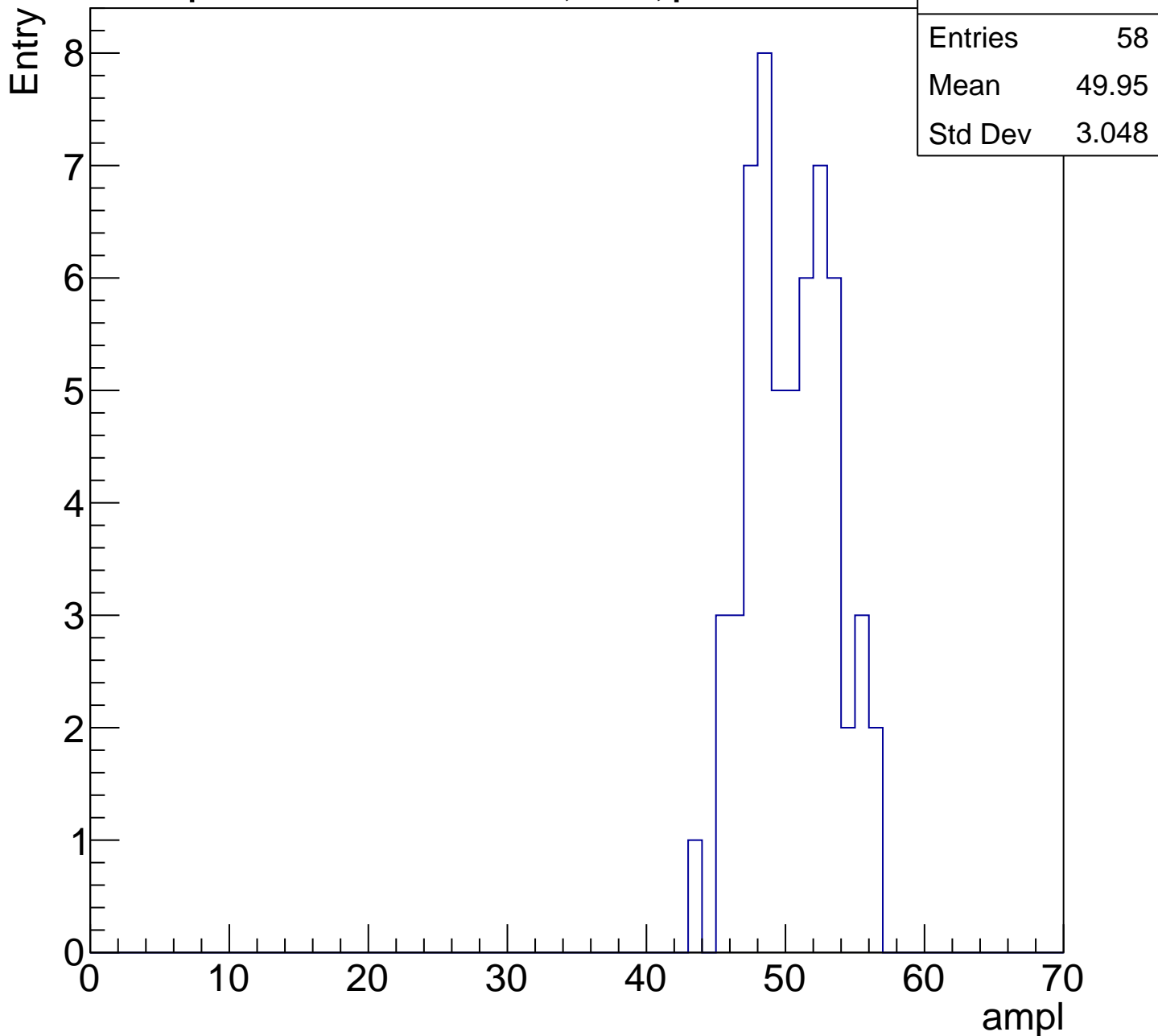
Gaus mean : 42.8832

Gaus Width: 4.0949



B1L100S, U1-ch85, adc3

calib_packv5_042523_0143.root, FC#4, port A2

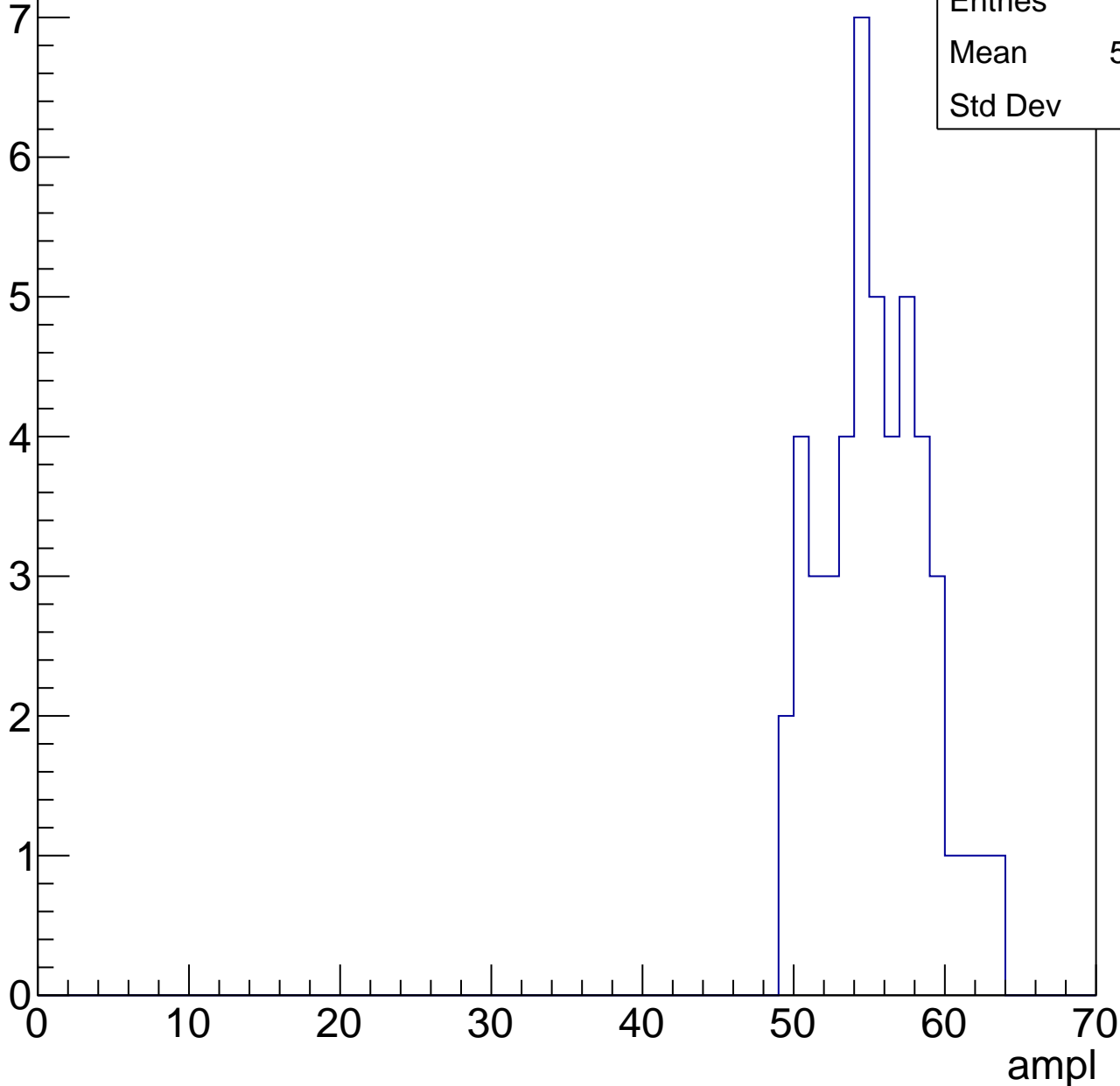


B1L100S, U1-ch85, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	54.92
Std Dev	3.39

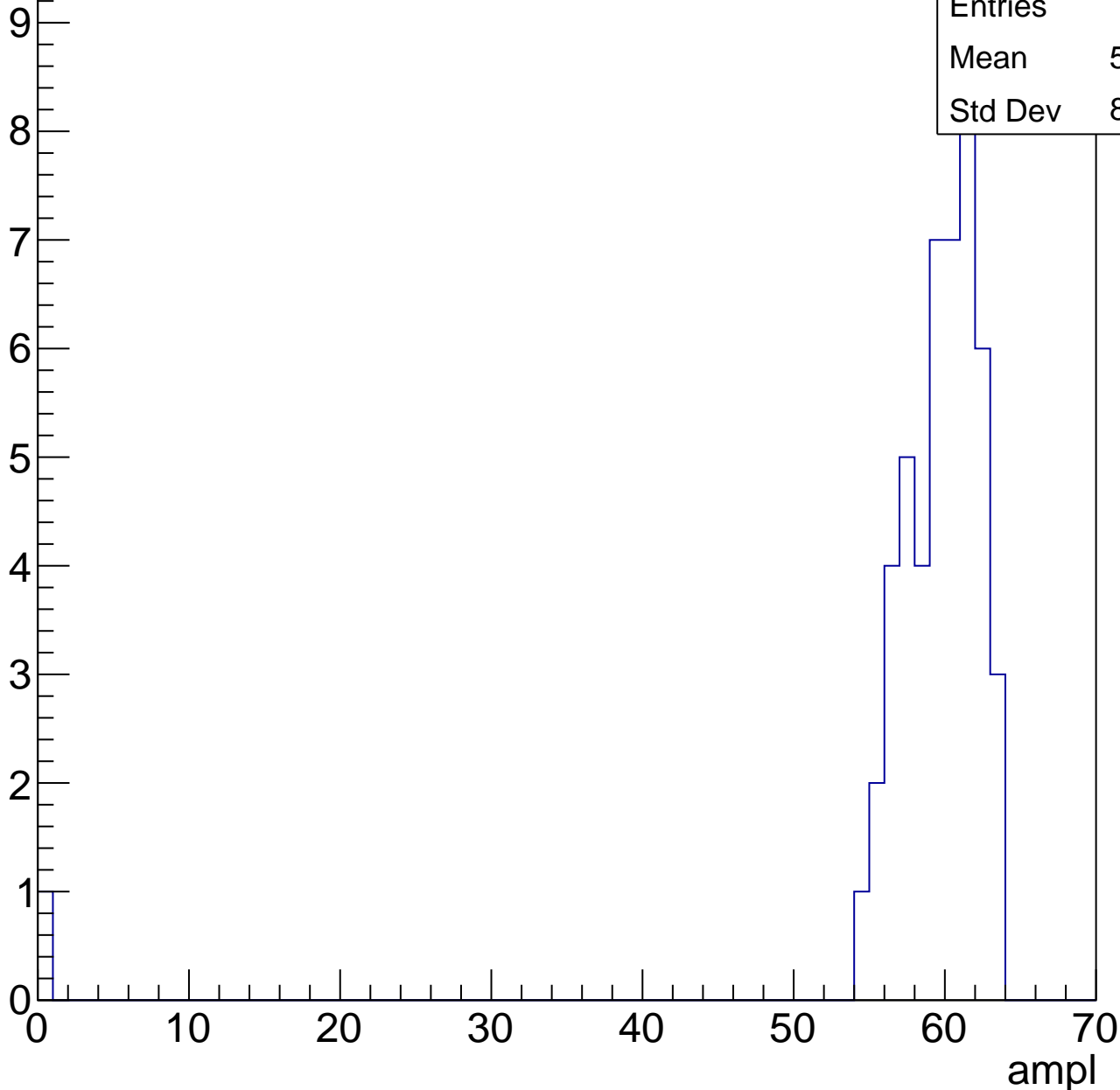


B1L100S, U1-ch85, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	58.12
Std Dev	8.696

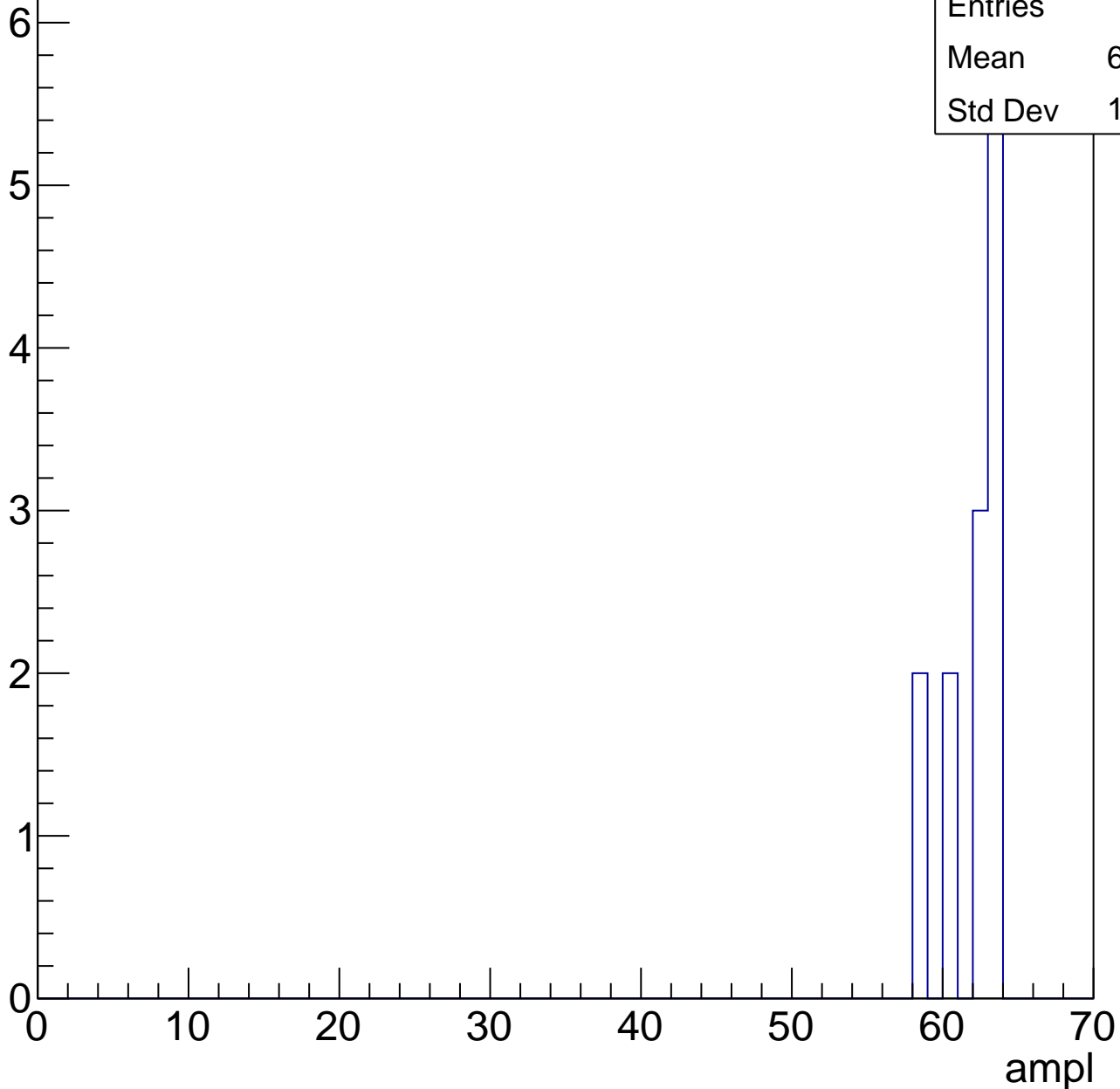


B1L100S, U1-ch85, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	61.54
Std Dev	1.824



B1L100S, U1-ch85, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch86, adc0

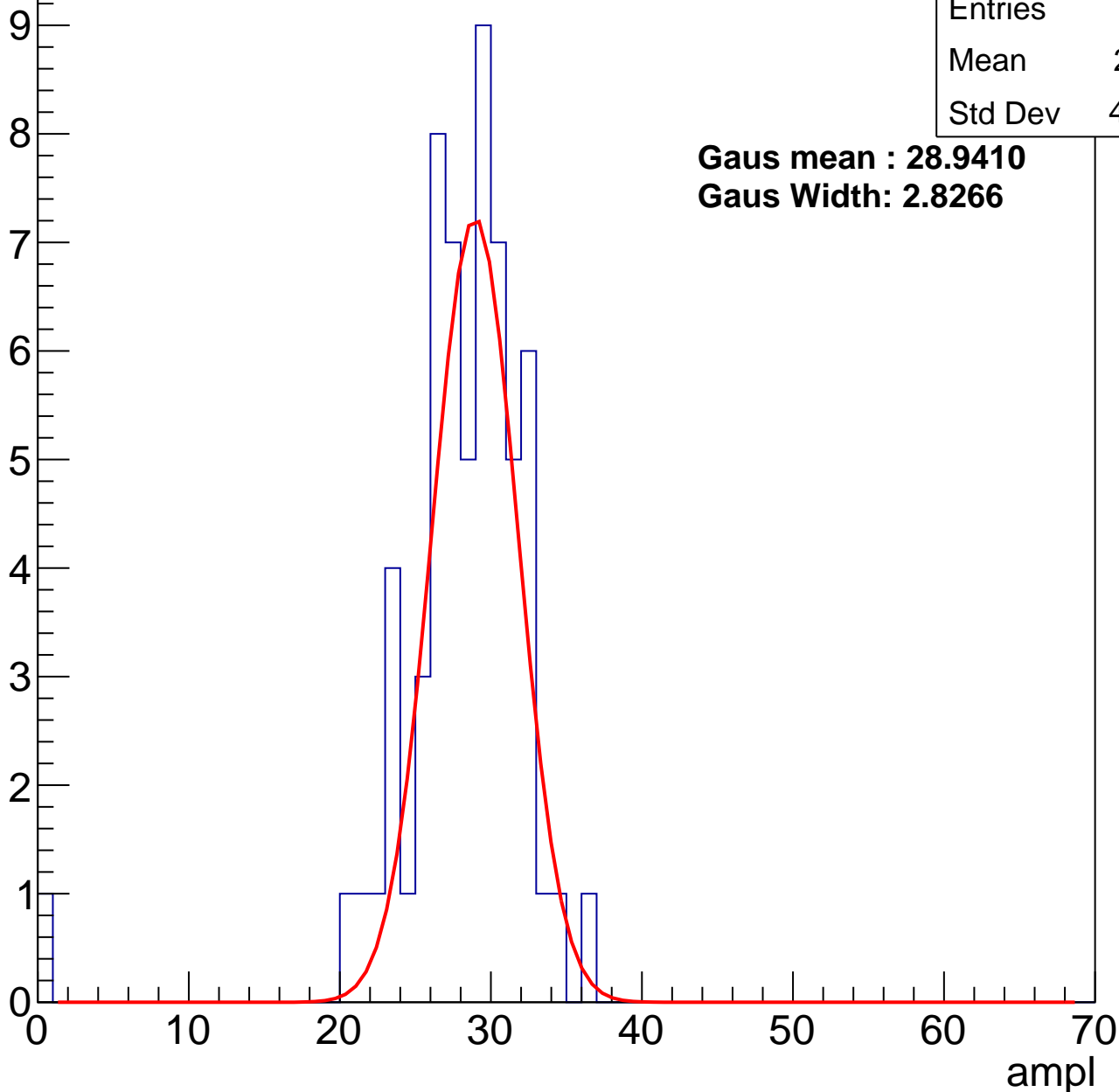
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.61
Std Dev	4.767

Gaus mean : 28.9410

Gaus Width: 2.8266



B1L100S, U1-ch86, adc1

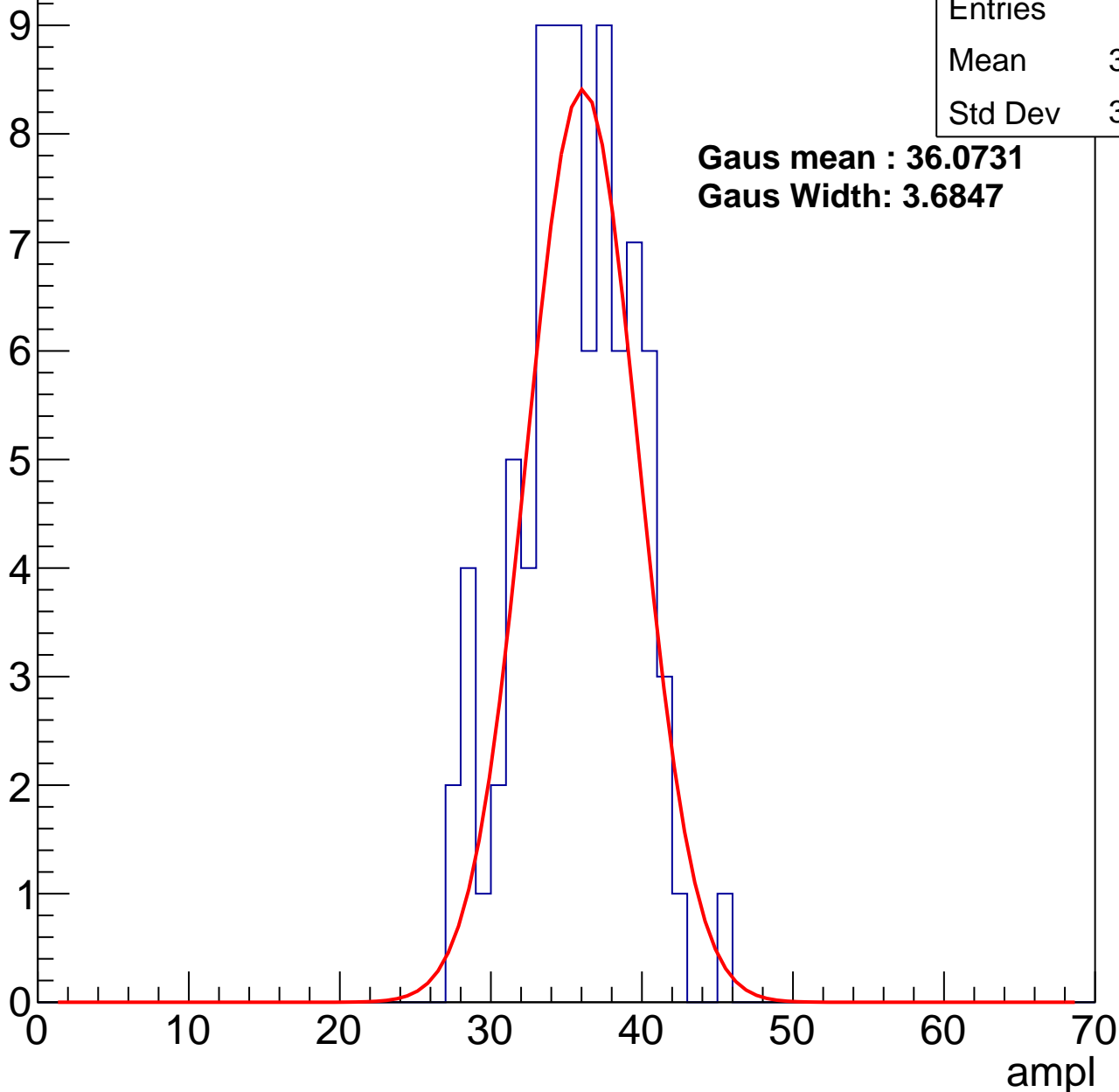
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	84
Mean	35.19
Std Dev	3.753

Gaus mean : 36.0731

Gaus Width: 3.6847



B1L100S, U1-ch86, adc2

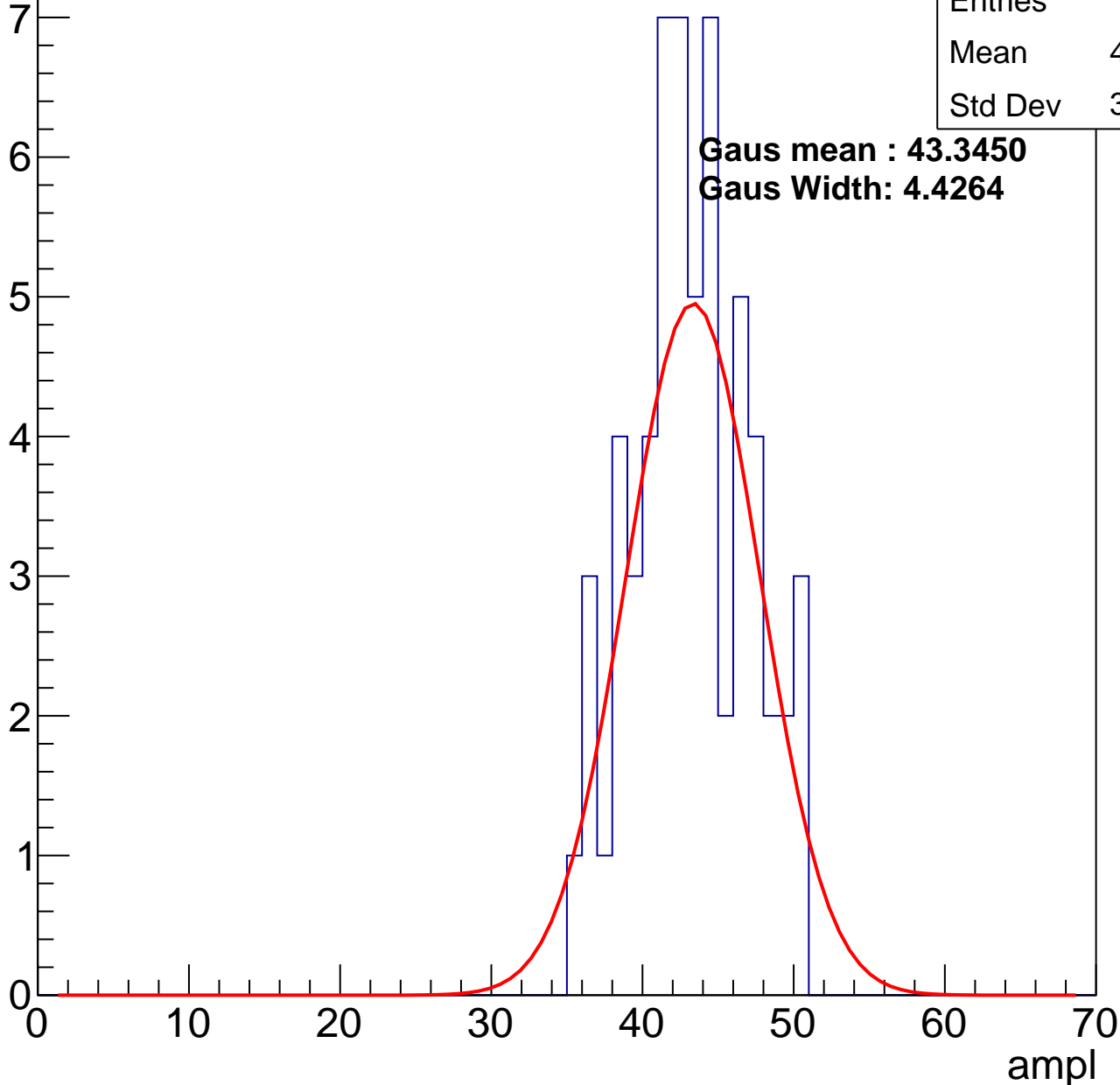
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	42.75
Std Dev	3.775

Gaus mean : 43.3450

Gaus Width: 4.4264



B1L100S, U1-ch86, adc3

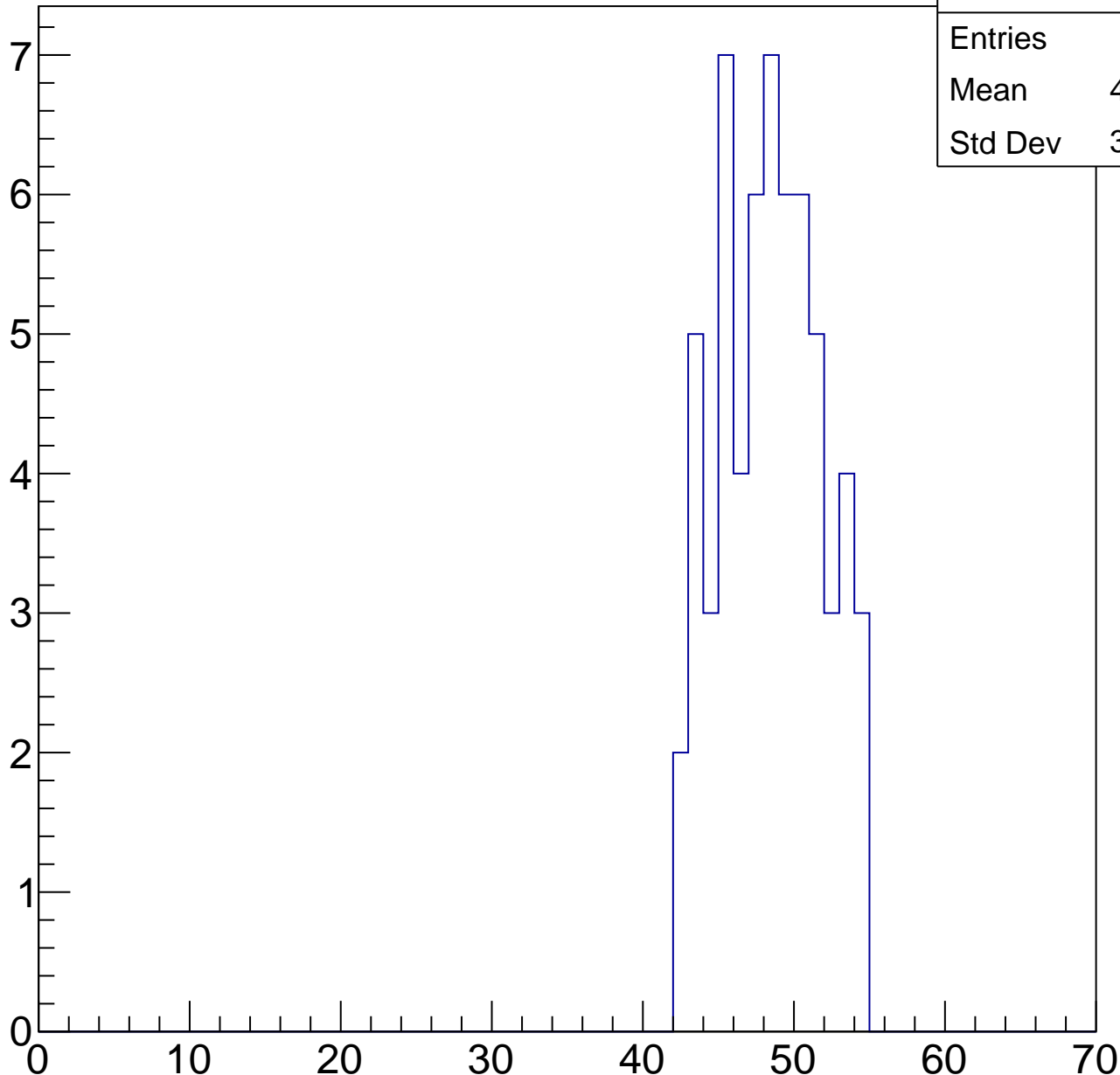
calib_packv5_042523_0143.root, FC#4, port A2

Entry

7
6
5
4
3
2
1
0

Entries	61
Mean	47.98
Std Dev	3.292

ampl

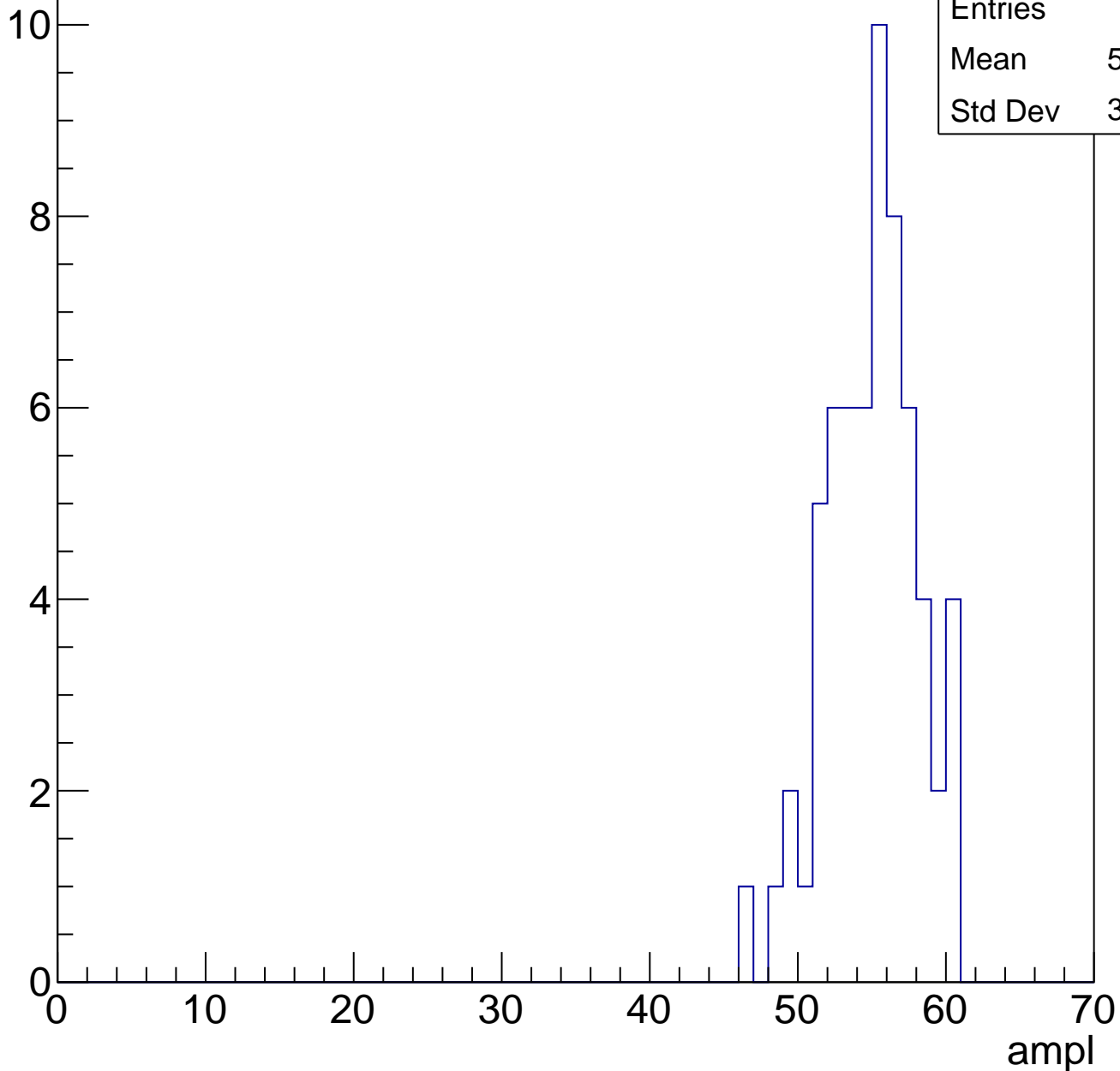


B1L100S, U1-ch86, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	54.53
Std Dev	3.052



B1L100S, U1-ch86, adc5

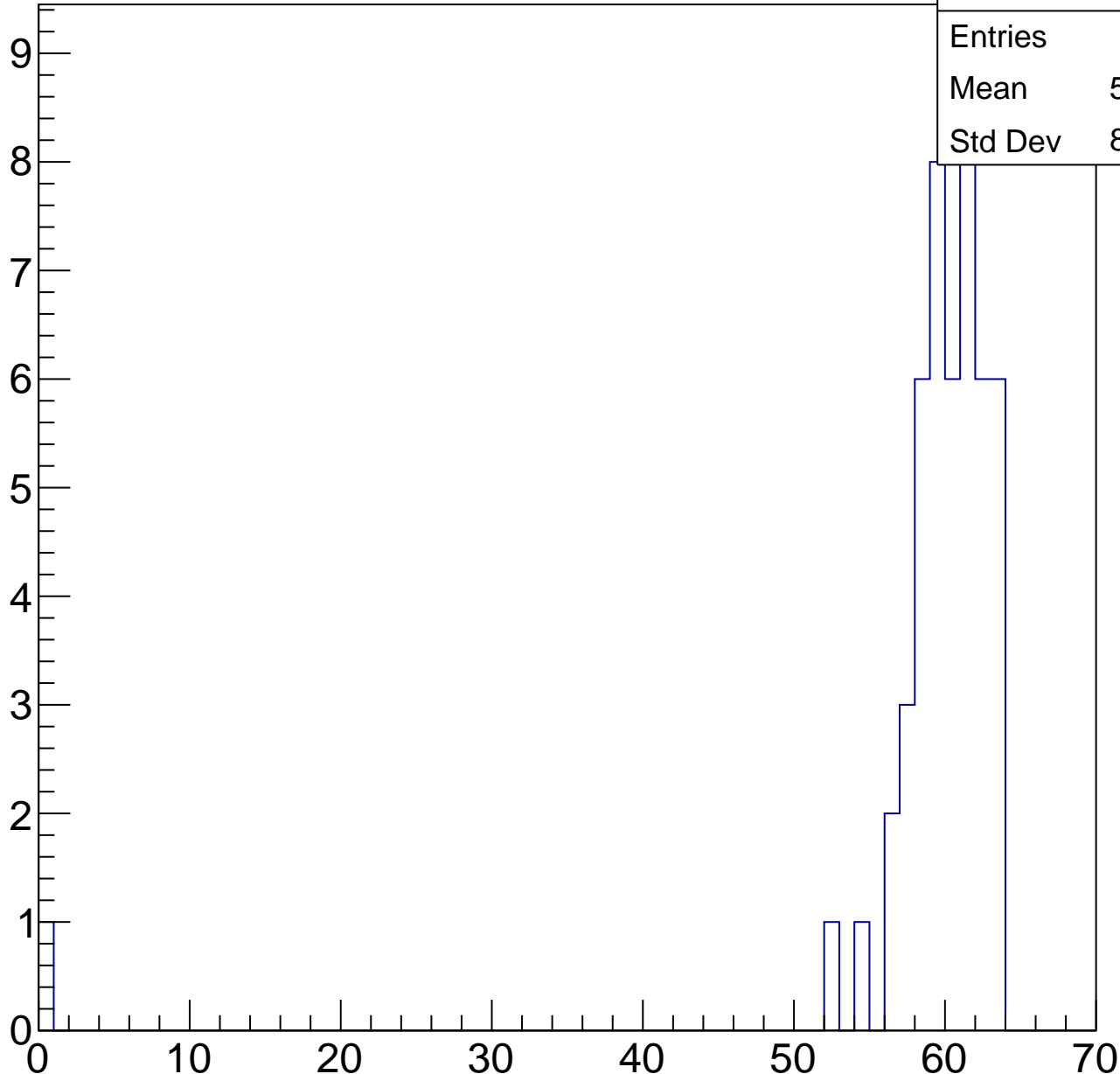
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	49
Mean	58.53
Std Dev	8.774

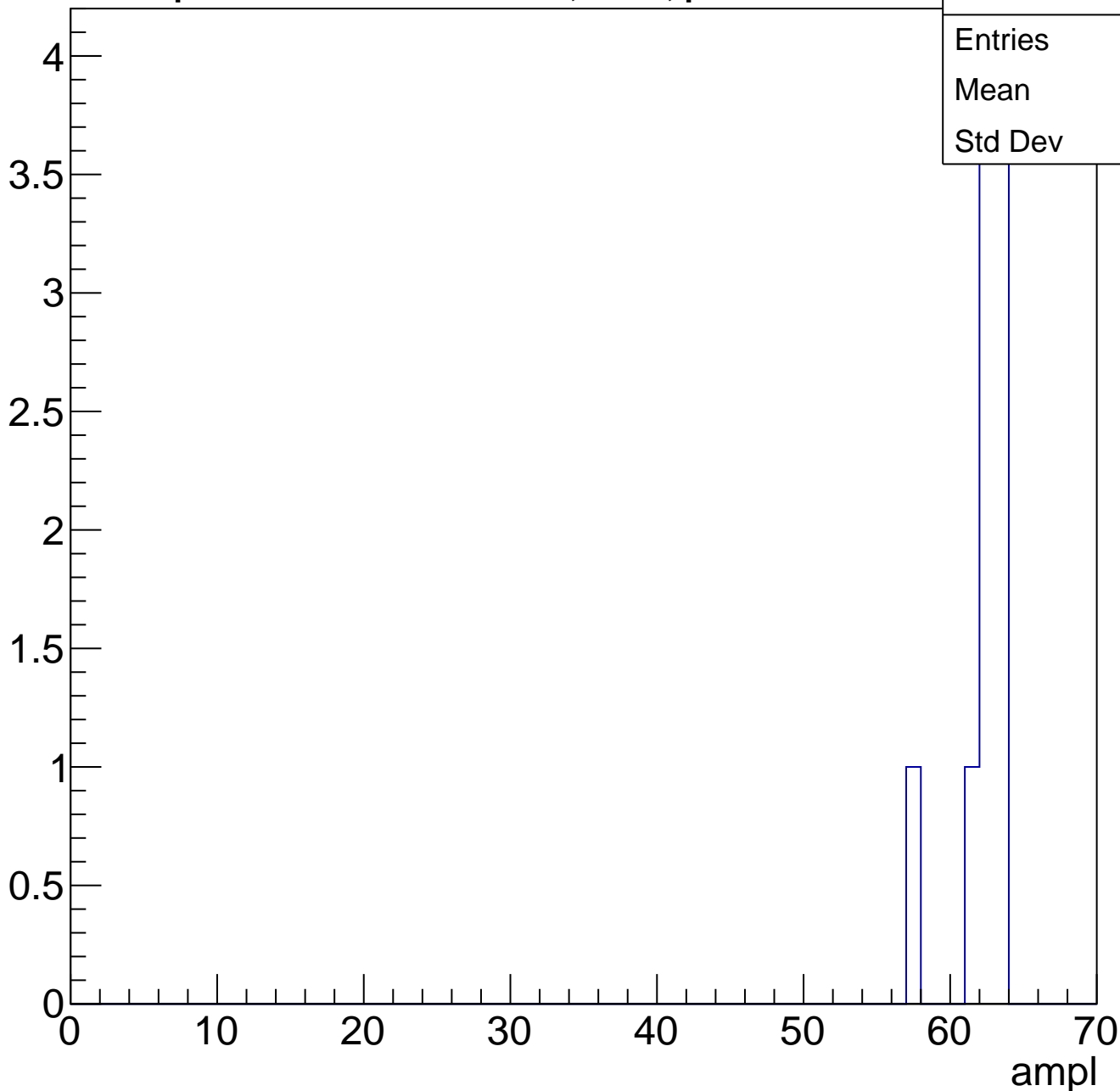
ampl



B1L100S, U1-ch86, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch86, adc7

calib_packv5_042523_0143.root, FC#4, port A2

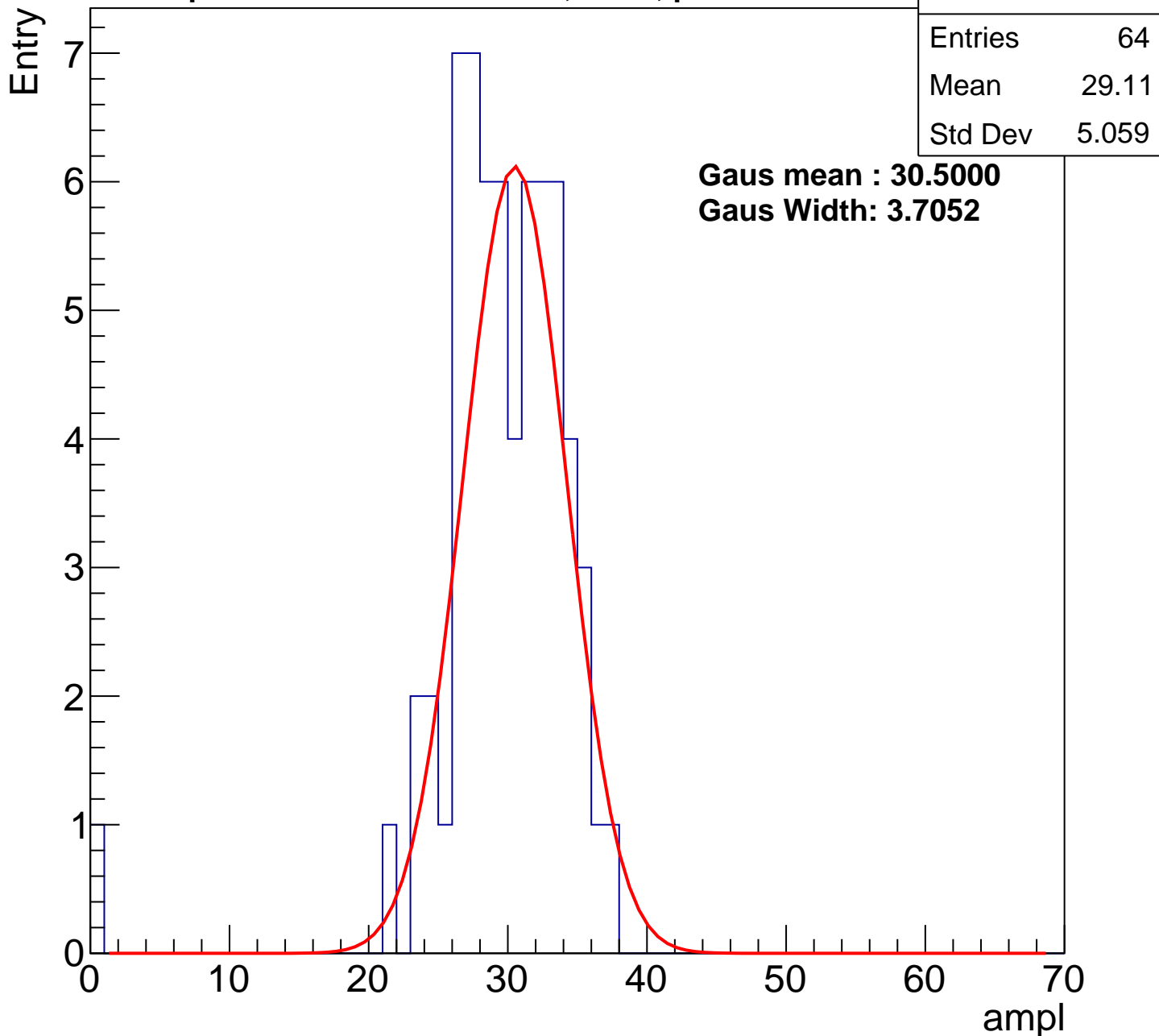
Entry



Entries	1
Mean	20
Std Dev	0

B1L100S, U1-ch87, adc0

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch87, adc1

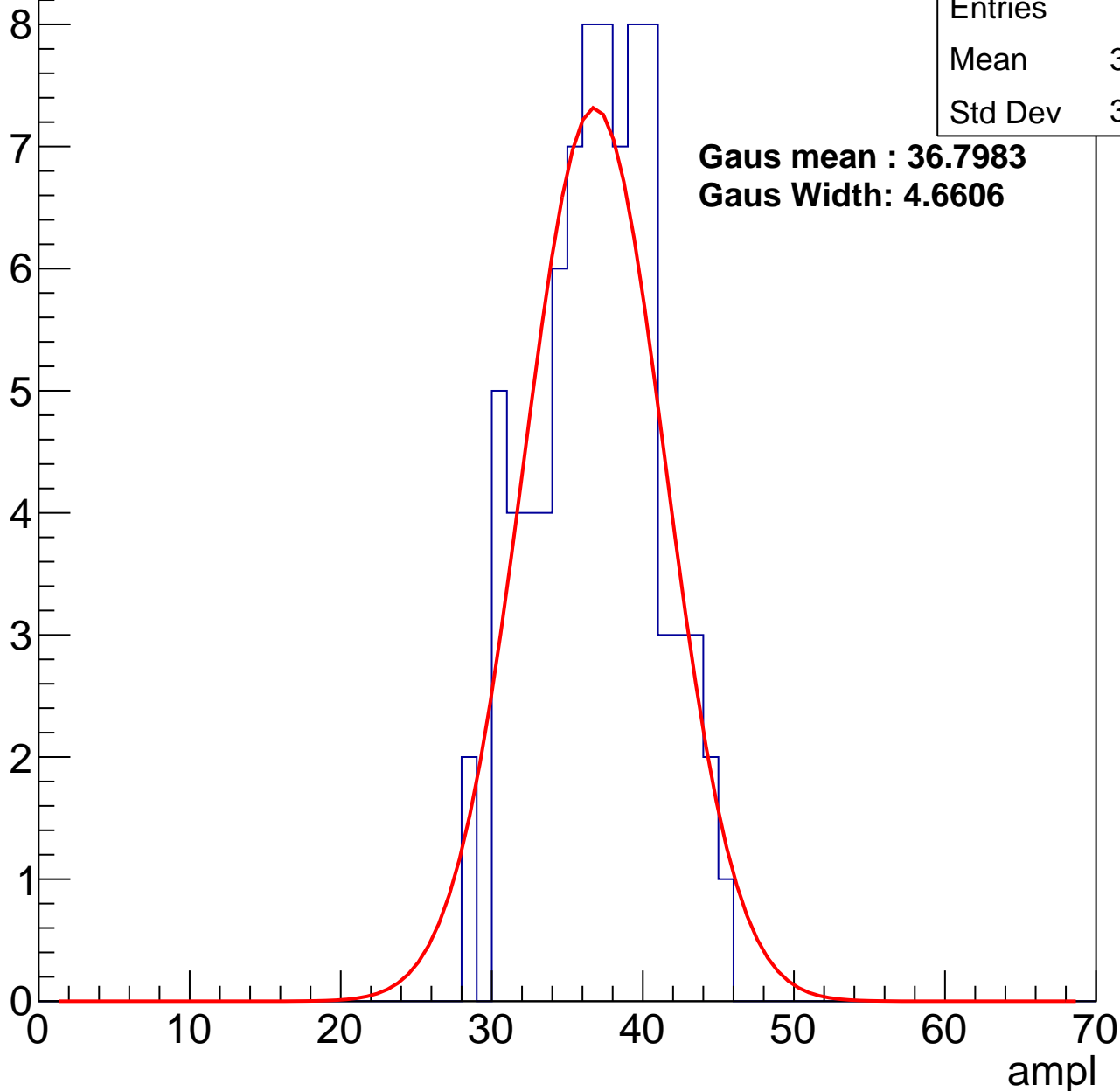
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	36.53
Std Dev	3.943

Gaus mean : 36.7983

Gaus Width: 4.6606



B1L100S, U1-ch87, adc2

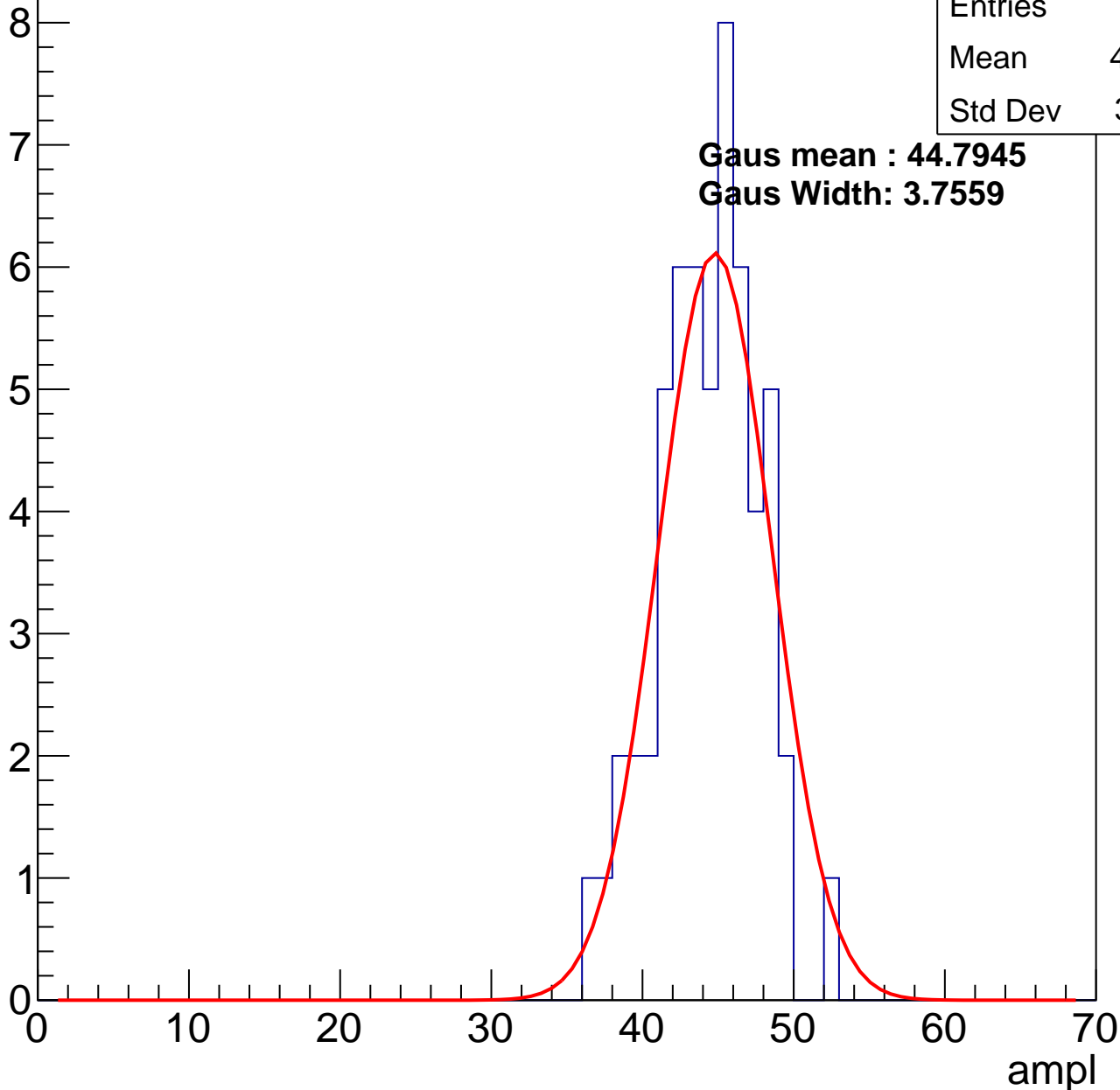
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	43.86
Std Dev	3.281

Gaus mean : 44.7945

Gaus Width: 3.7559



B1L100S, U1-ch87, adc3

calib_packv5_042523_0143.root, FC#4, port A2

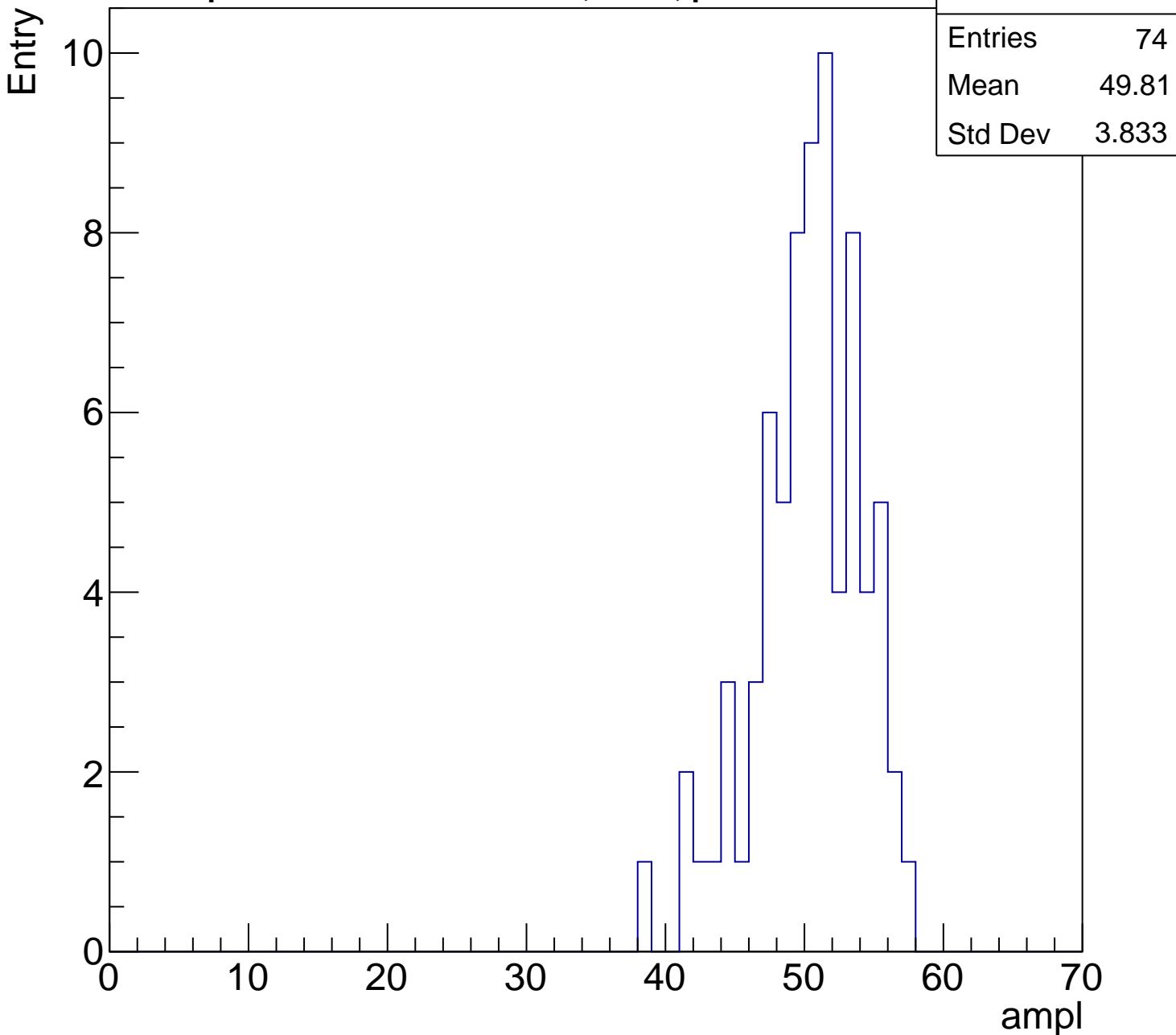
Entries	74
Mean	49.81
Std Dev	3.833

Entry

10
8
6
4
2
0

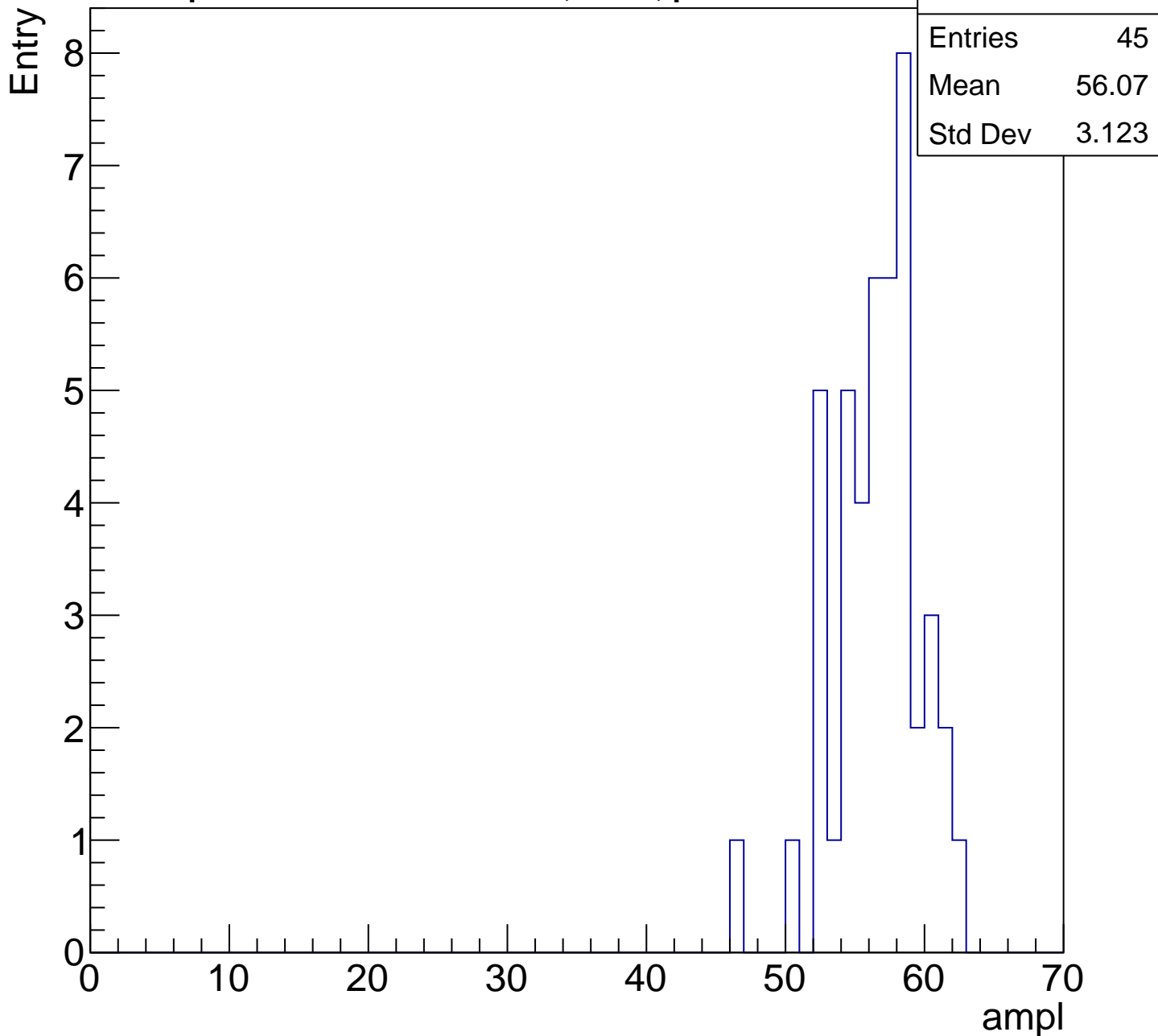
ampl

0 10 20 30 40 50 60 70



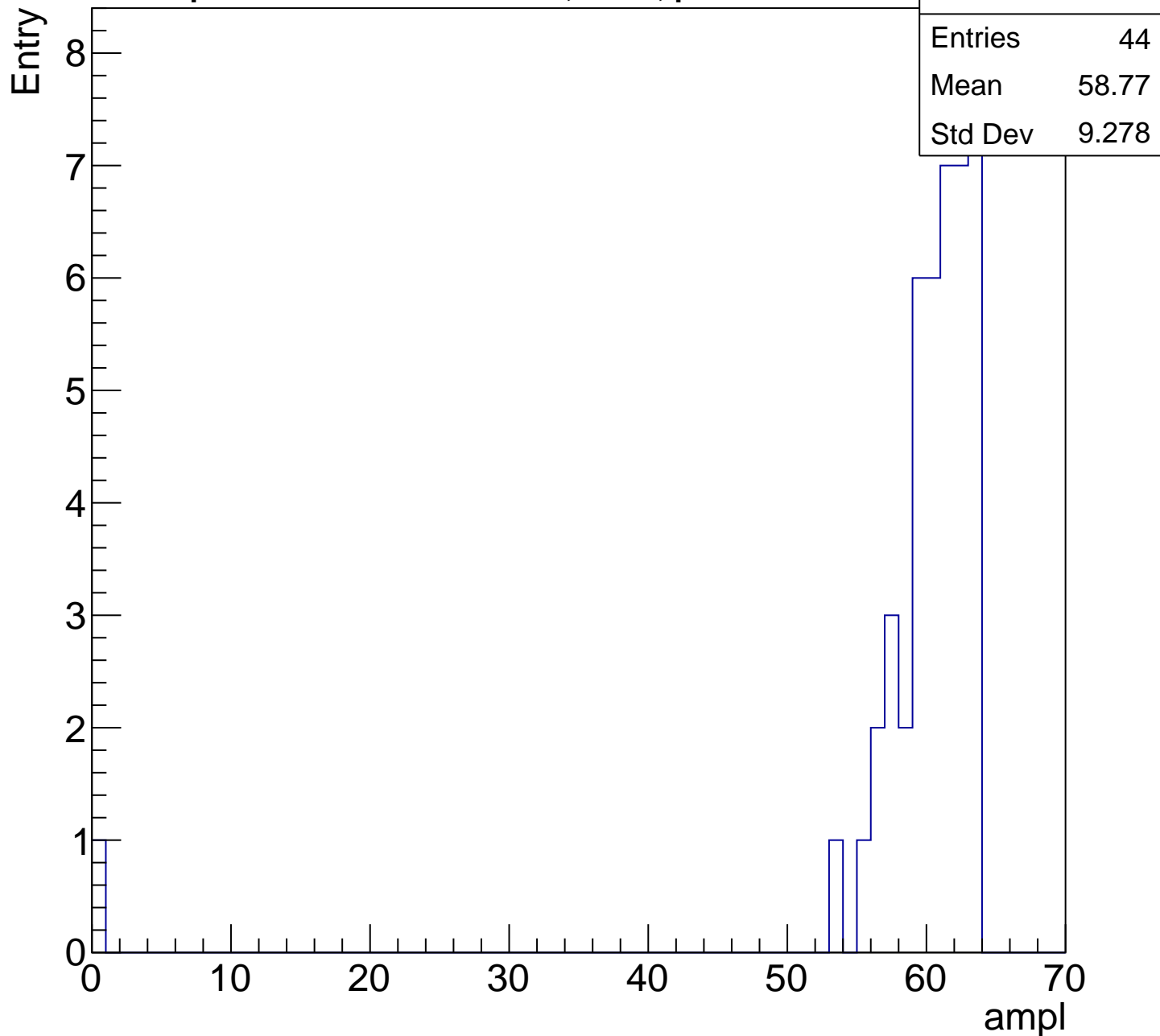
B1L100S, U1-ch87, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch87, adc5

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch87, adc6

calib_packv5_042523_0143.root, FC#4, port A2

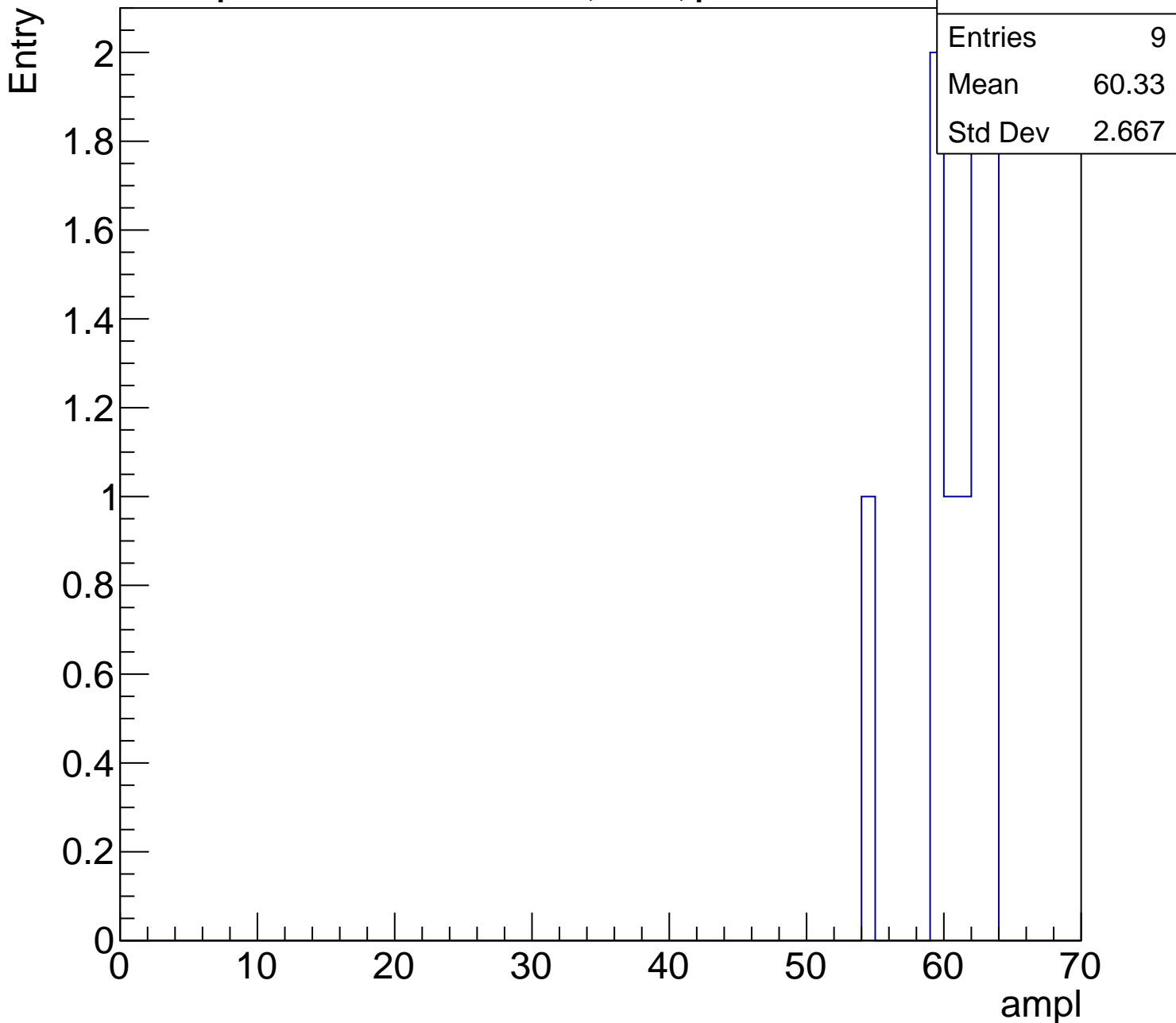
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	9
Mean	60.33
Std Dev	2.667

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch87, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch88, adc0

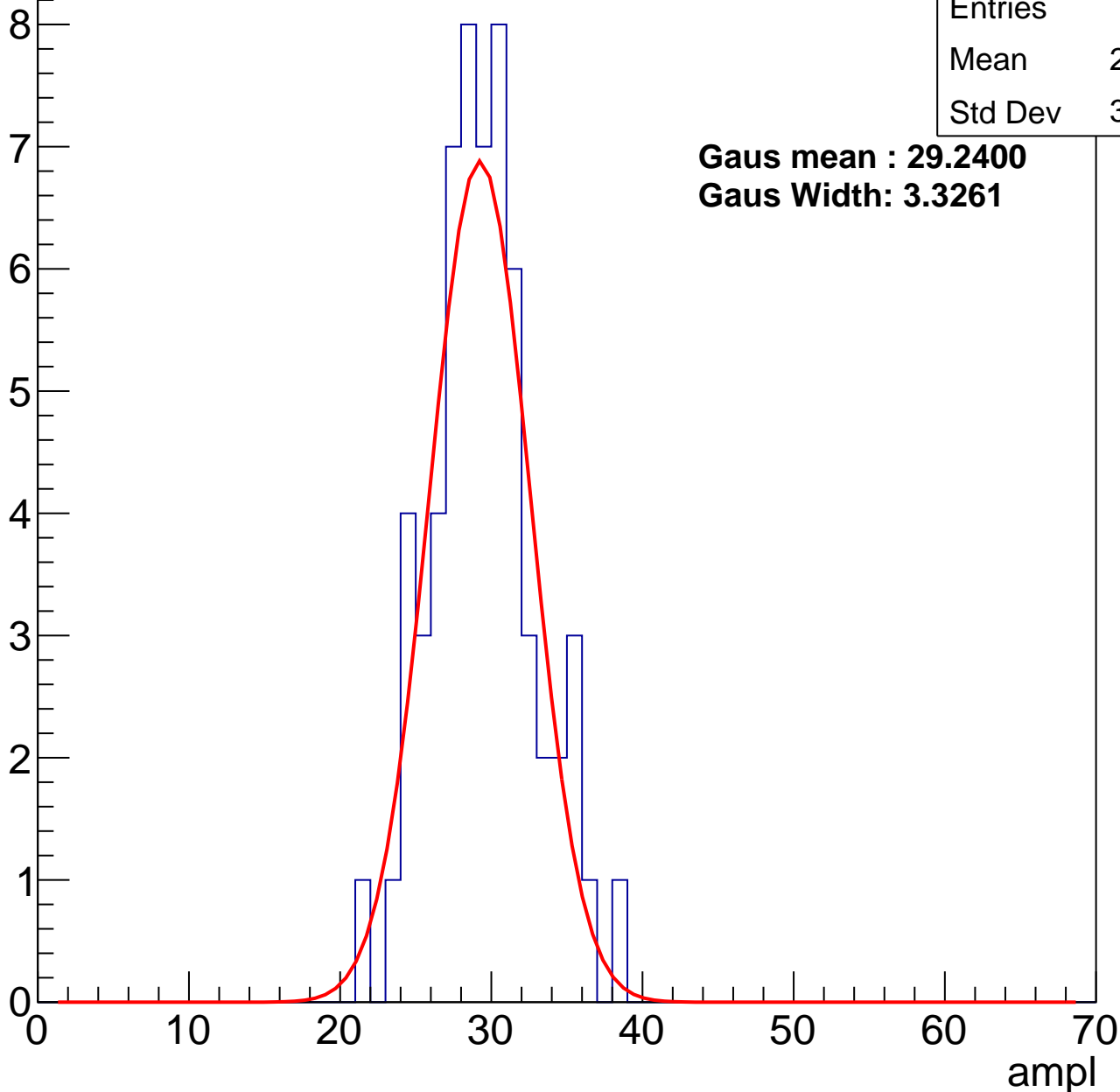
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.02
Std Dev	3.385

Gaus mean : 29.2400

Gaus Width: 3.3261



B1L100S, U1-ch88, adc1

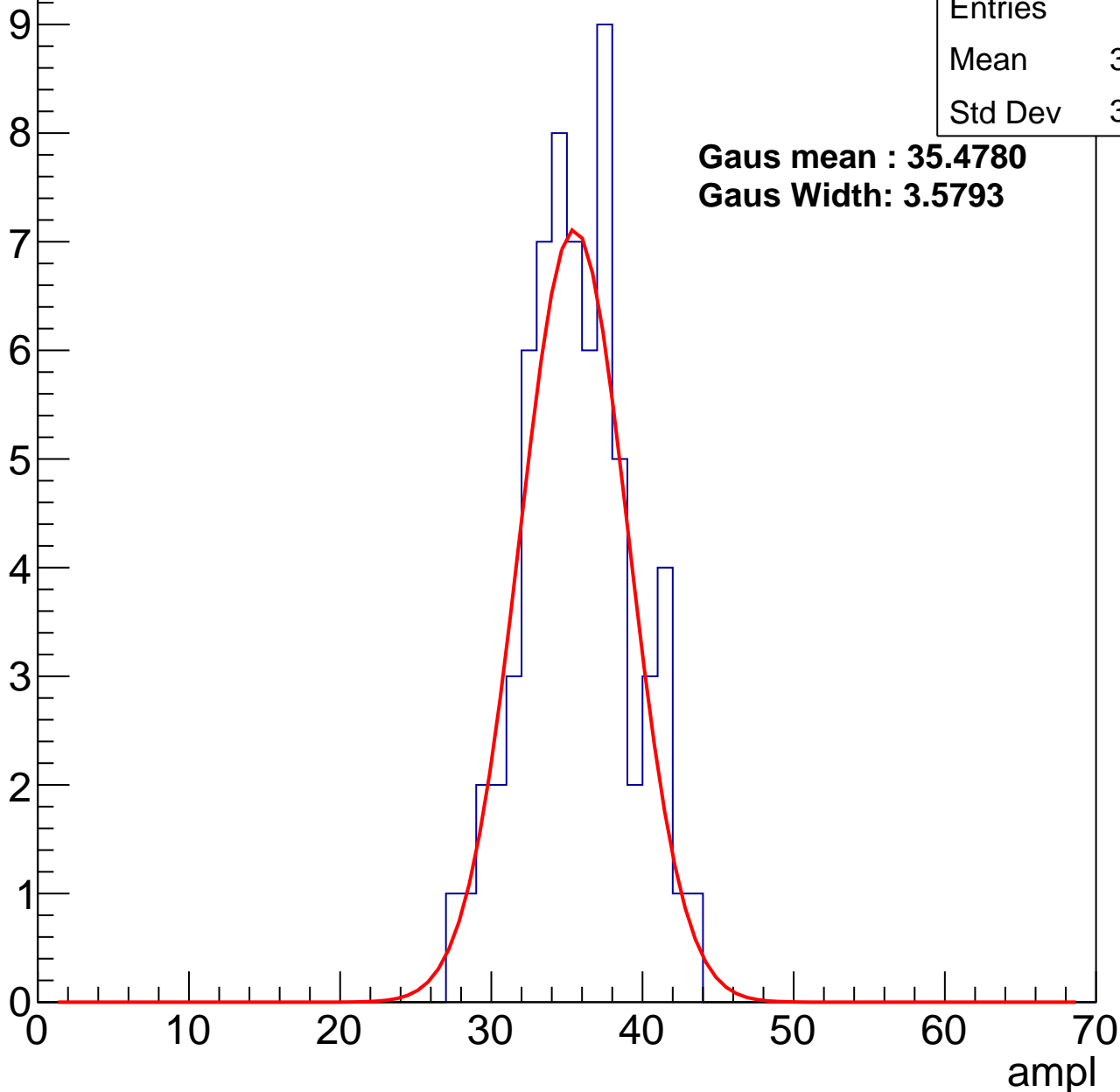
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	35.18
Std Dev	3.477

Gaus mean : 35.4780

Gaus Width: 3.5793



B1L100S, U1-ch88, adc2

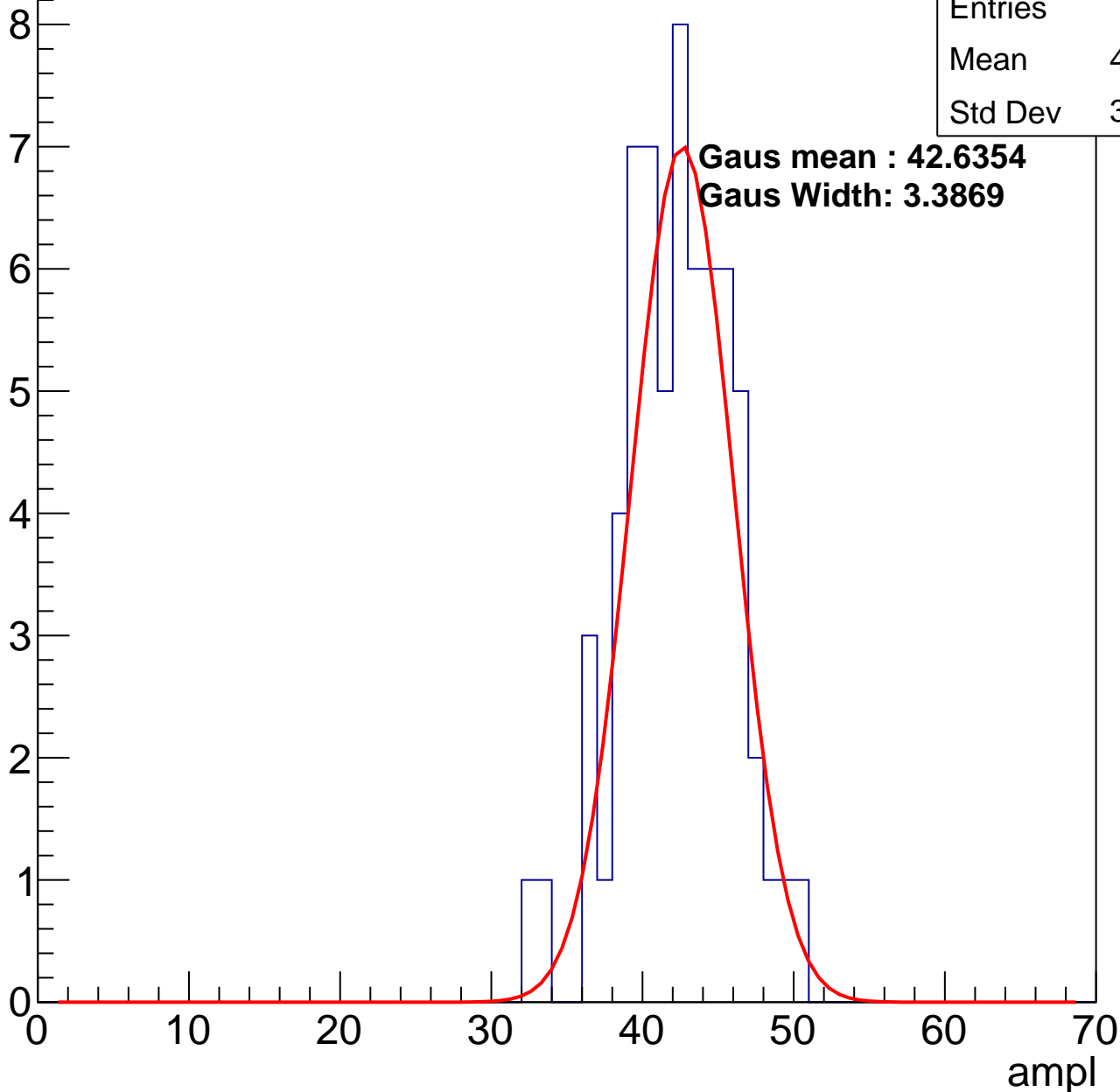
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	41.83
Std Dev	3.584

Gaus mean : 42.6354

Gaus Width: 3.3869

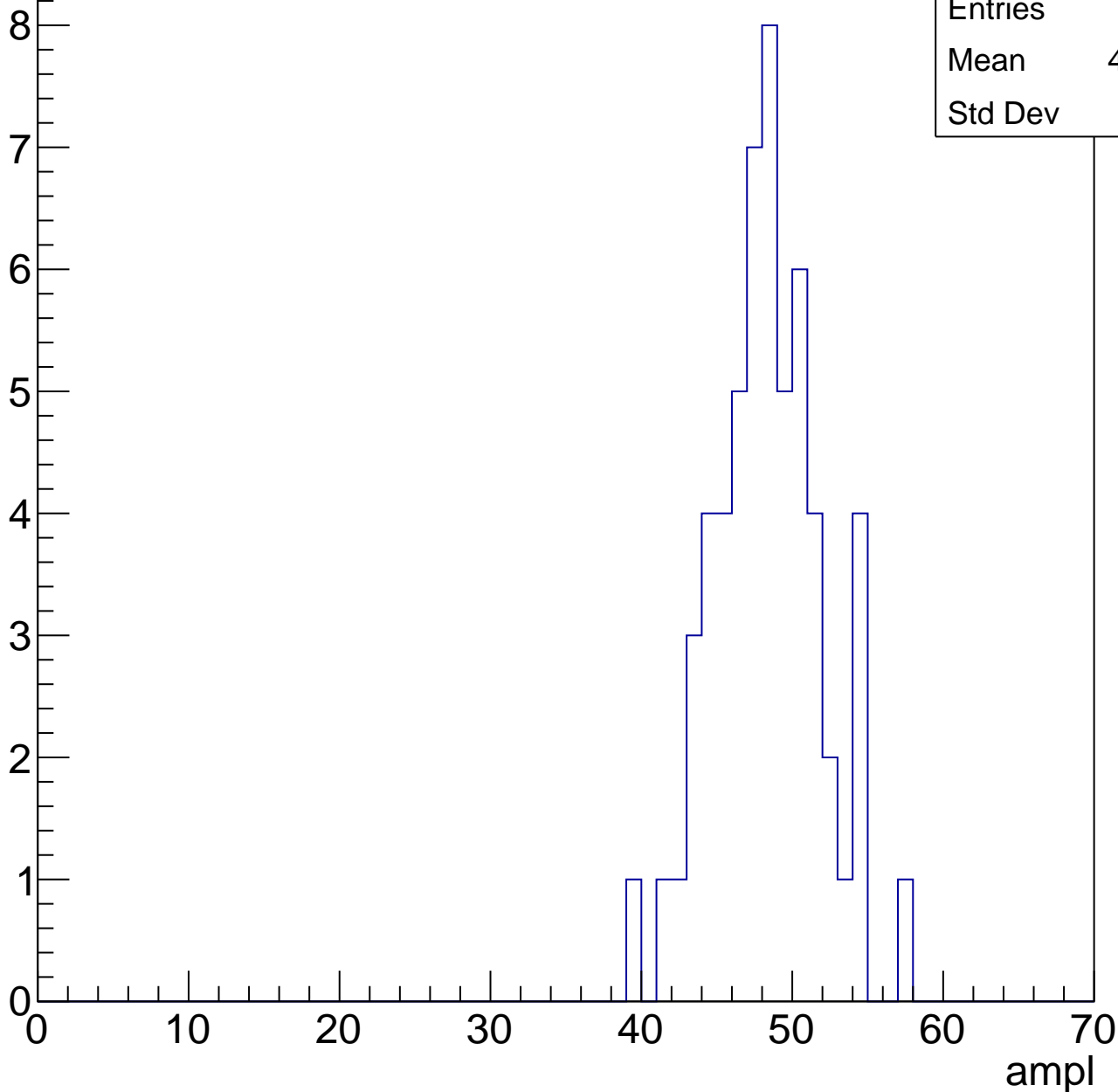


B1L100S, U1-ch88, adc3

calib_packv5_042523_0143.root, FC#4, port A2

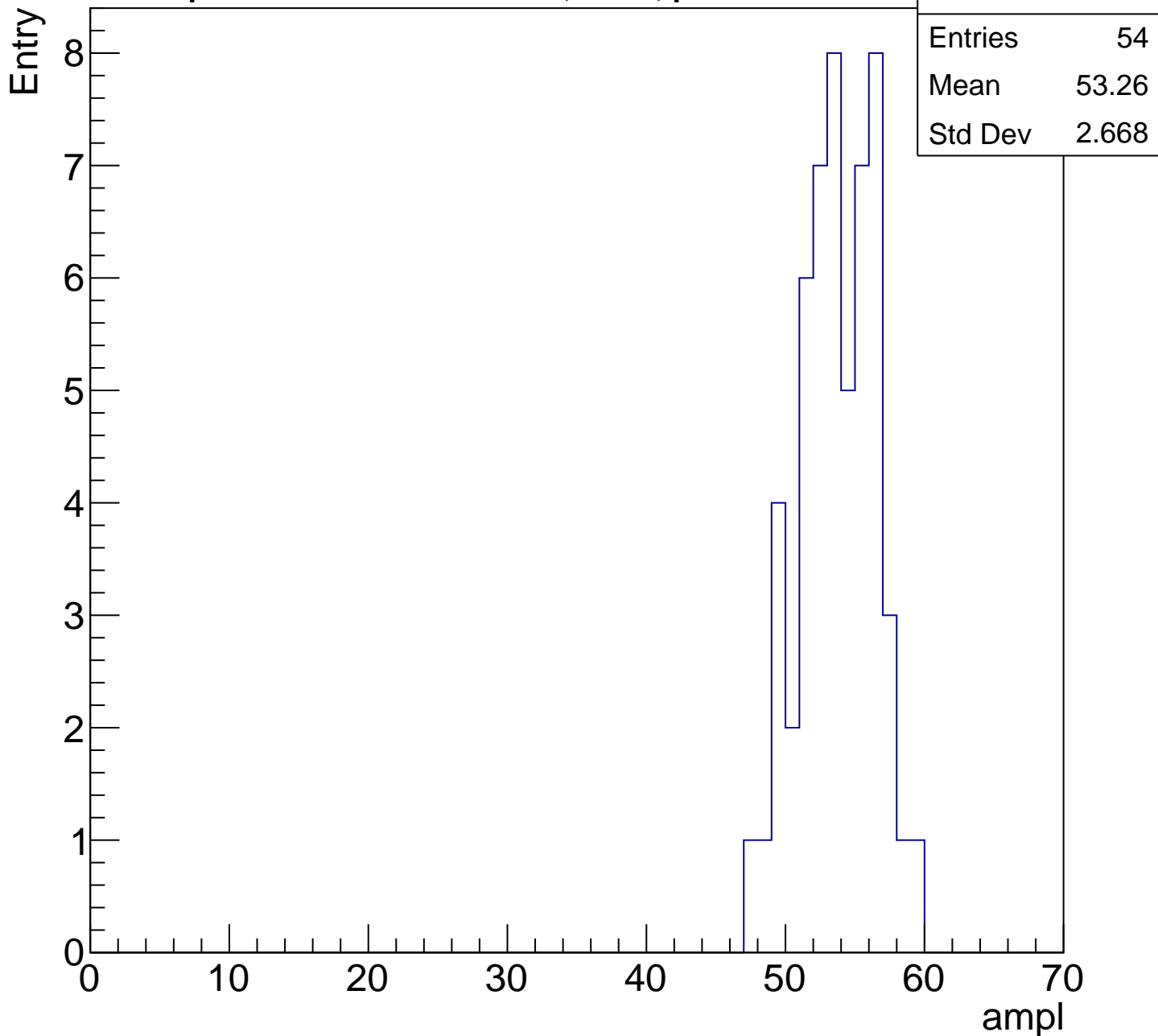
Entry

Entries	57
Mean	47.88
Std Dev	3.54



B1L100S, U1-ch88, adc4

calib_packv5_042523_0143.root, FC#4, port A2

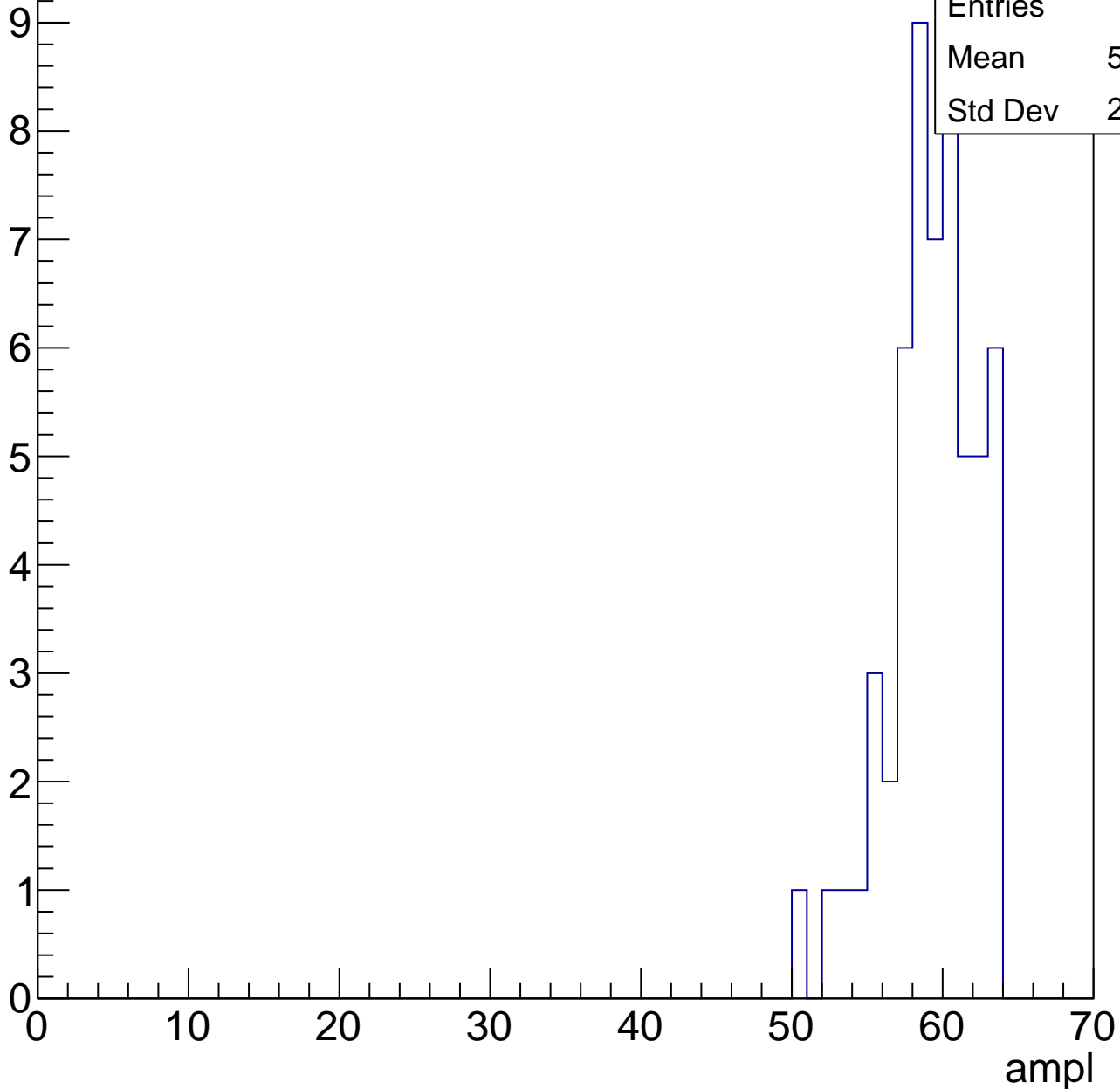


B1L100S, U1-ch88, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	58.84
Std Dev	2.884

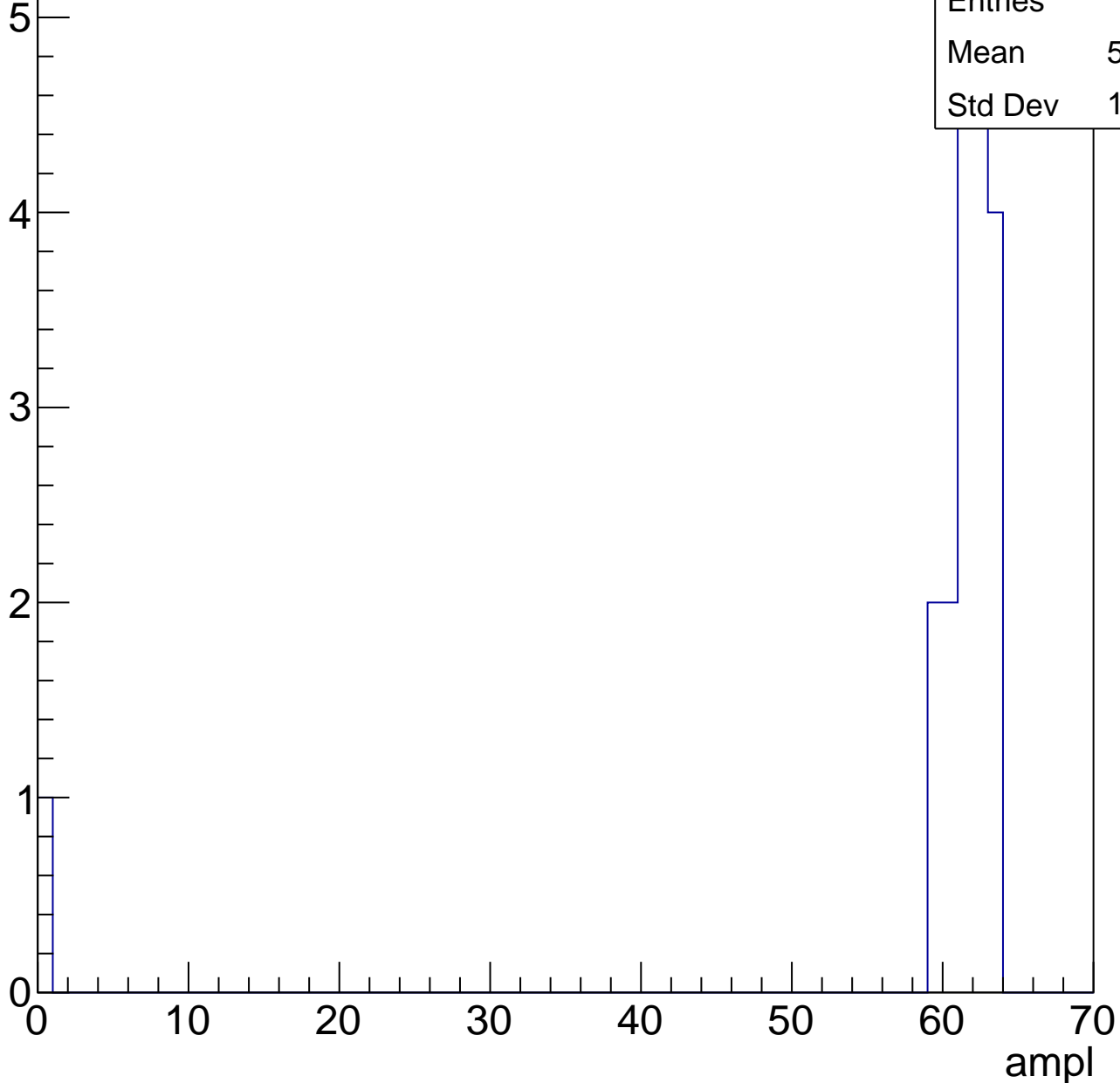


B1L100S, U1-ch88, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	58.16
Std Dev	13.76



B1L100S, U1-ch88, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch89, adc0

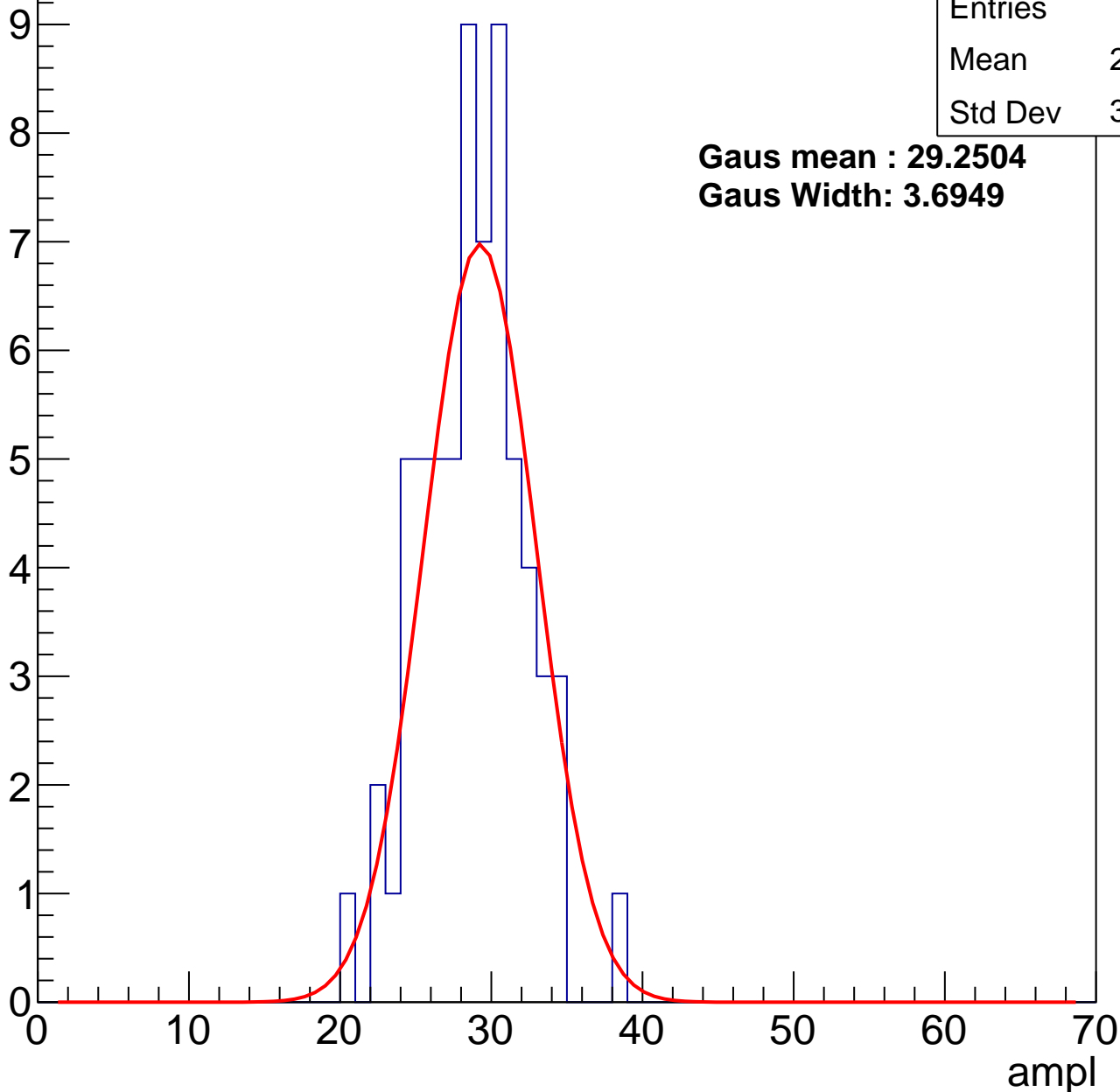
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	28.37
Std Dev	3.368

Gaus mean : 29.2504

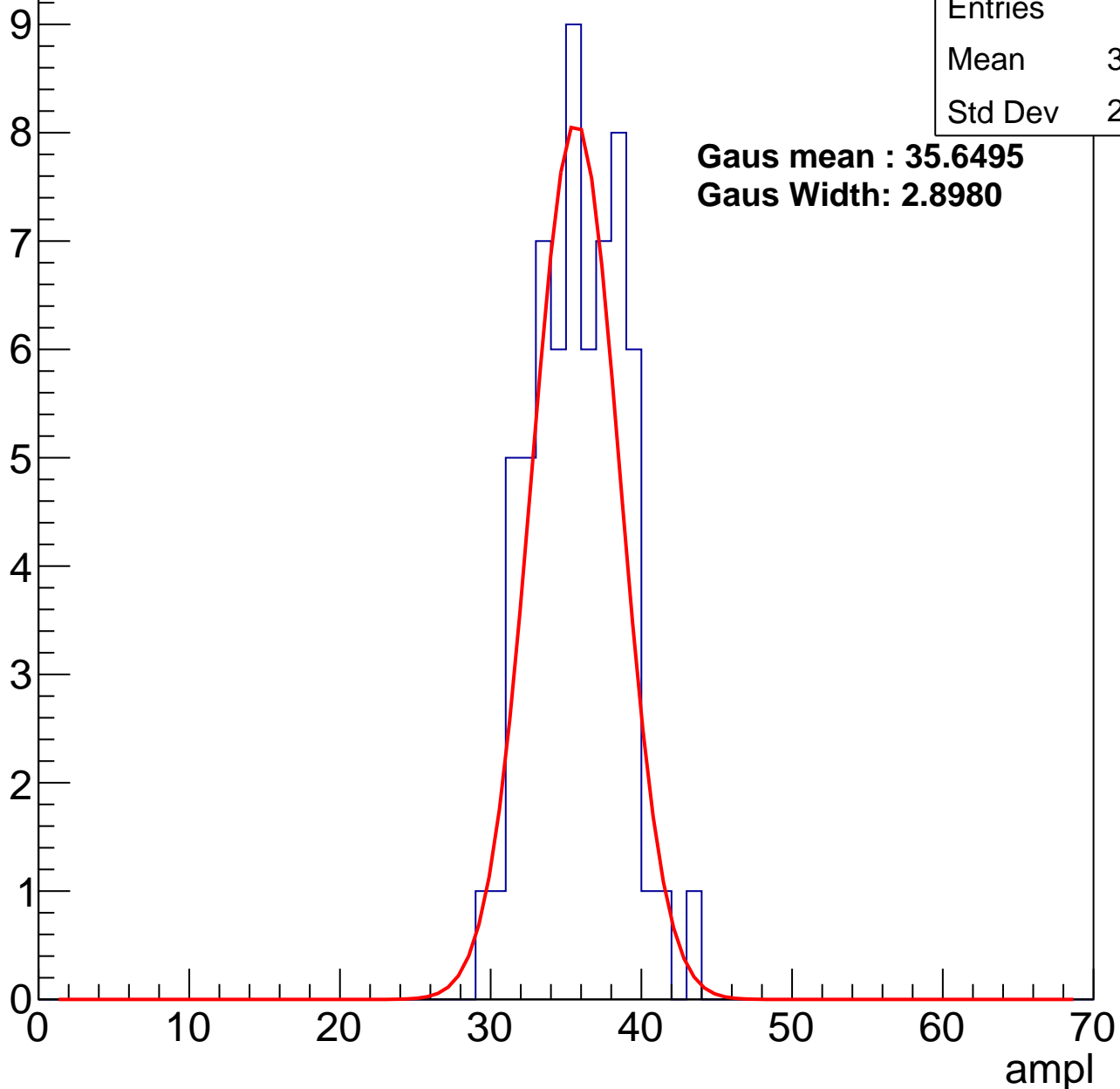
Gaus Width: 3.6949



B1L100S, U1-ch89, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch89, adc2

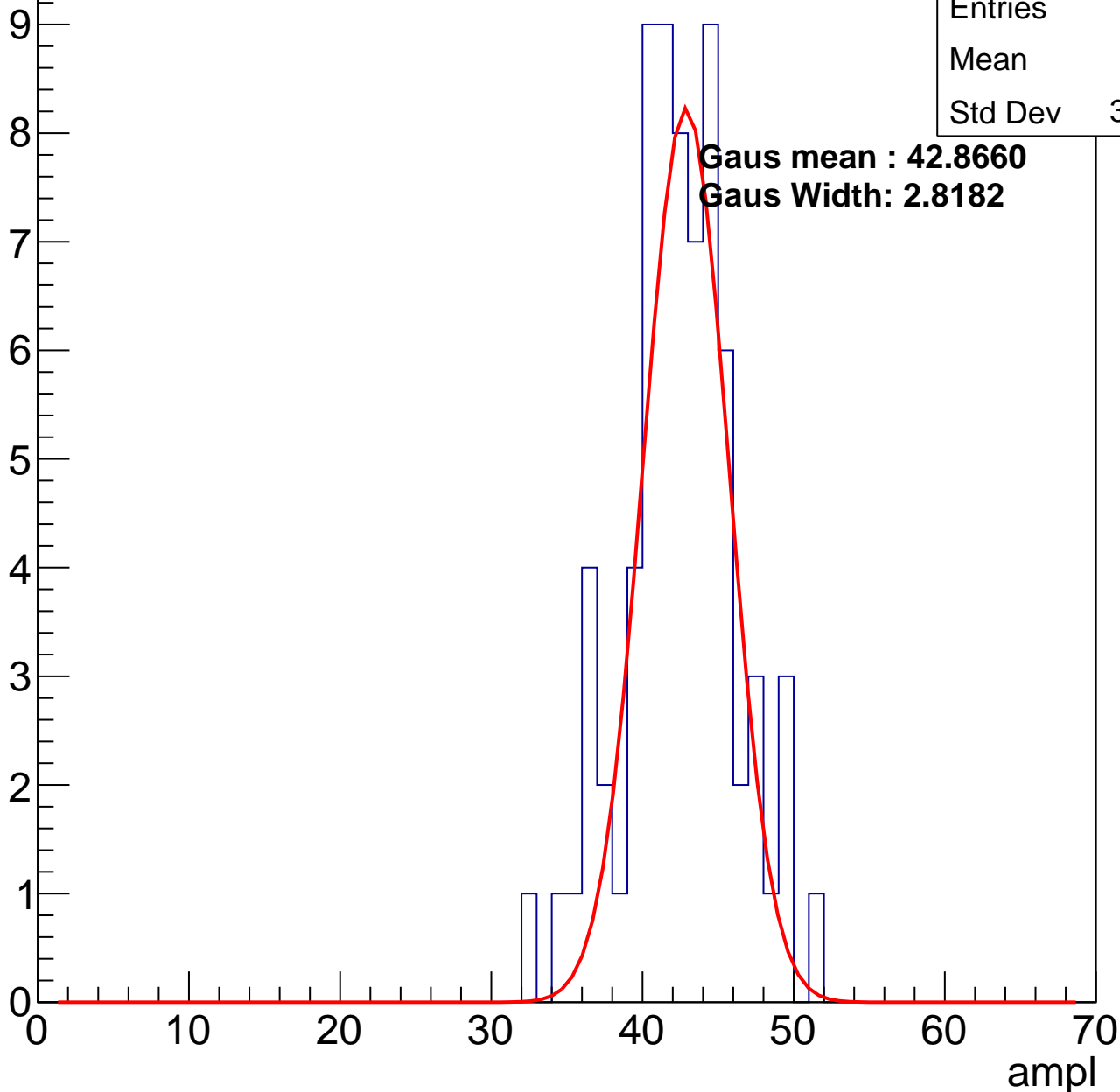
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	42
Std Dev	3.674

Gaus mean : 42.8660

Gaus Width: 2.8182

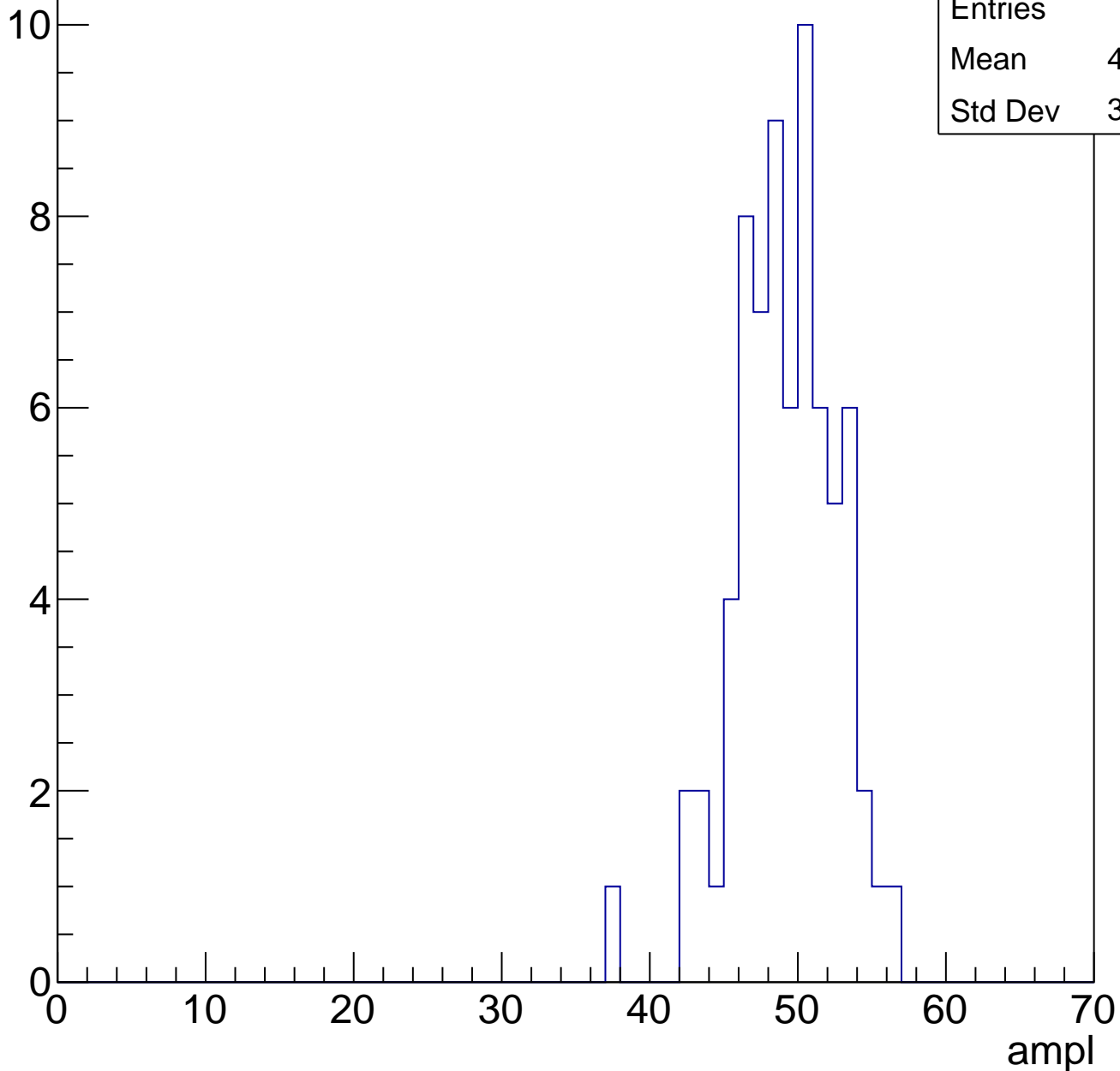


B1L100S, U1-ch89, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	71
Mean	48.69
Std Dev	3.388

Entry

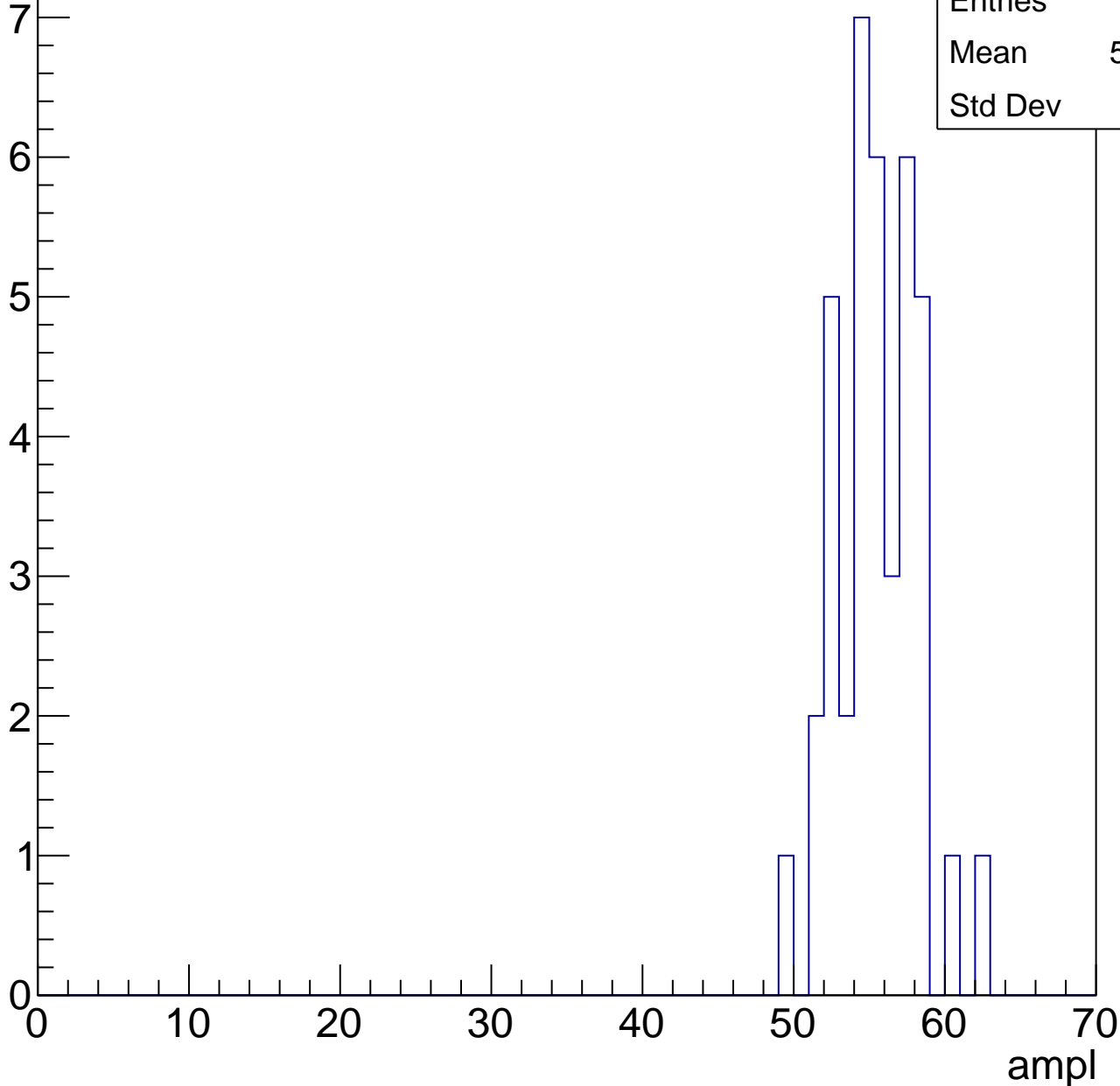


B1L100S, U1-ch89, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	39
Mean	55.05
Std Dev	2.65

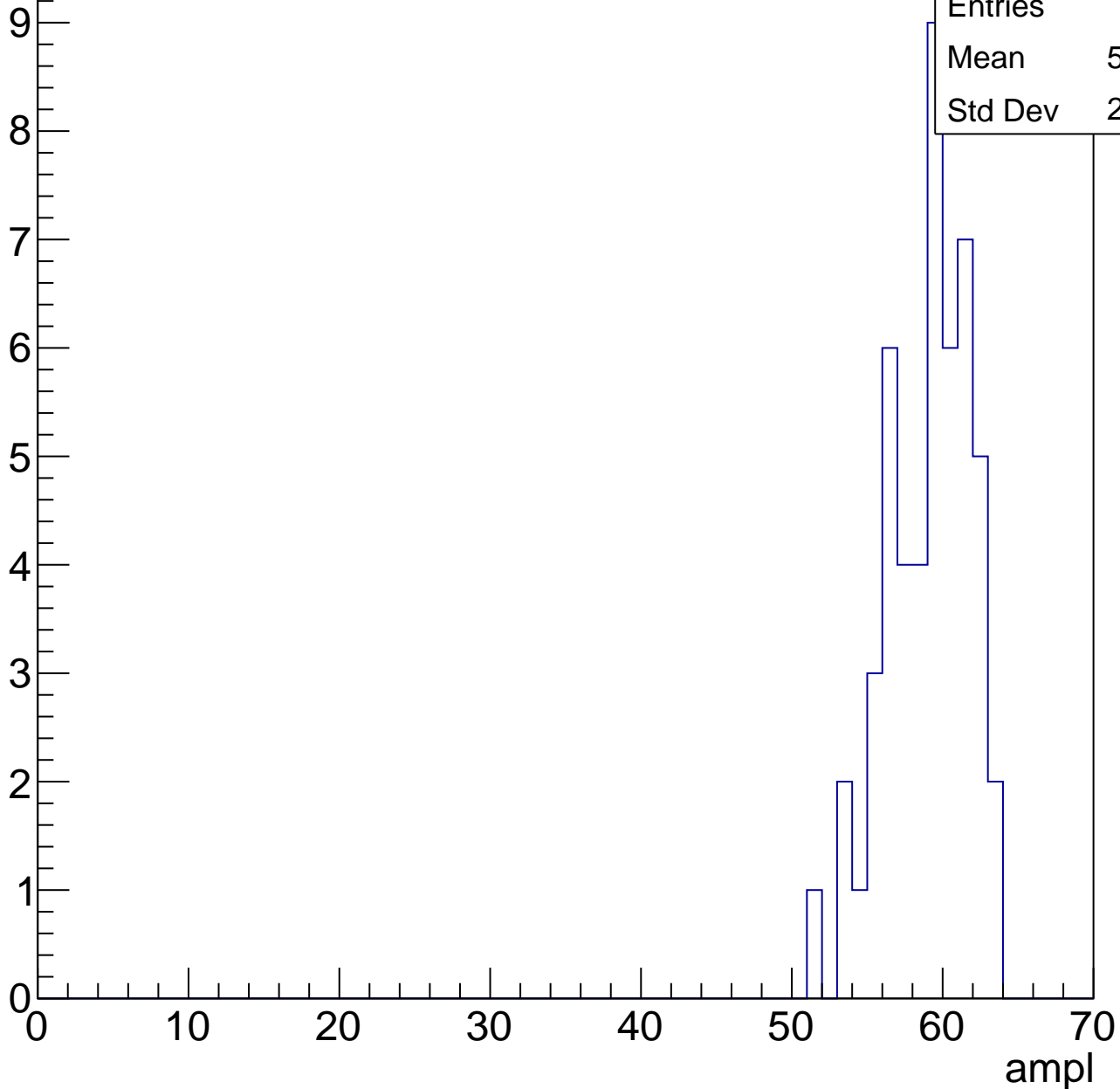


B1L100S, U1-ch89, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.52
Std Dev	2.766

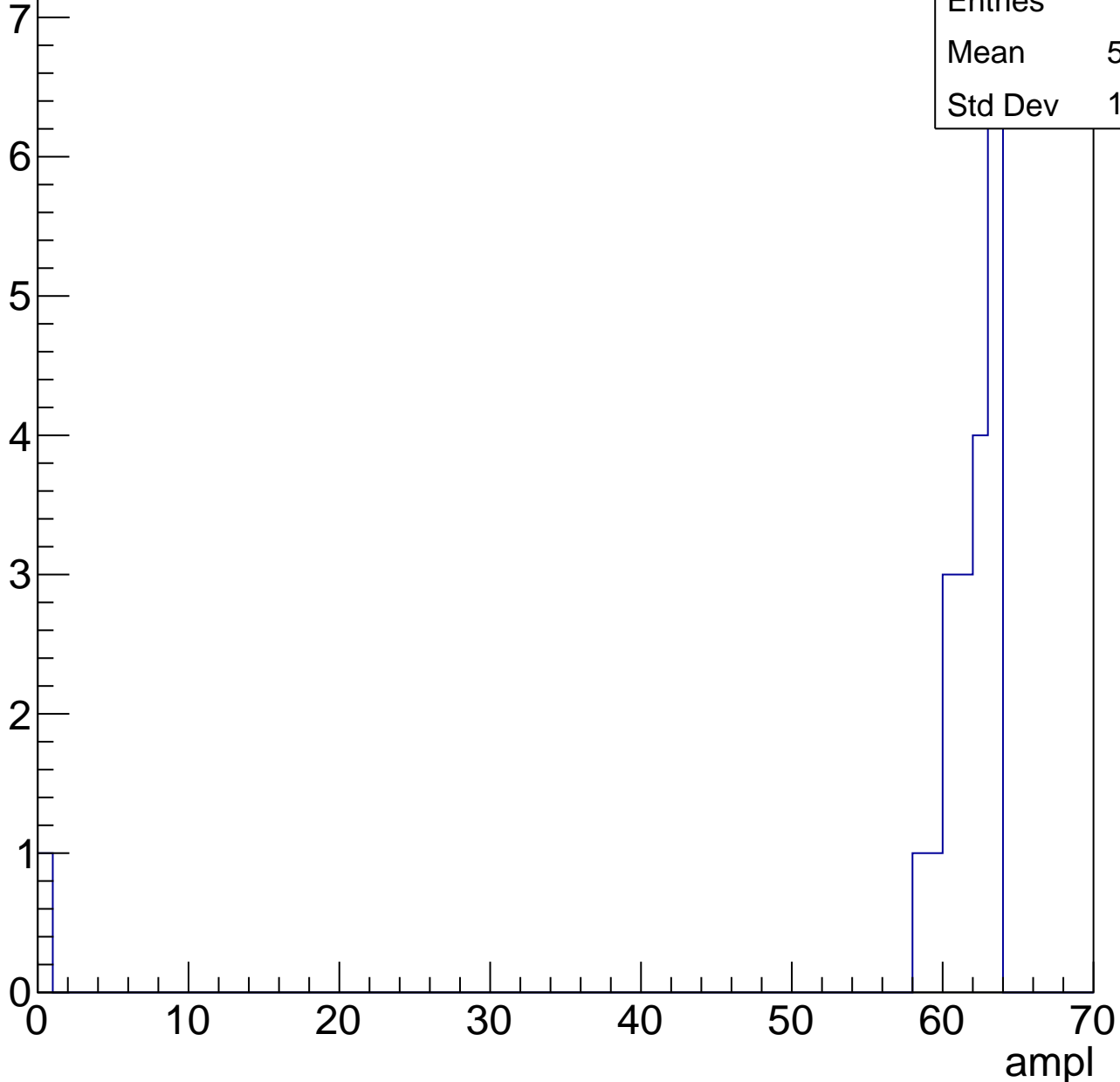


B1L100S, U1-ch89, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	20
Mean	58.45
Std Dev	13.49



B1L100S, U1-ch89, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch90, adc0

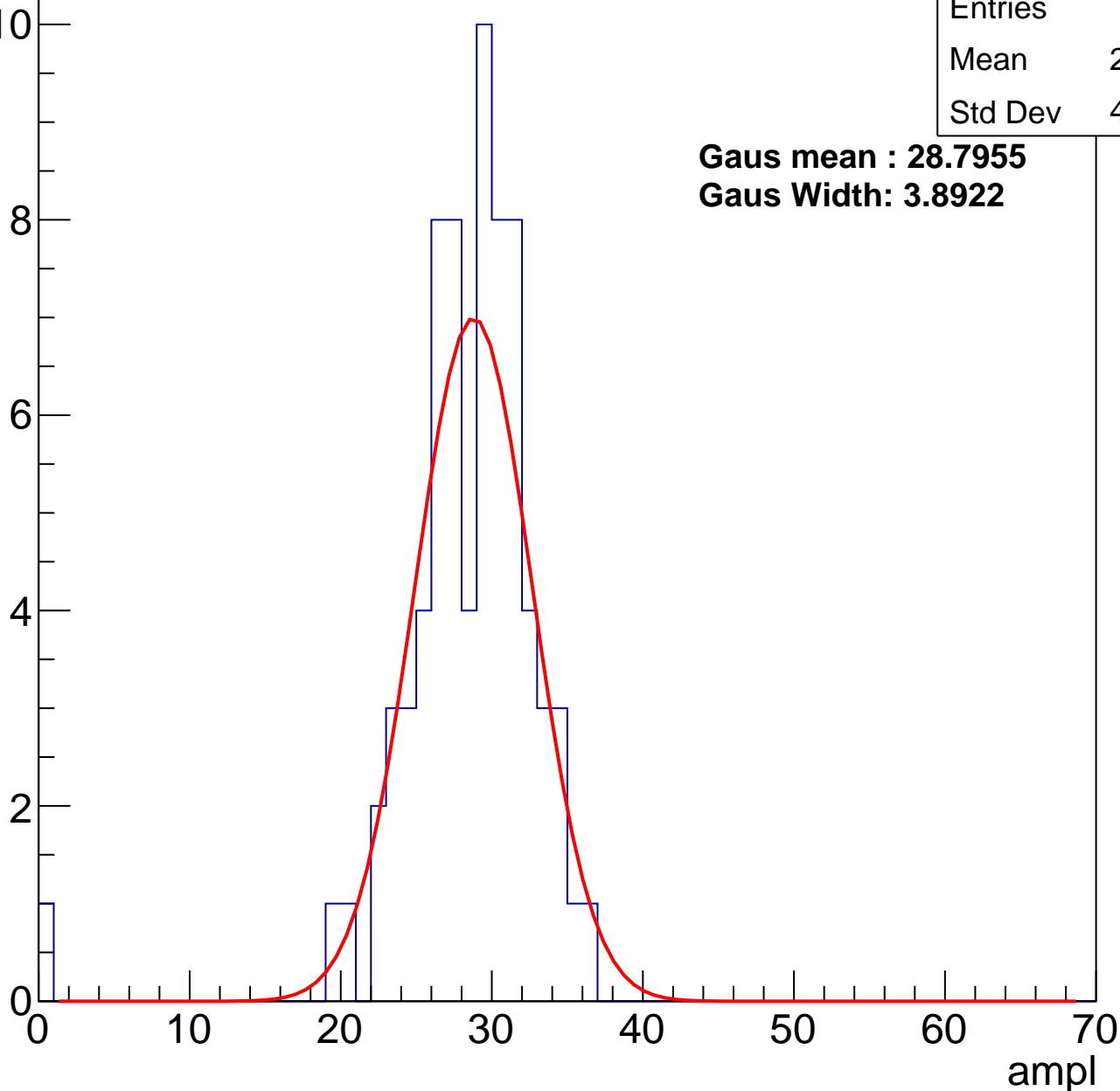
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	27.92
Std Dev	4.788

Gaus mean : 28.7955

Gaus Width: 3.8922



B1L100S, U1-ch90, adc1

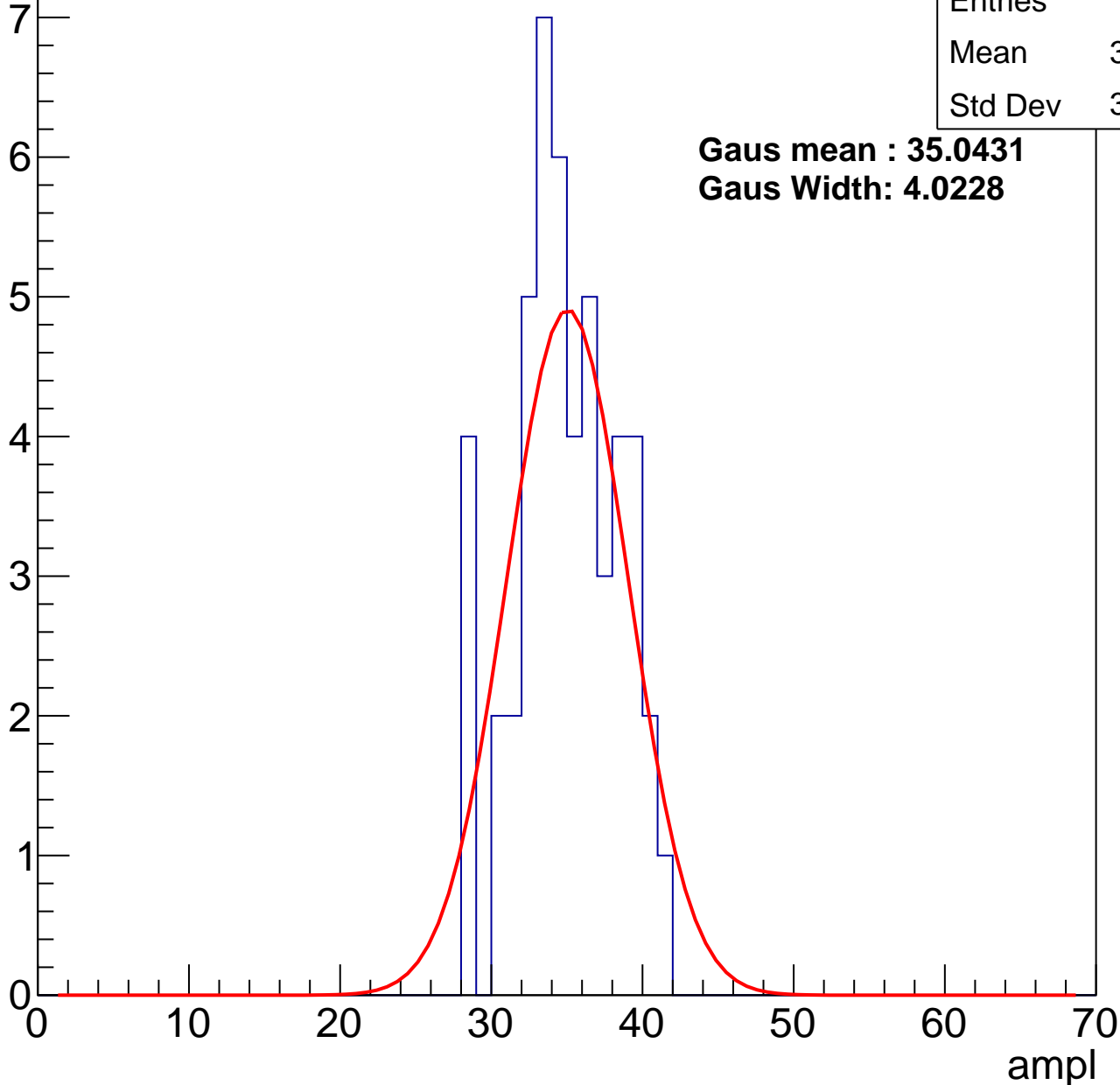
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	34.47
Std Dev	3.339

Gaus mean : 35.0431

Gaus Width: 4.0228



B1L100S, U1-ch90, adc2

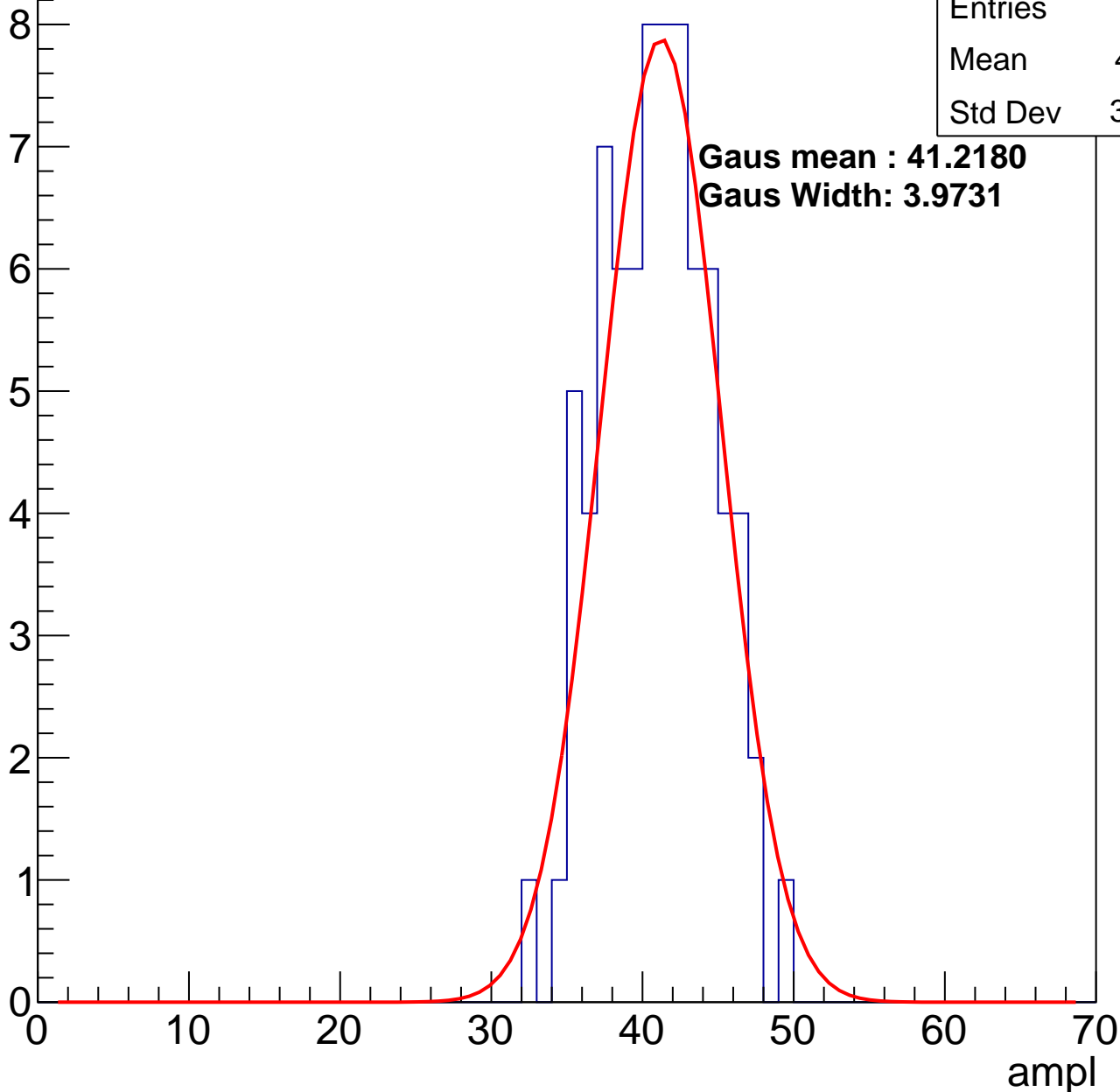
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	40.51
Std Dev	3.559

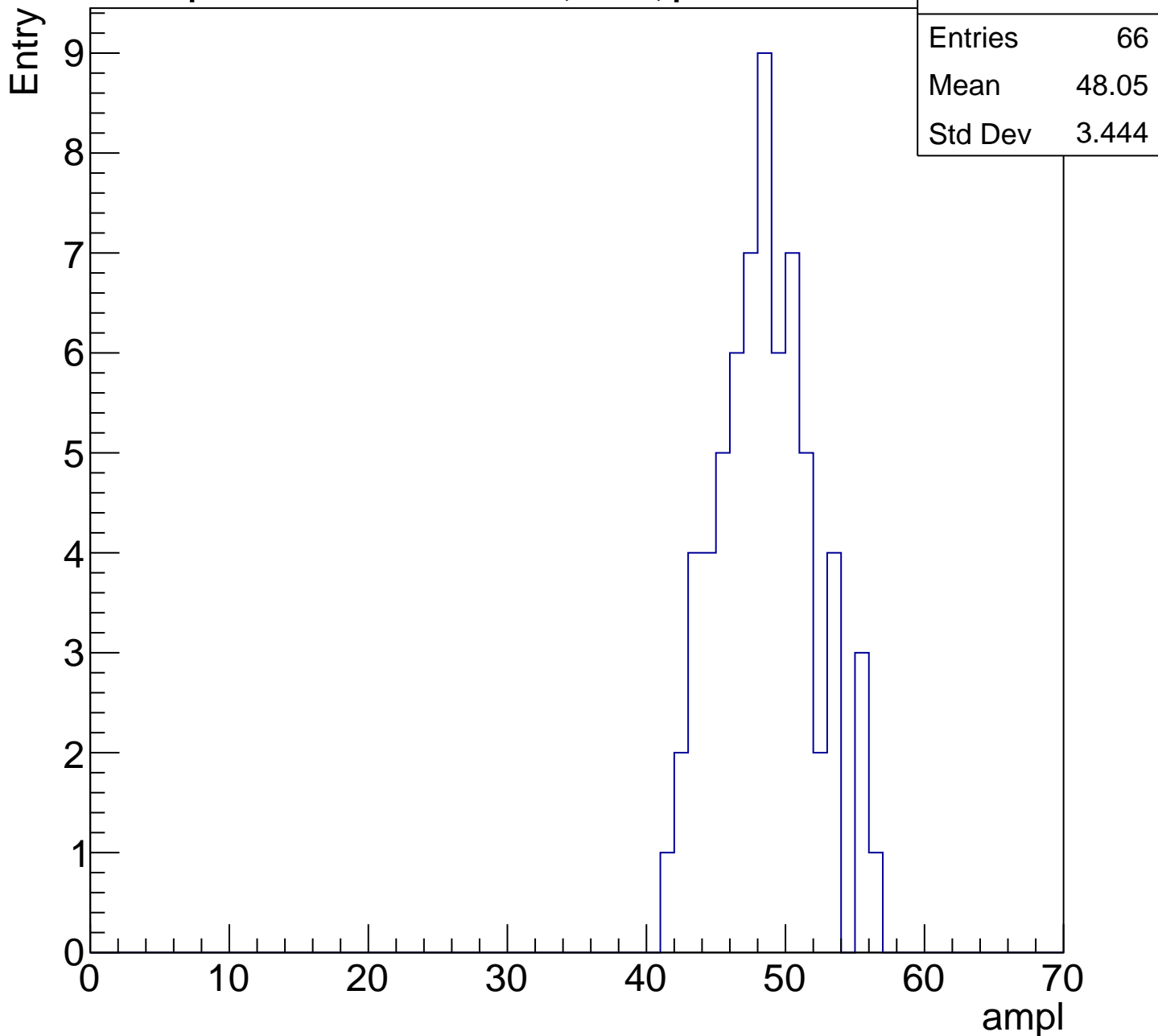
Gaus mean : 41.2180

Gaus Width: 3.9731



B1L100S, U1-ch90, adc3

calib_packv5_042523_0143.root, FC#4, port A2

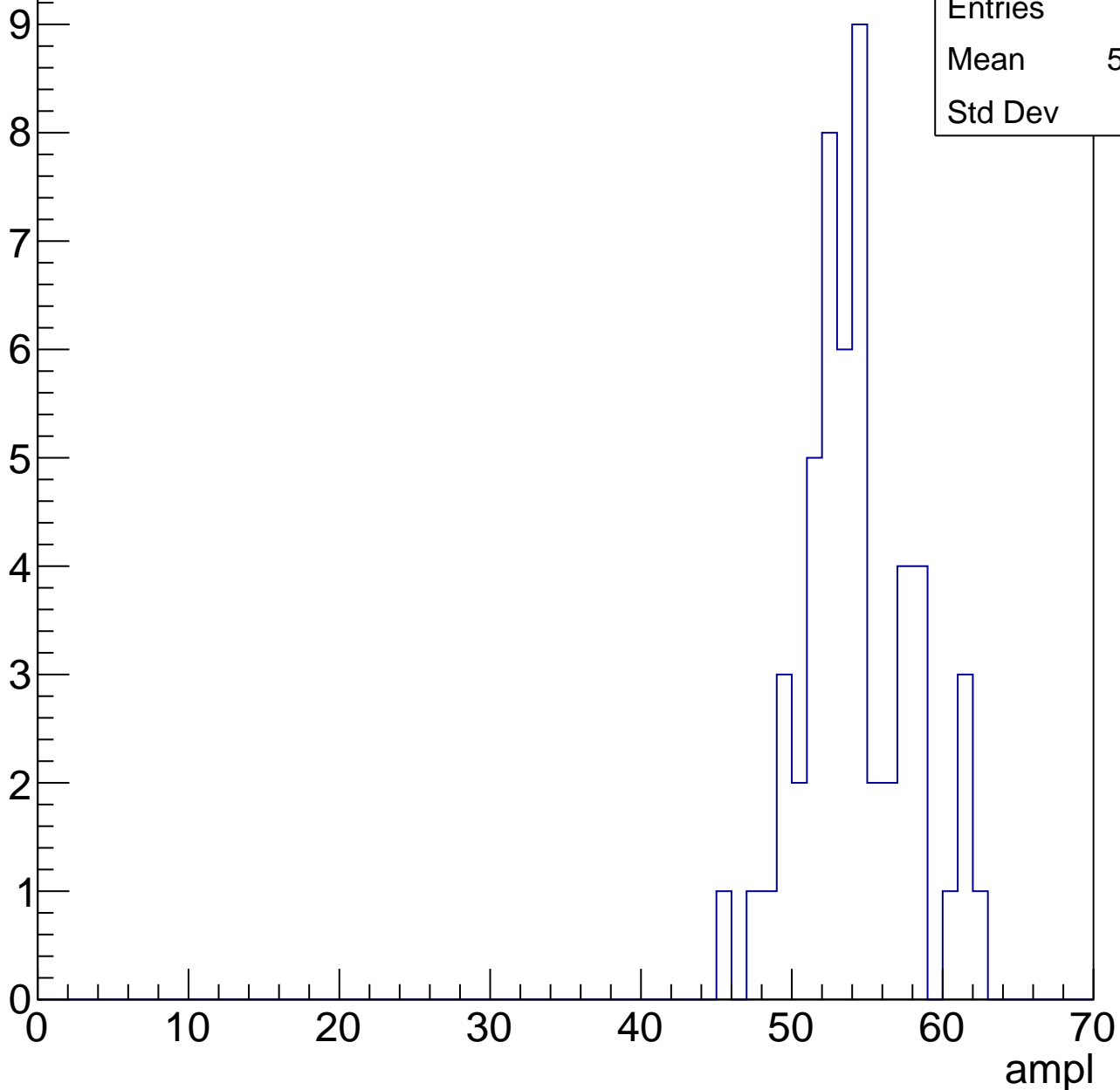


B1L100S, U1-ch90, adc4

calib_packv5_042523_0143.root, FC#4, port A2

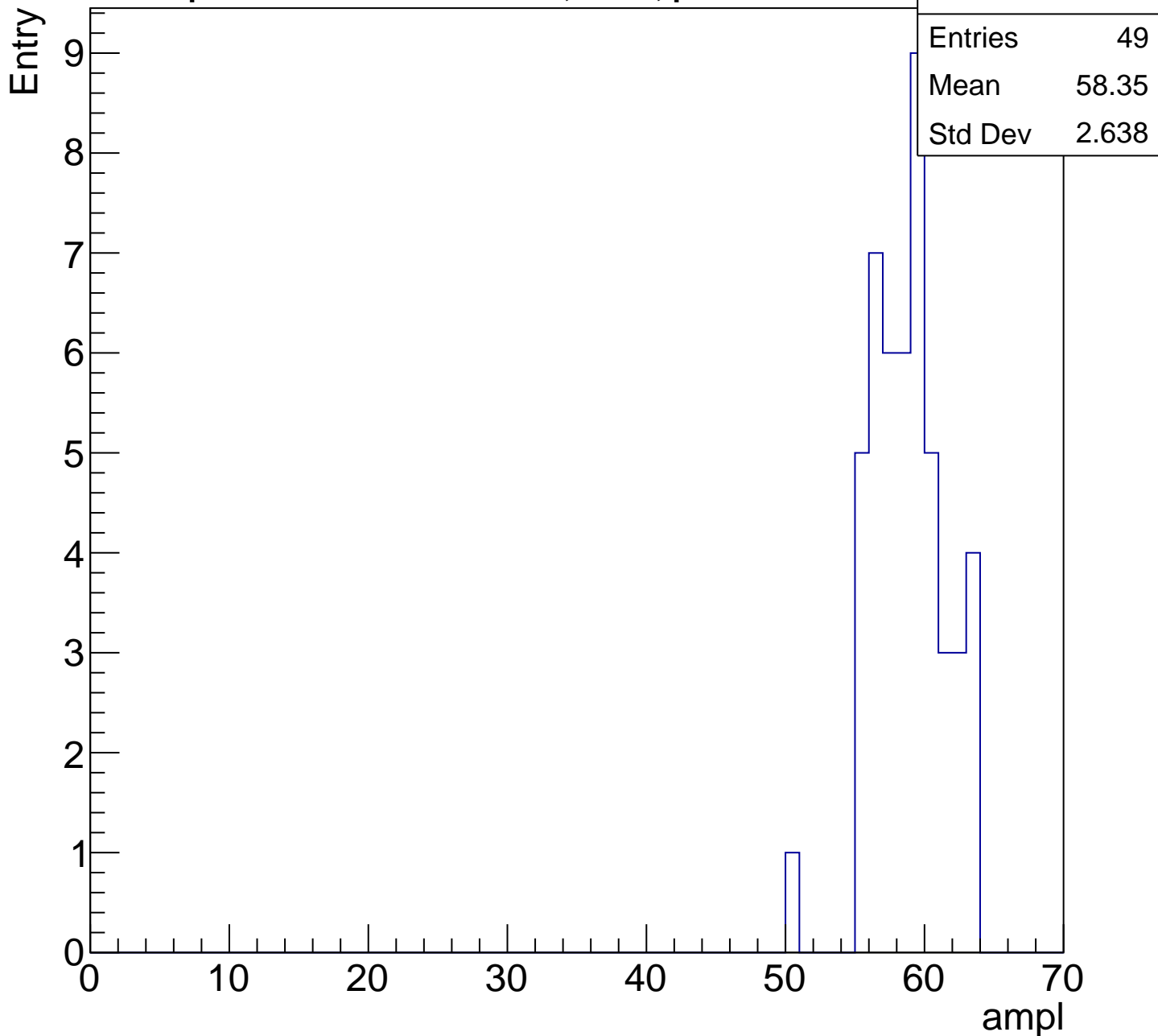
Entry

Entries	53
Mean	53.75
Std Dev	3.66



B1L100S, U1-ch90, adc5

calib_packv5_042523_0143.root, FC#4, port A2

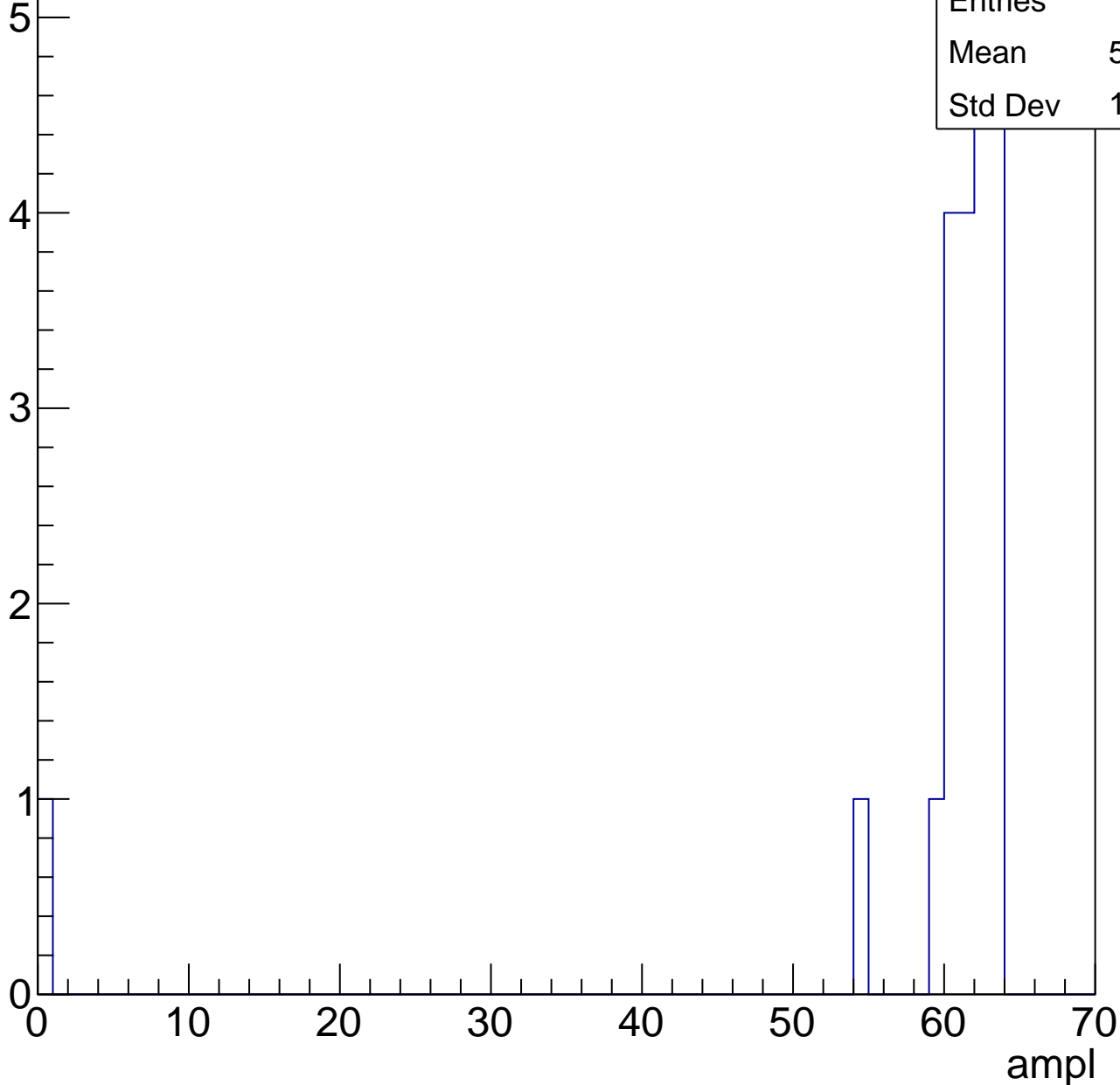


B1L100S, U1-ch90, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	21
Mean	58.19
Std Dev	13.16



B1L100S, U1-ch90, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry

1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	48
Std Dev	20.51

ampl

B1L100S, U1-ch91, adc0

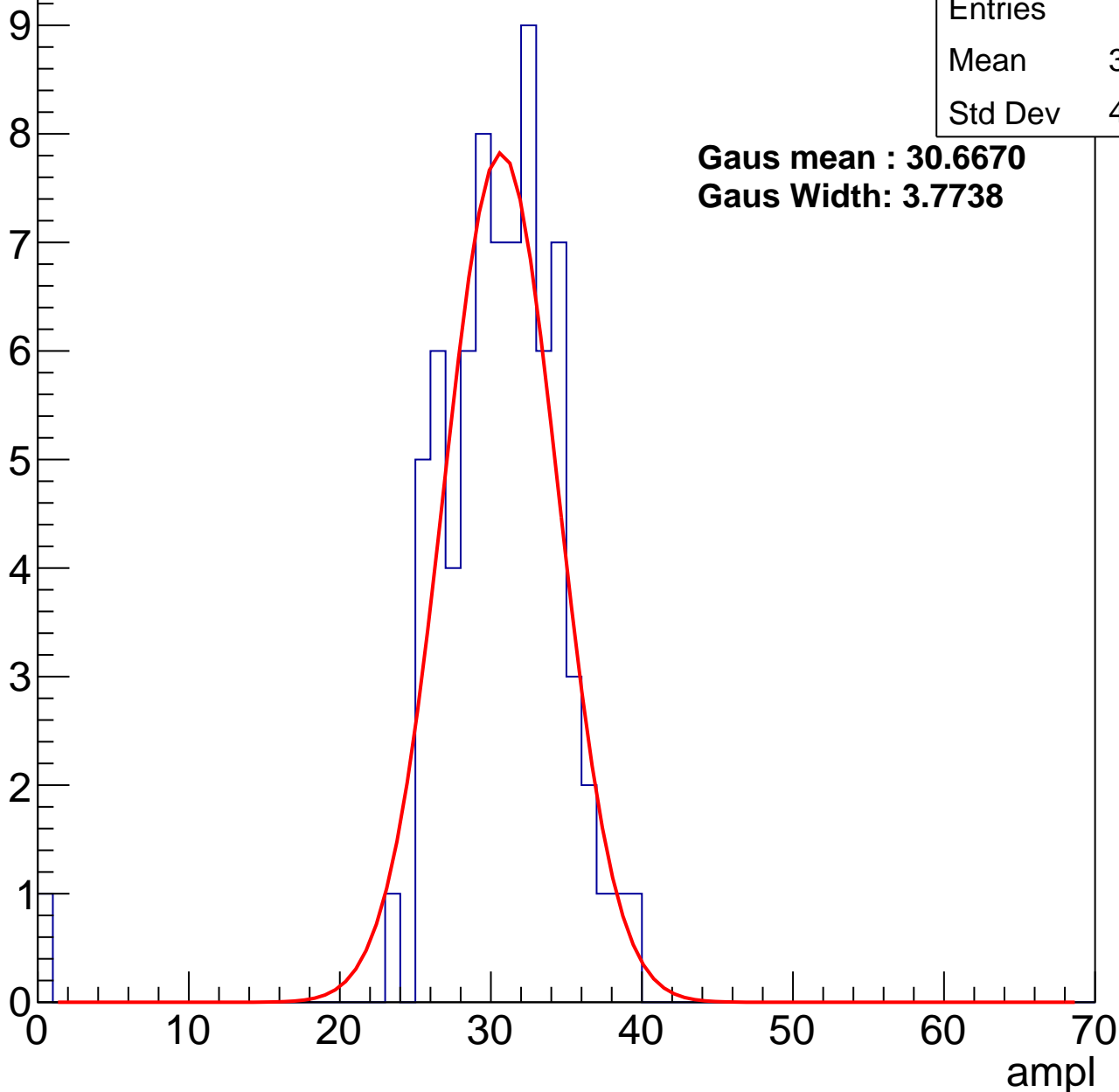
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	30.05
Std Dev	4.882

Gaus mean : 30.6670

Gaus Width: 3.7738



B1L100S, U1-ch91, adc1

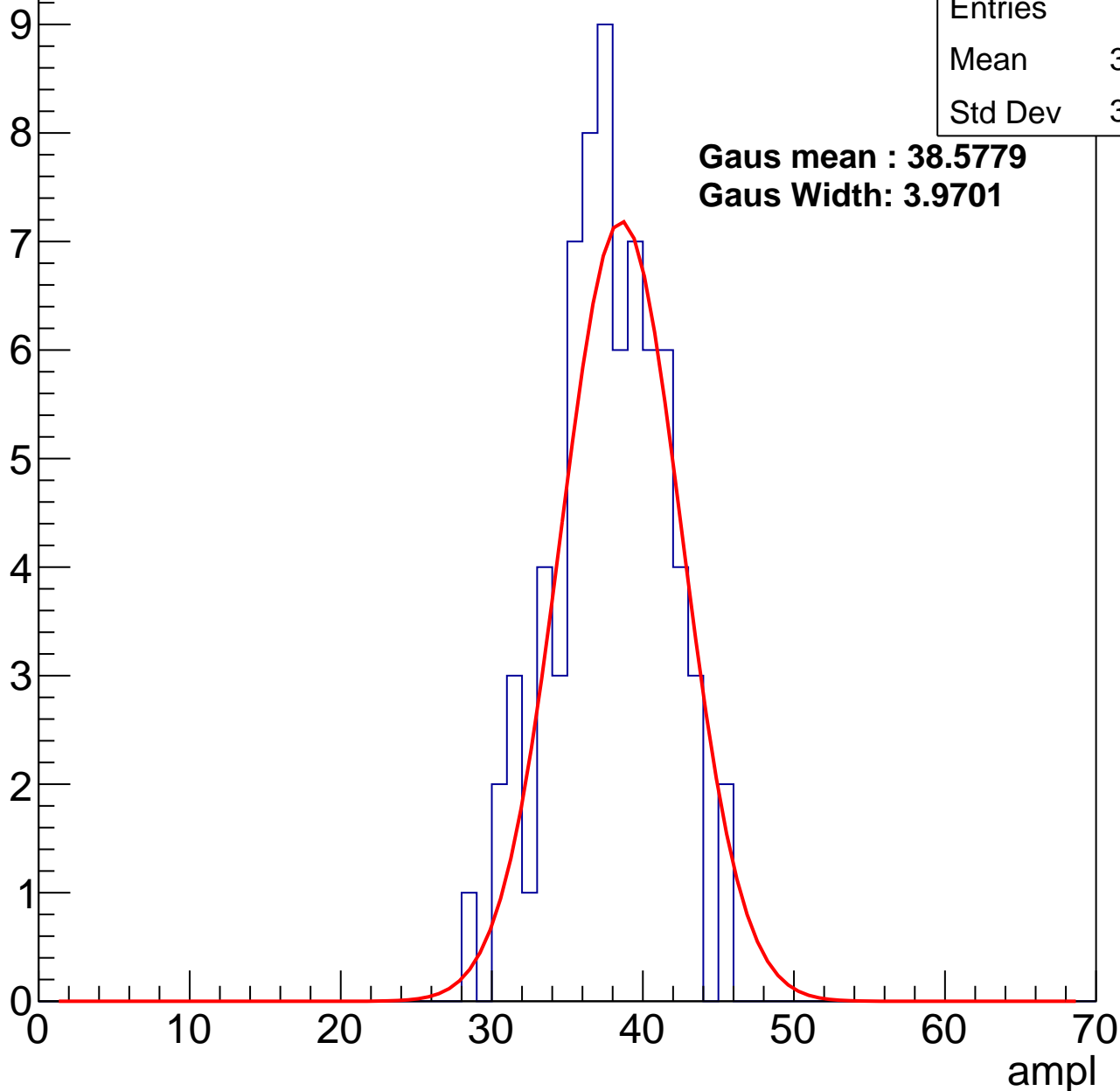
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	37.32
Std Dev	3.636

Gaus mean : 38.5779

Gaus Width: 3.9701



B1L100S, U1-ch91, adc2

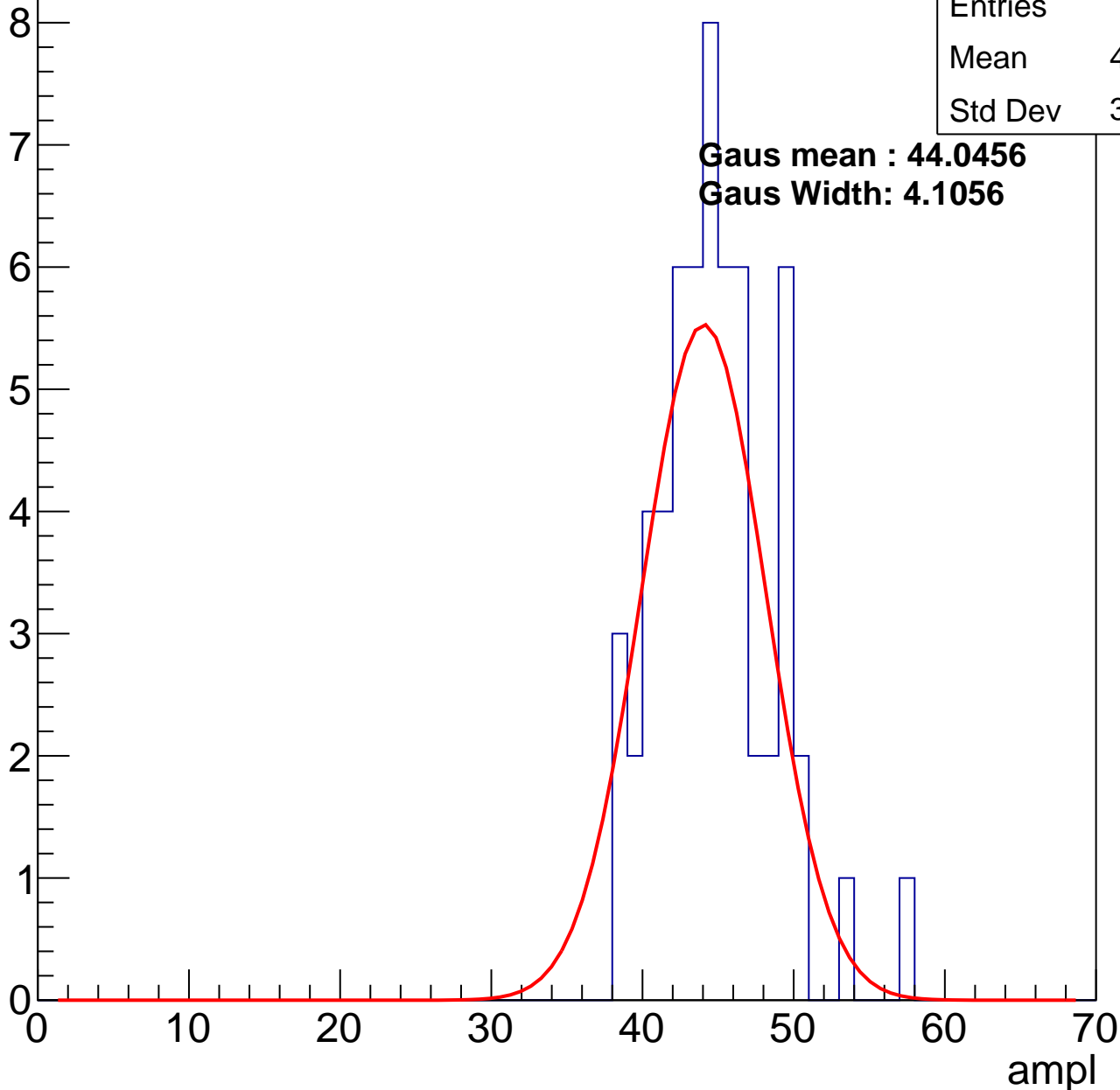
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	44.37
Std Dev	3.755

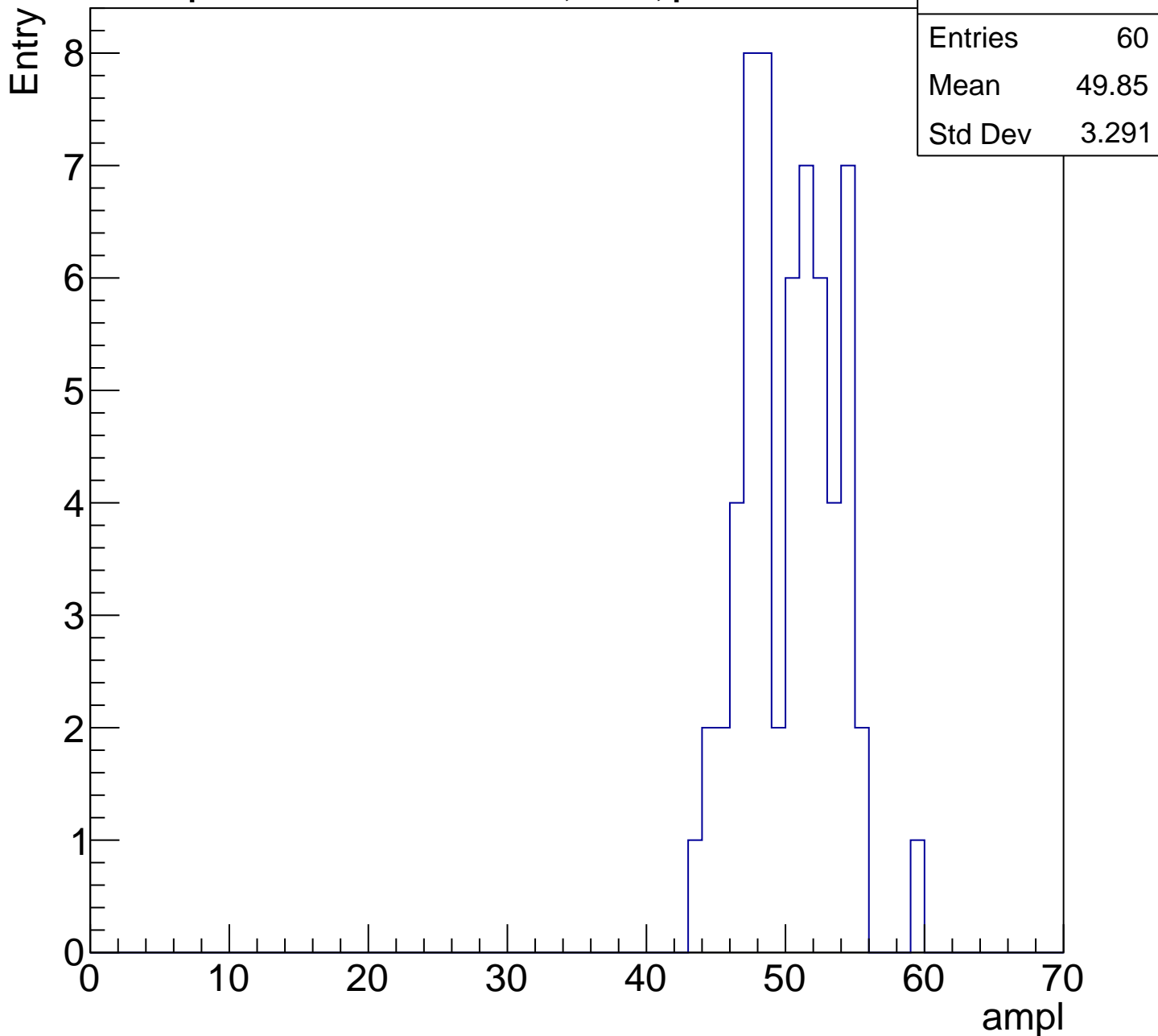
Gaus mean : 44.0456

Gaus Width: 4.1056



B1L100S, U1-ch91, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch91, adc4

calib_packv5_042523_0143.root, FC#4, port A2

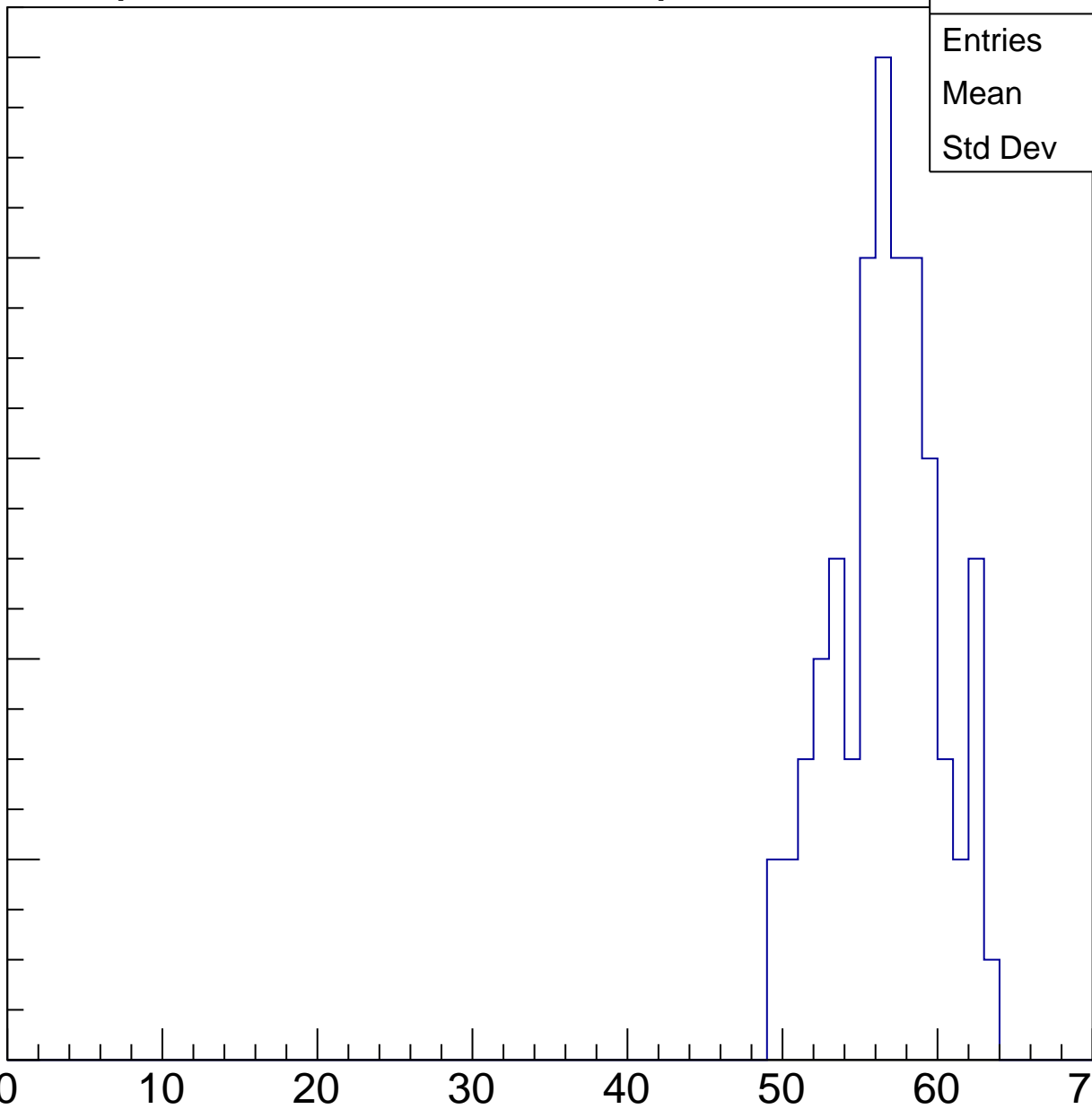
Entries	70
Mean	56.21
Std Dev	3.363

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

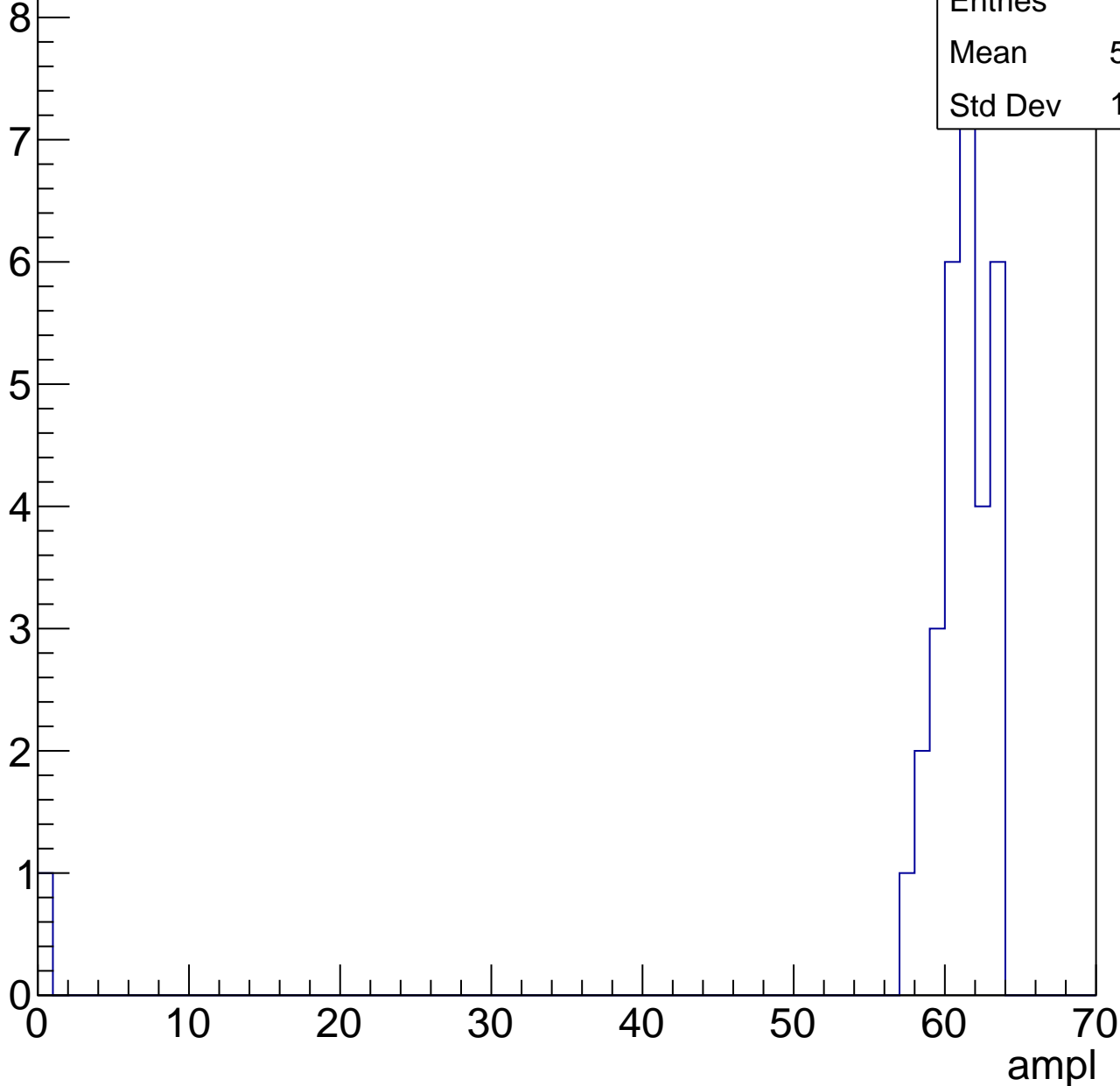


B1L100S, U1-ch91, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

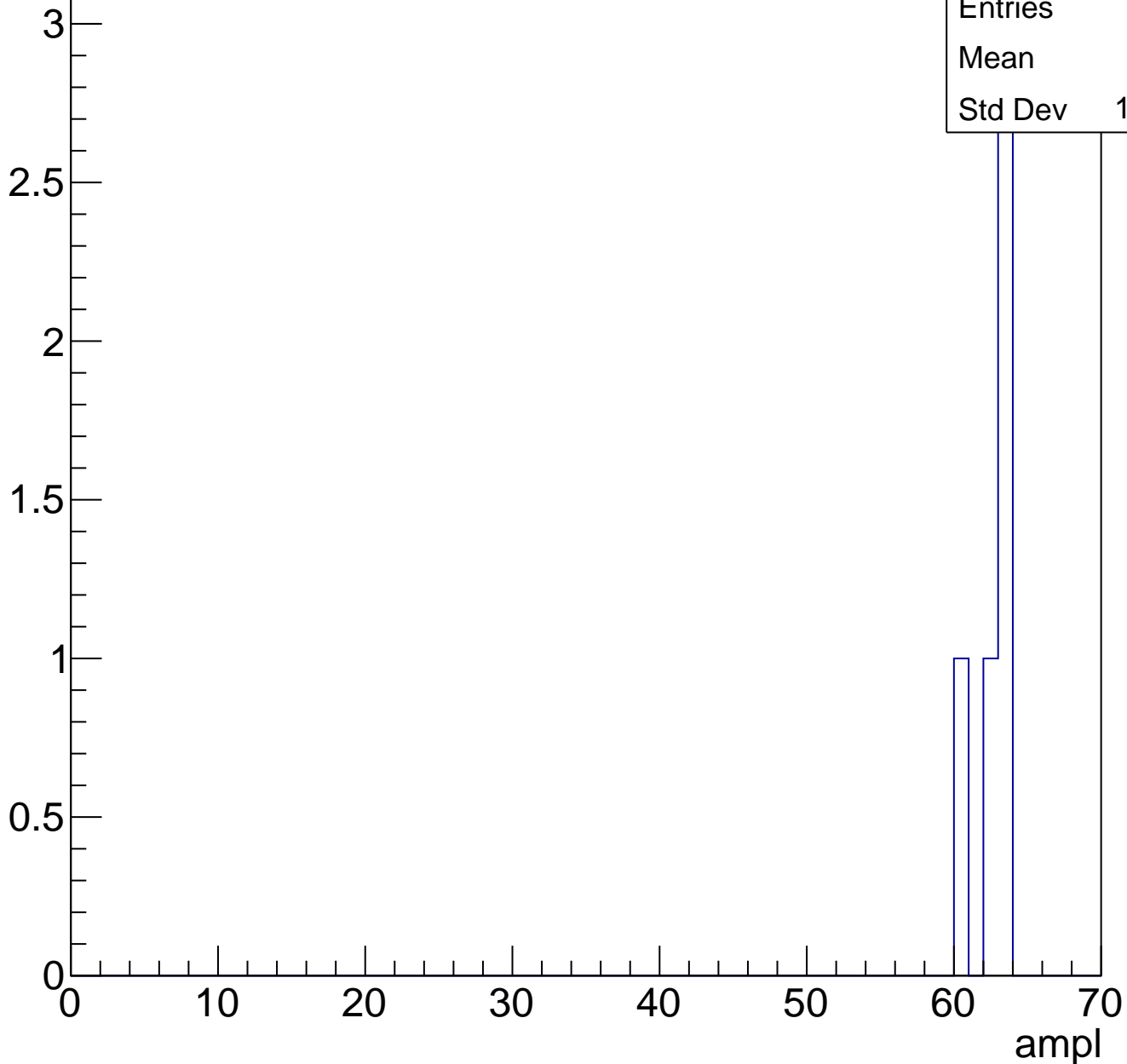
Entries	31
Mean	58.84
Std Dev	10.86



B1L100S, U1-ch91, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch91, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch92, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	28.47
Std Dev	3.5

Gaus mean : 29.0743

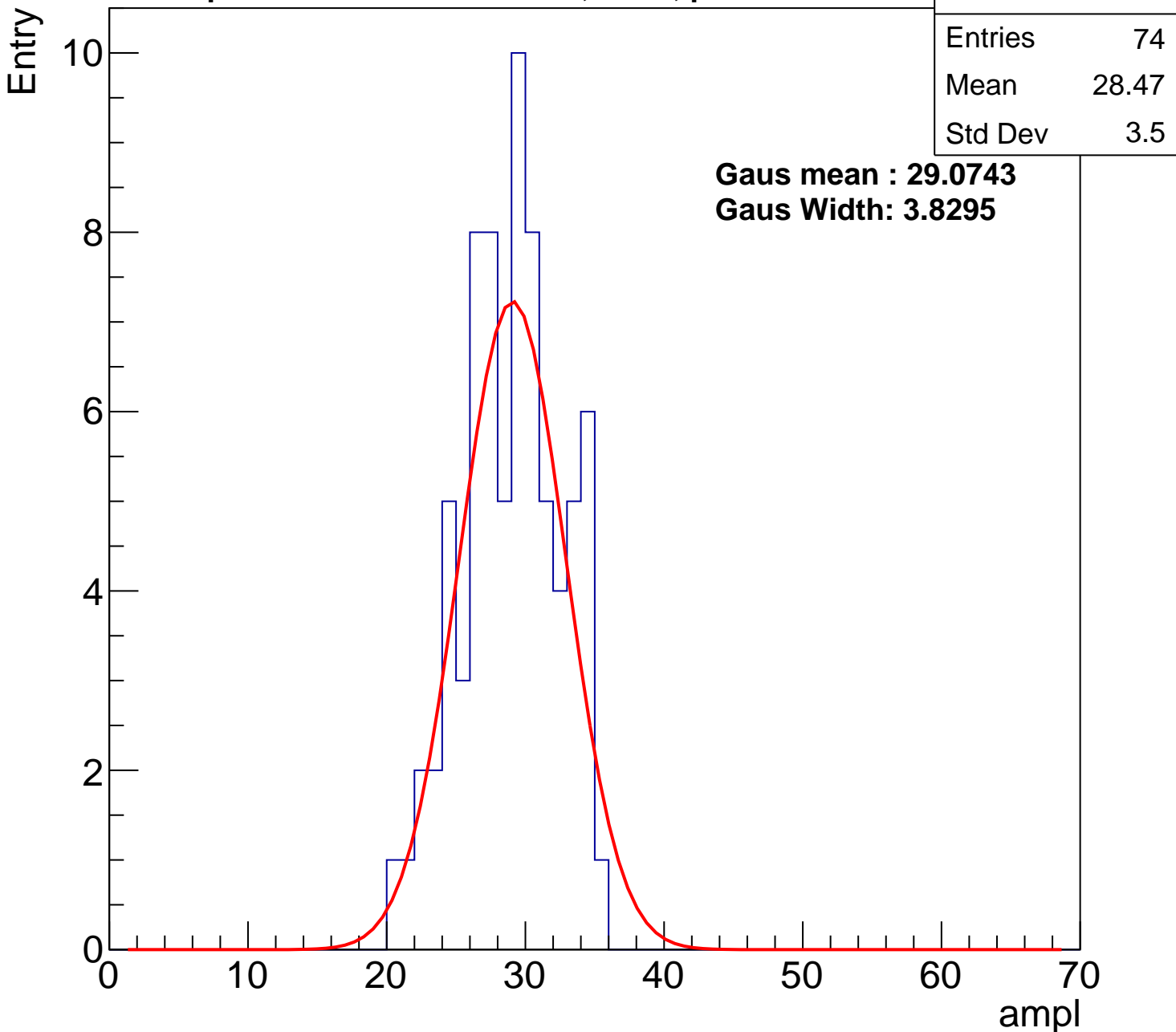
Gaus Width: 3.8295

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch92, adc1

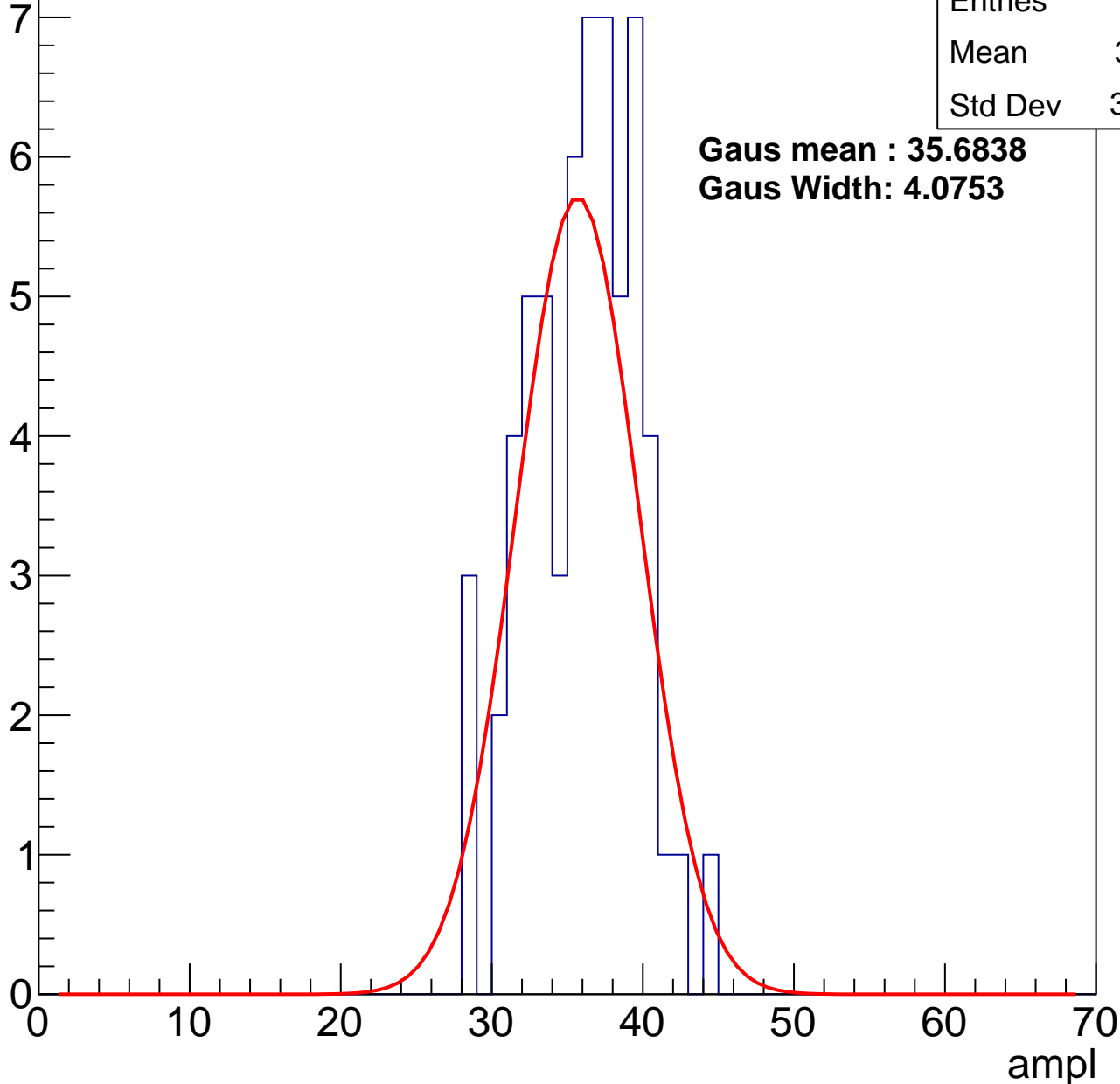
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	35.51
Std Dev	3.556

Gaus mean : 35.6838

Gaus Width: 4.0753



B1L100S, U1-ch92, adc2

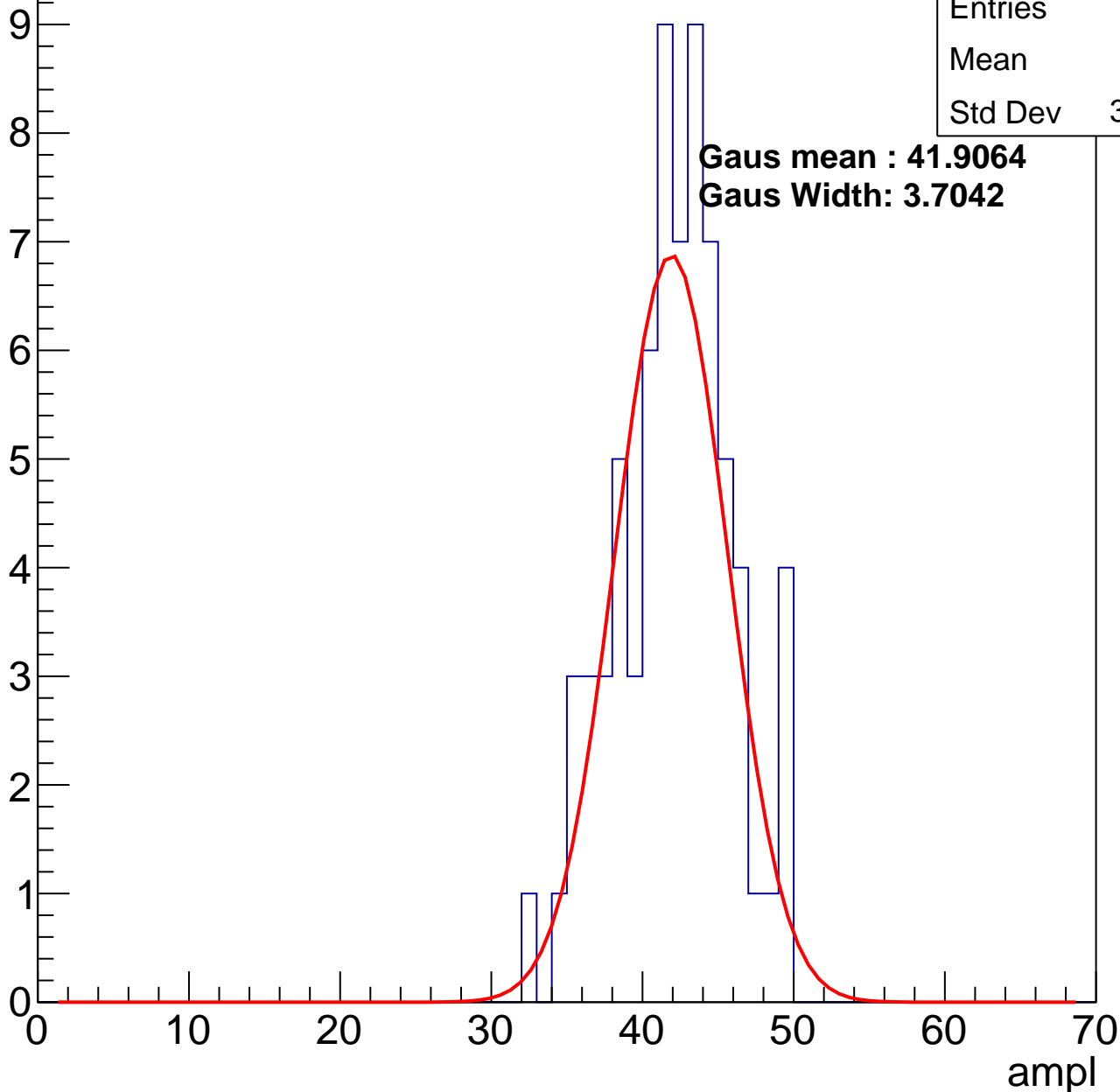
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.6
Std Dev	3.774

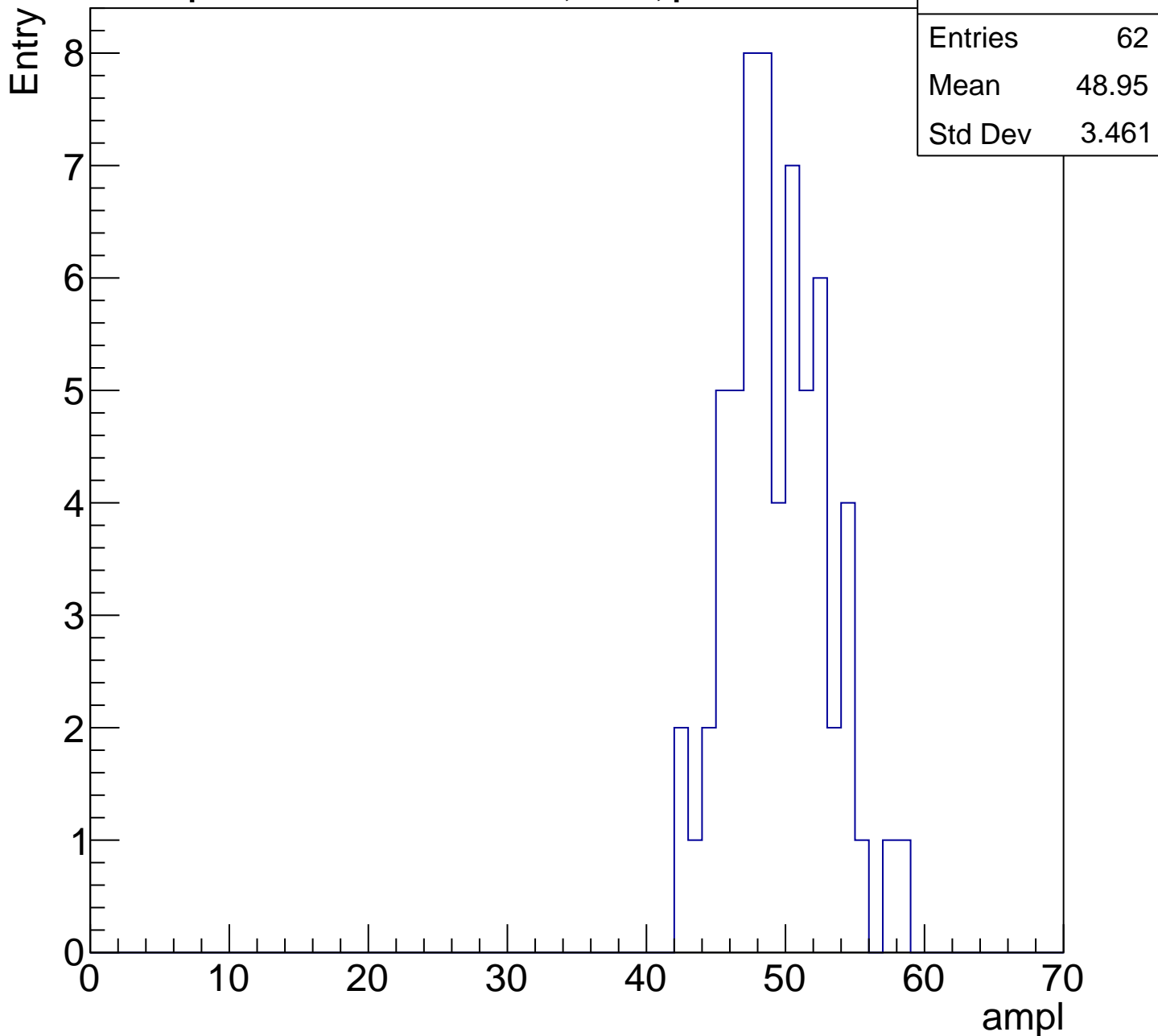
Gaus mean : 41.9064

Gaus Width: 3.7042



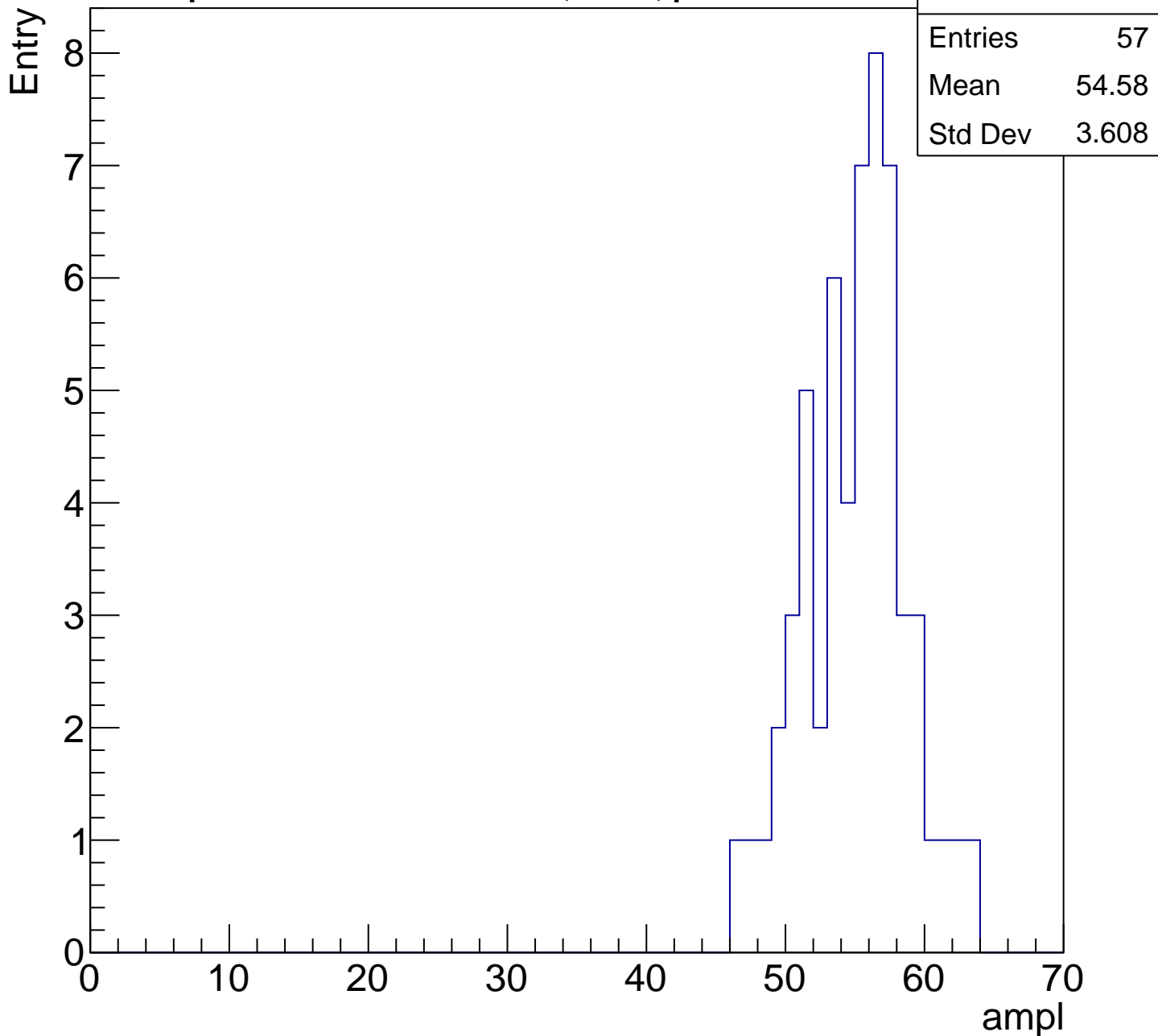
B1L100S, U1-ch92, adc3

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch92, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch92, adc5

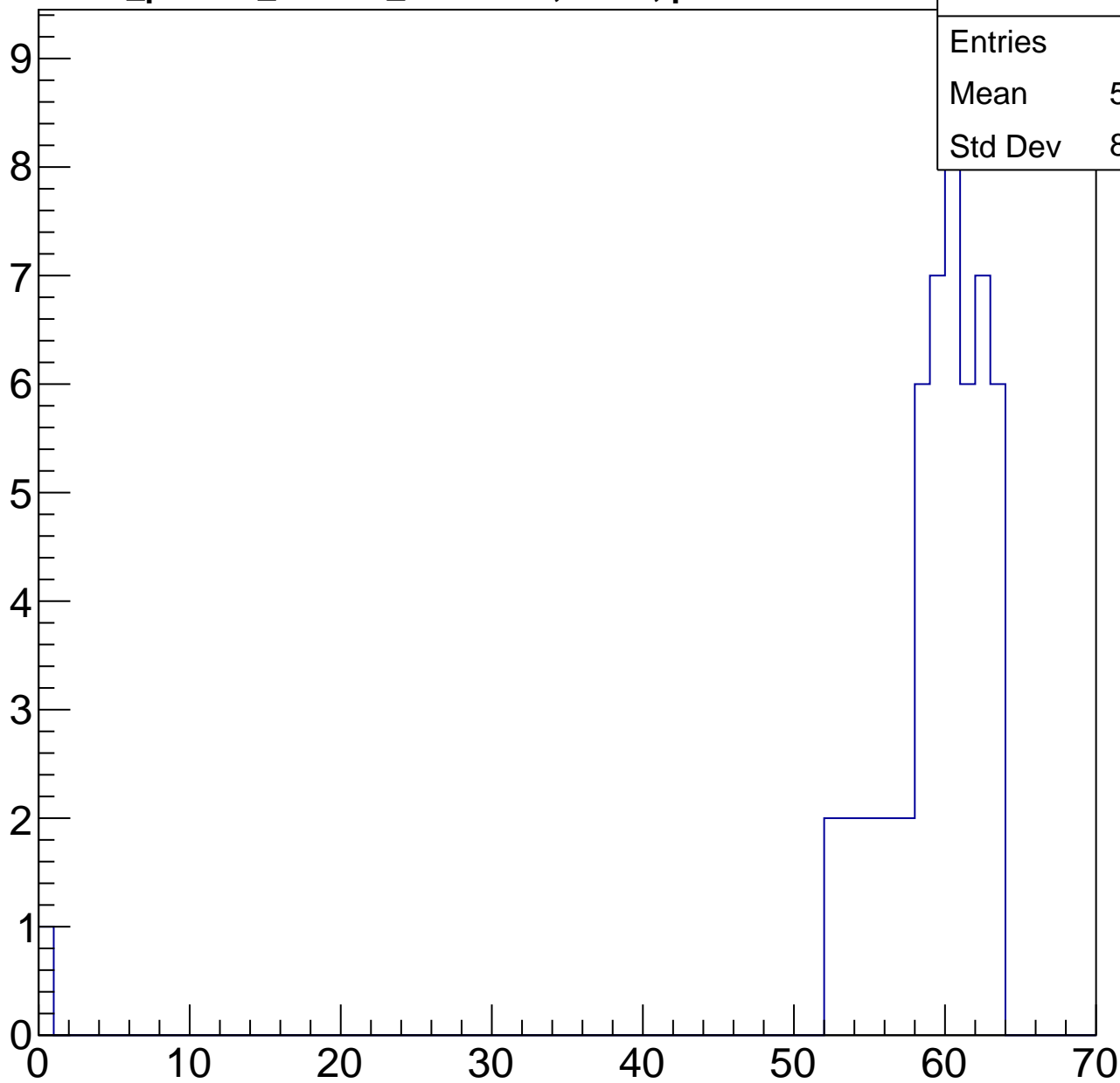
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	54
Mean	58.02
Std Dev	8.504

ampl



B1L100S, U1-ch92, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch92, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch93, adc0

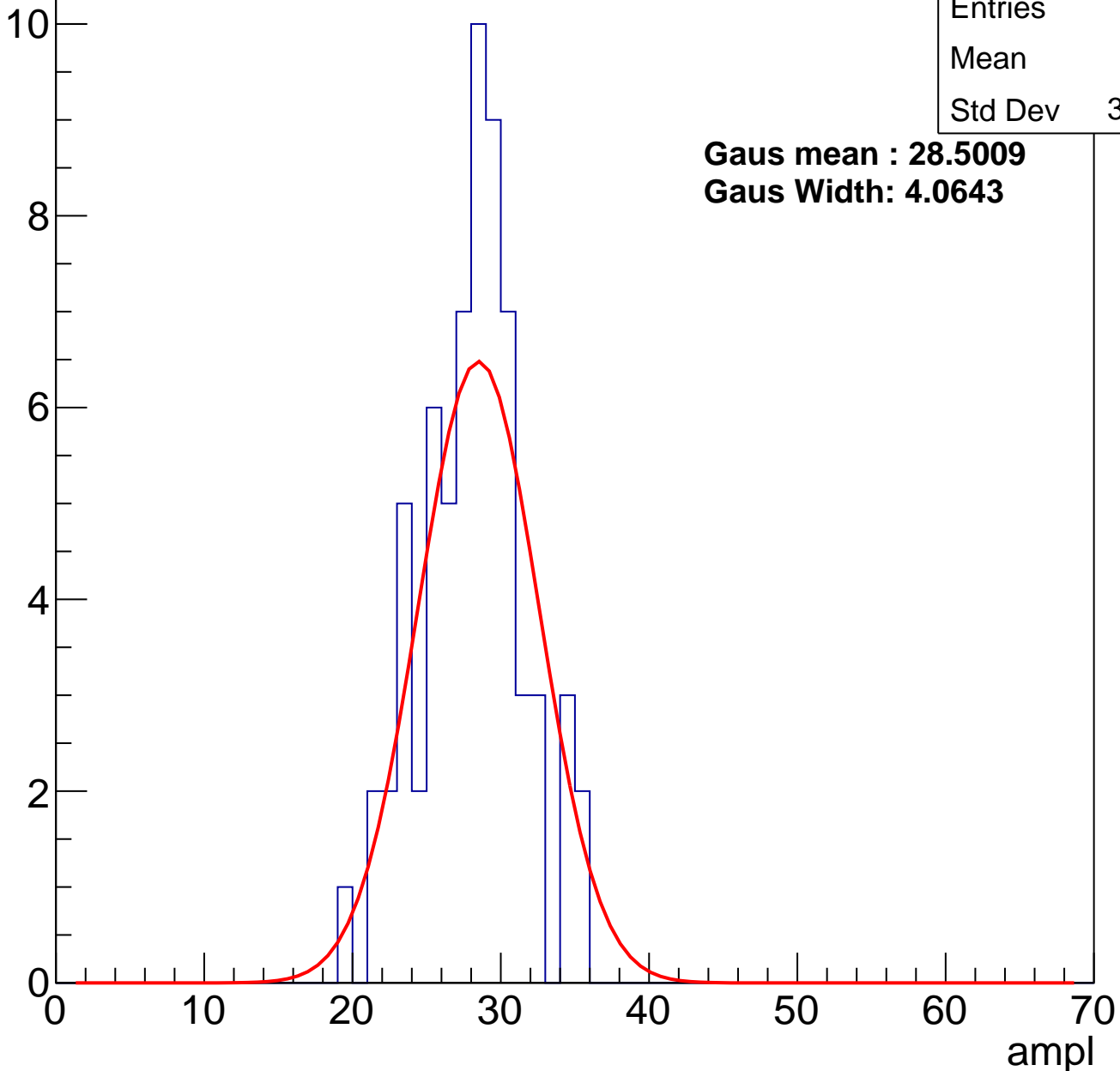
calib_packv5_042523_0143.root, FC#4, port A2

Entries	67
Mean	27.6
Std Dev	3.447

Gaus mean : 28.5009

Gaus Width: 4.0643

Entry



B1L100S, U1-ch93, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	73
Mean	35.16
Std Dev	3.668

Gaus mean : 35.3248

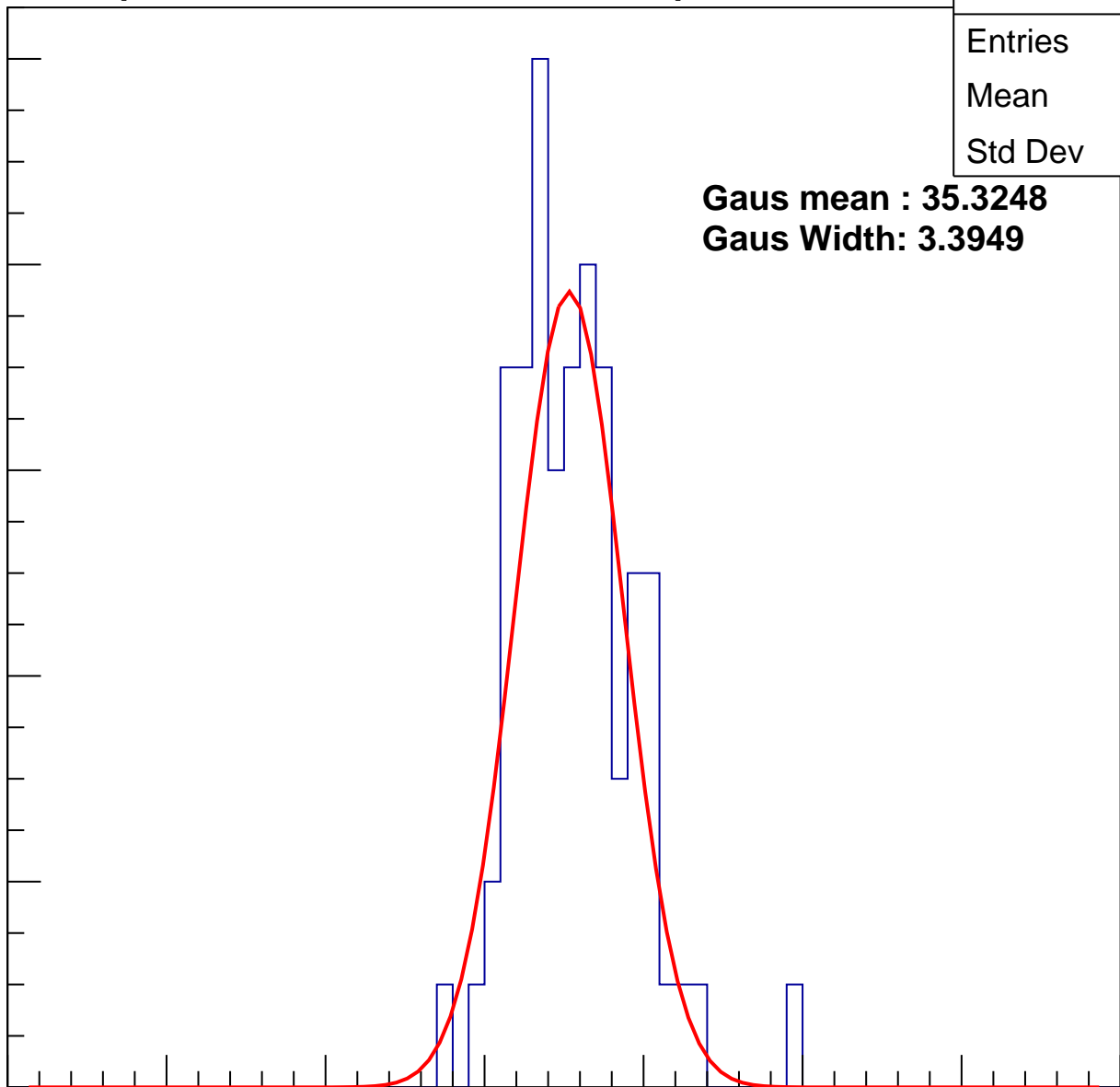
Gaus Width: 3.3949

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch93, adc2

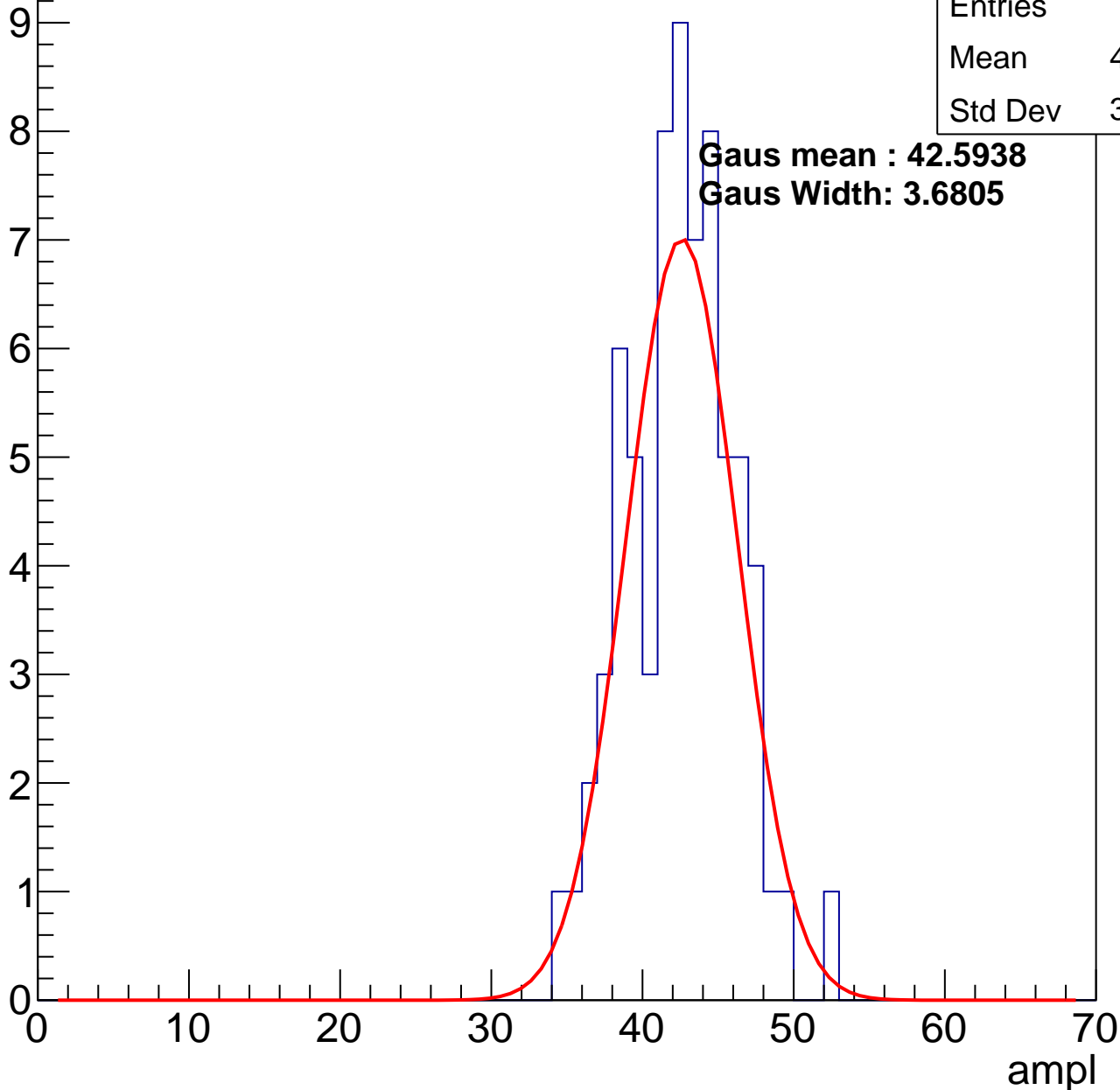
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	42.09
Std Dev	3.524

Gaus mean : 42.5938

Gaus Width: 3.6805

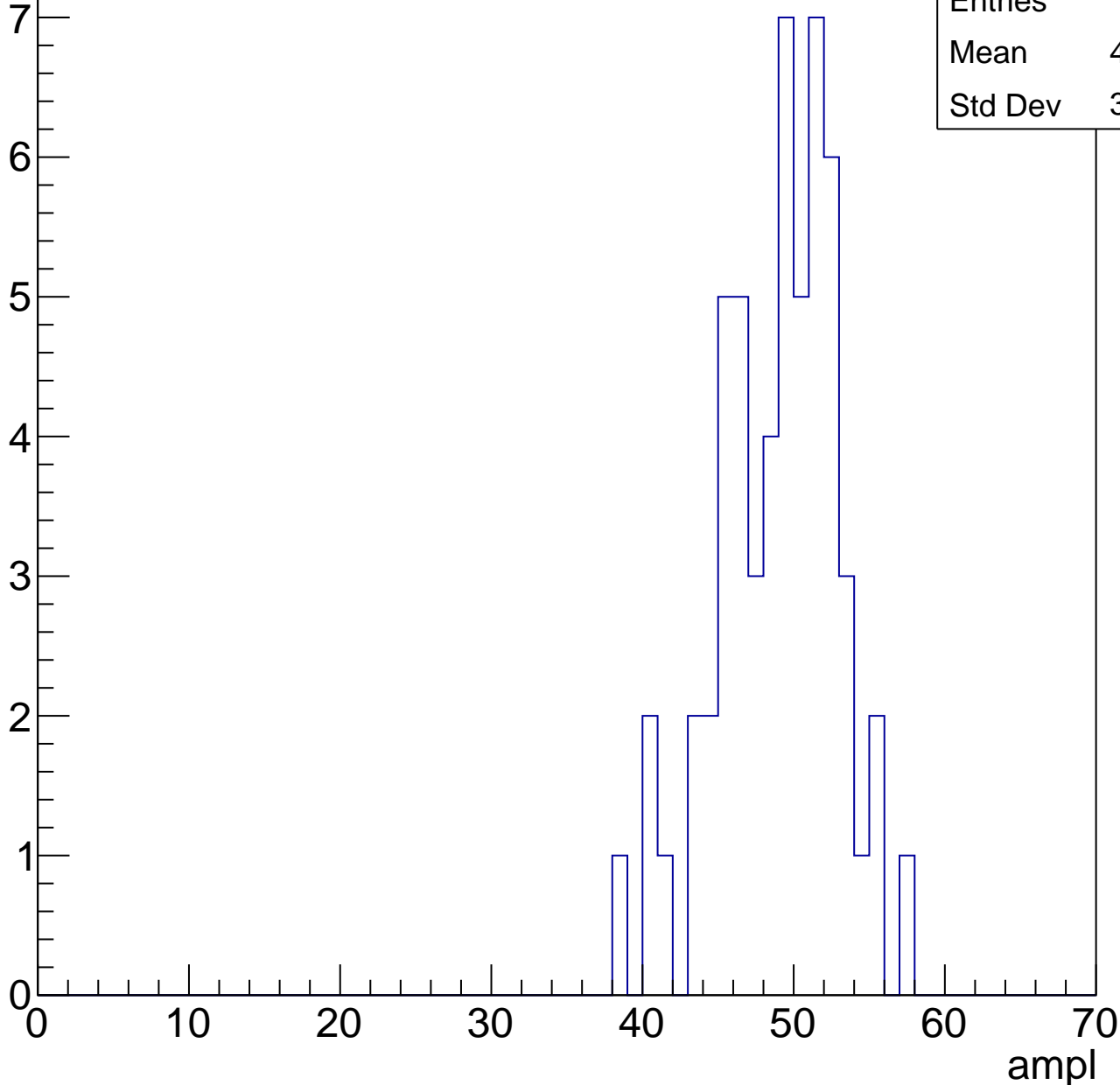


B1L100S, U1-ch93, adc3

calib_packv5_042523_0143.root, FC#4, port A2

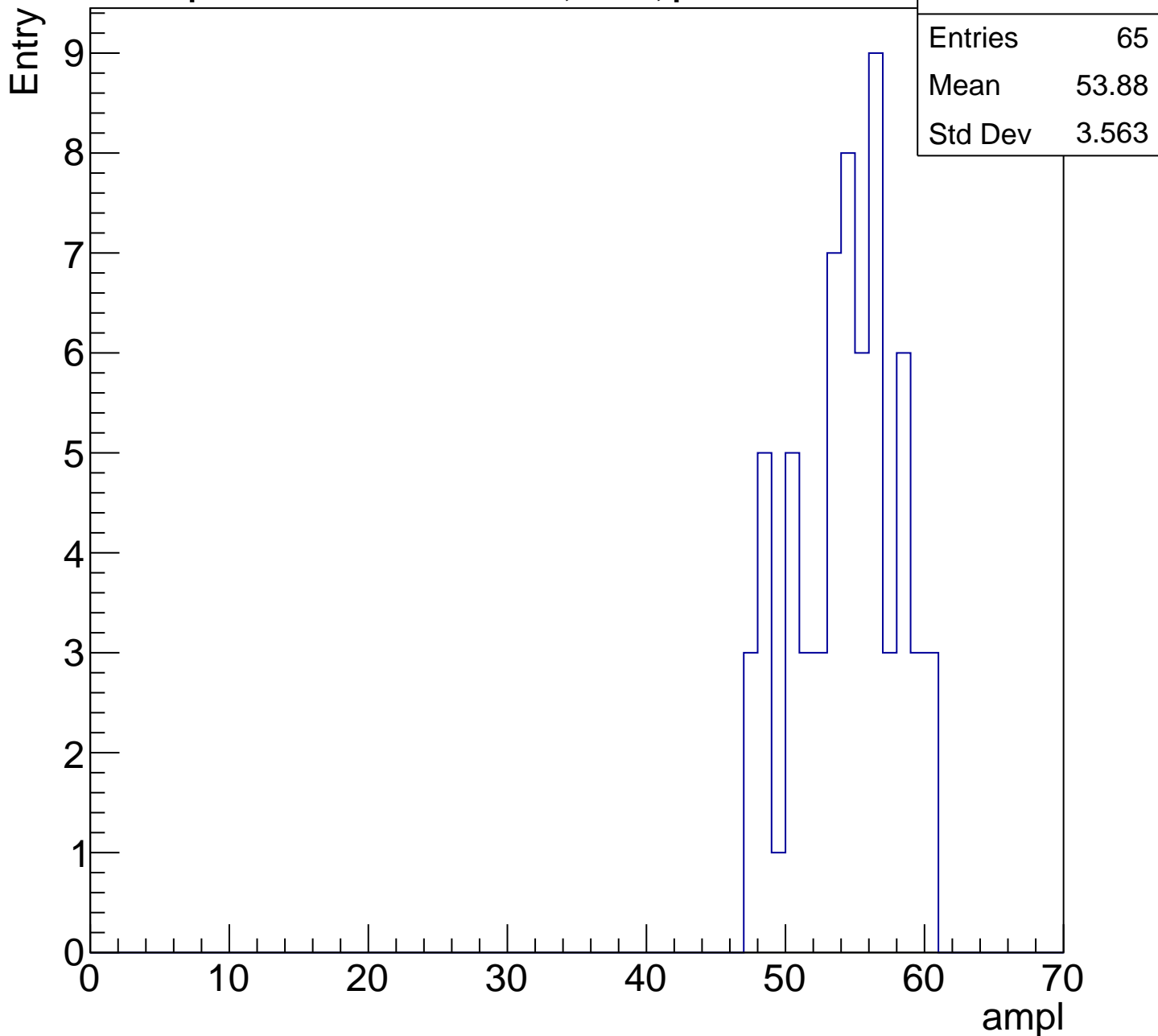
Entry

Entries	57
Mean	48.47
Std Dev	3.952



B1L100S, U1-ch93, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch93, adc5

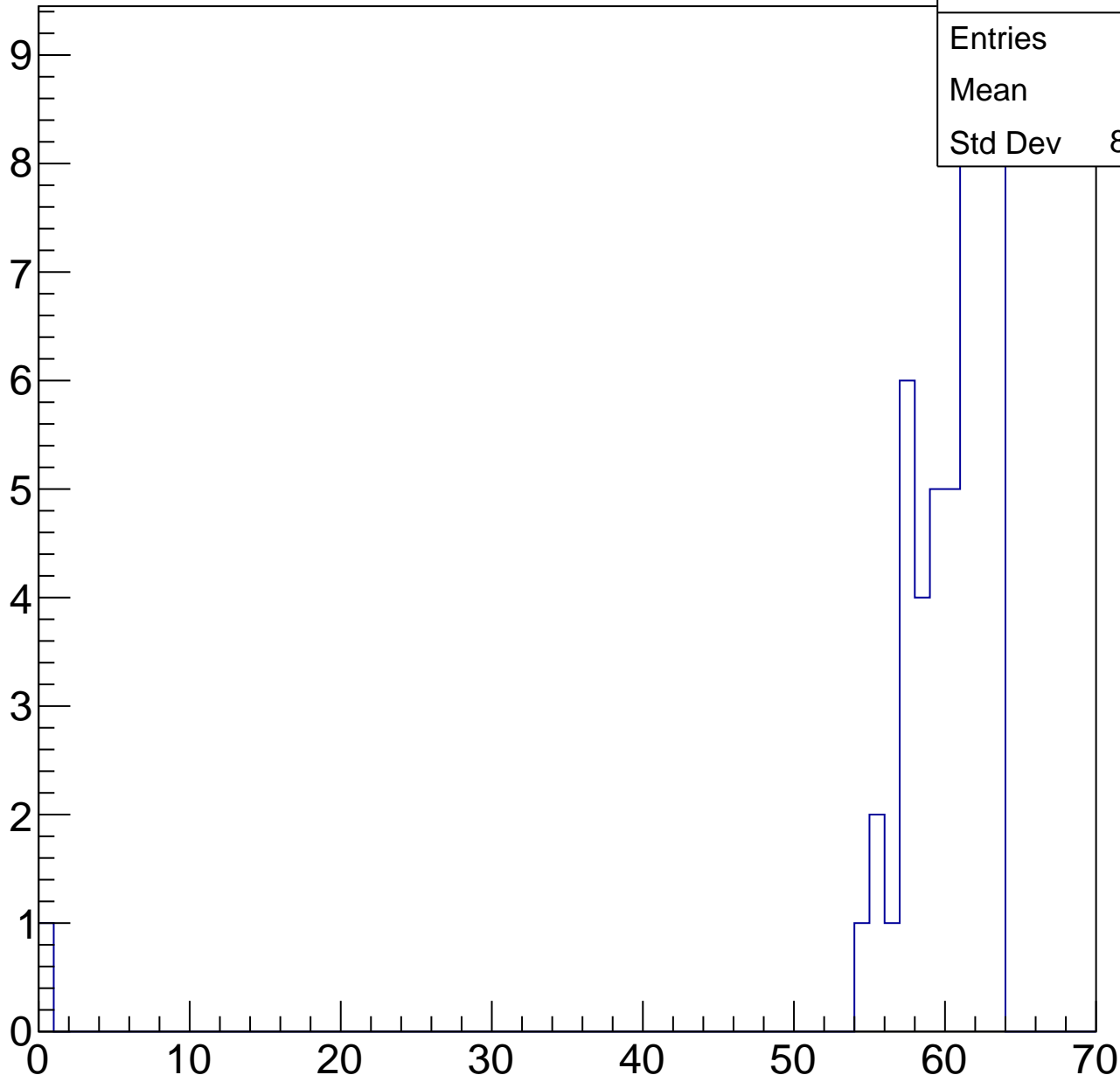
calib_packv5_042523_0143.root, FC#4, port A2

Entry

9
8
7
6
5
4
3
2
1
0

Entries	52
Mean	58.9
Std Dev	8.594

ampl



B1L100S, U1-ch93, adc6

calib_packv5_042523_0143.root, FC#4, port A2

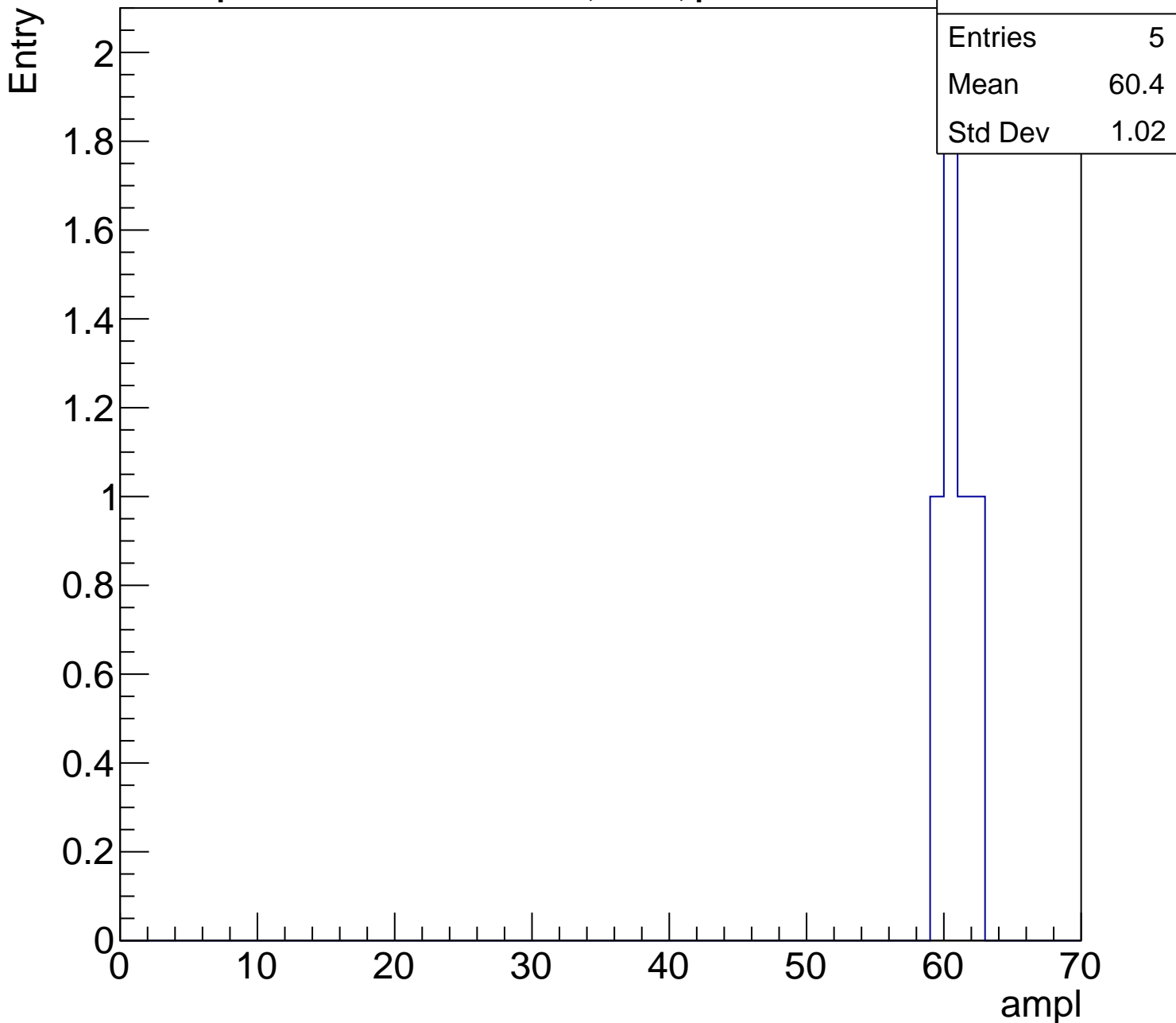
Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	5
Mean	60.4
Std Dev	1.02

ampl

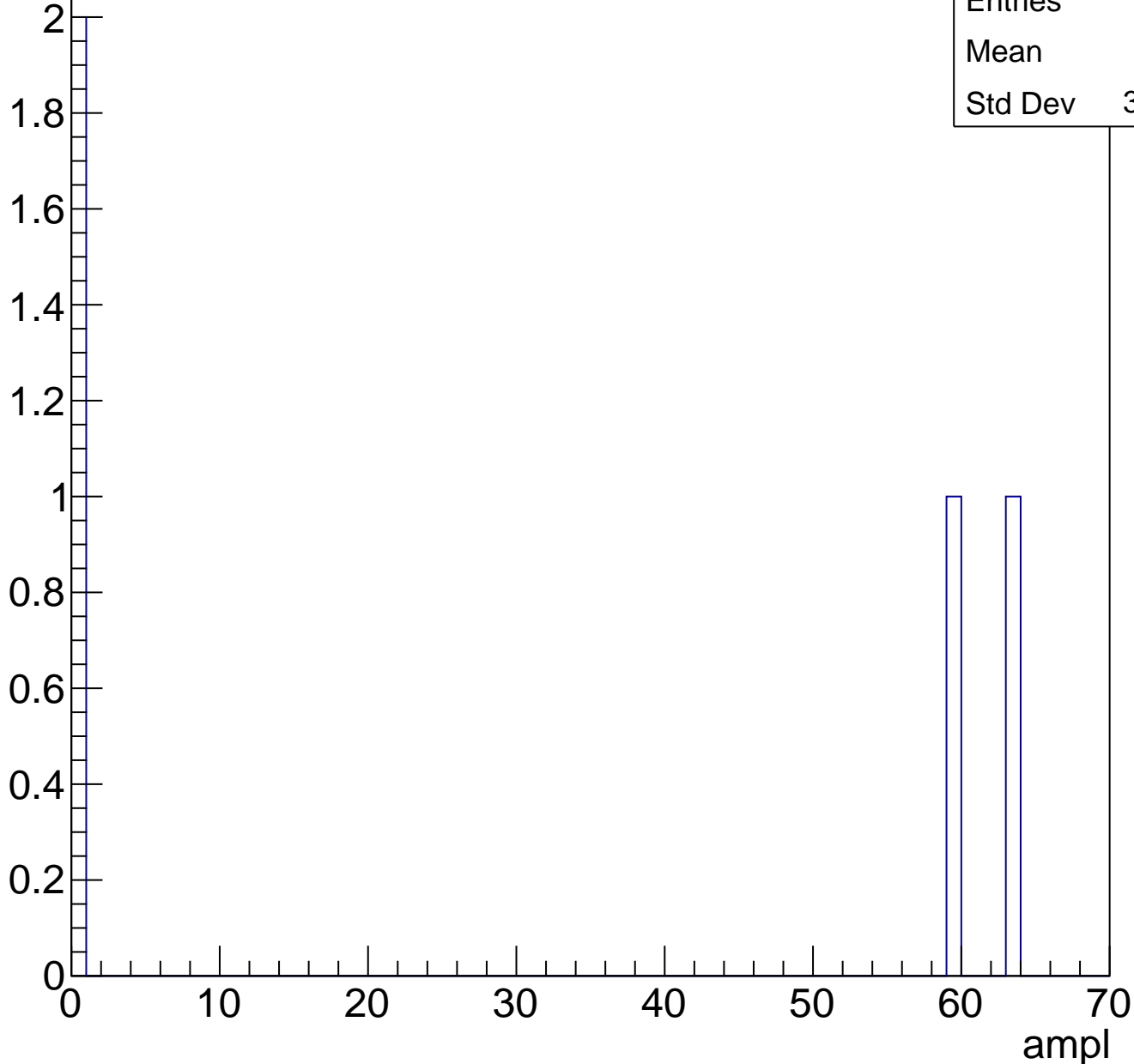
0 10 20 30 40 50 60 70



B1L100S, U1-ch93, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	30.5
Std Dev	30.53

B1L100S, U1-ch94, adc0

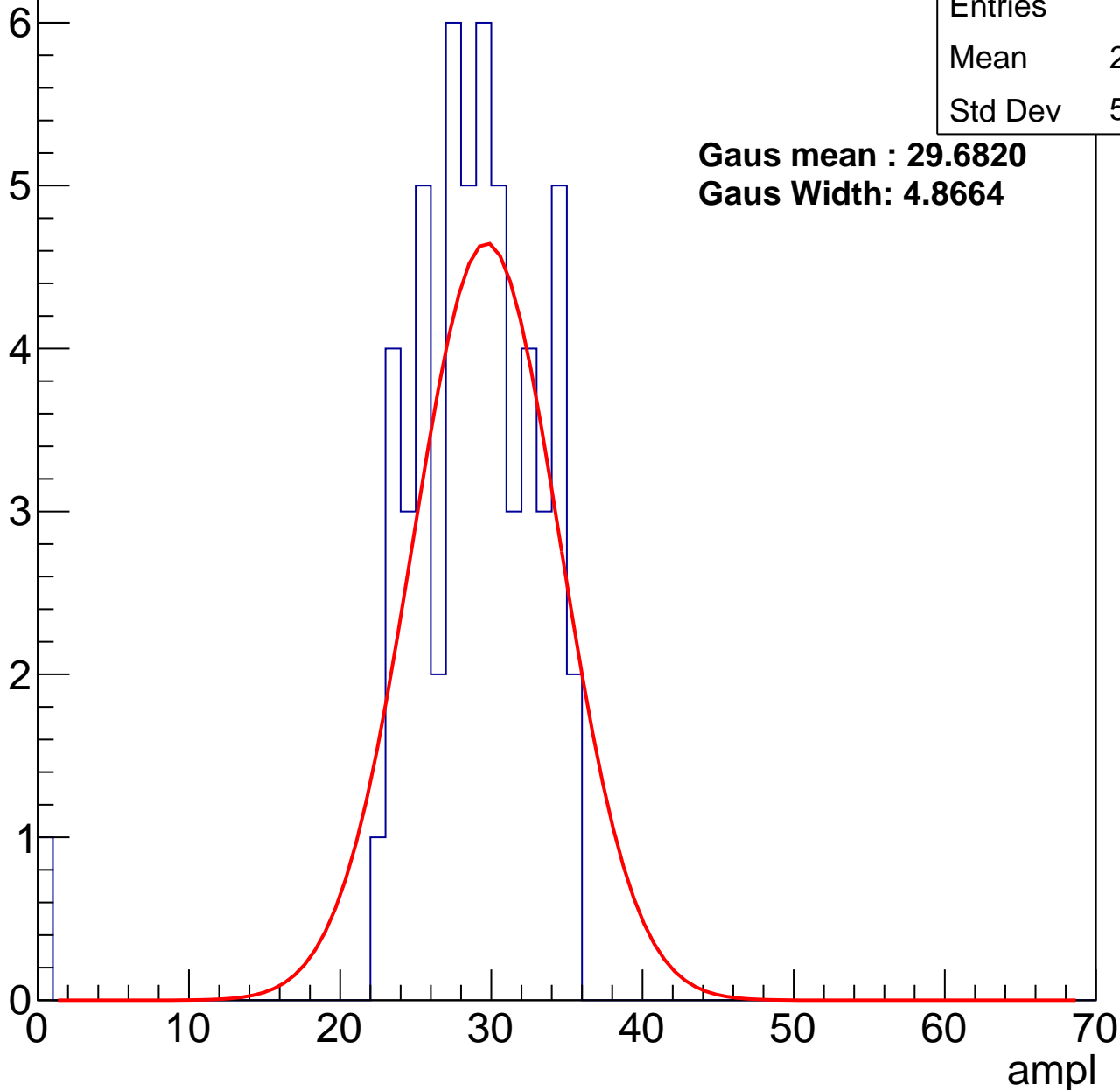
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	28.16
Std Dev	5.215

Gaus mean : 29.6820

Gaus Width: 4.8664



B1L100S, U1-ch94, adc1

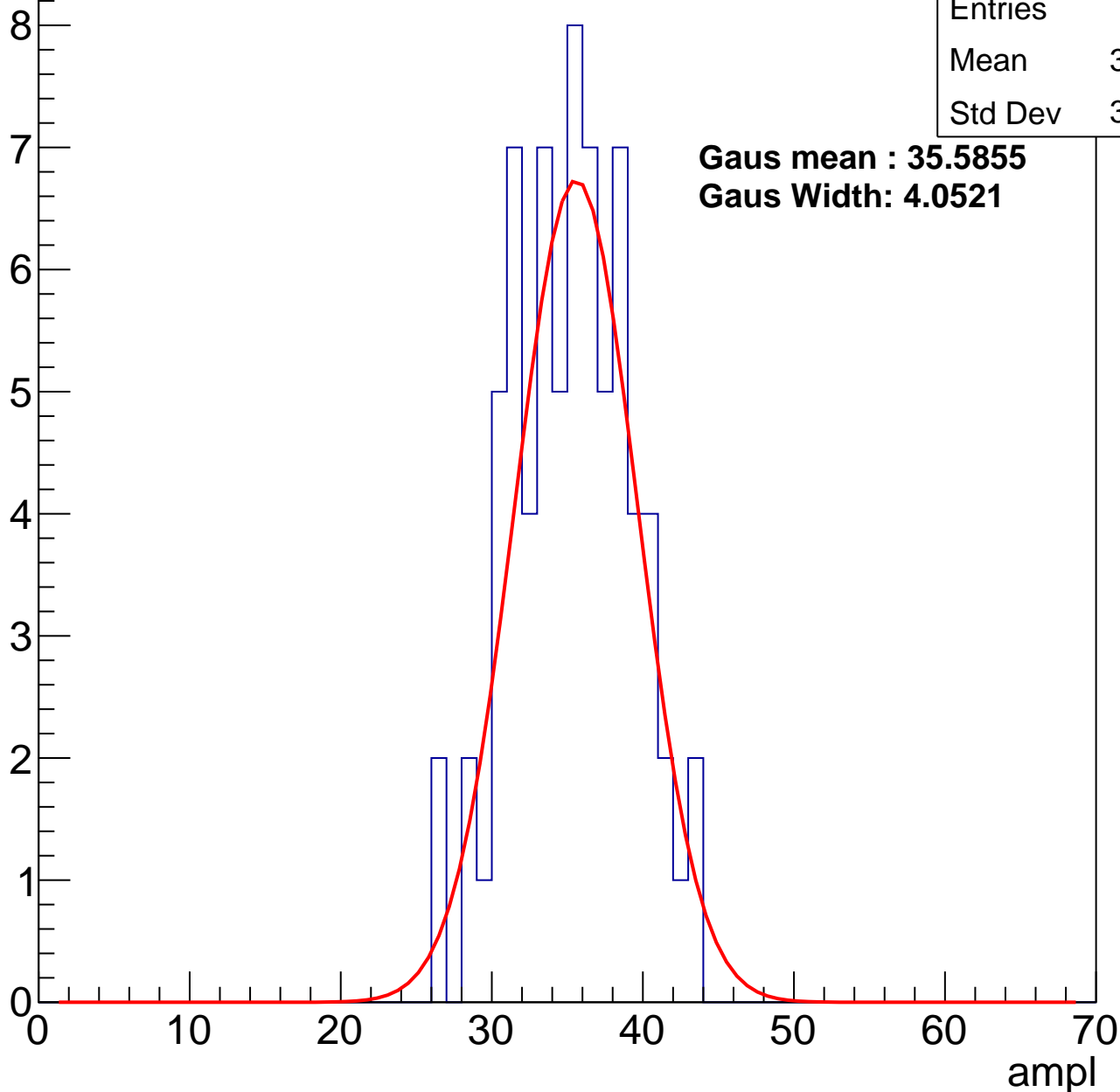
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	34.82
Std Dev	3.887

Gaus mean : 35.5855

Gaus Width: 4.0521



B1L100S, U1-ch94, adc2

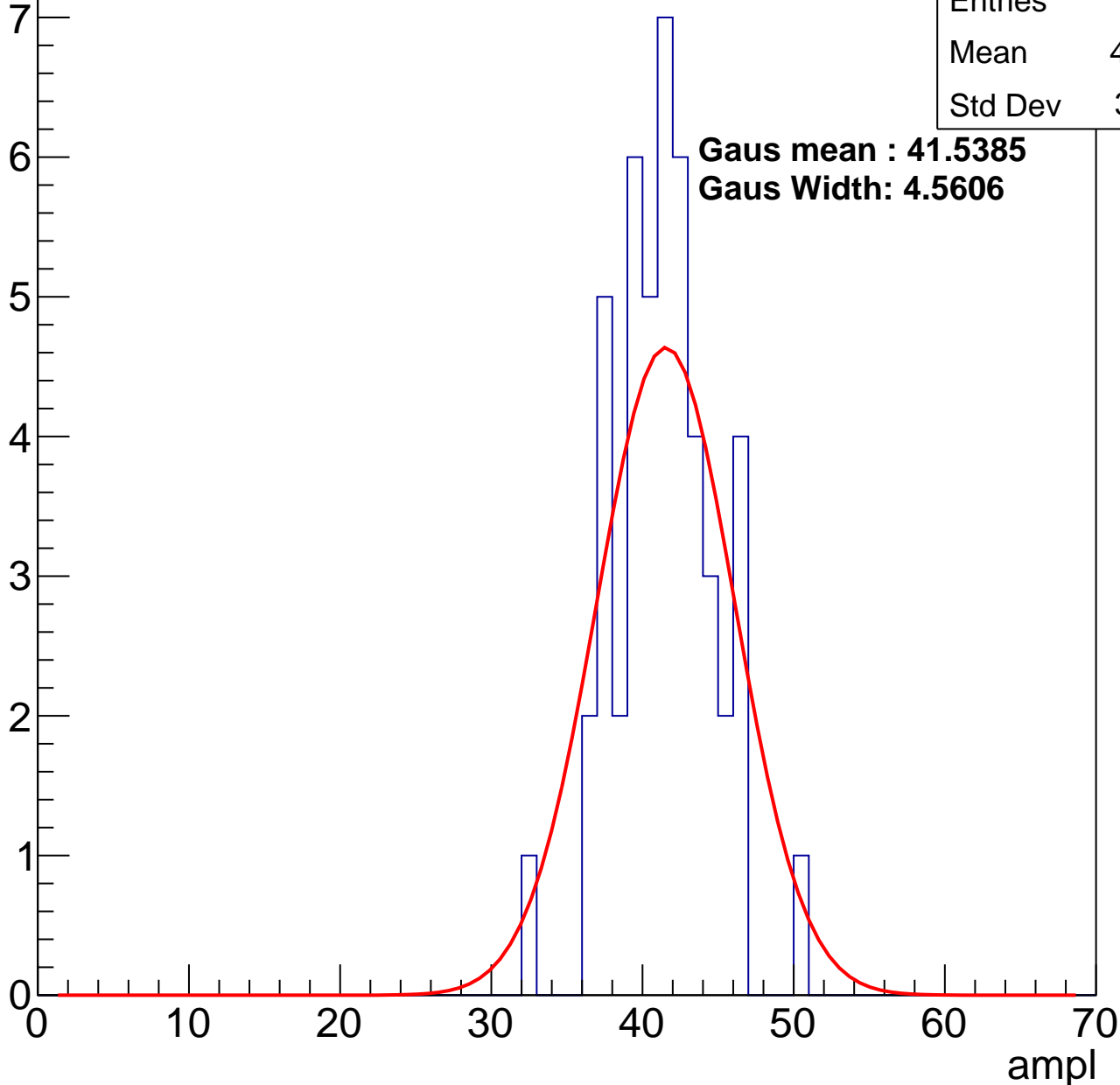
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	40.96
Std Dev	3.291

Gaus mean : 41.5385

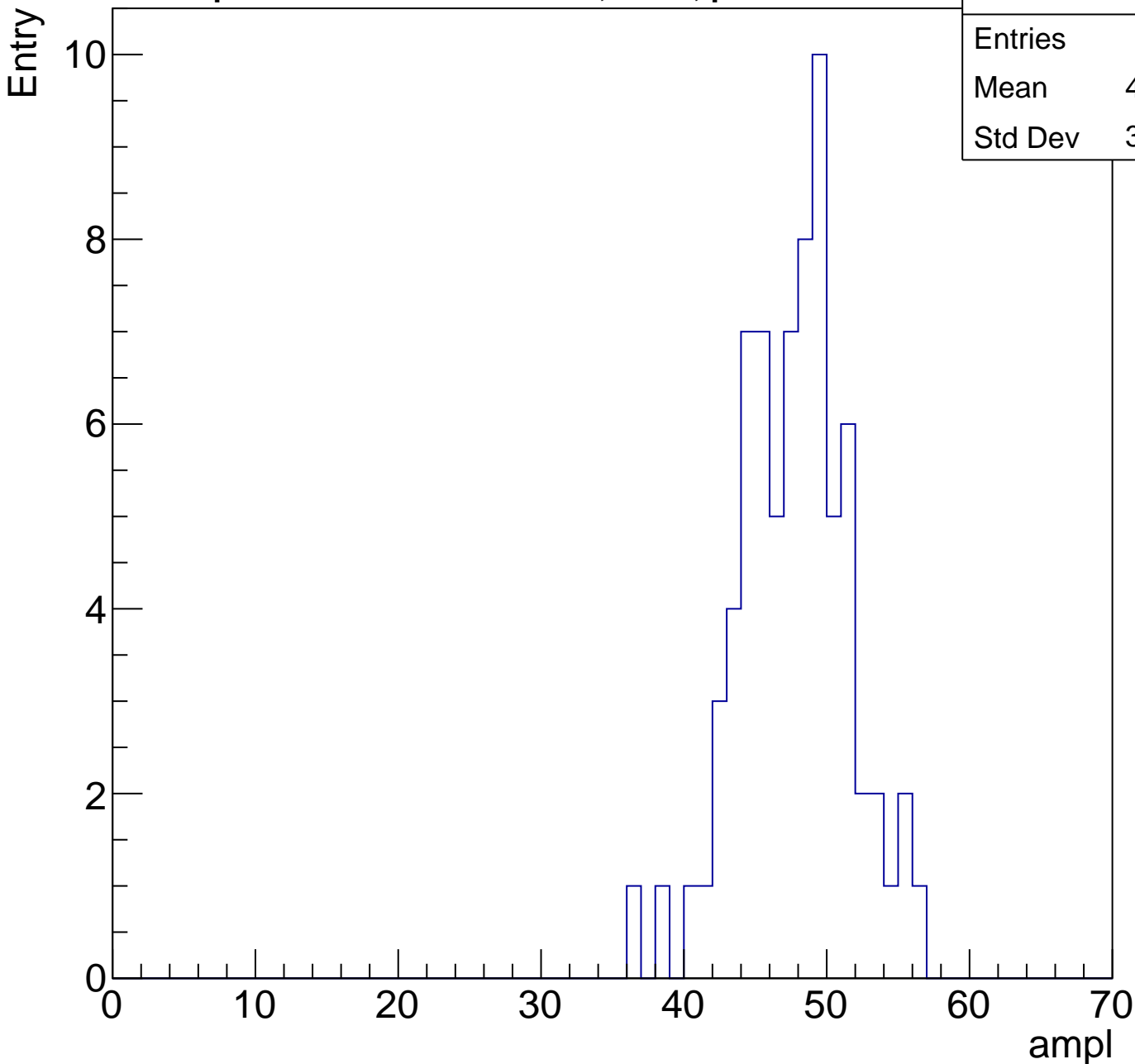
Gaus Width: 4.5606



B1L100S, U1-ch94, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	74
Mean	47.23
Std Dev	3.833

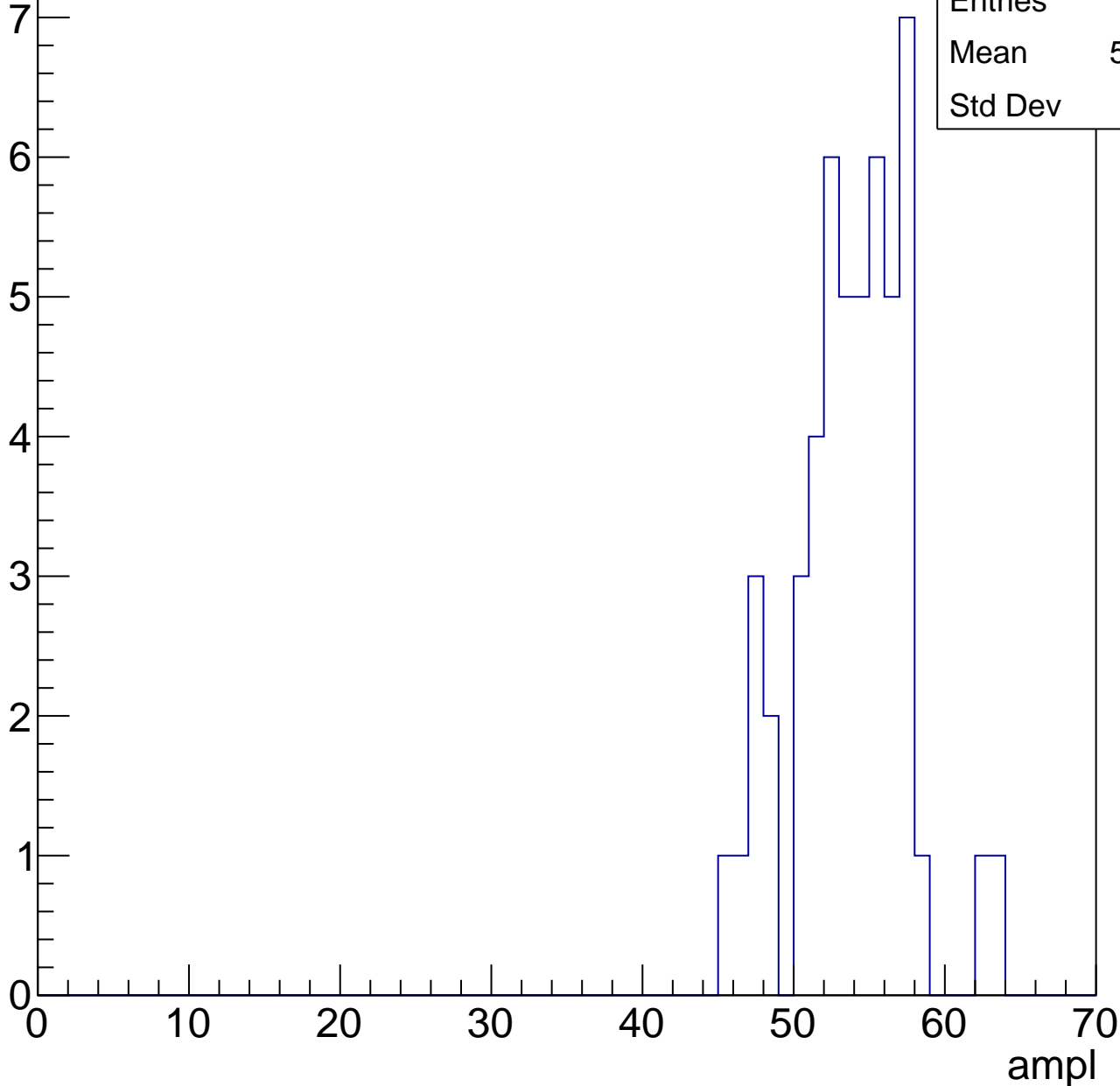


B1L100S, U1-ch94, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

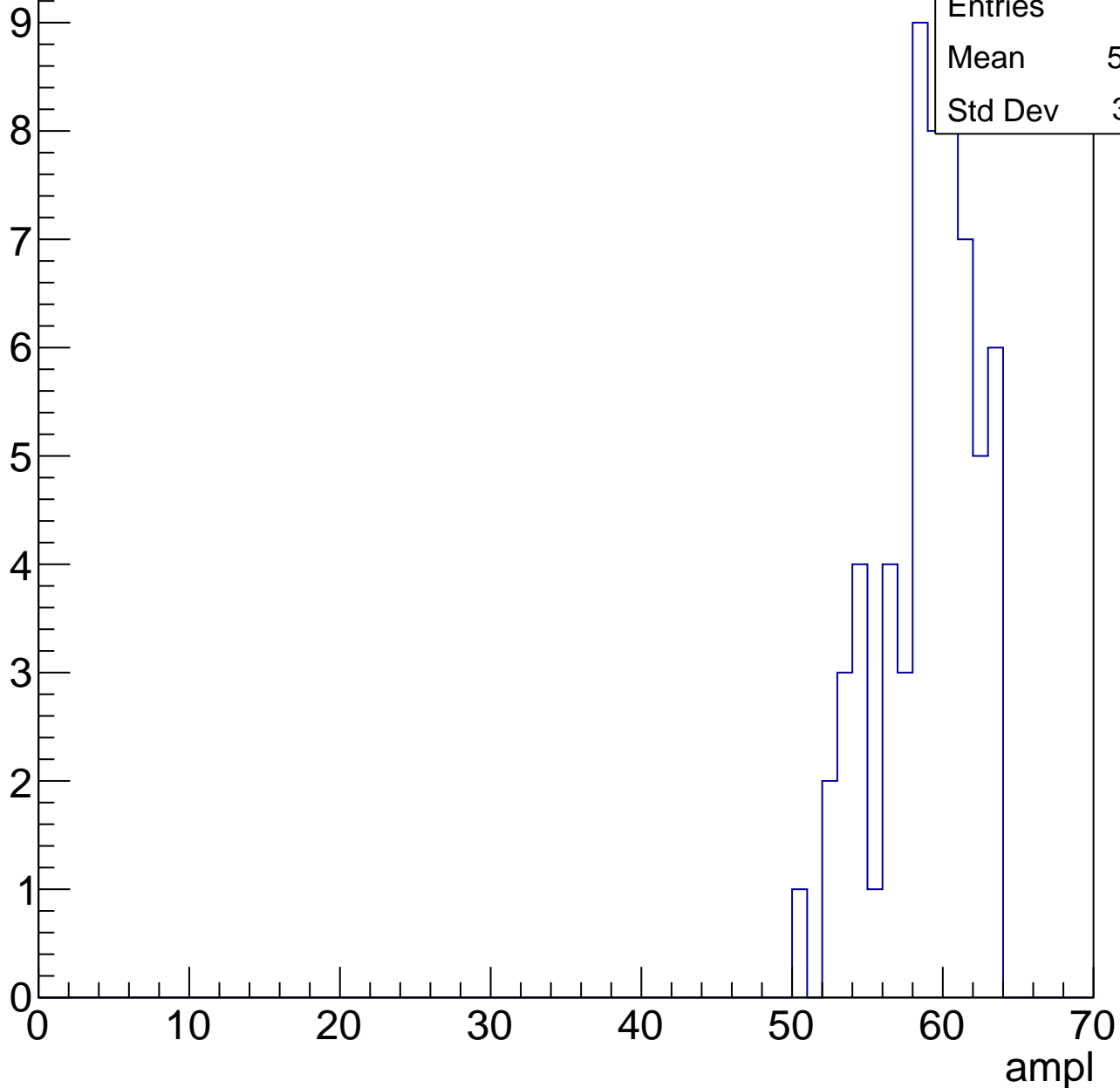
Entries	51
Mean	53.35
Std Dev	3.72



B1L100S, U1-ch94, adc5

calib_packv5_042523_0143.root, FC#4, port A2

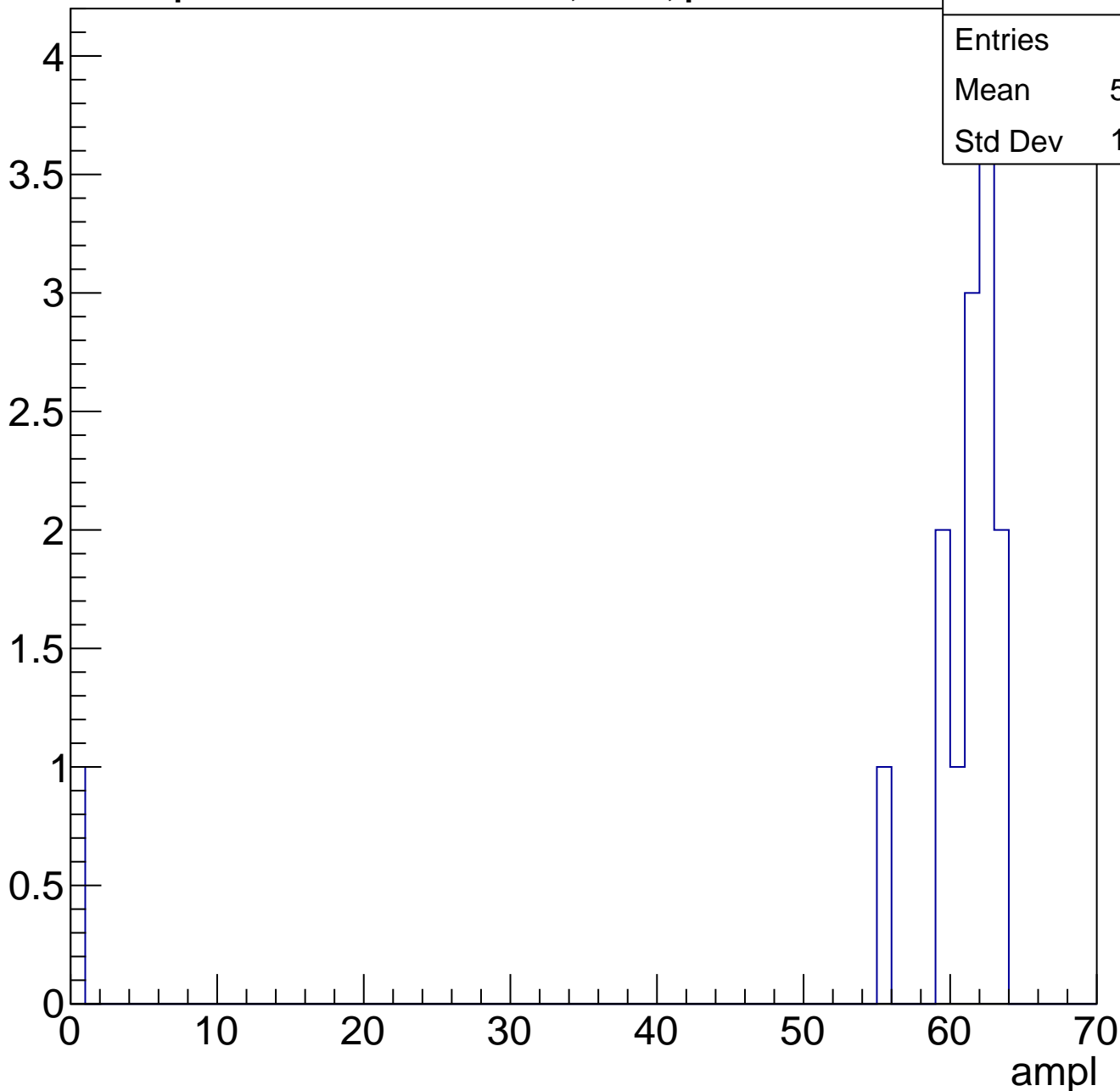
Entry



B1L100S, U1-ch94, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch94, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch95, adc0

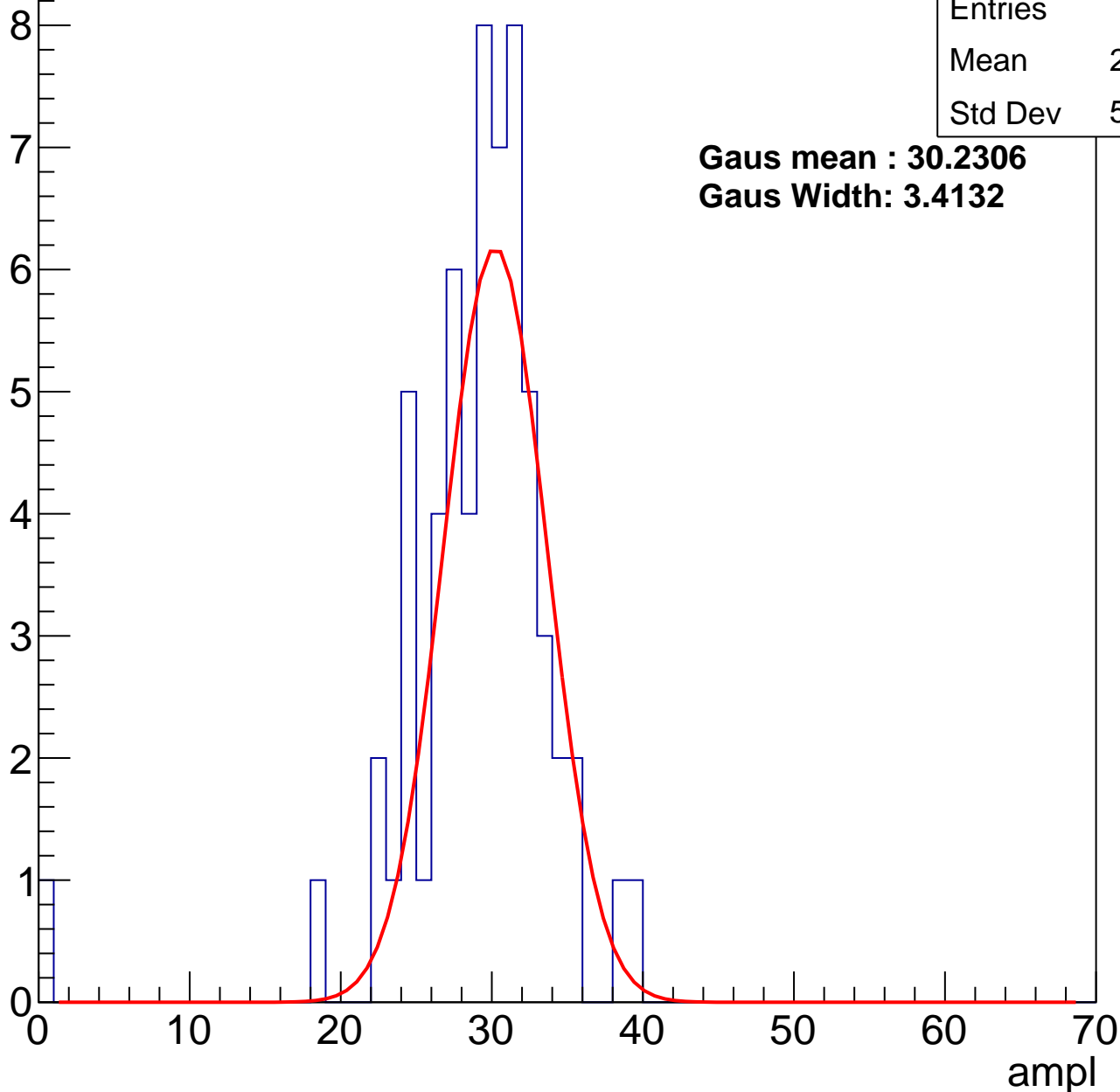
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.58
Std Dev	5.275

Gaus mean : 30.2306

Gaus Width: 3.4132



B1L100S, U1-ch95, adc1

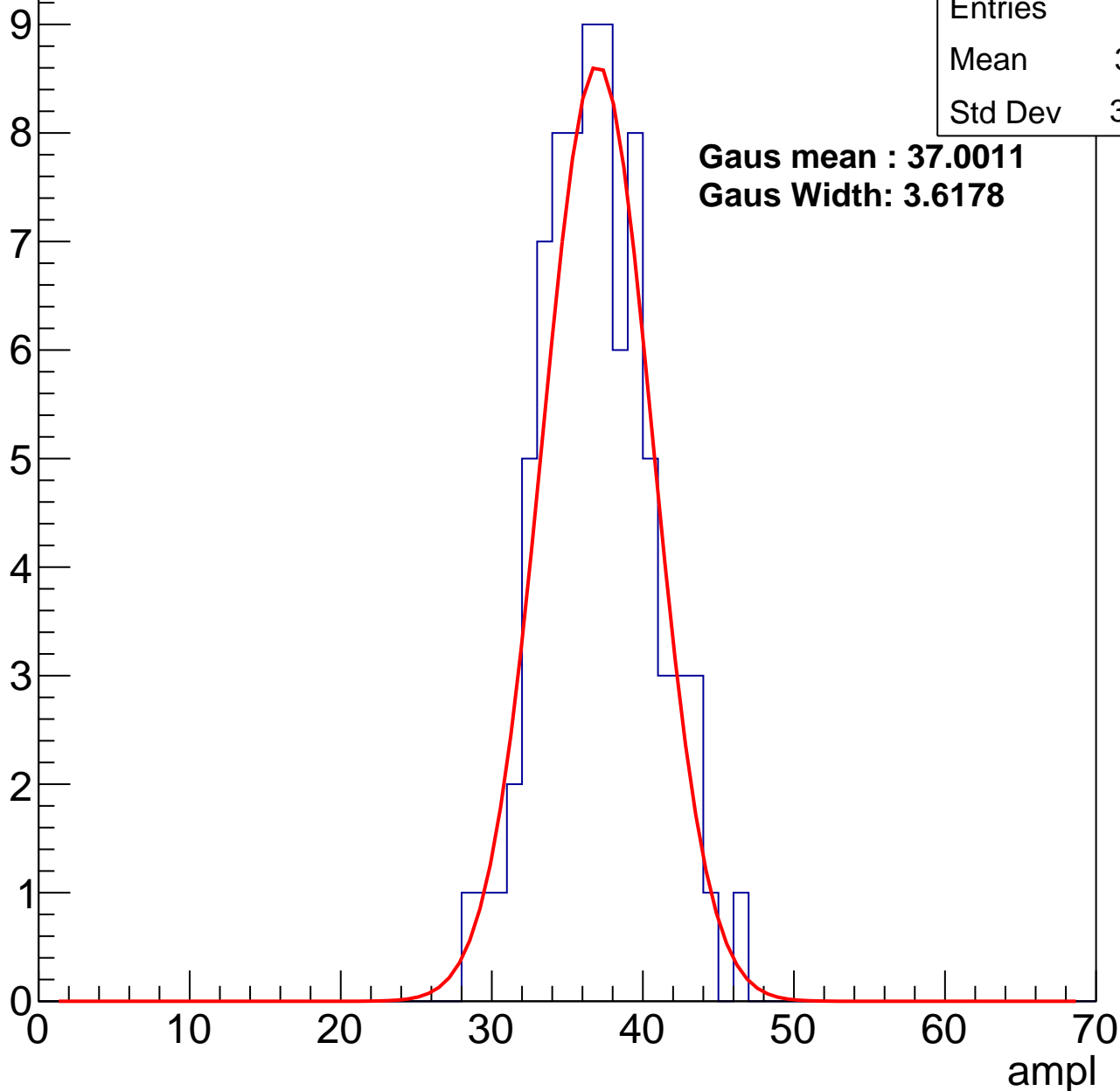
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	81
Mean	36.51
Std Dev	3.584

Gaus mean : 37.0011

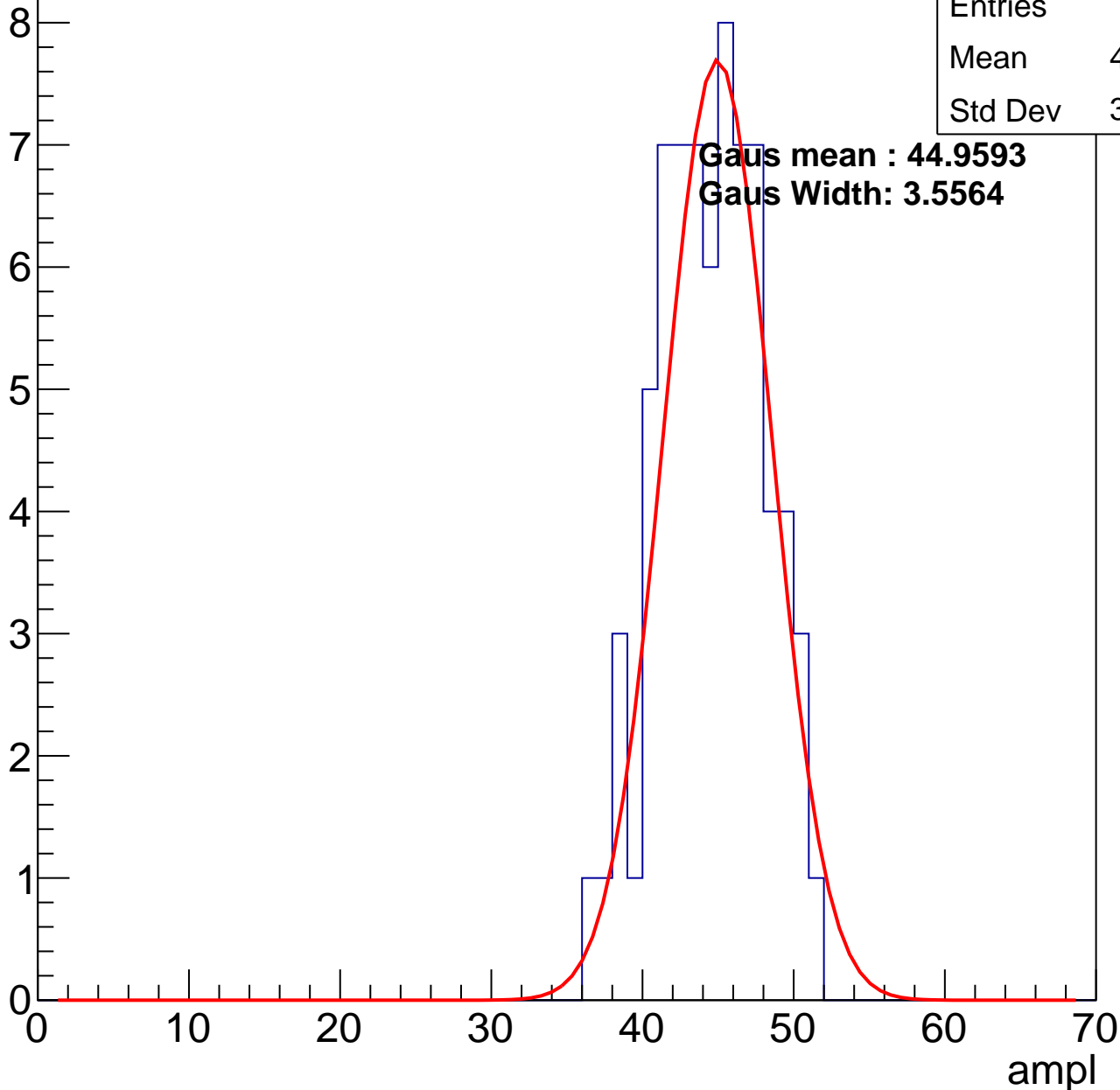
Gaus Width: 3.6178



B1L100S, U1-ch95, adc2

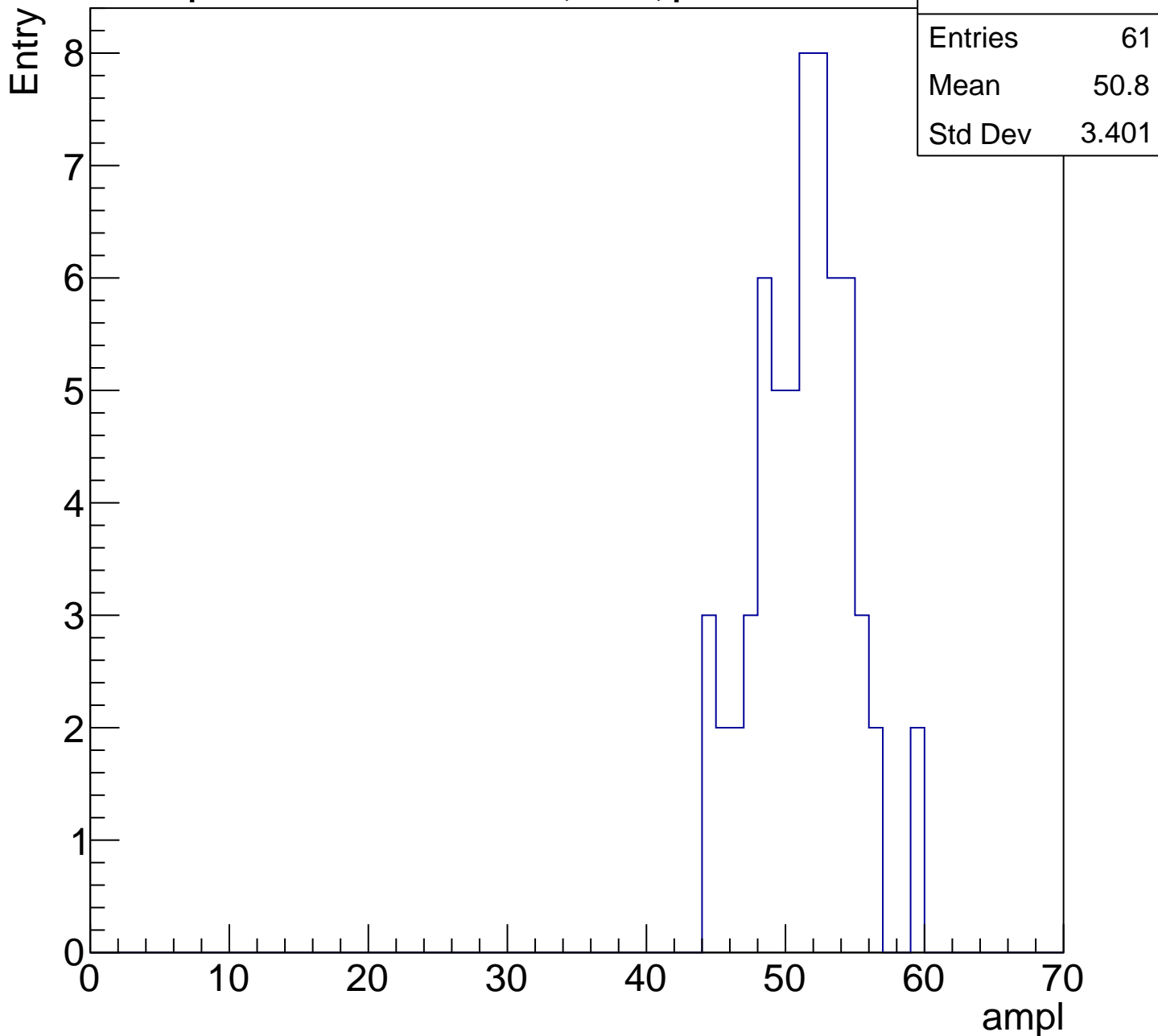
calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch95, adc3

calib_packv5_042523_0143.root, FC#4, port A2

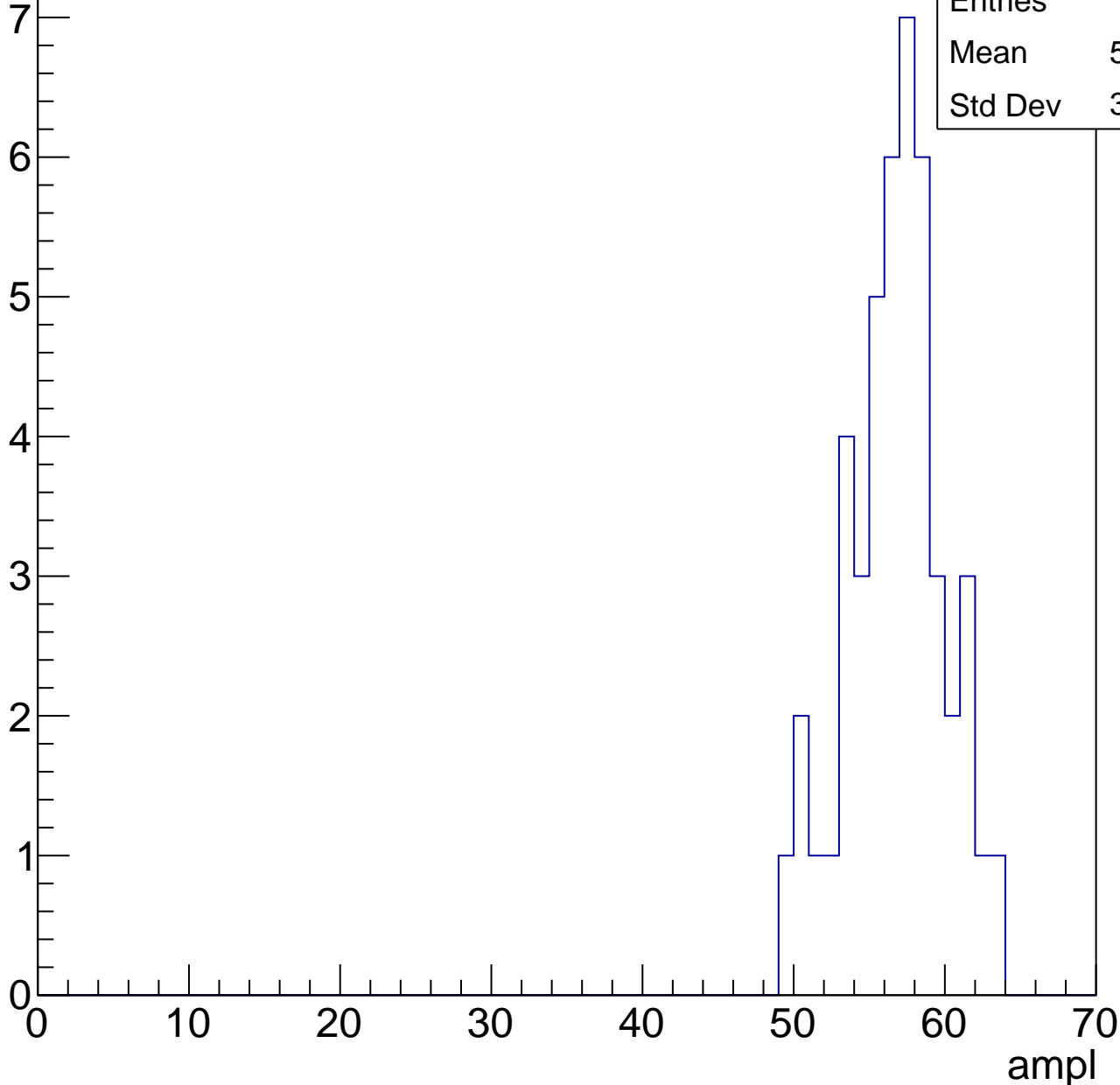


B1L100S, U1-ch95, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	46
Mean	56.28
Std Dev	3.167



B1L100S, U1-ch95, adc5

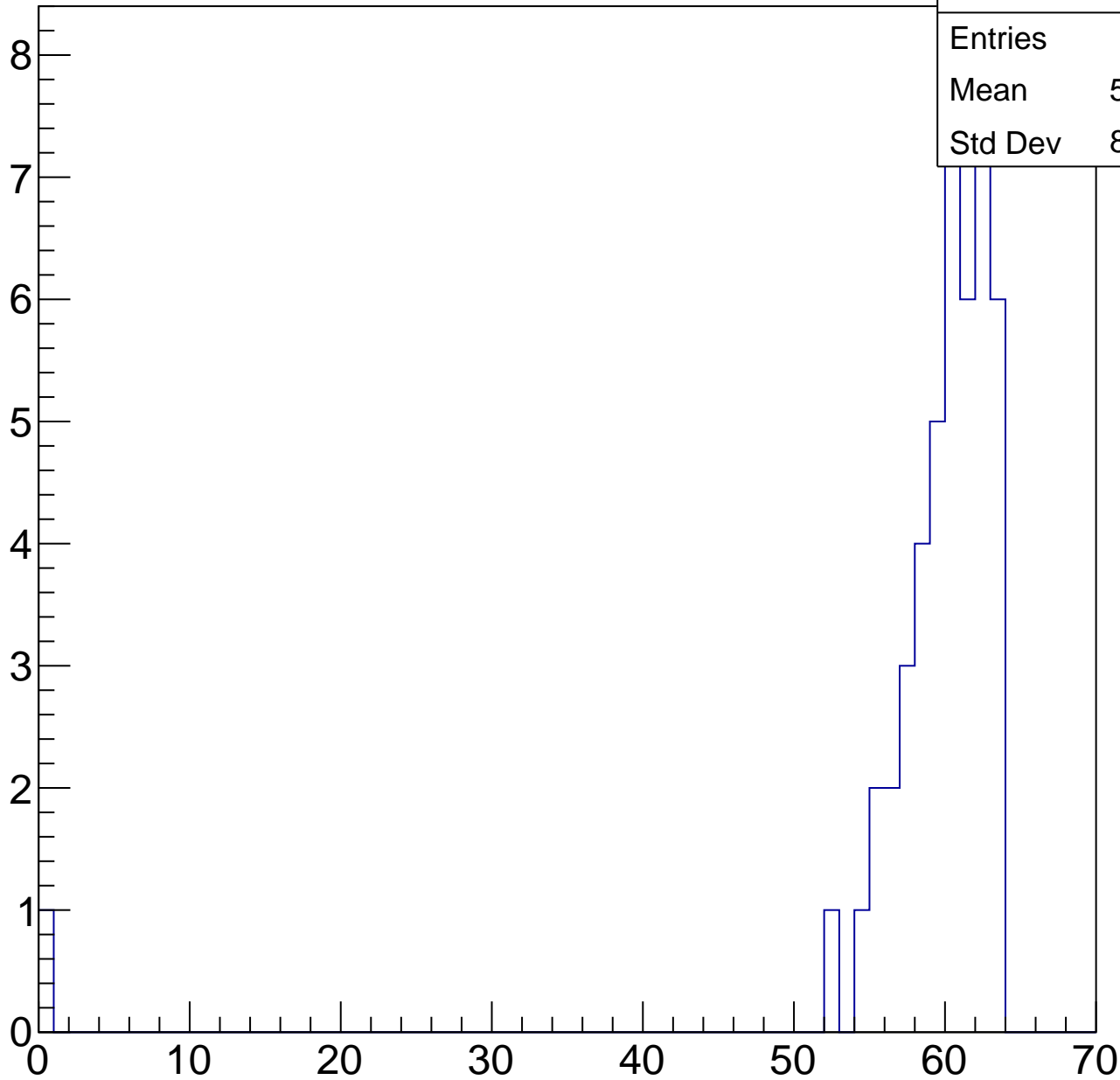
calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	47
Mean	58.43
Std Dev	8.998

ampl



B1L100S, U1-ch95, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch95, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch96, adc0

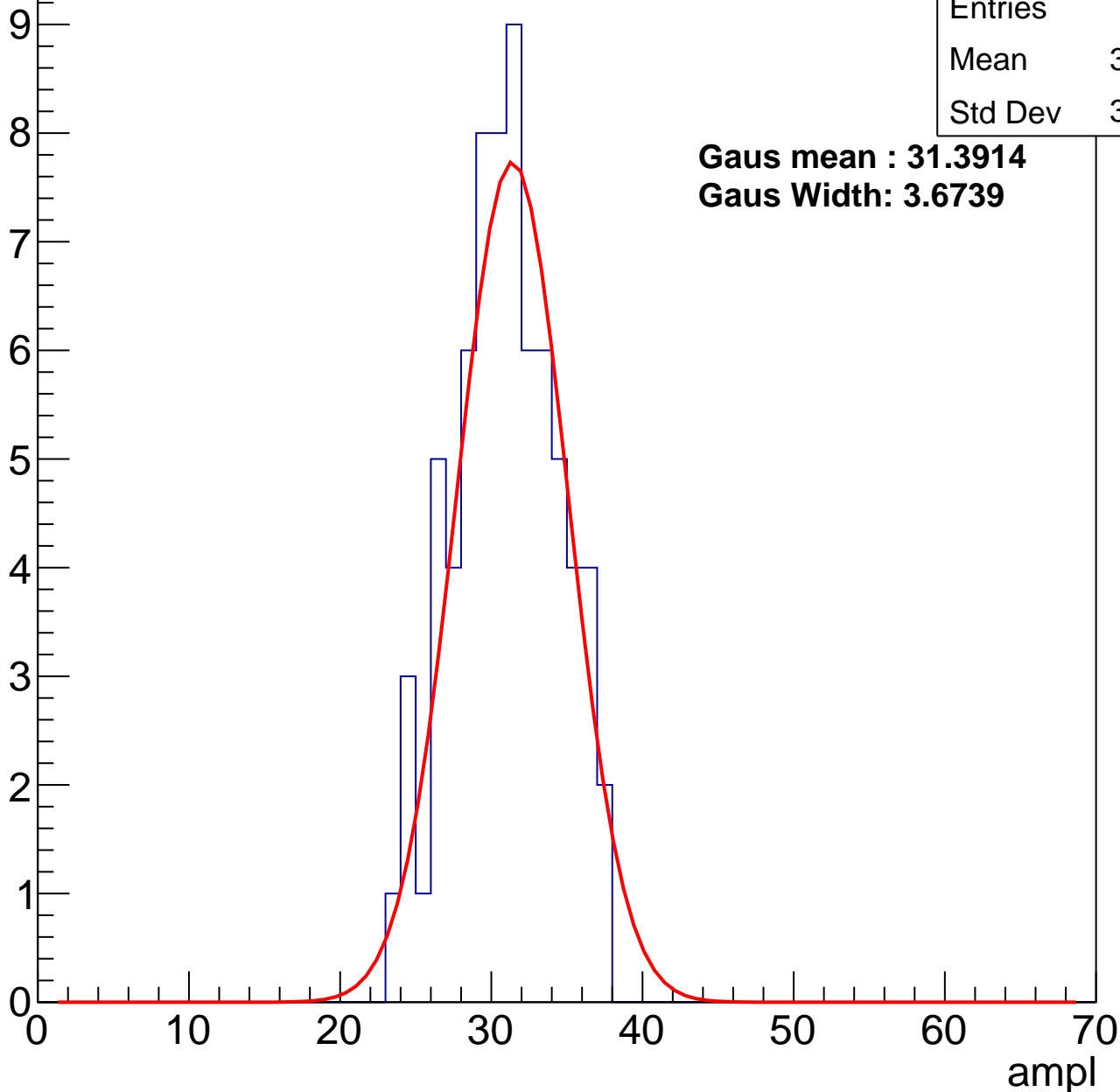
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	30.49
Std Dev	3.379

Gaus mean : 31.3914

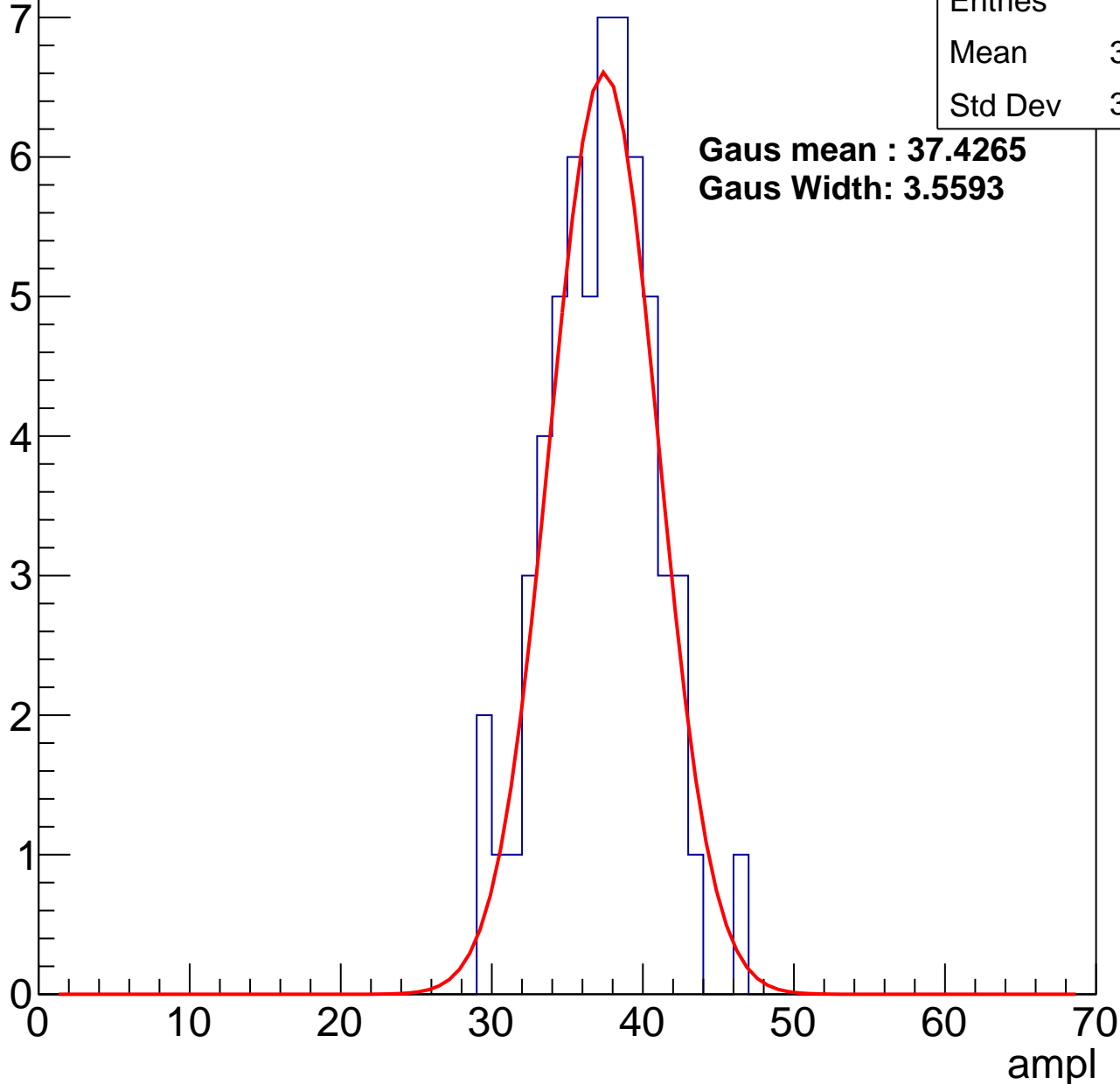
Gaus Width: 3.6739



B1L100S, U1-ch96, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch96, adc2

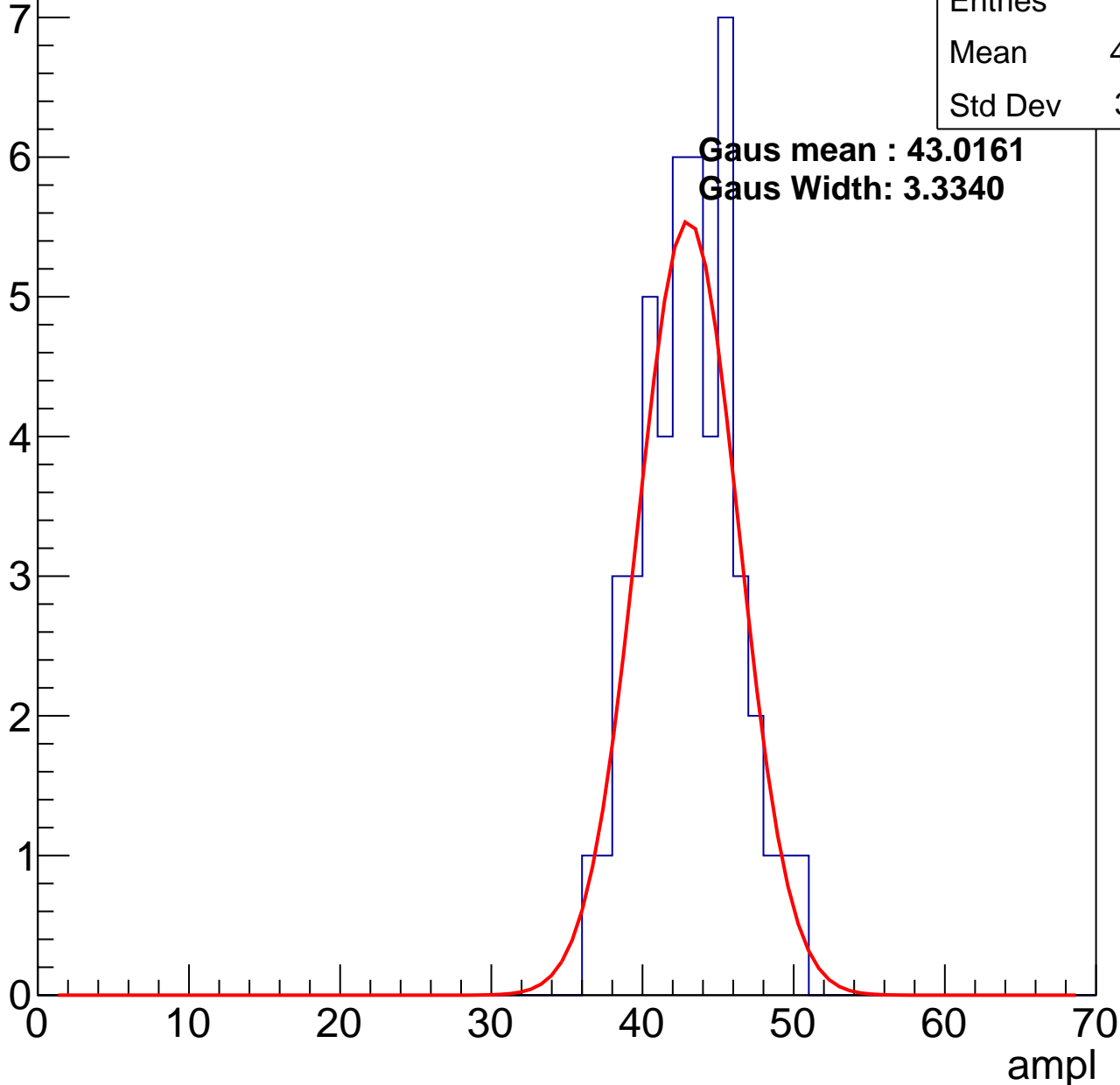
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	42.67
Std Dev	3.131

Gaus mean : 43.0161

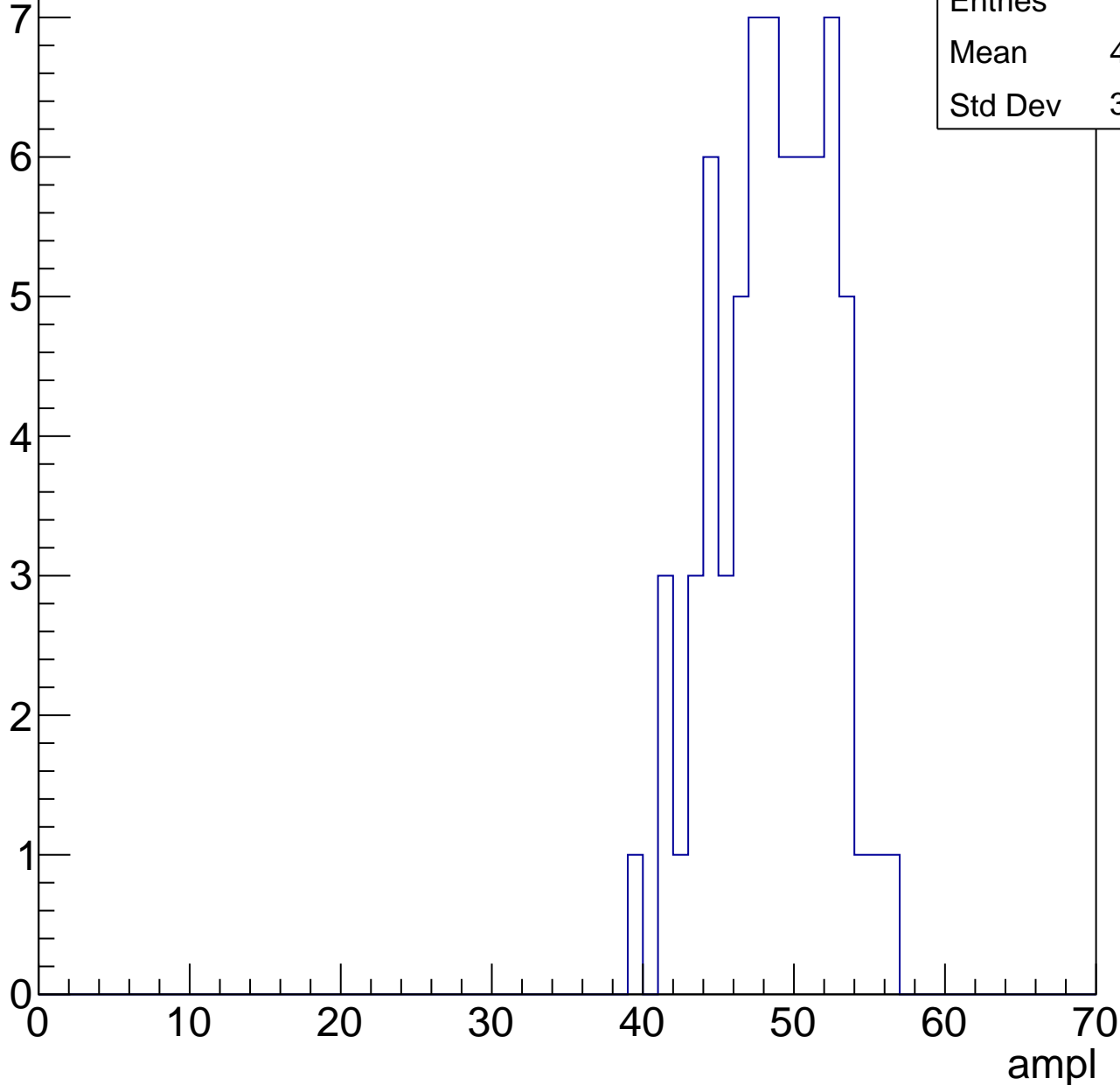
Gaus Width: 3.3340



B1L100S, U1-ch96, adc3

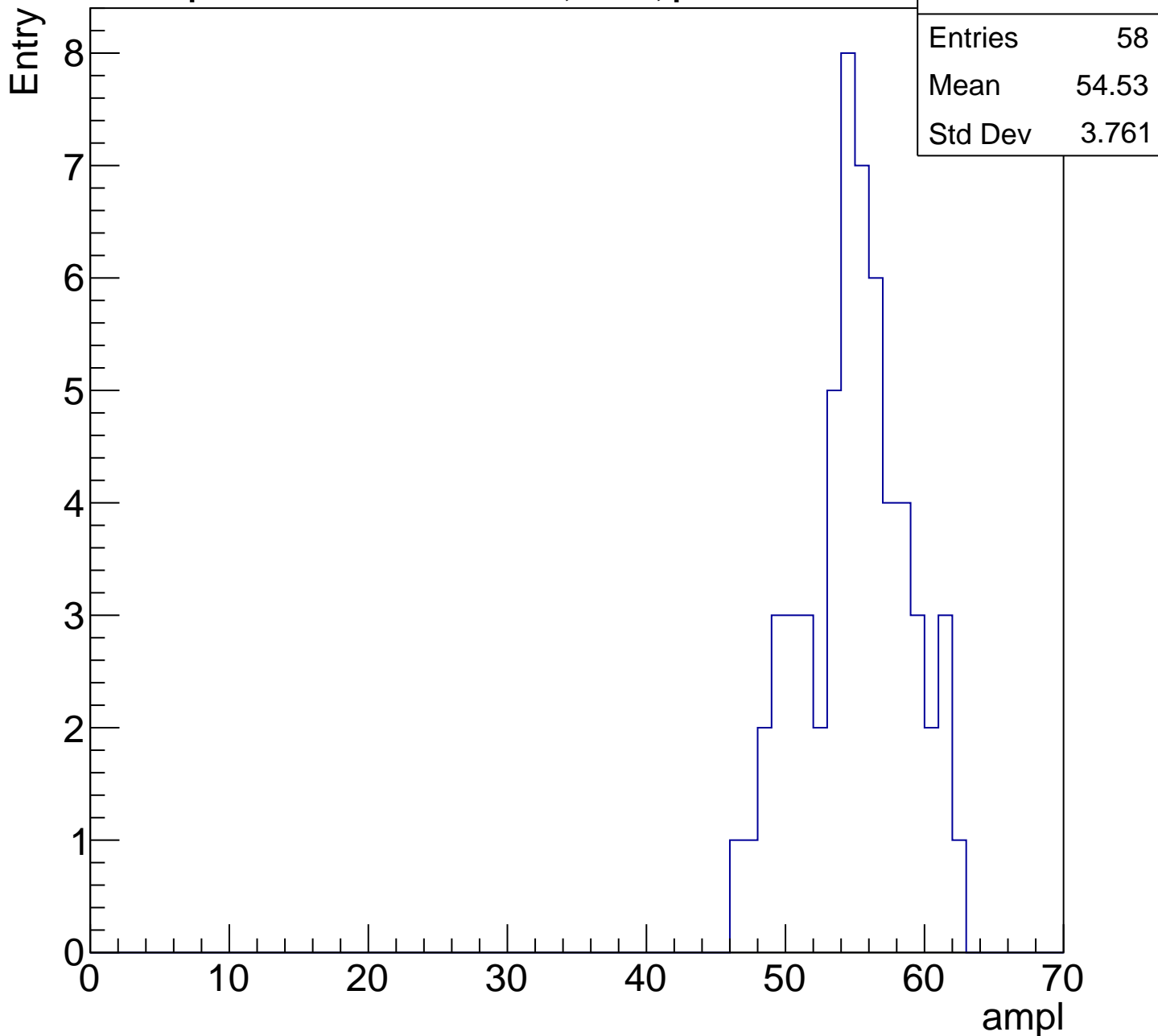
calib_packv5_042523_0143.root, FC#4, port A2

Entry



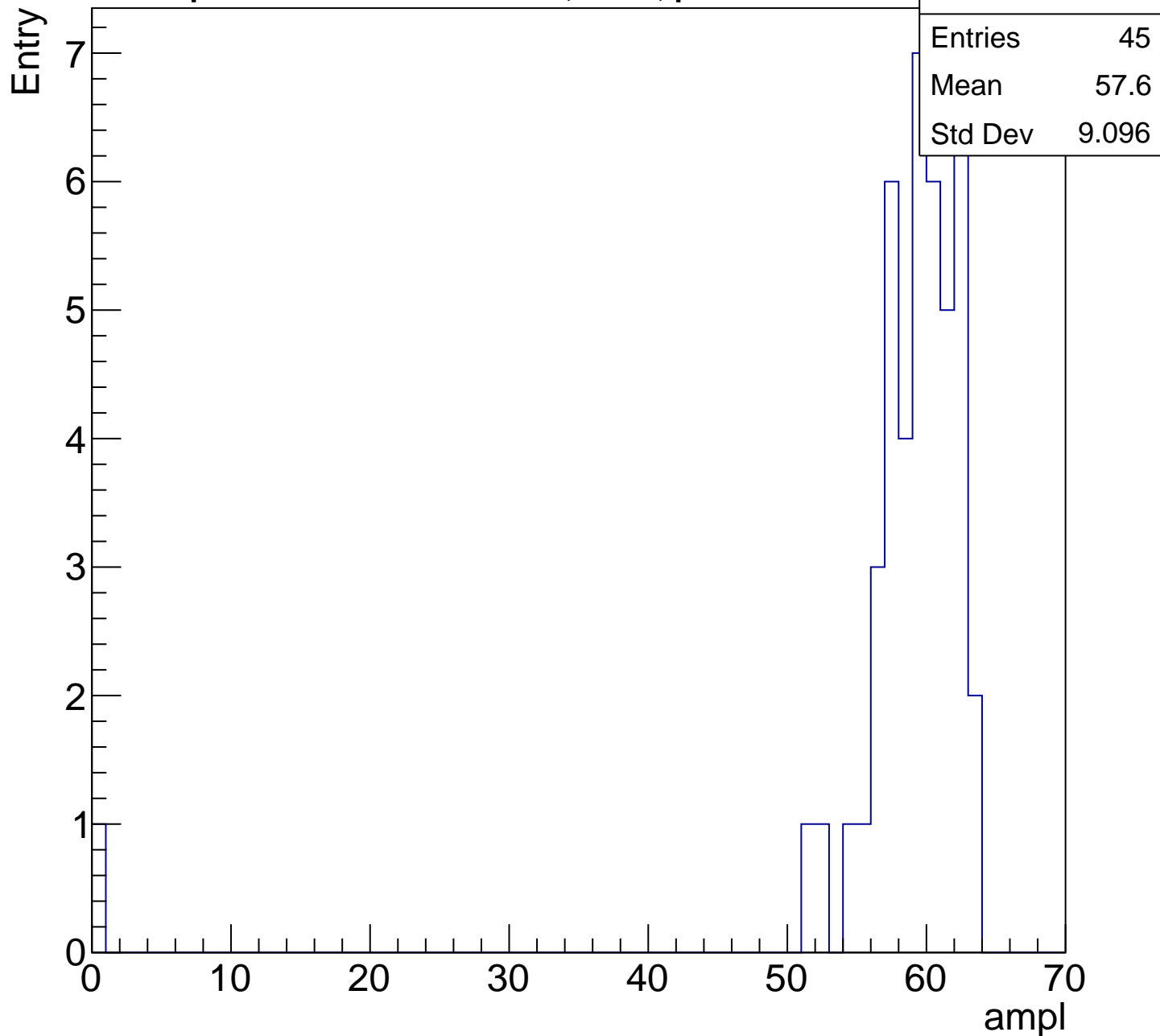
B1L100S, U1-ch96, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch96, adc5

calib_packv5_042523_0143.root, FC#4, port A2

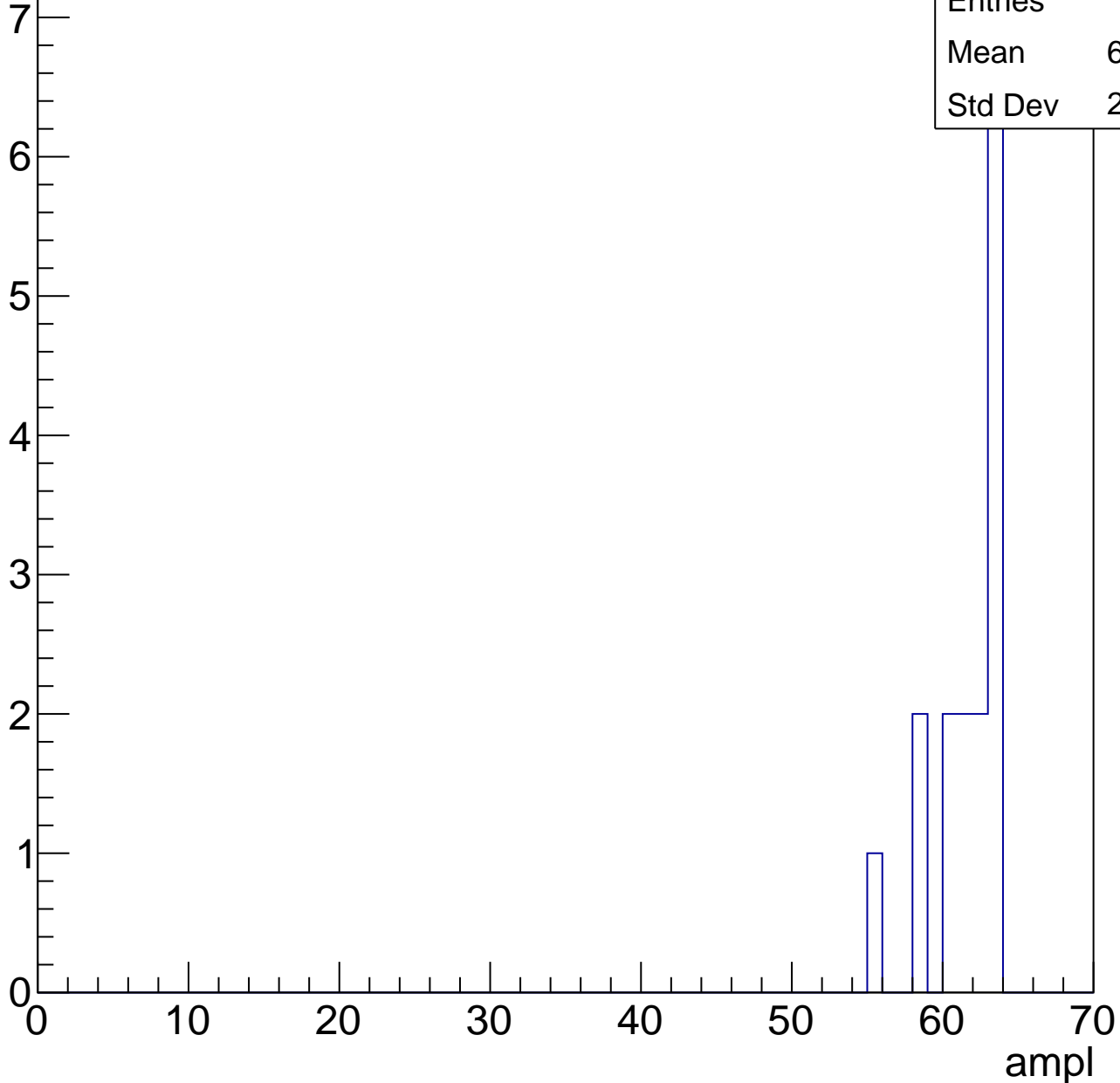


B1L100S, U1-ch96, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	61.12
Std Dev	2.315



B1L100S, U1-ch96, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch97, adc0

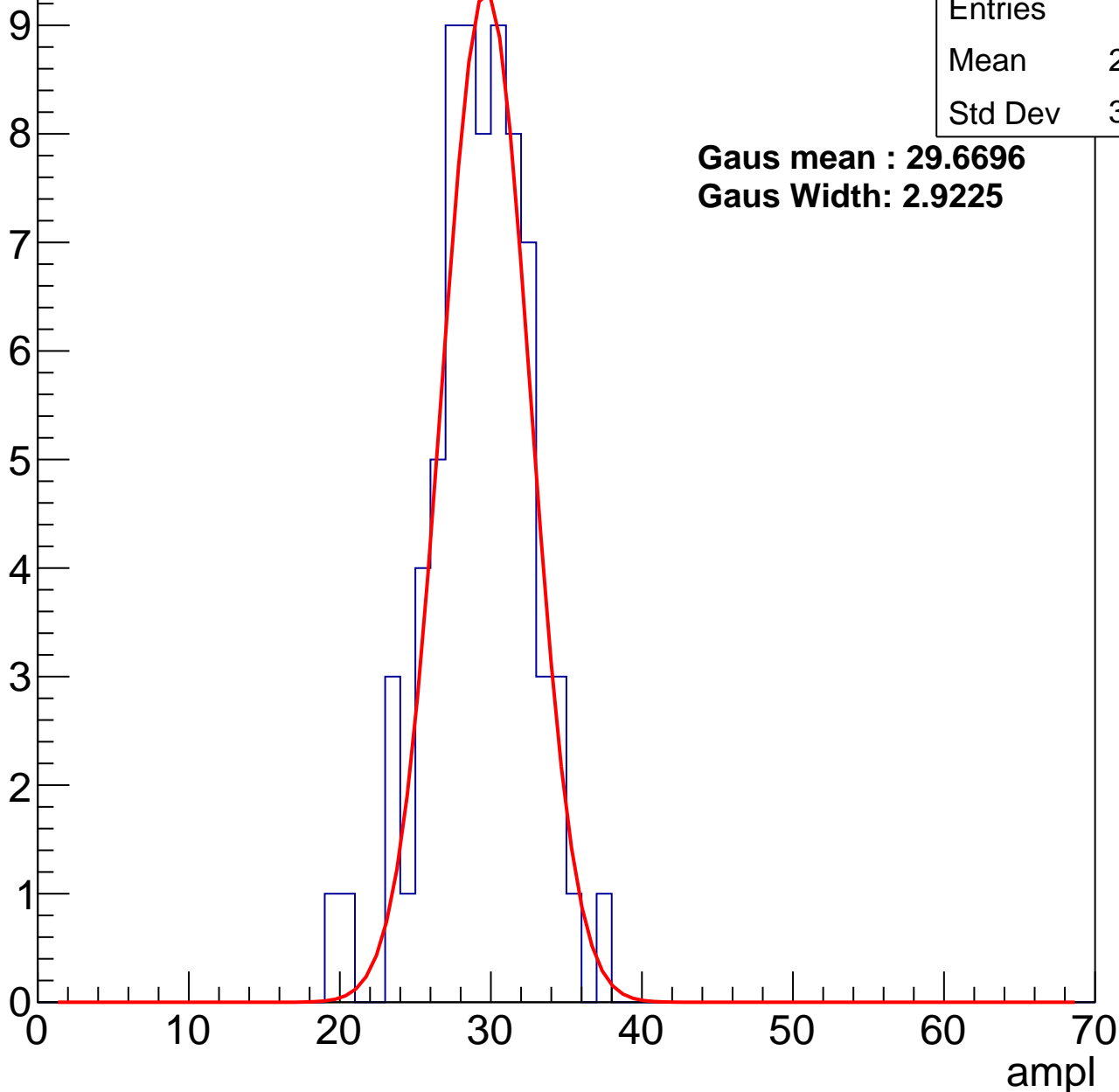
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	28.82
Std Dev	3.295

Gaus mean : 29.6696

Gaus Width: 2.9225



B1L100S, U1-ch97, adc1

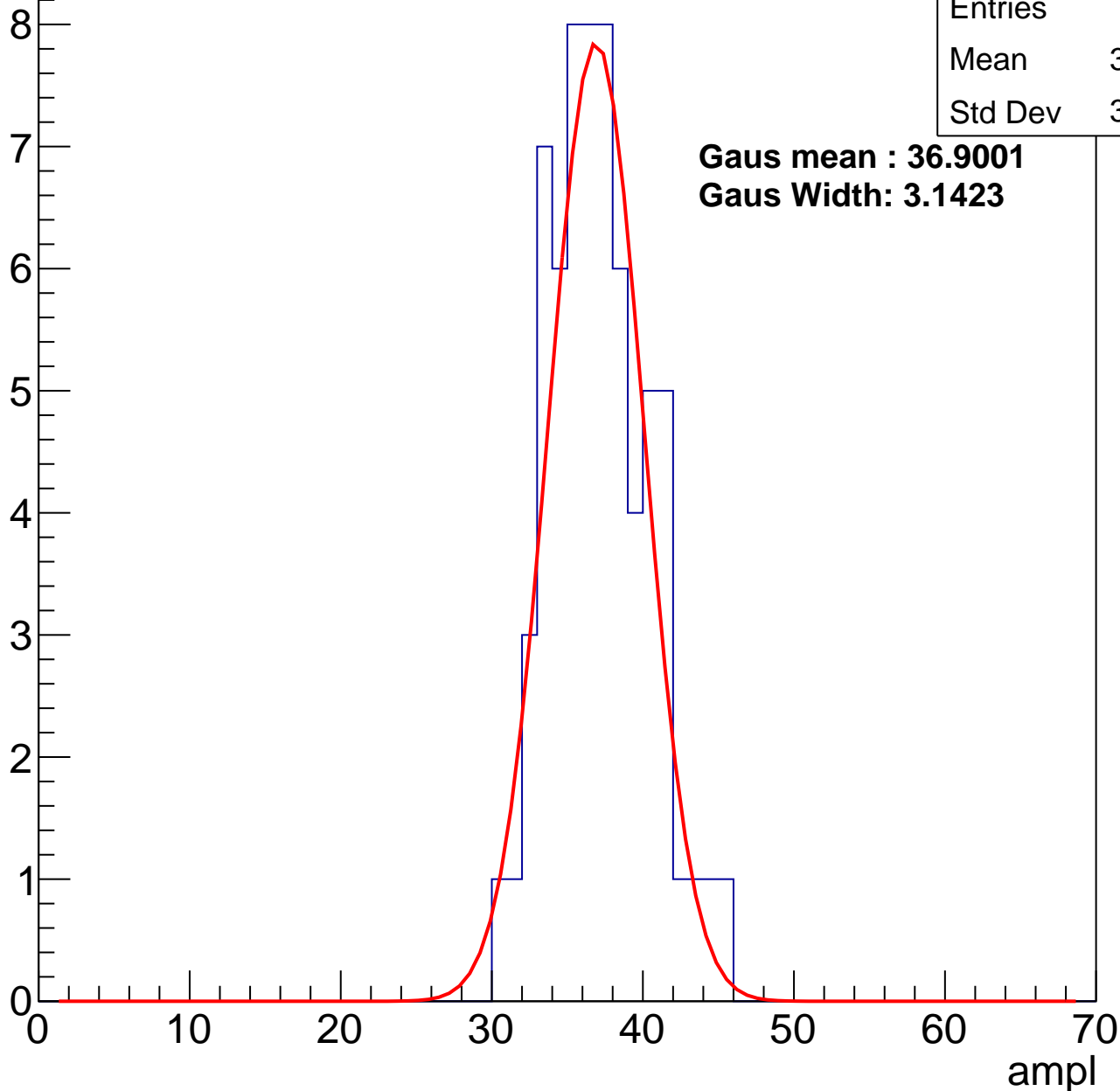
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	36.65
Std Dev	3.207

Gaus mean : 36.9001

Gaus Width: 3.1423

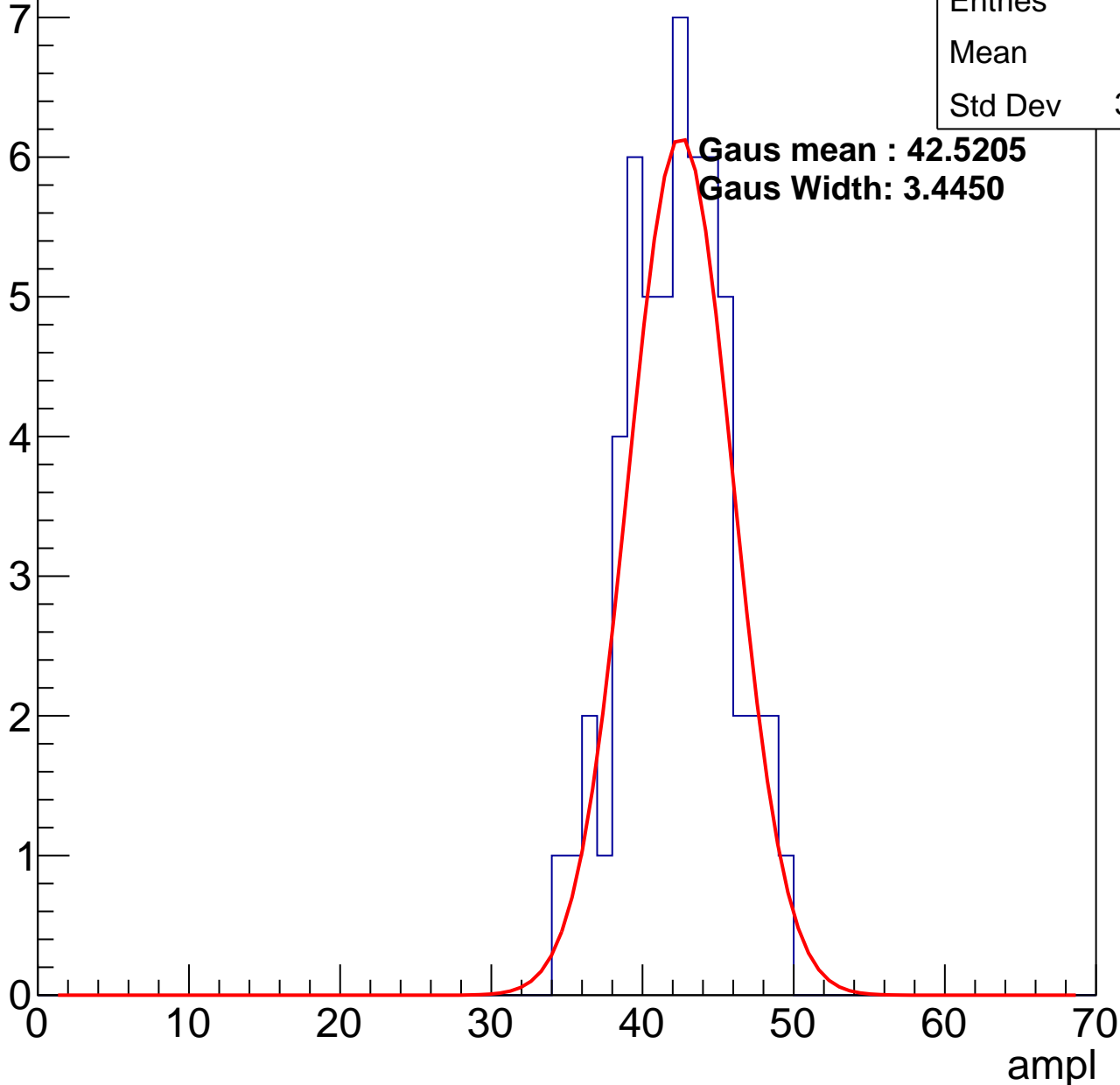


B1L100S, U1-ch97, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	41.8
Std Dev	3.351

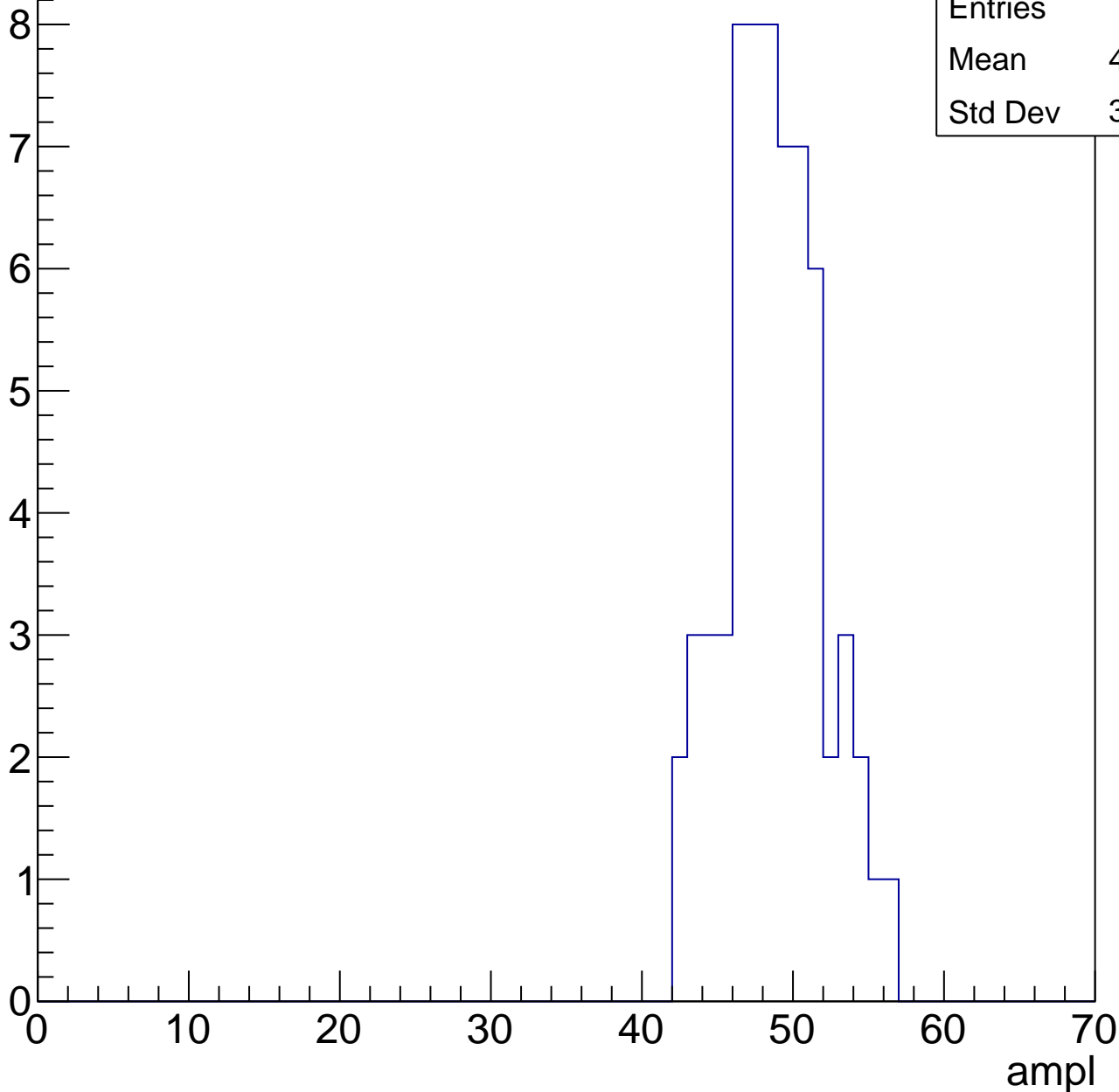


B1L100S, U1-ch97, adc3

calib_packv5_042523_0143.root, FC#4, port A2

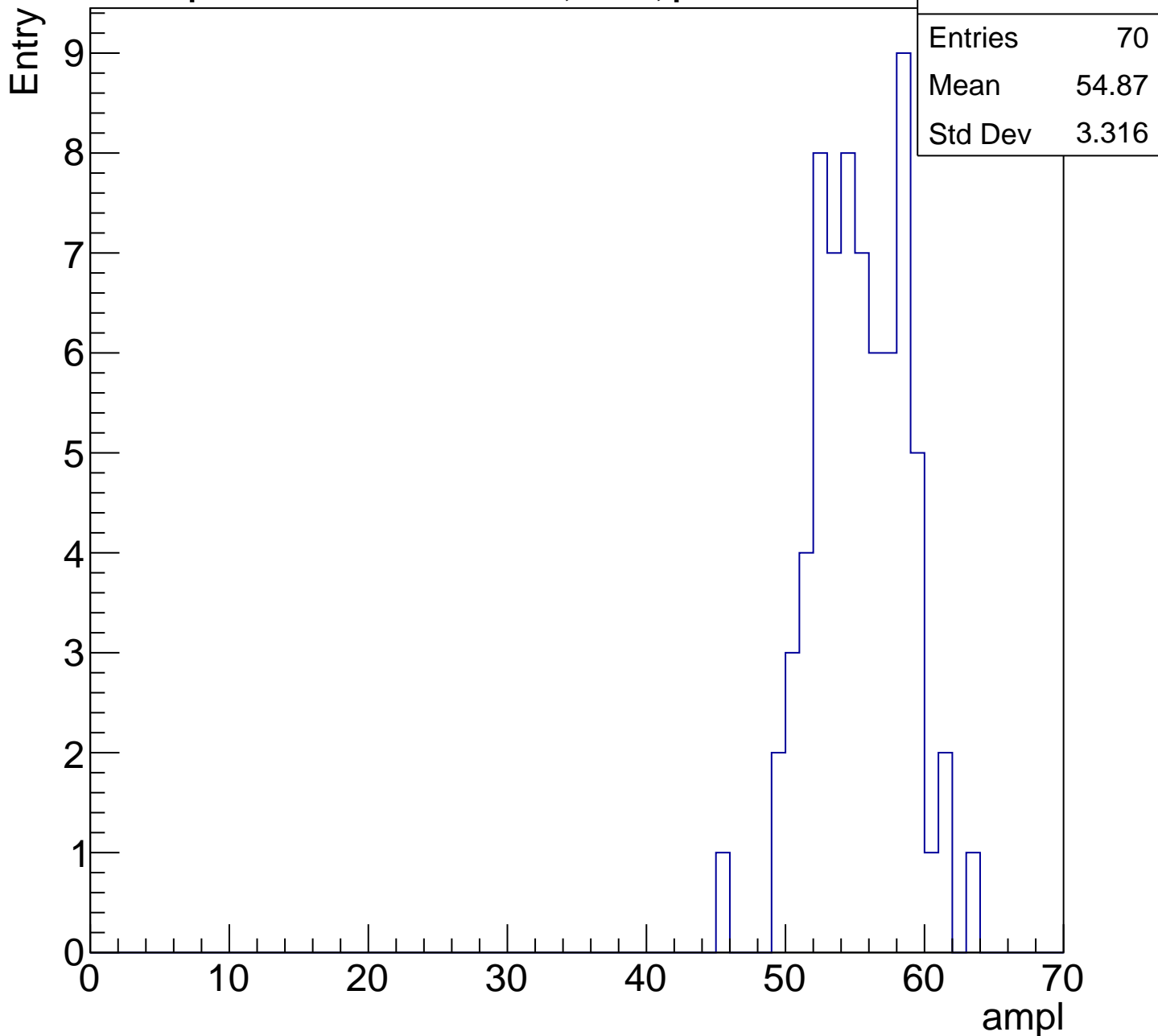
Entry

Entries	64
Mean	48.27
Std Dev	3.159



B1L100S, U1-ch97, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch97, adc5

calib_packv5_042523_0143.root, FC#4, port A2

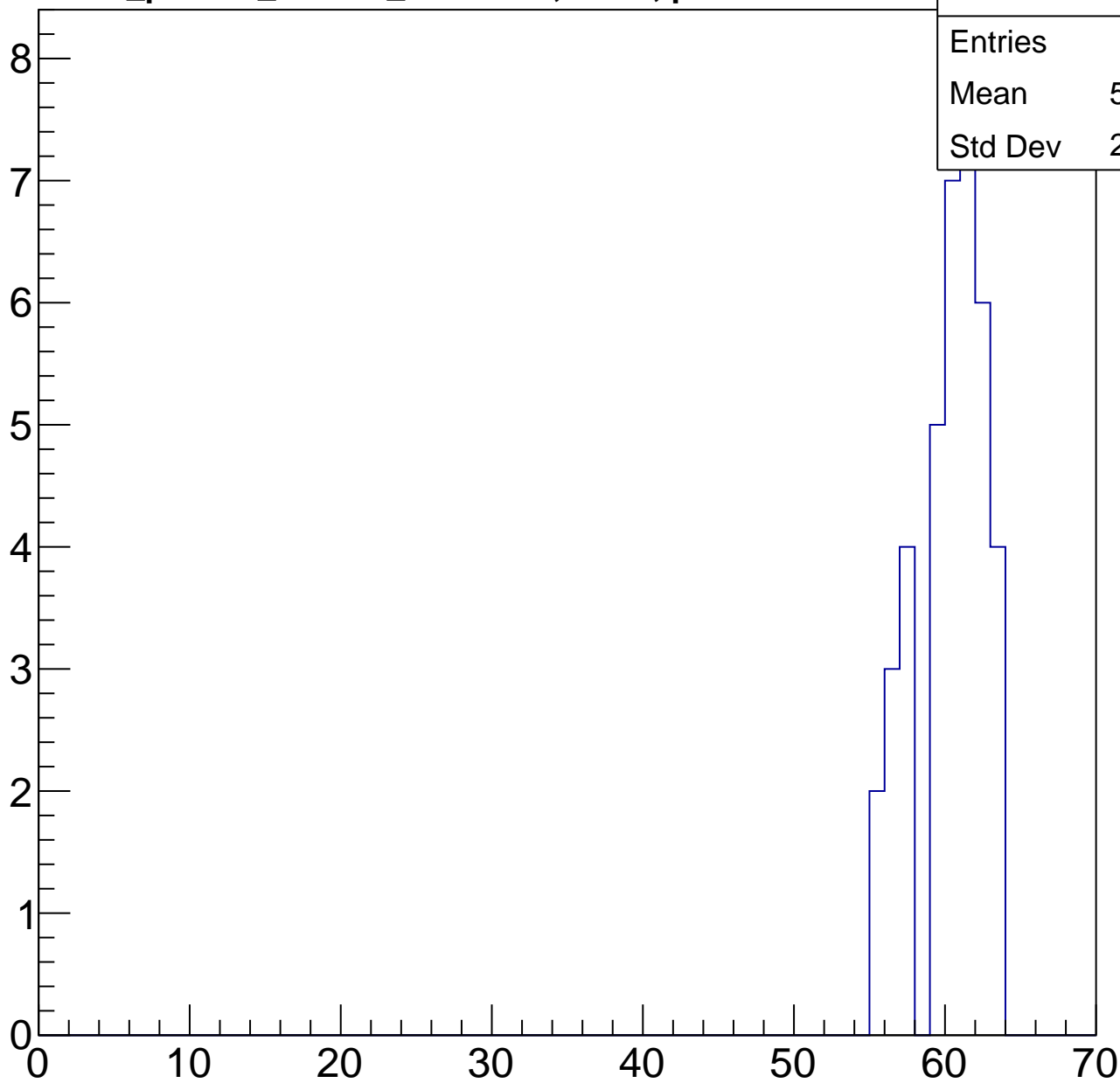
Entry

8
7
6
5
4
3
2
1
0

Entries	39
Mean	59.82
Std Dev	2.297

ampl

0 10 20 30 40 50 60 70

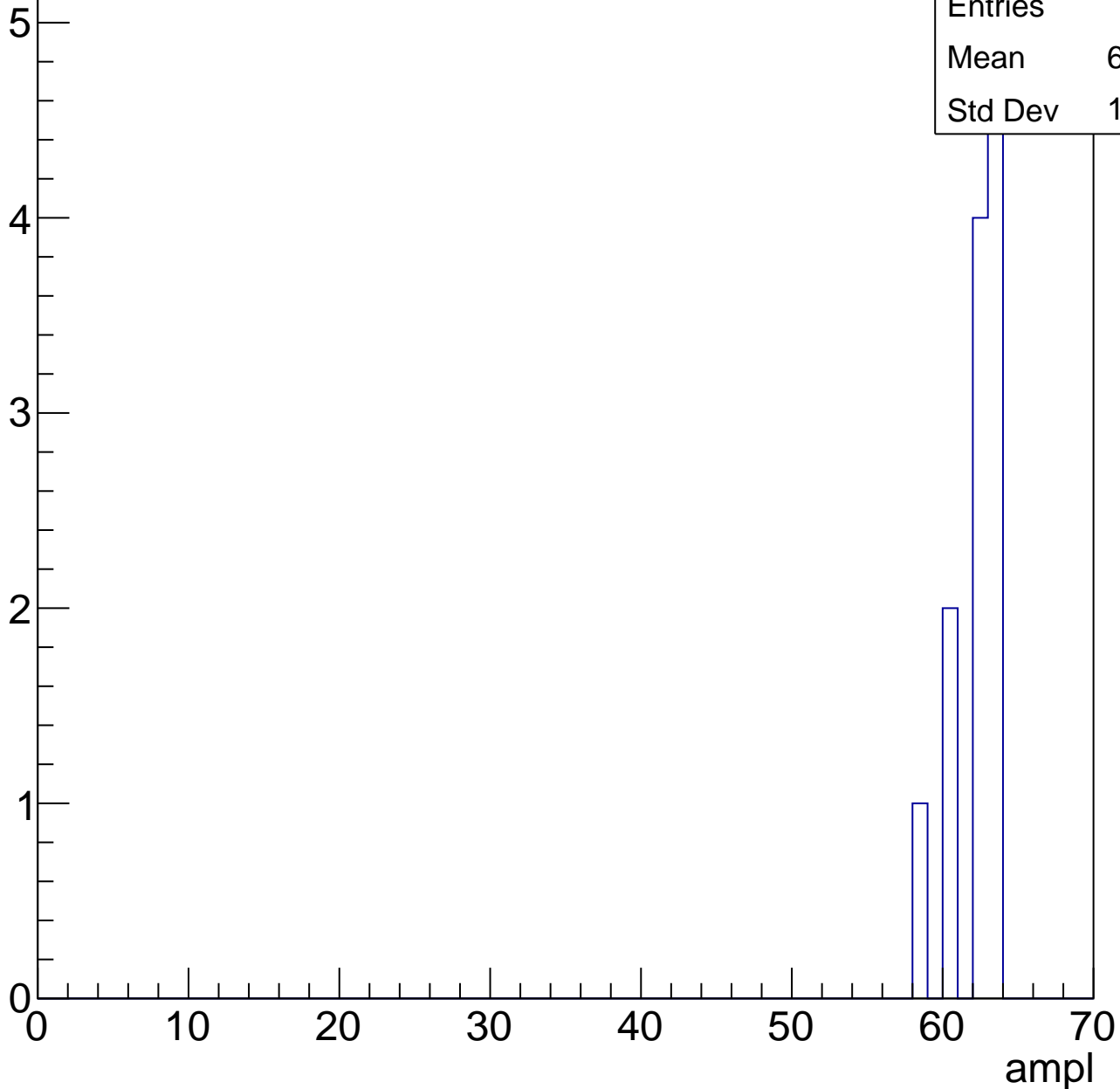


B1L100S, U1-ch97, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	61.75
Std Dev	1.534



B1L100S, U1-ch97, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch98, adc0

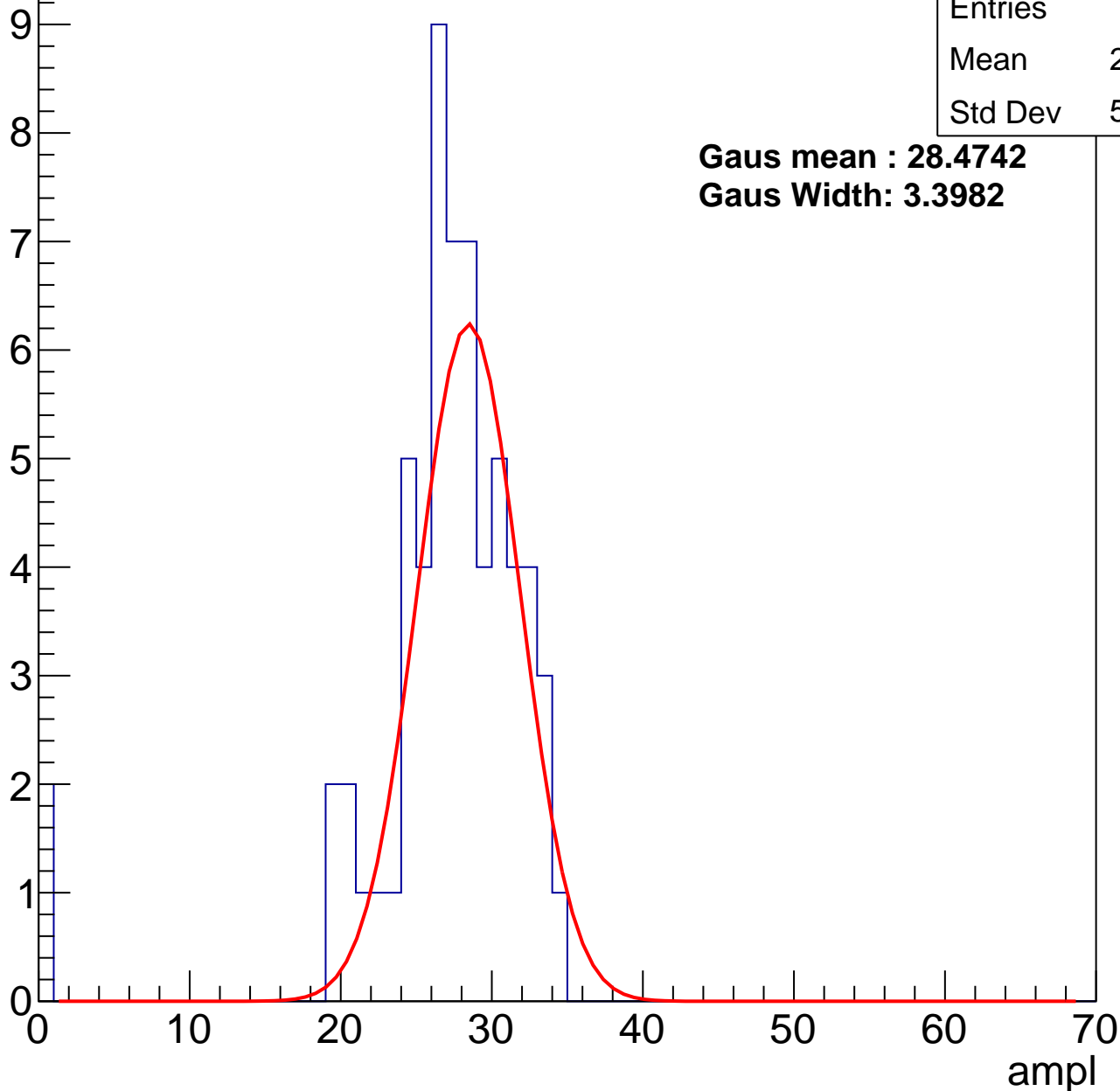
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	26.35
Std Dev	5.949

Gaus mean : 28.4742

Gaus Width: 3.3982



B1L100S, U1-ch98, adc1

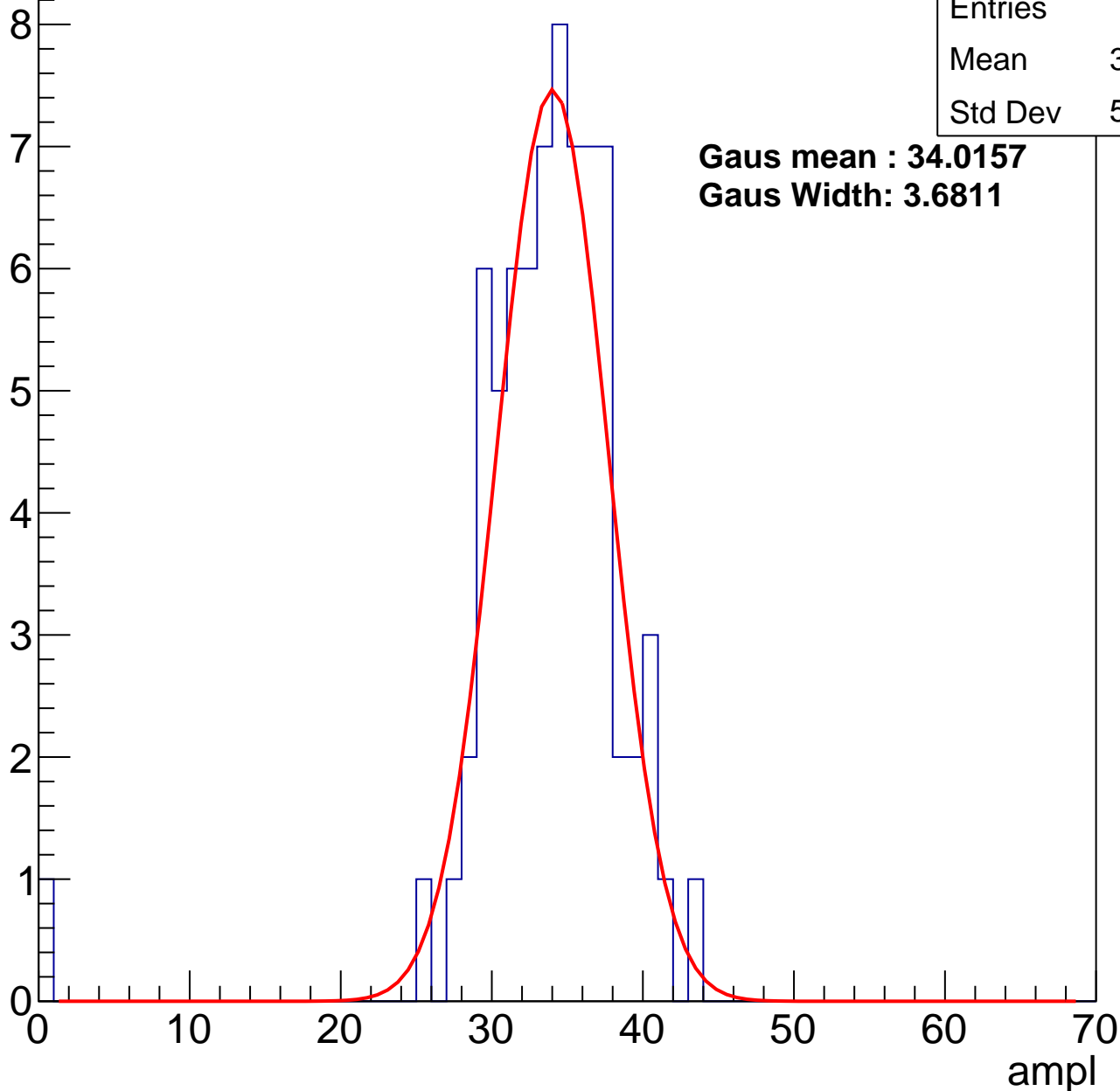
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	33.25
Std Dev	5.299

Gaus mean : 34.0157

Gaus Width: 3.6811



B1L100S, U1-ch98, adc2

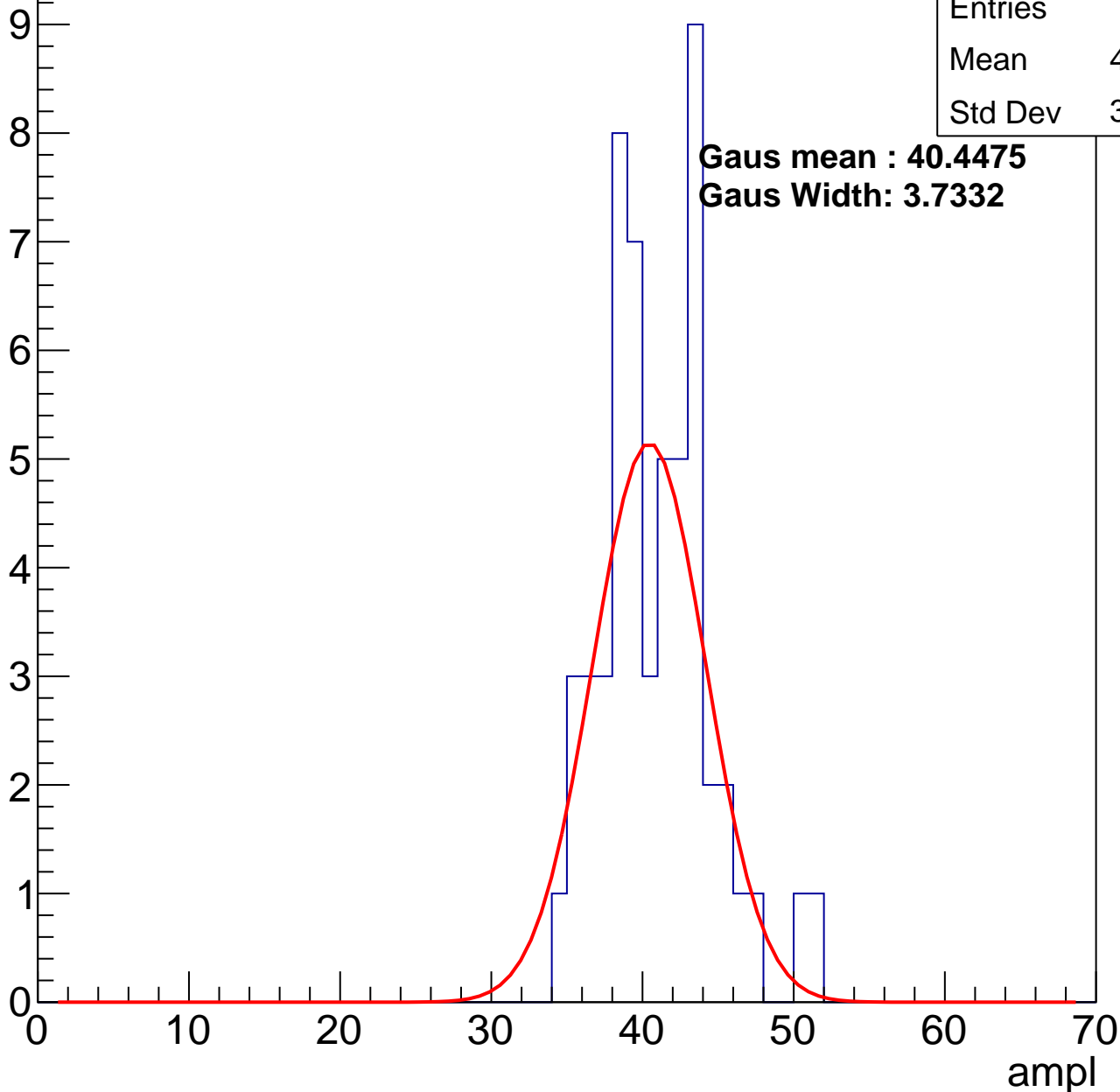
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	40.53
Std Dev	3.577

Gaus mean : 40.4475

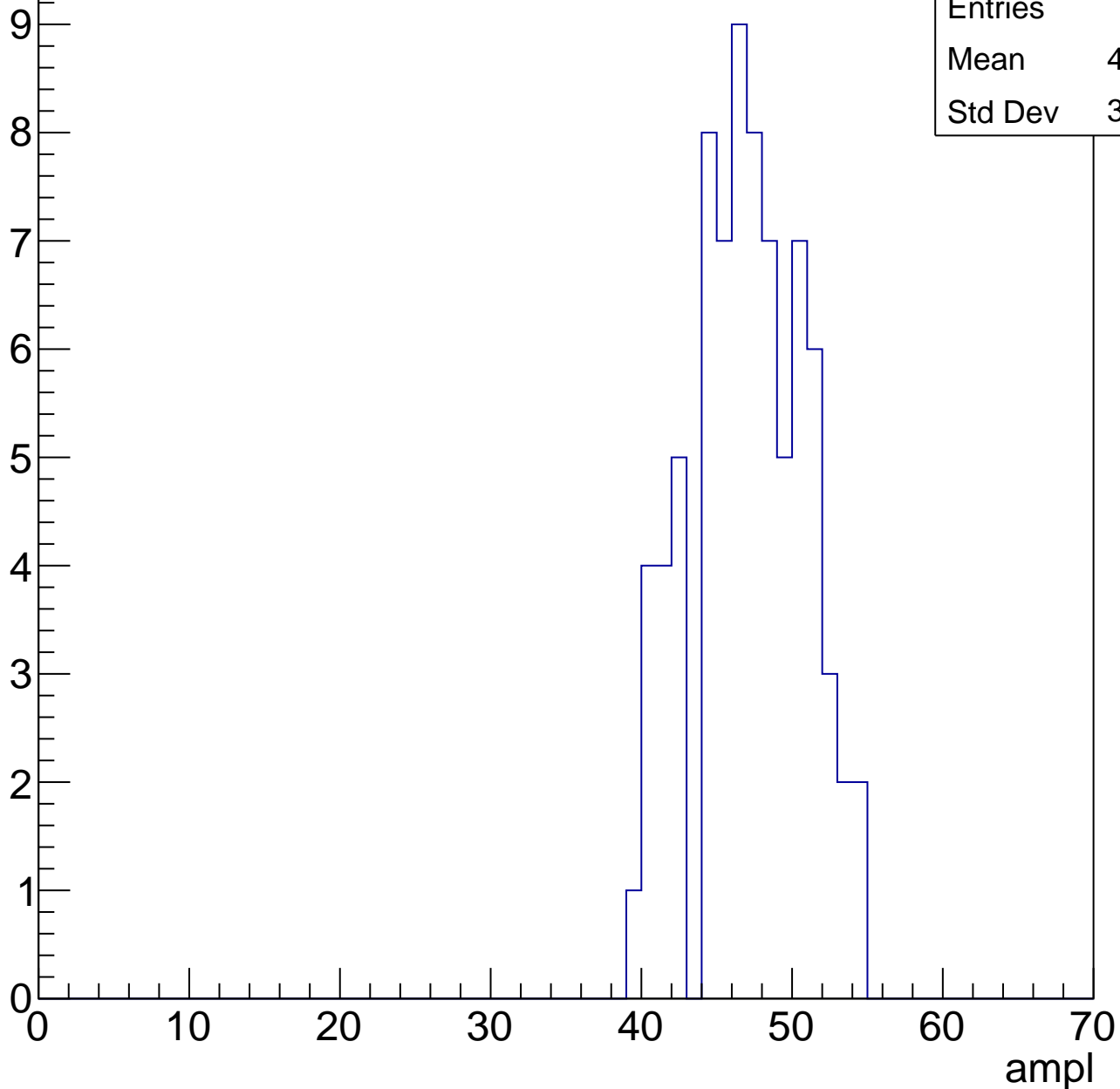
Gaus Width: 3.7332



B1L100S, U1-ch98, adc3

calib_packv5_042523_0143.root, FC#4, port A2

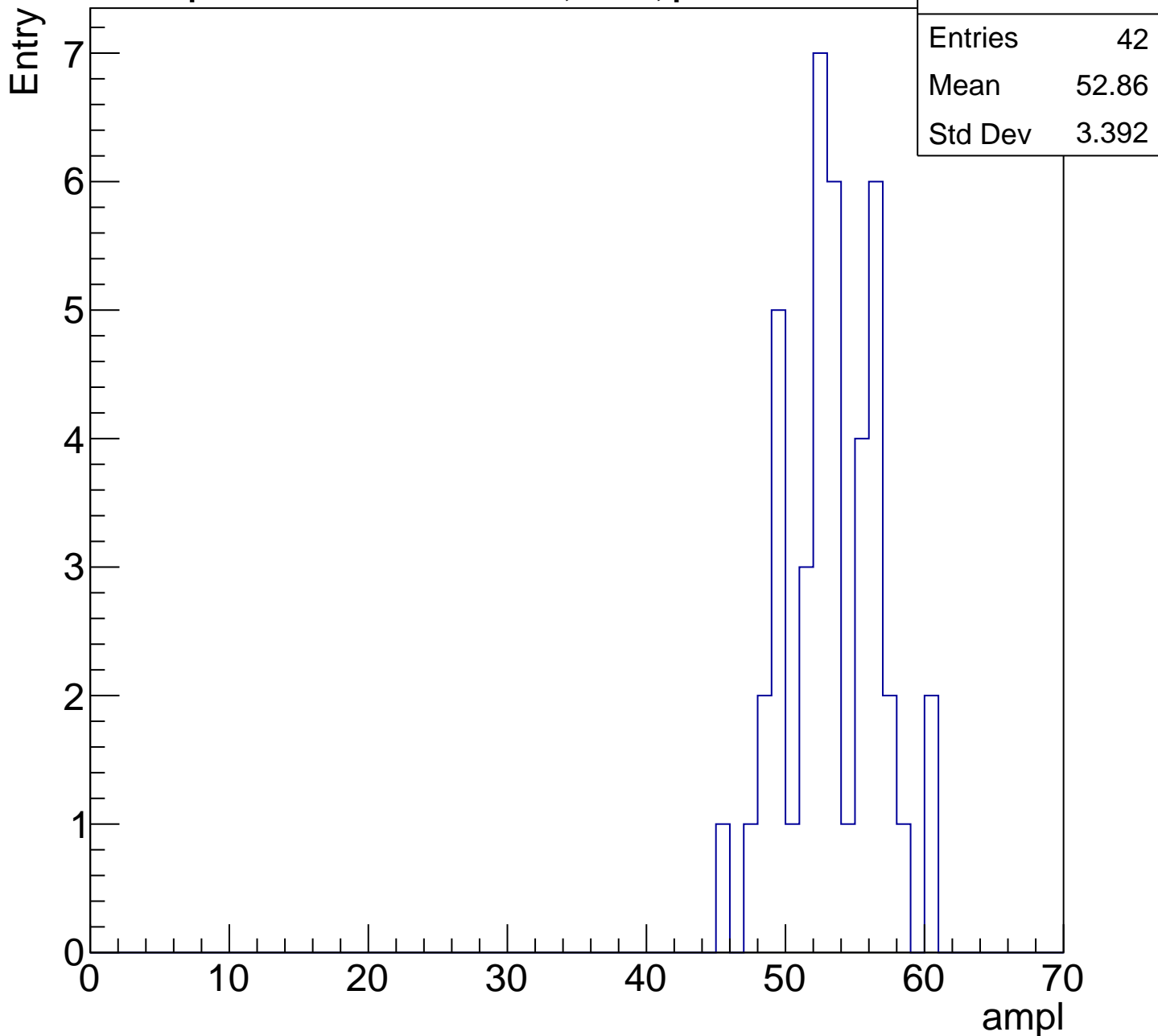
Entry



Entries	78
Mean	46.63
Std Dev	3.683

B1L100S, U1-ch98, adc4

calib_packv5_042523_0143.root, FC#4, port A2



B1L100S, U1-ch98, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

8
7
6
5
4
3
2
1
0

Entries	64
Mean	56.81
Std Dev	7.816

ampl

0

10

20

30

40

50

60

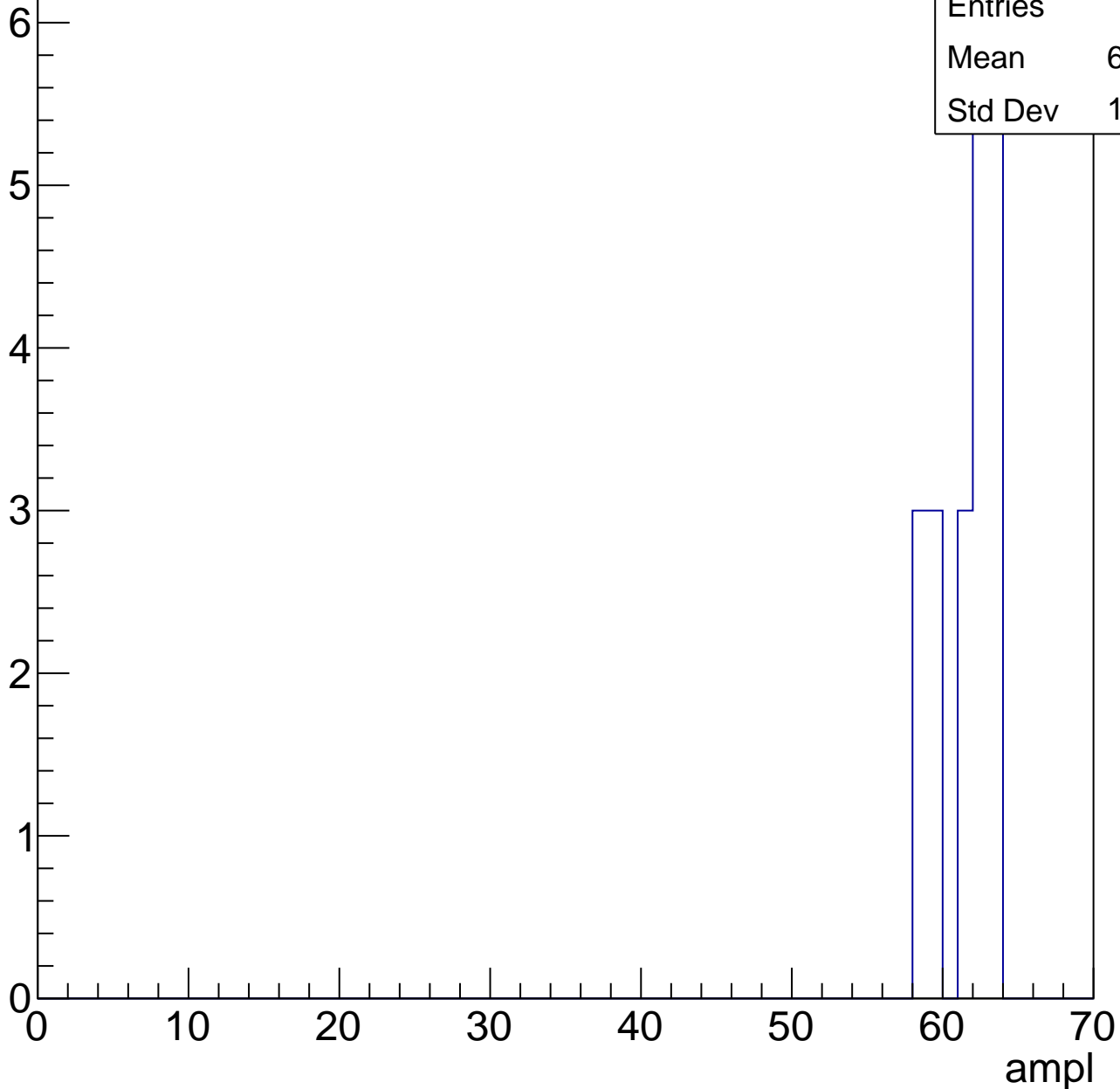
70

B1L100S, U1-ch98, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

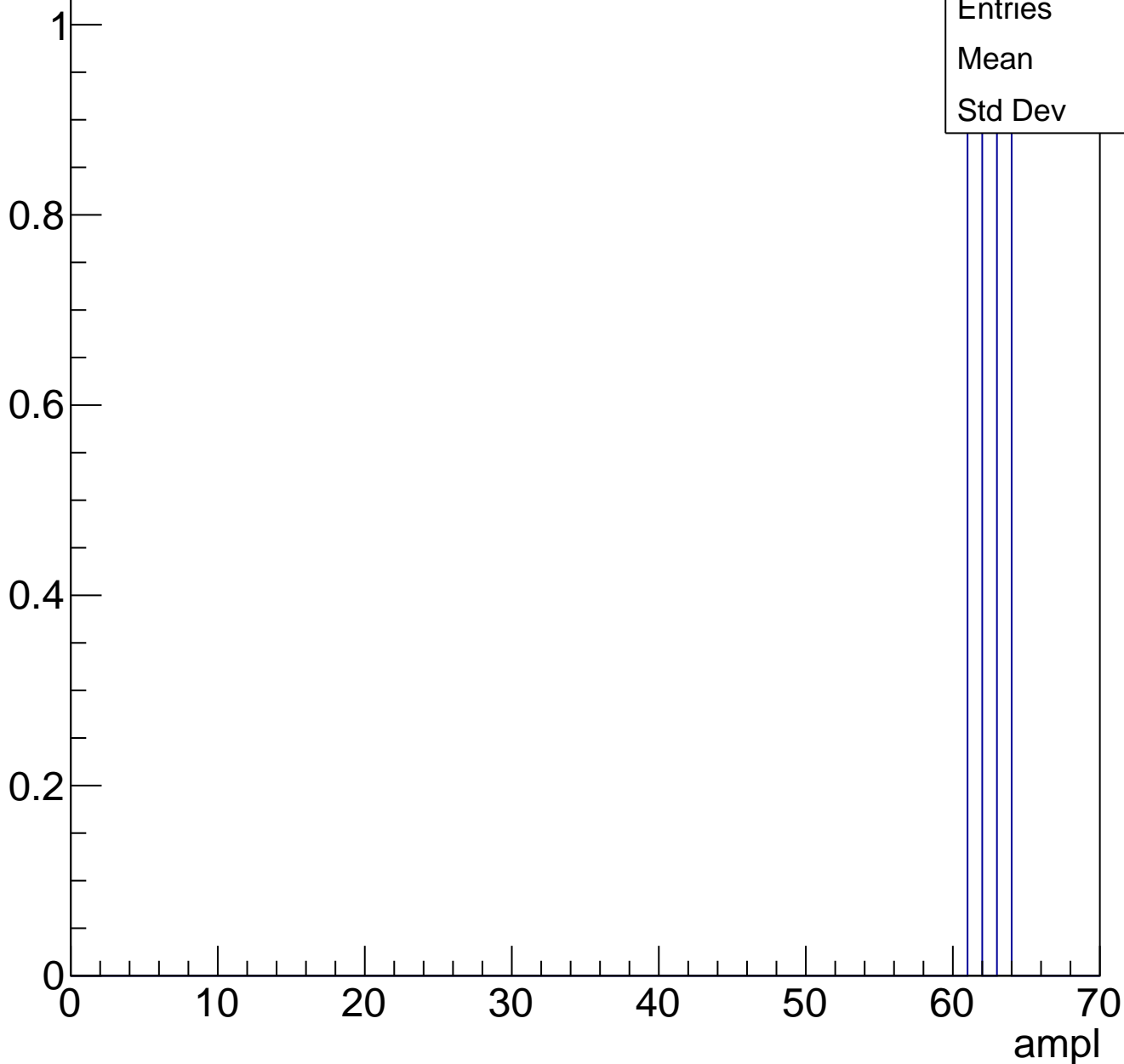
Entries	21
Mean	61.14
Std Dev	1.807



B1L100S, U1-ch98, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch99, adc0

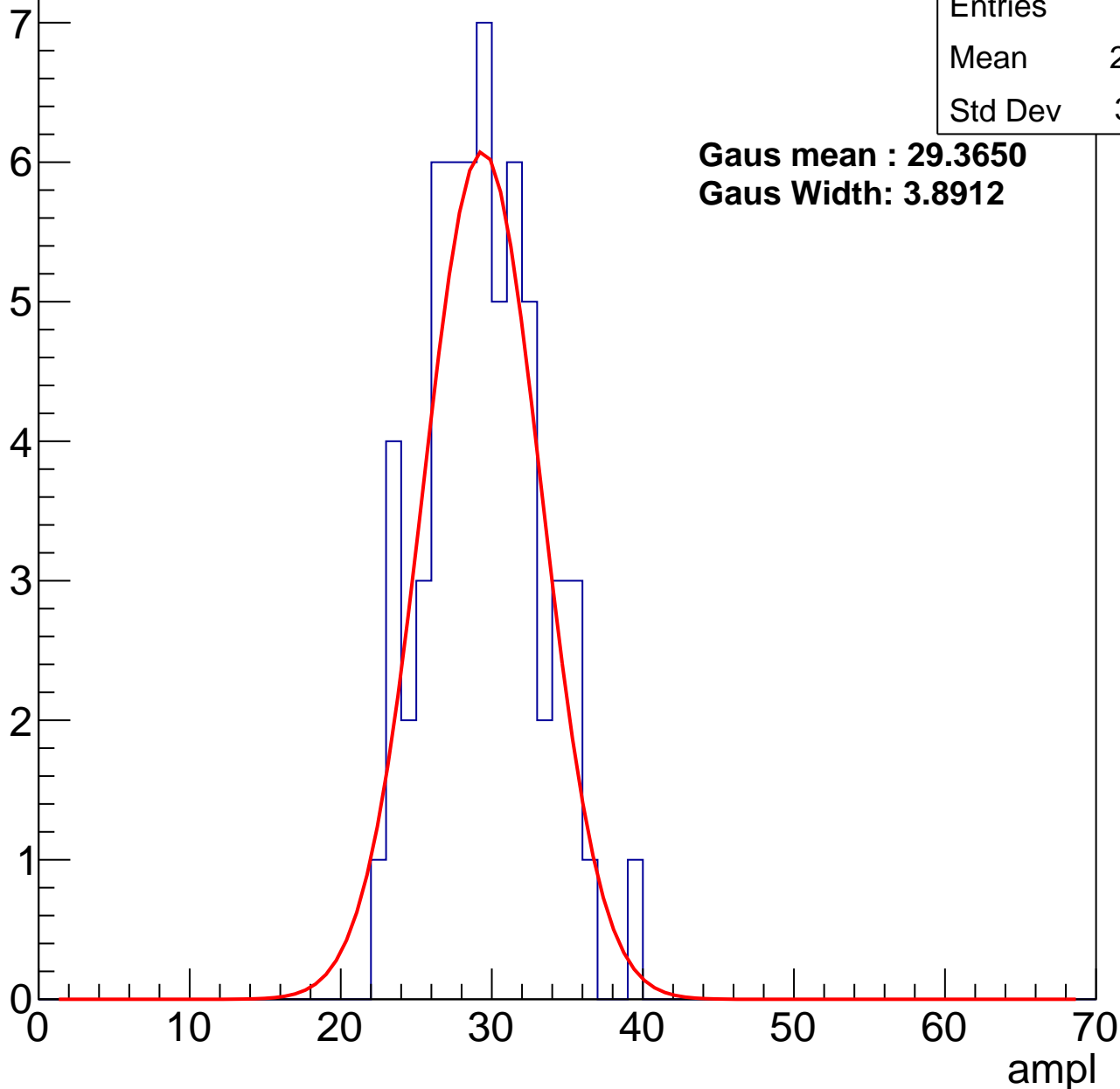
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	29.02
Std Dev	3.651

Gaus mean : 29.3650

Gaus Width: 3.8912



B1L100S, U1-ch99, adc1

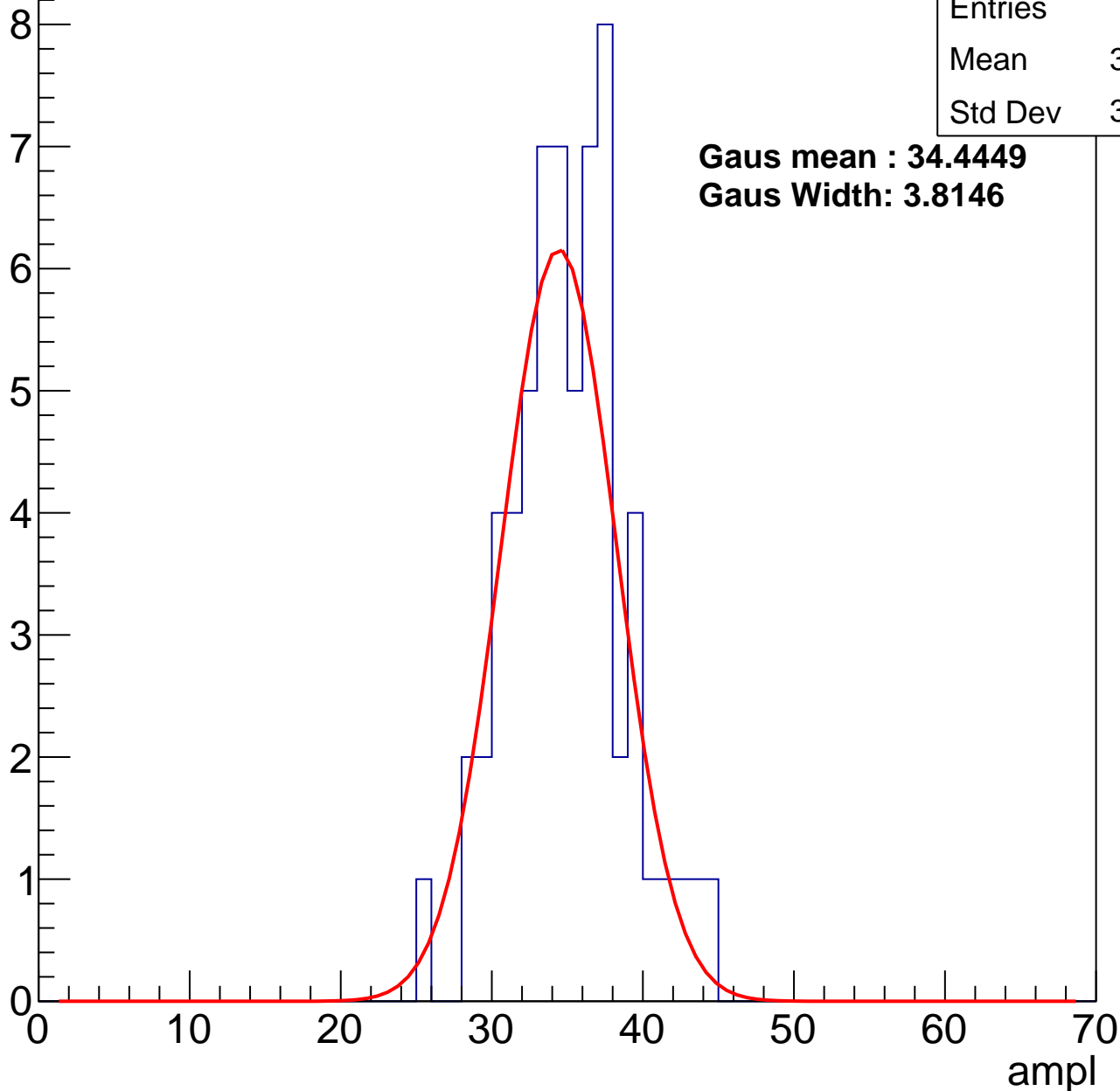
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	34.56
Std Dev	3.732

Gaus mean : 34.4449

Gaus Width: 3.8146



B1L100S, U1-ch99, adc2

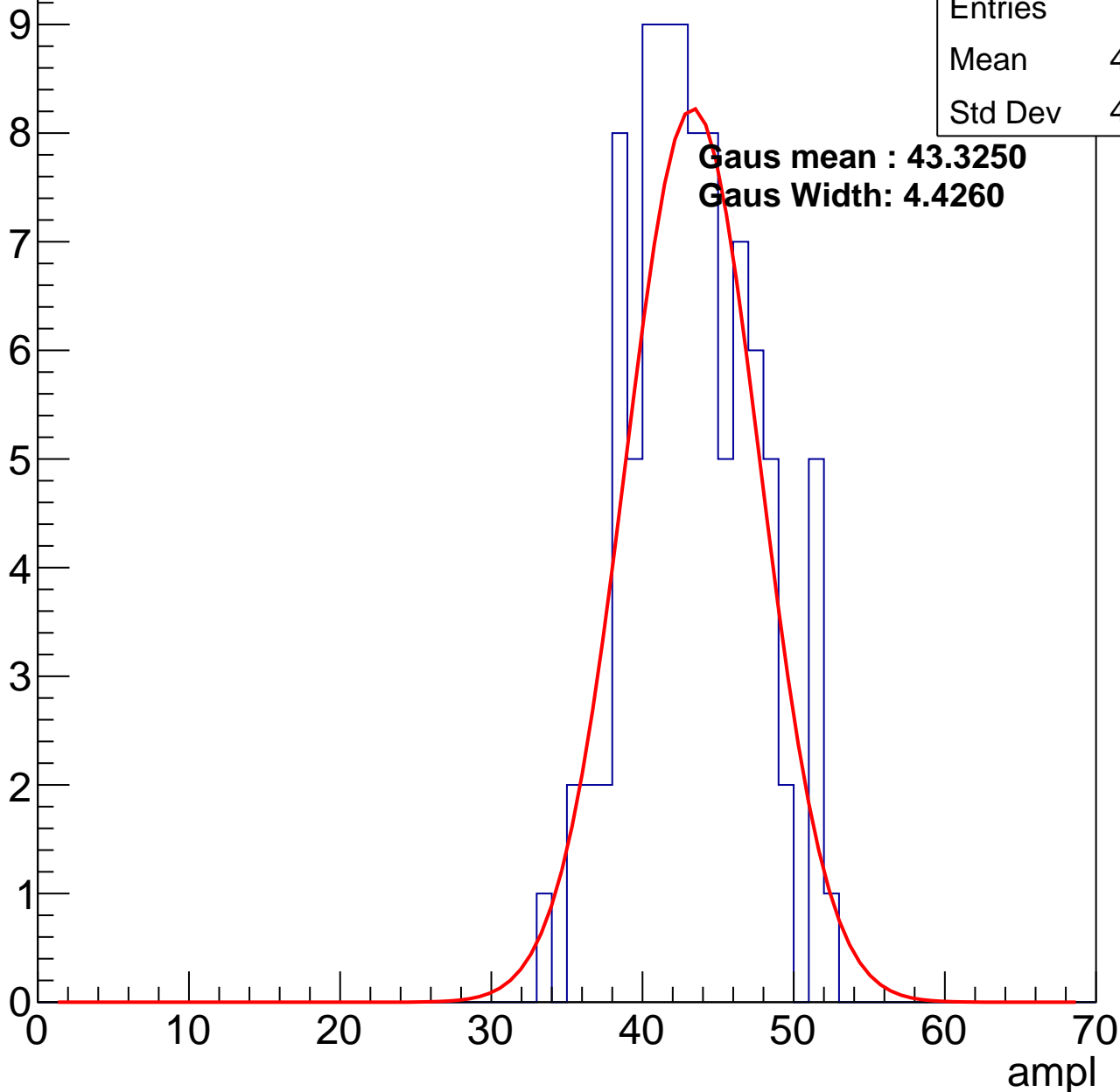
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	94
Mean	42.82
Std Dev	4.107

Gaus mean : 43.3250

Gaus Width: 4.4260

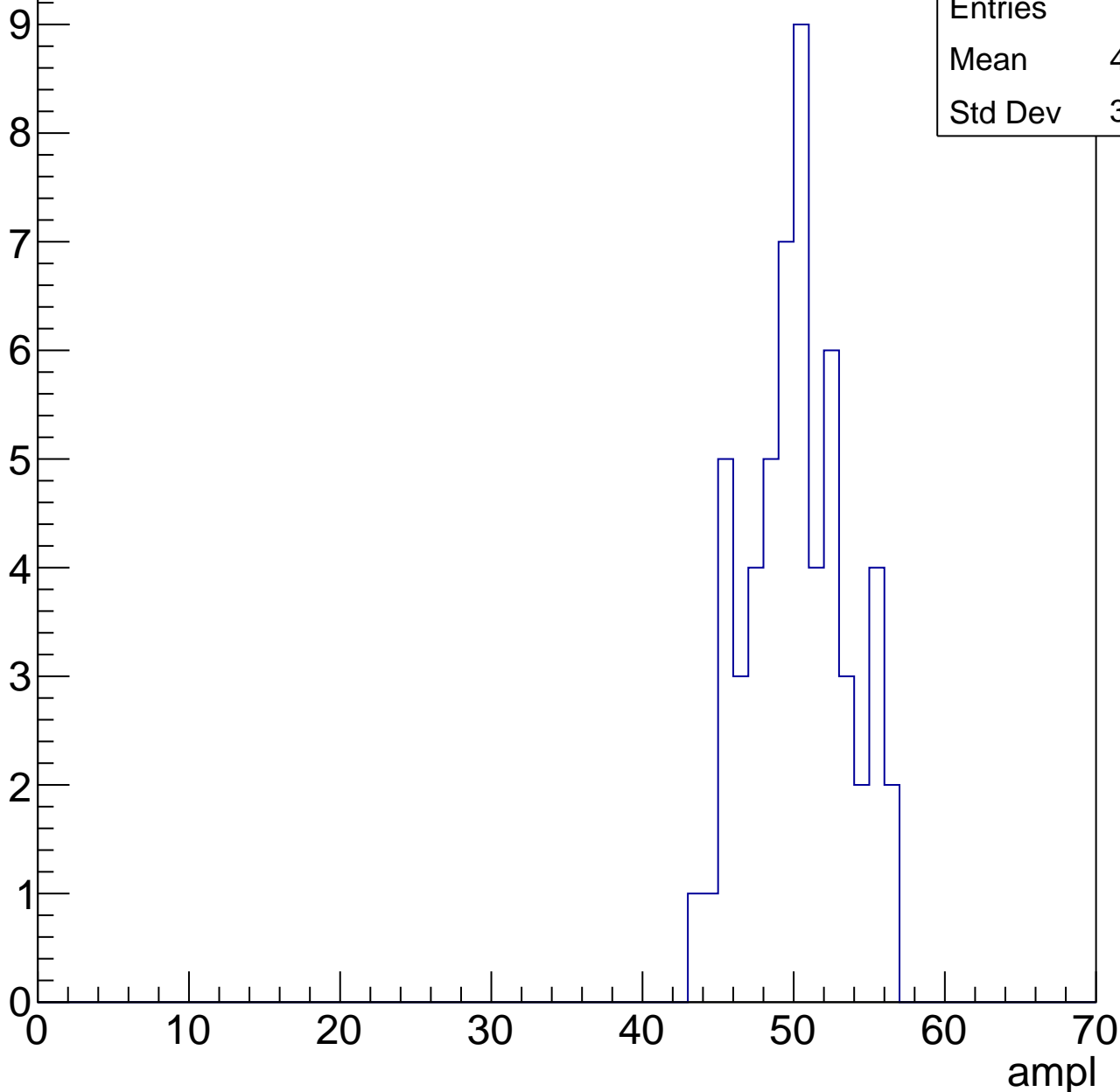


B1L100S, U1-ch99, adc3

calib_packv5_042523_0143.root, FC#4, port A2

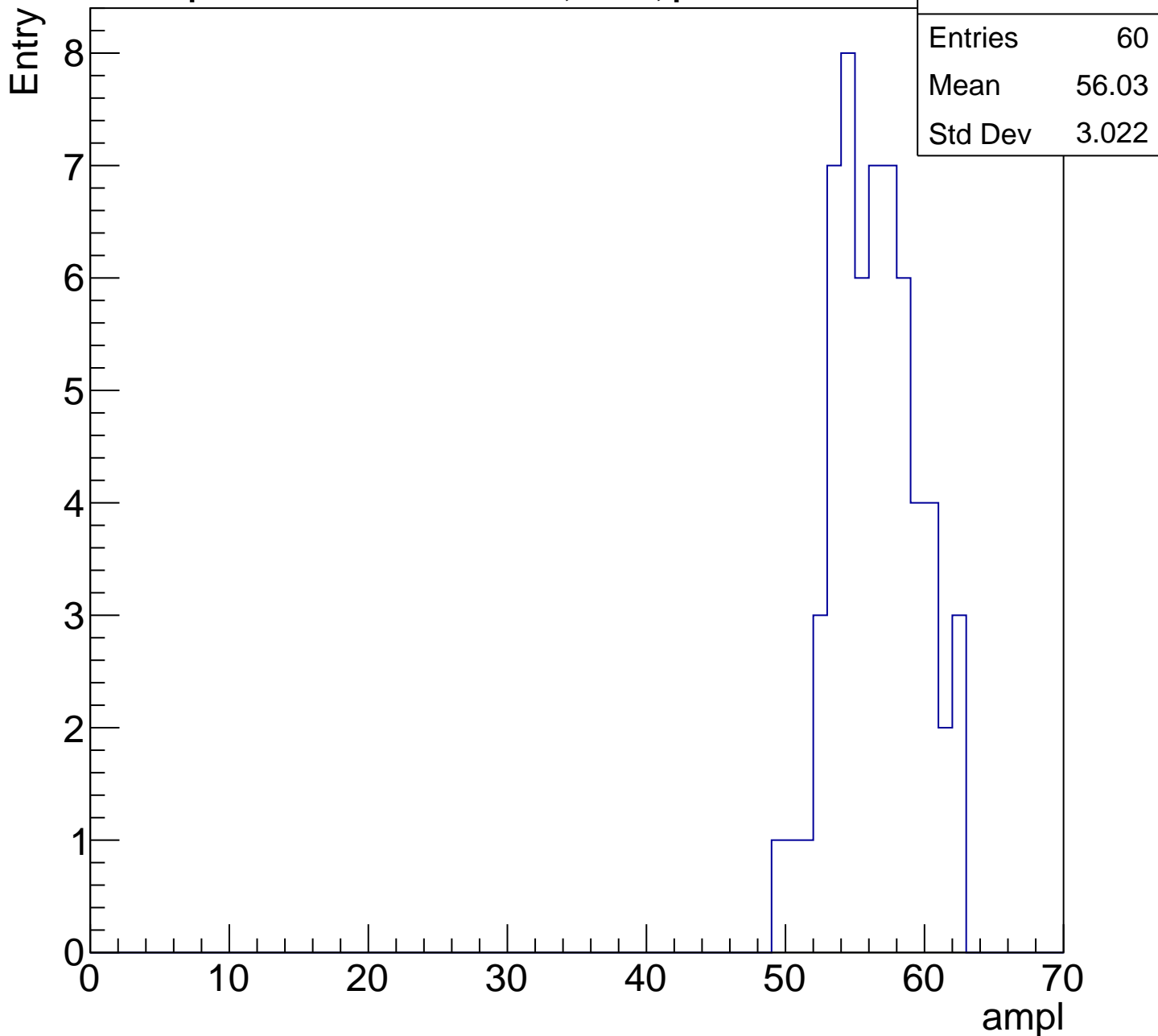
Entry

Entries	56
Mean	49.75
Std Dev	3.209



B1L100S, U1-ch99, adc4

calib_packv5_042523_0143.root, FC#4, port A2

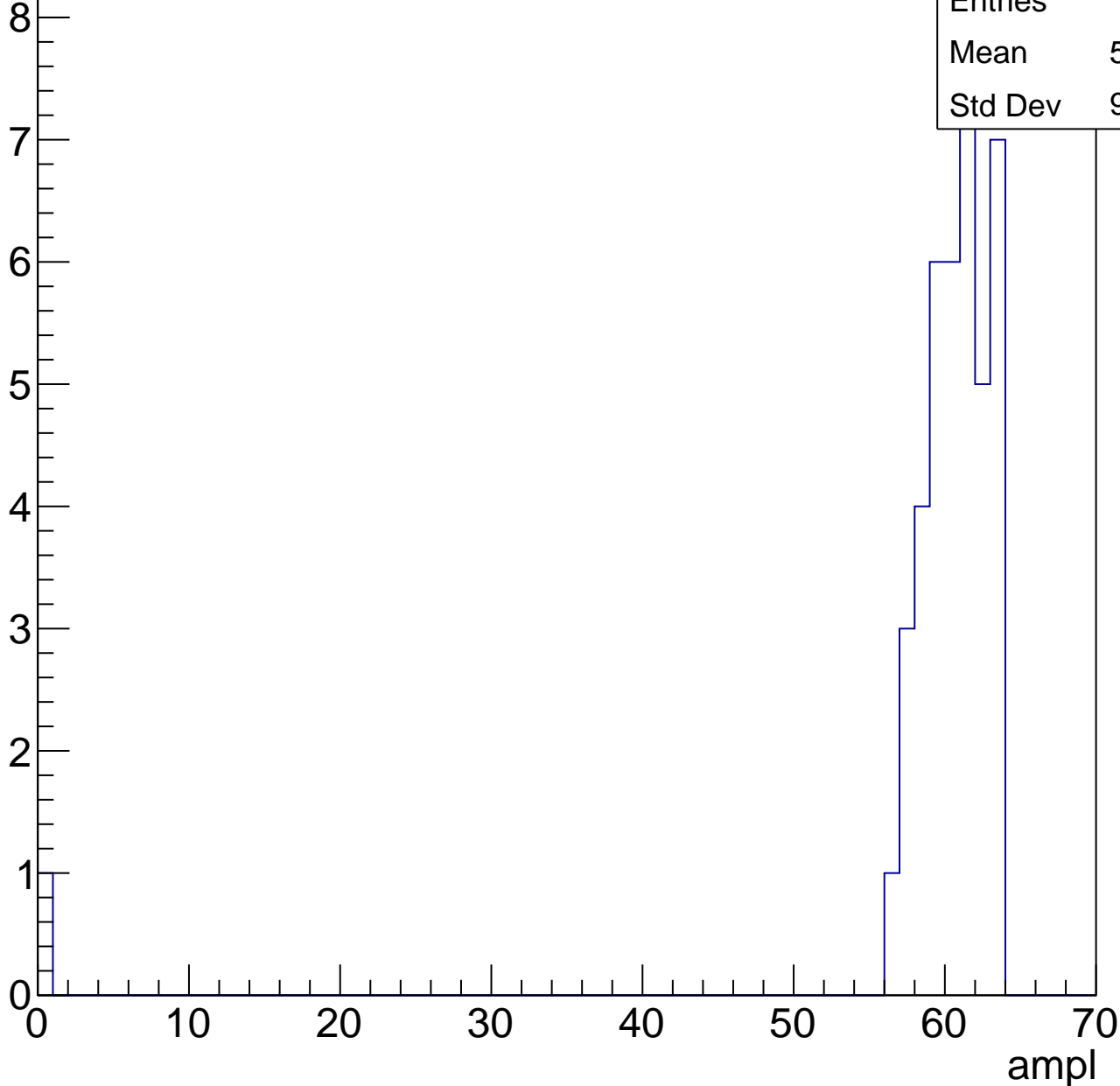


B1L100S, U1-ch99, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	41
Mean	58.83
Std Dev	9.499



B1L100S, U1-ch99, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch99, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch100, adc0

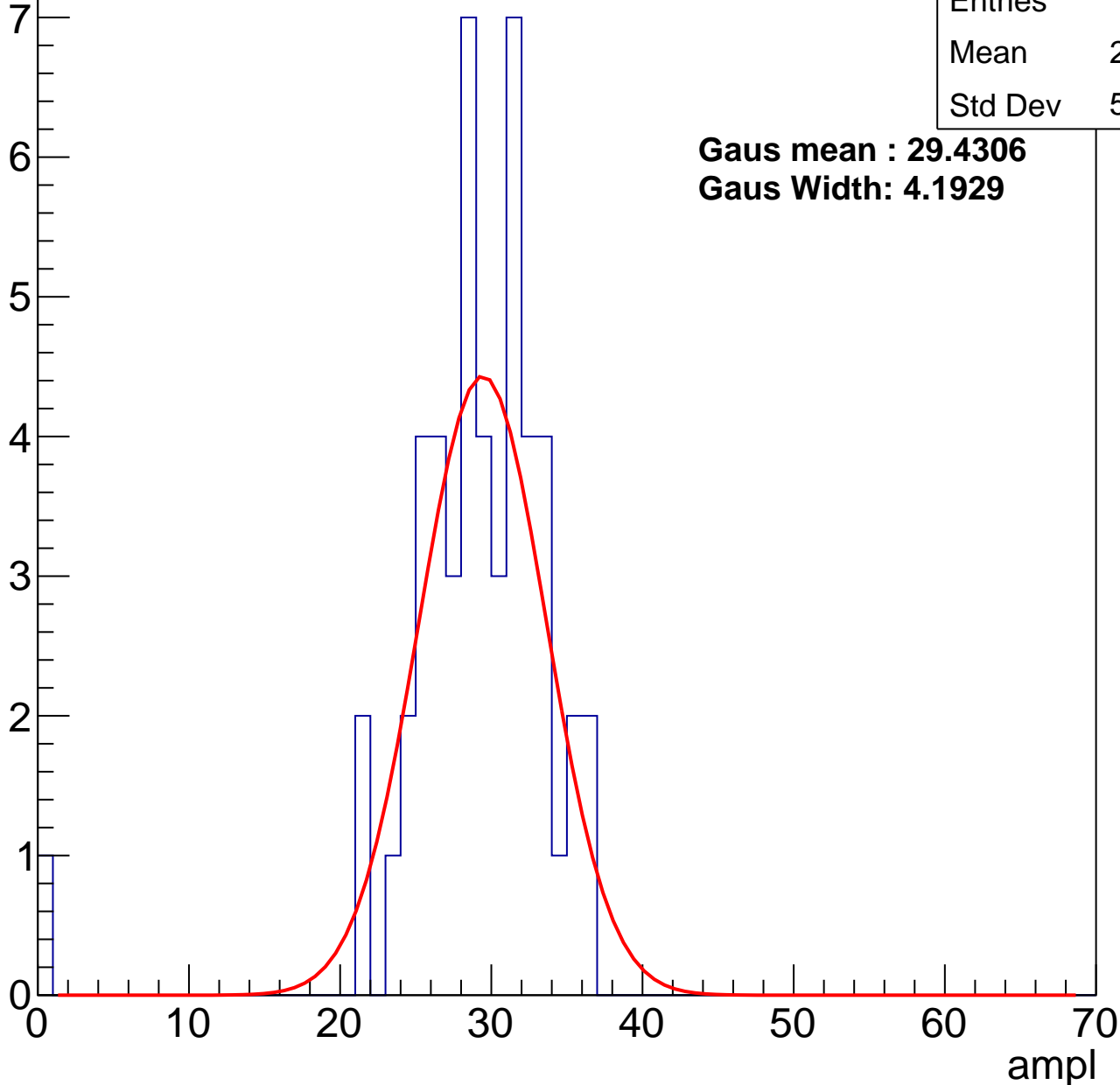
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	28.49
Std Dev	5.399

Gaus mean : 29.4306

Gaus Width: 4.1929



B1L100S, U1-ch100, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	83
Mean	35.36
Std Dev	3.619

Gaus mean : 35.5621

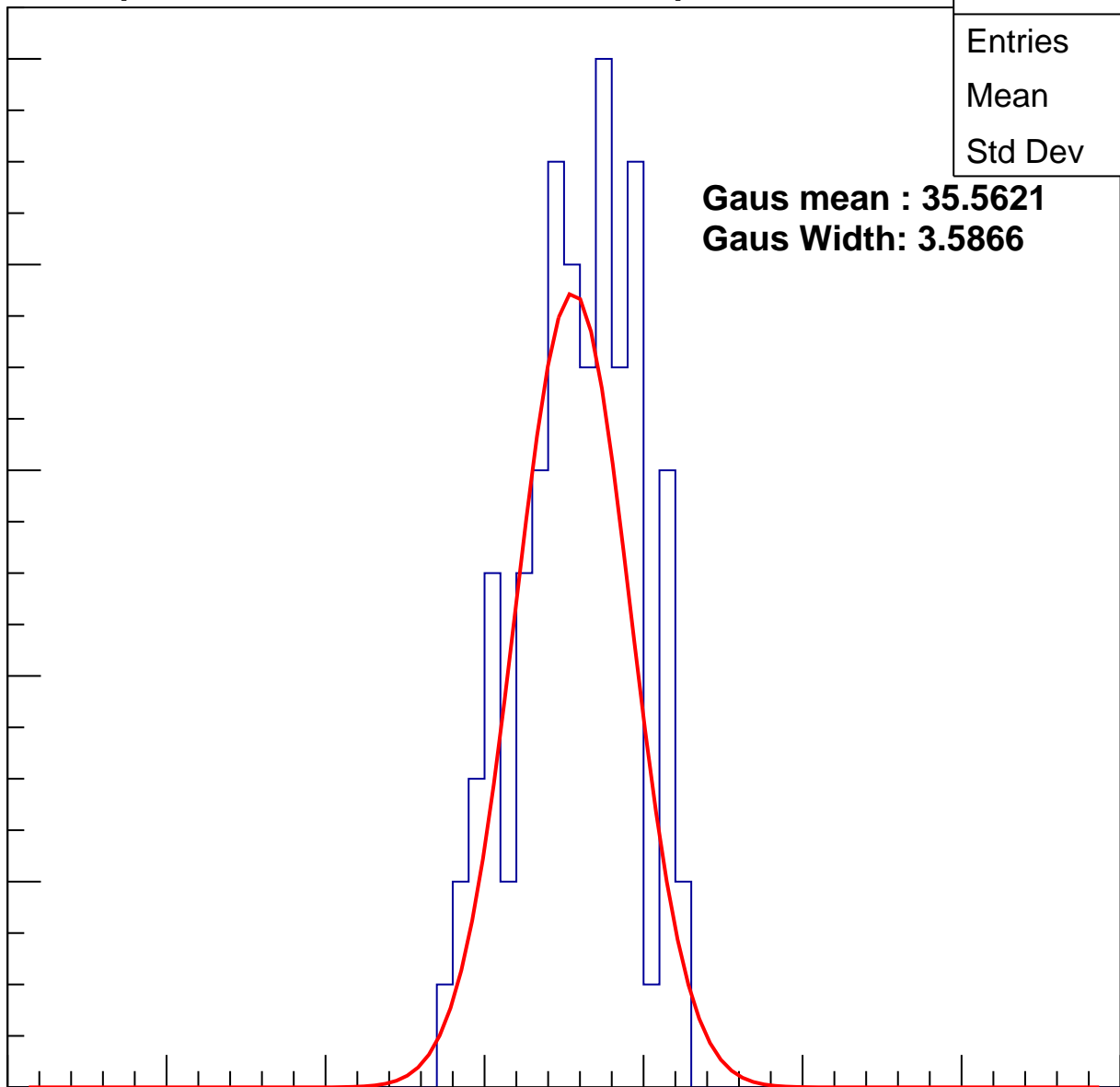
Gaus Width: 3.5866

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch100, adc2

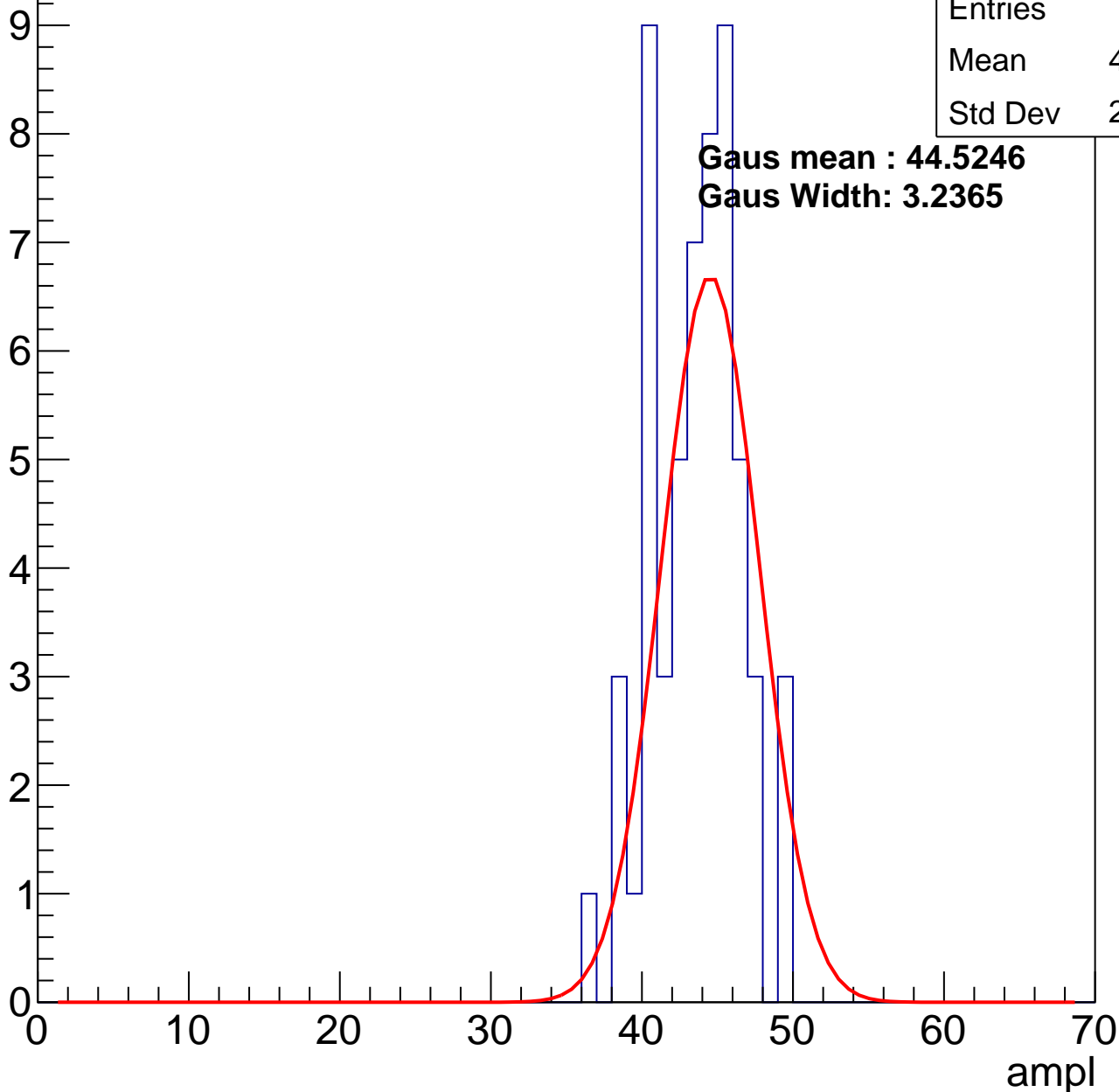
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	43.12
Std Dev	2.908

Gaus mean : 44.5246

Gaus Width: 3.2365

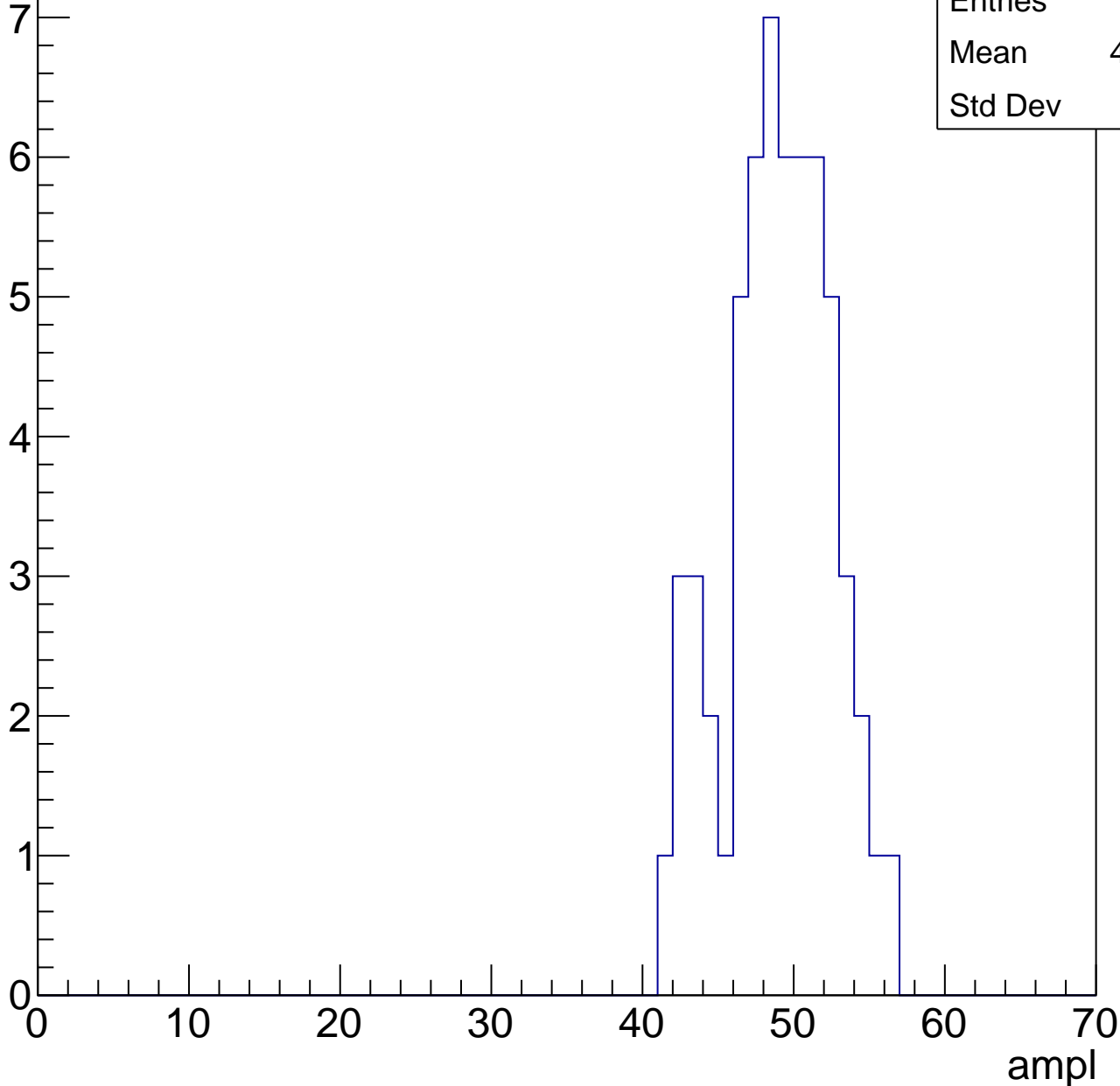


B1L100S, U1-ch100, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.53
Std Dev	3.49

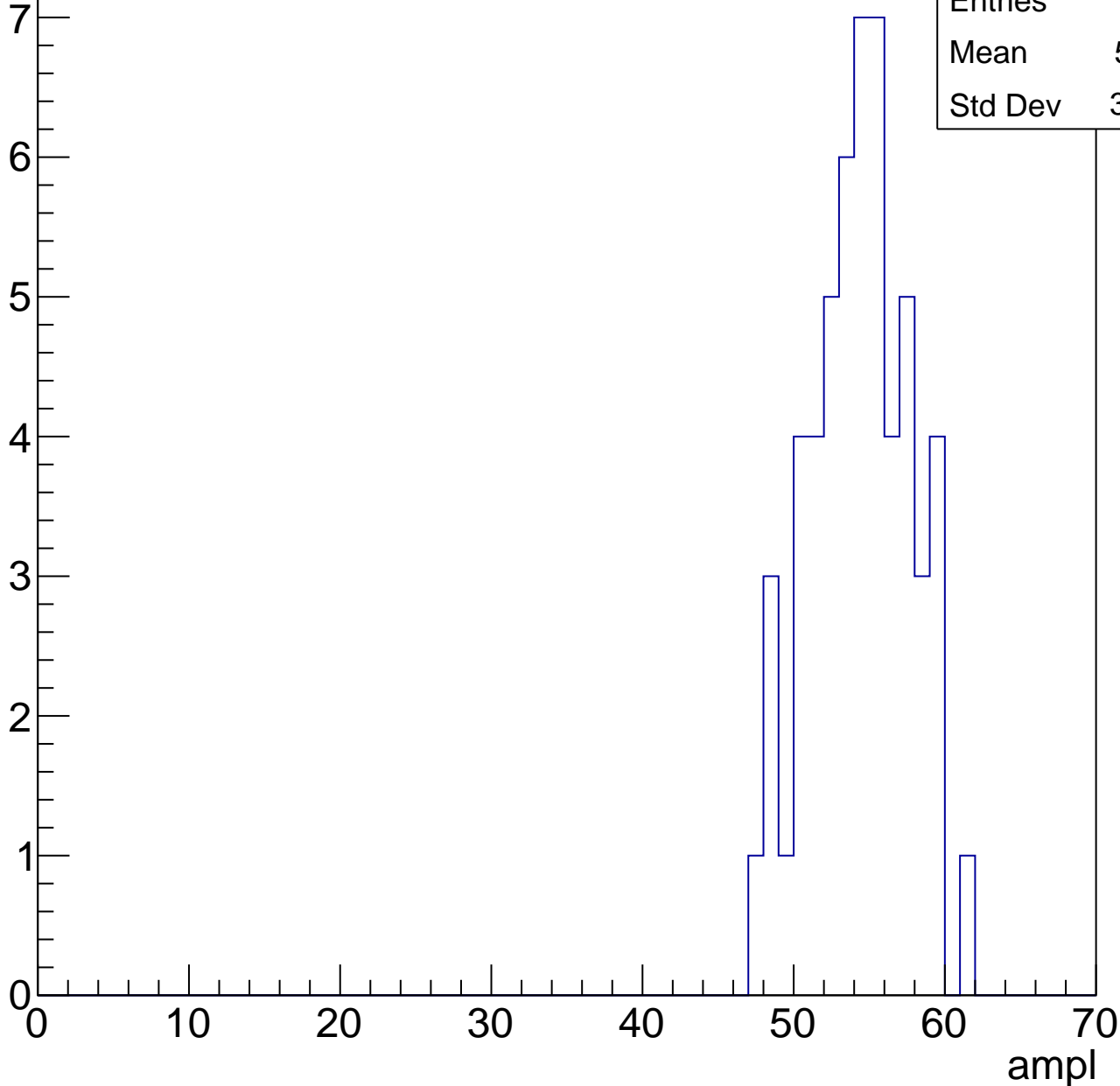


B1L100S, U1-ch100, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

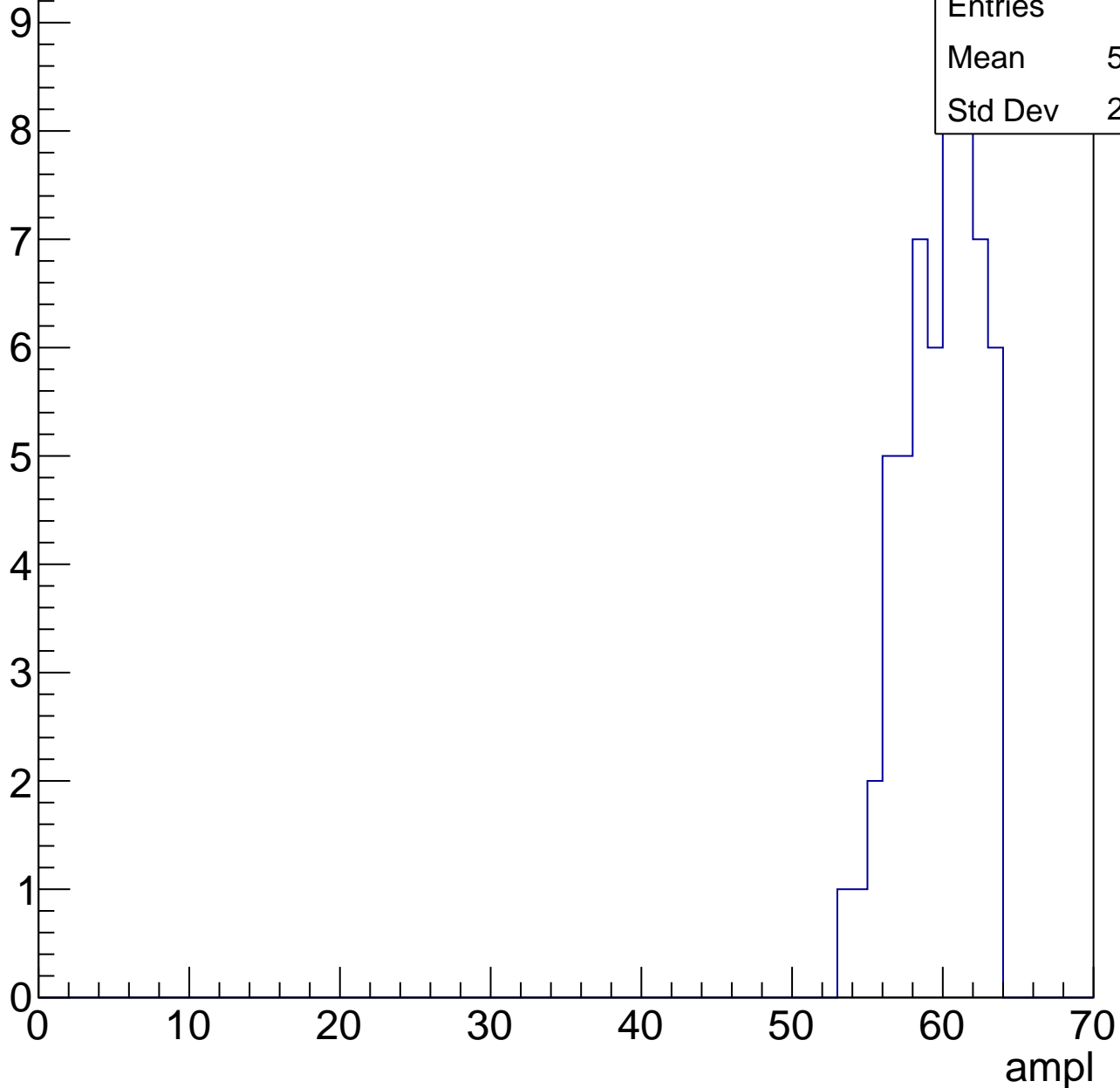
Entries	55
Mean	53.91
Std Dev	3.226



B1L100S, U1-ch100, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch100, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch100, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	21
Std Dev	29.7

B1L100S, U1-ch101, adc0

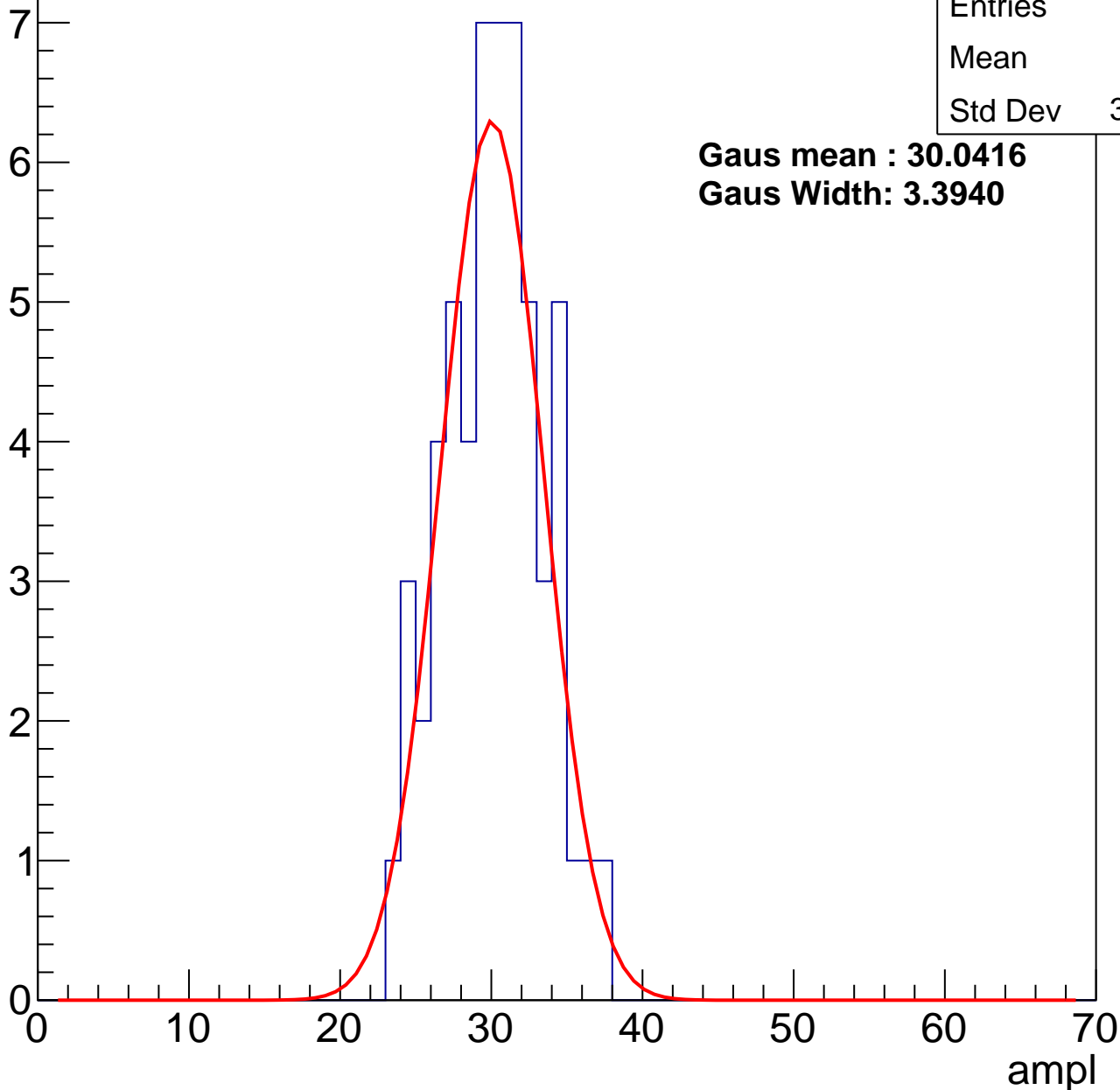
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.7
Std Dev	3.212

Gaus mean : 30.0416

Gaus Width: 3.3940



B1L100S, U1-ch101, adc1

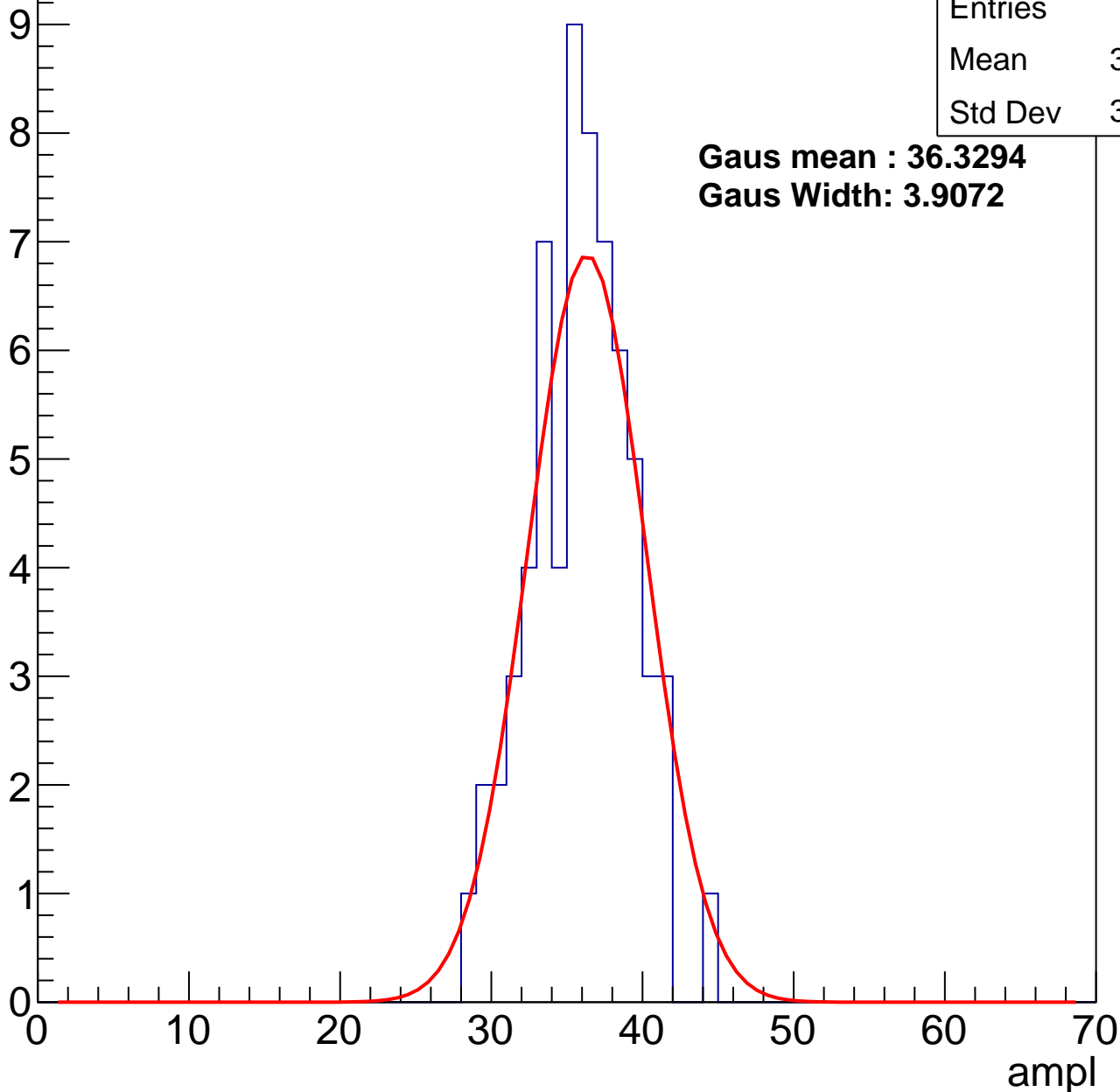
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	35.48
Std Dev	3.296

Gaus mean : 36.3294

Gaus Width: 3.9072



B1L100S, U1-ch101, adc2

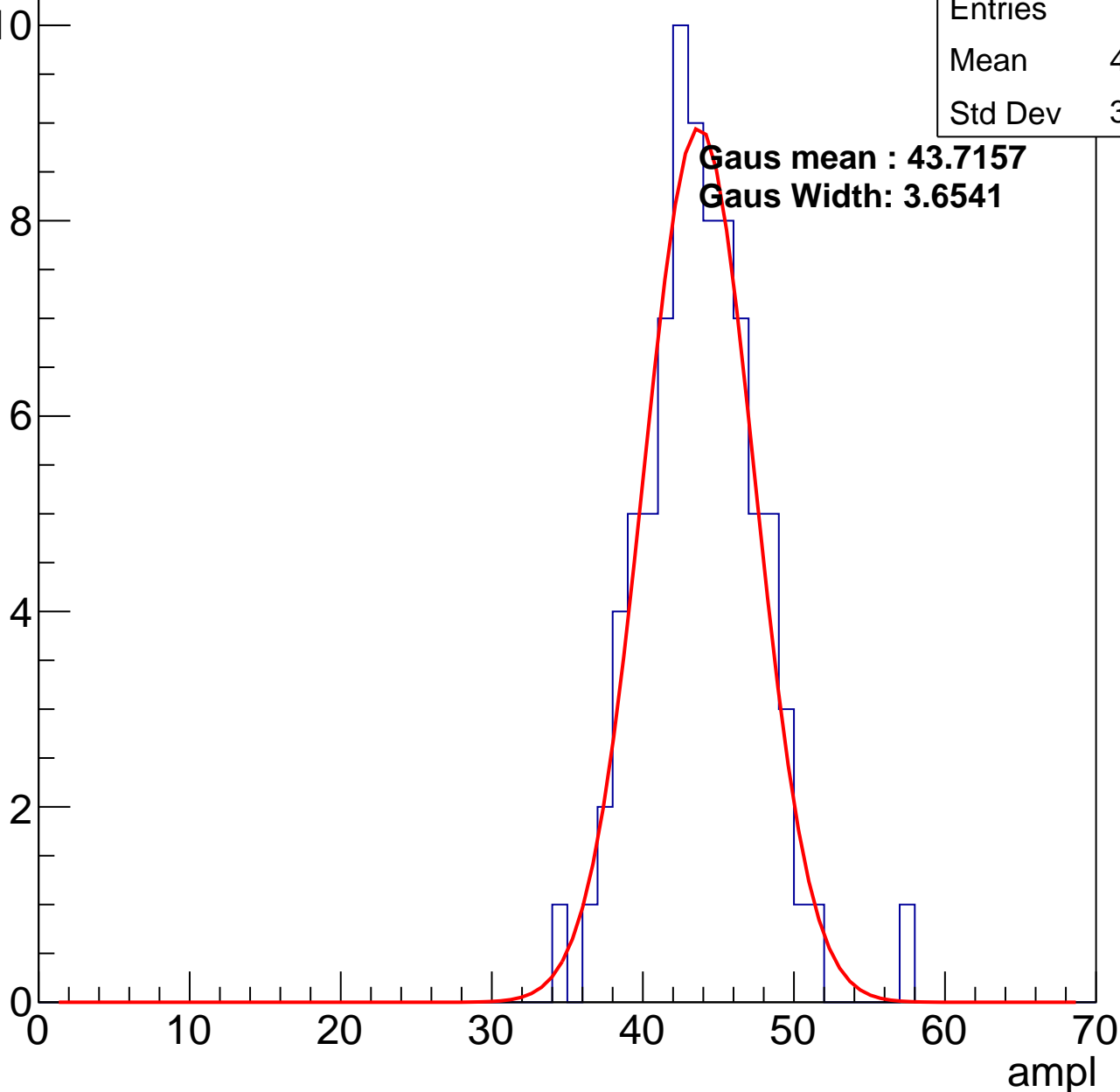
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	43.36
Std Dev	3.769

Gaus mean : 43.7157

Gaus Width: 3.6541

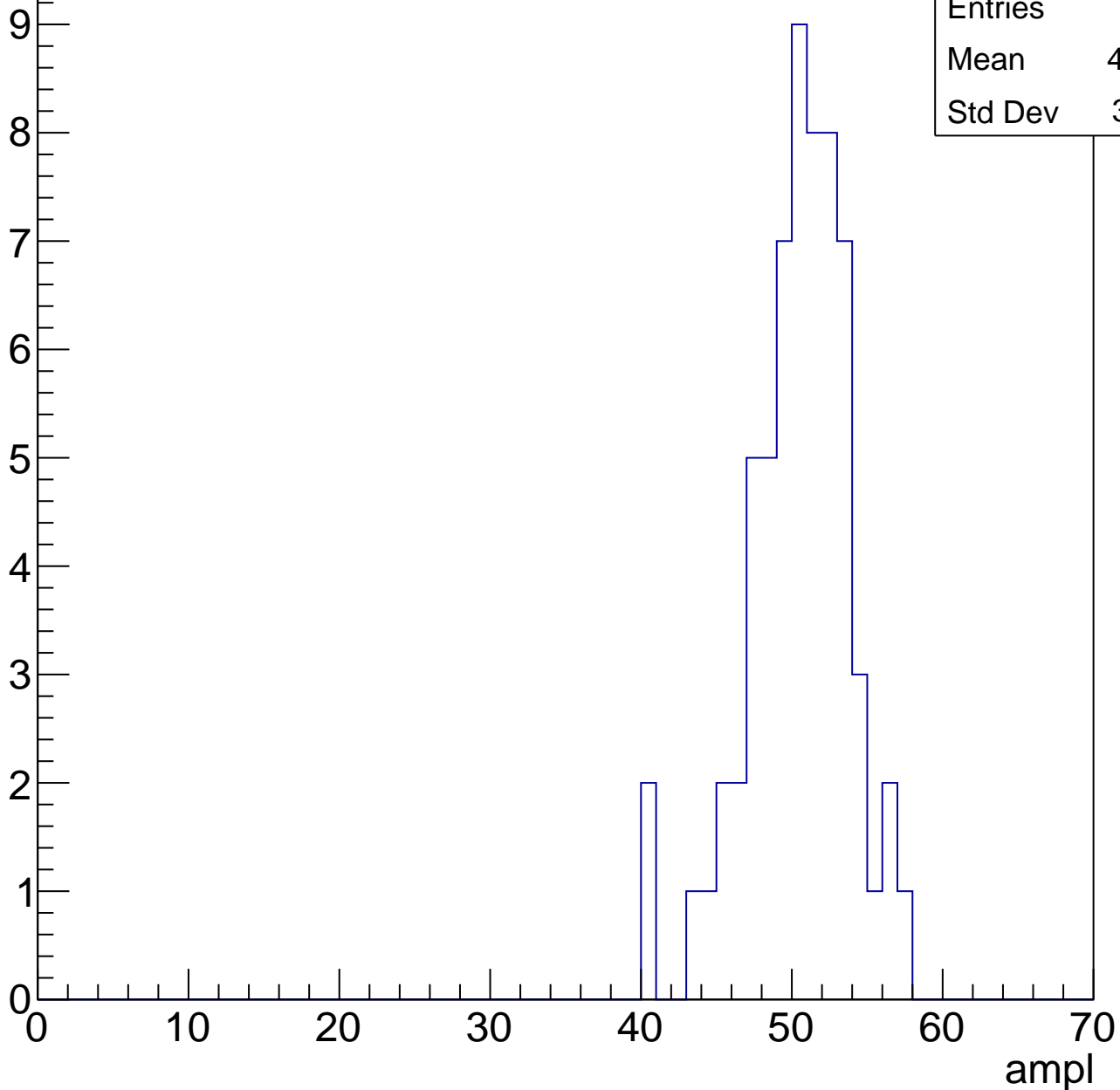


B1L100S, U1-ch101, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	49.97
Std Dev	3.391

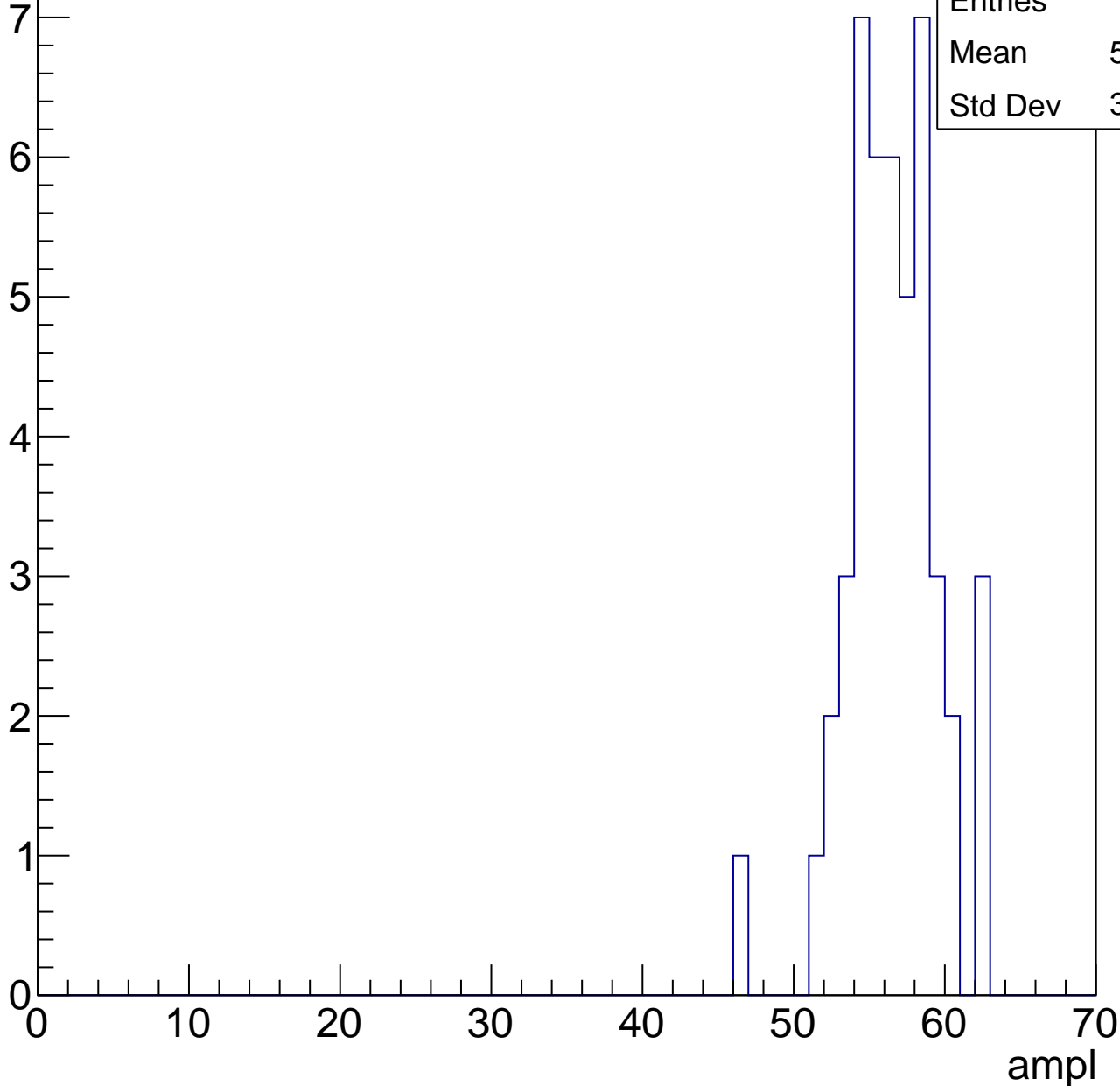


B1L100S, U1-ch101, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

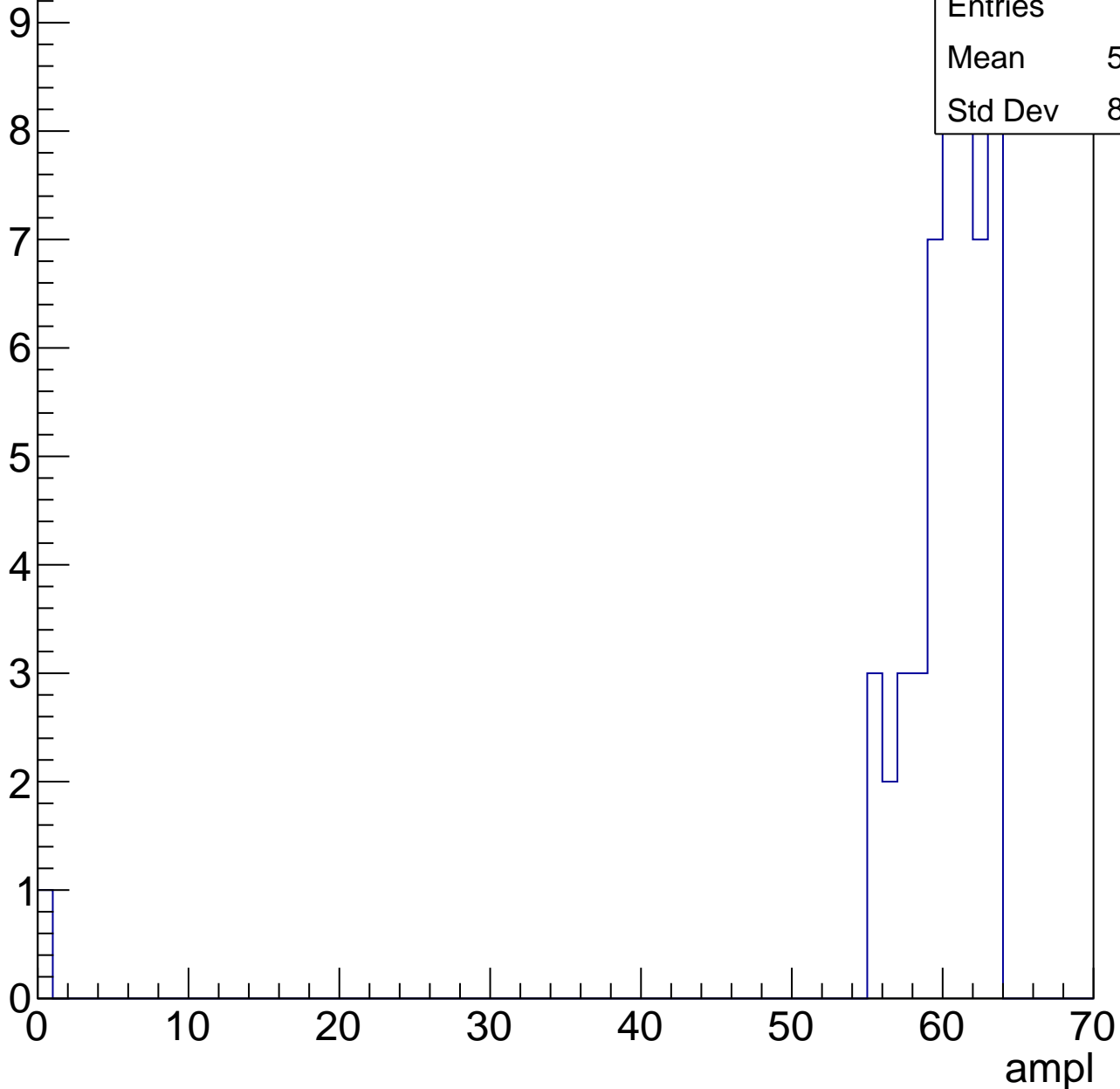
Entries	46
Mean	56.04
Std Dev	3.014



B1L100S, U1-ch101, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	52
Mean	58.94
Std Dev	8.563

B1L100S, U1-ch101, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch101, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch102, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	64
Mean	27.31
Std Dev	4.876

Gaus mean : 28.4765

Gaus Width: 3.9413

Entry

10
8
6
4
2
0

ampl

0

10

20

30

40

50

60

70

0

2

4

6

8

10

B1L100S, U1-ch102, adc1

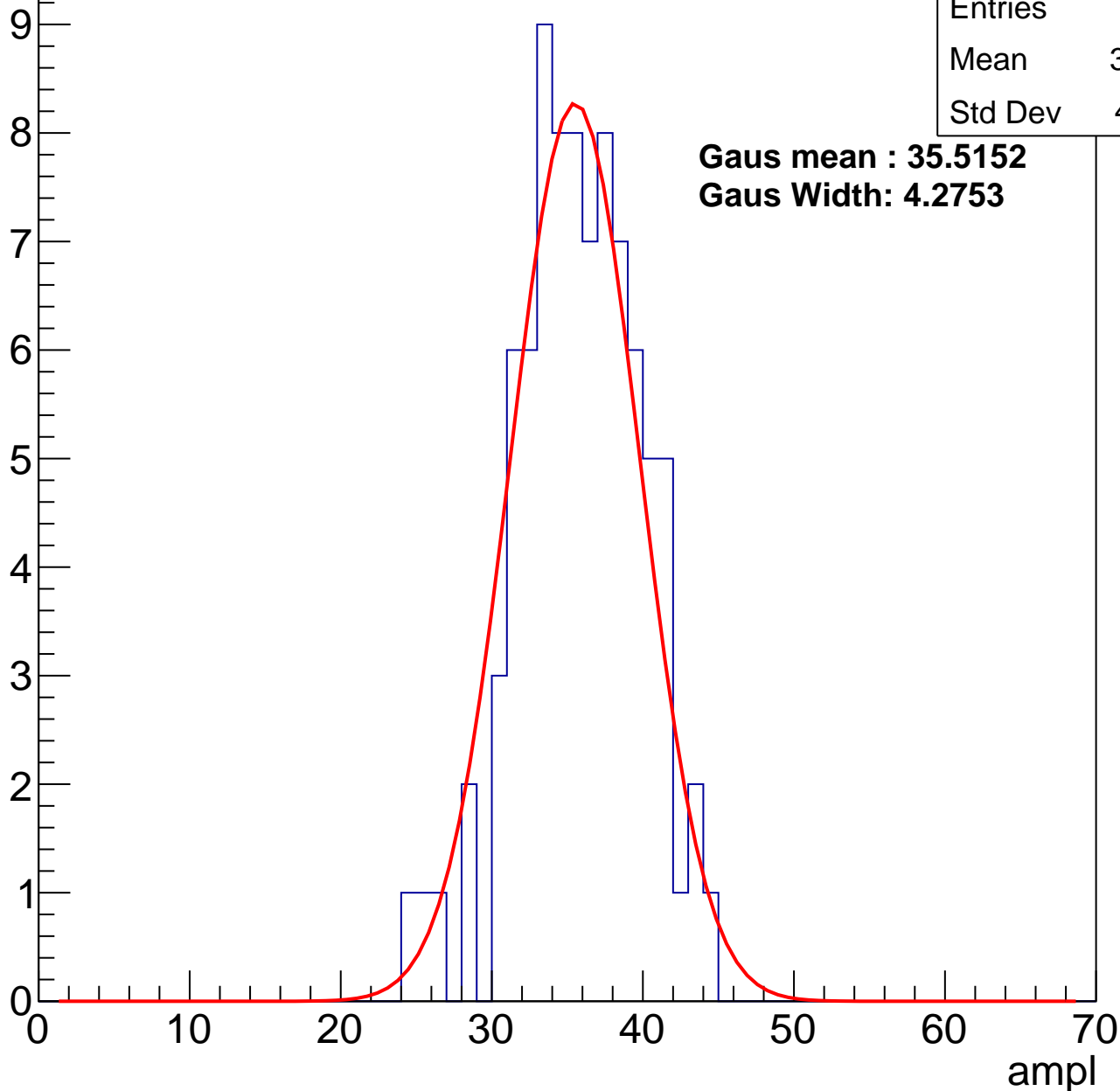
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	35.32
Std Dev	4.041

Gaus mean : 35.5152

Gaus Width: 4.2753



B1L100S, U1-ch102, adc2

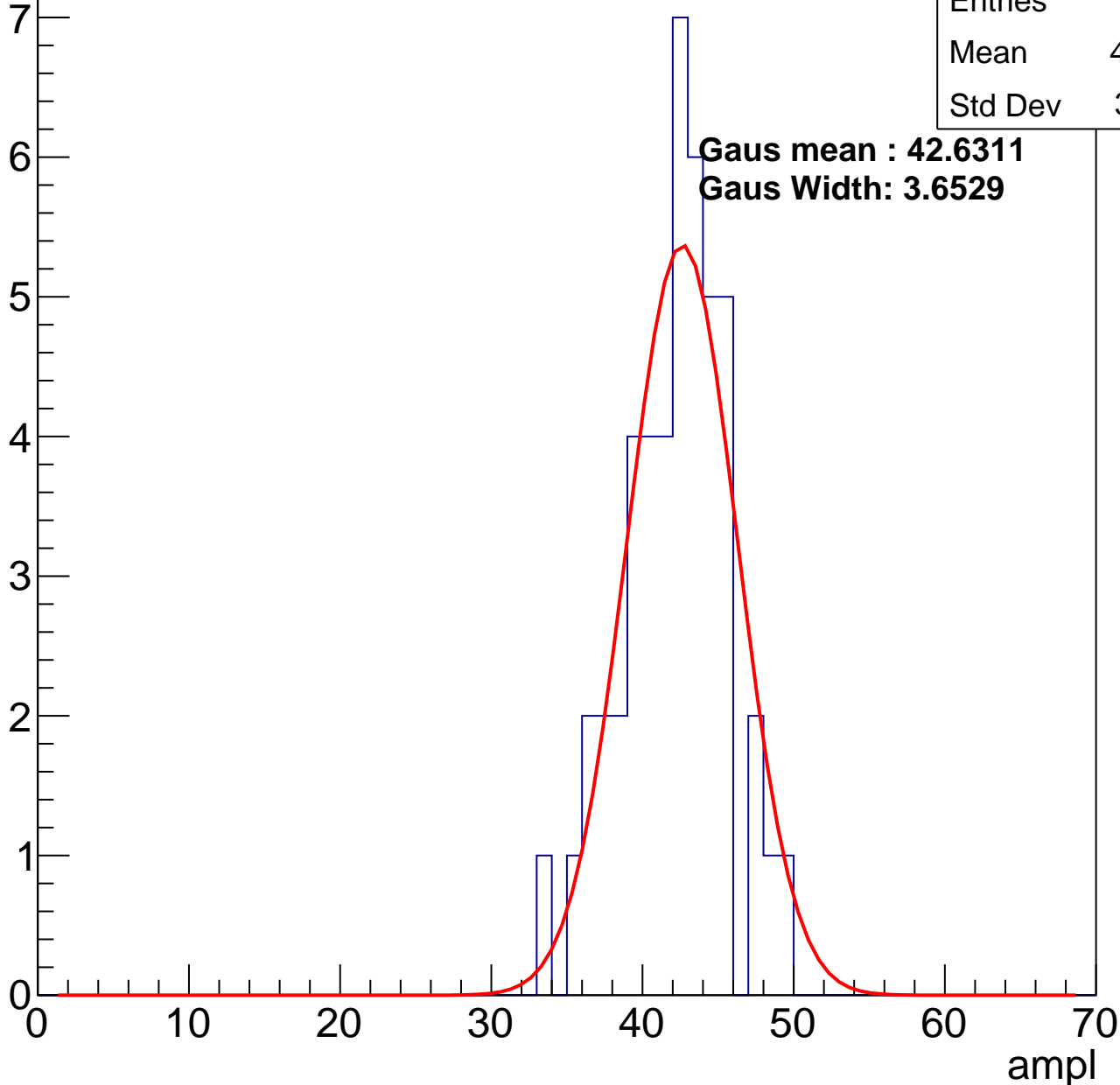
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	47
Mean	41.66
Std Dev	3.391

Gaus mean : 42.6311

Gaus Width: 3.6529

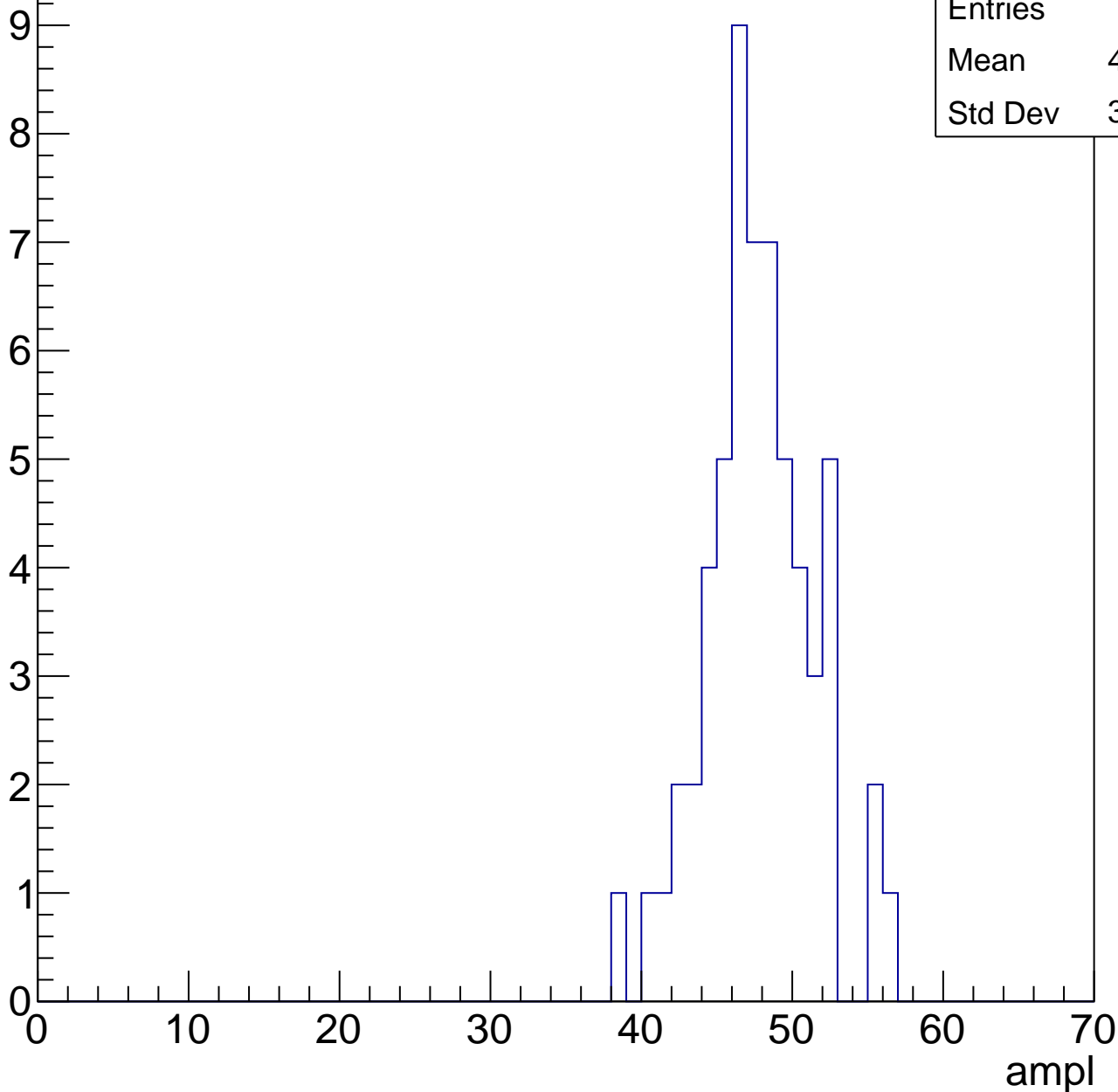


B1L100S, U1-ch102, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	47.34
Std Dev	3.568

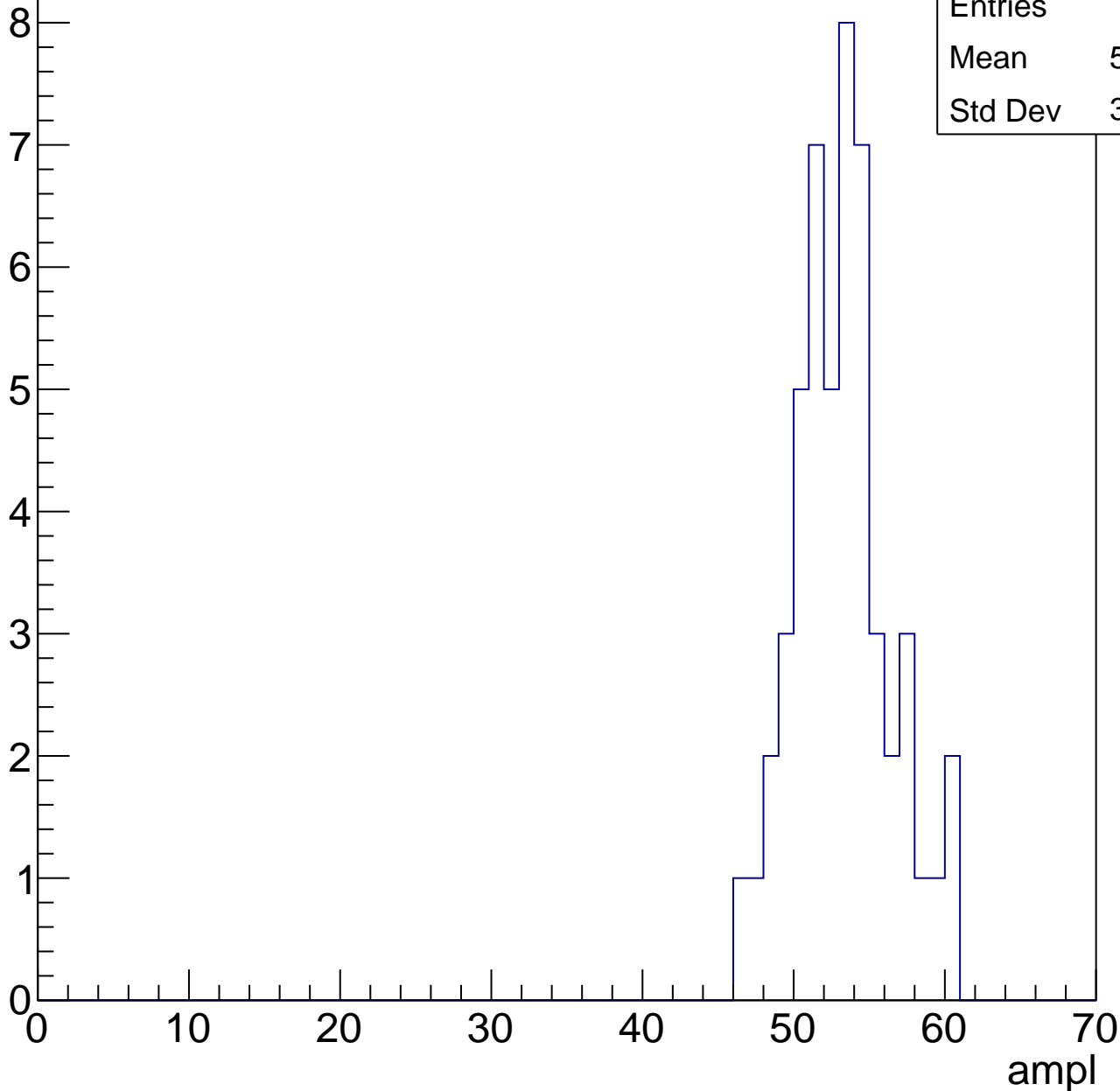


B1L100S, U1-ch102, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	52.75
Std Dev	3.136

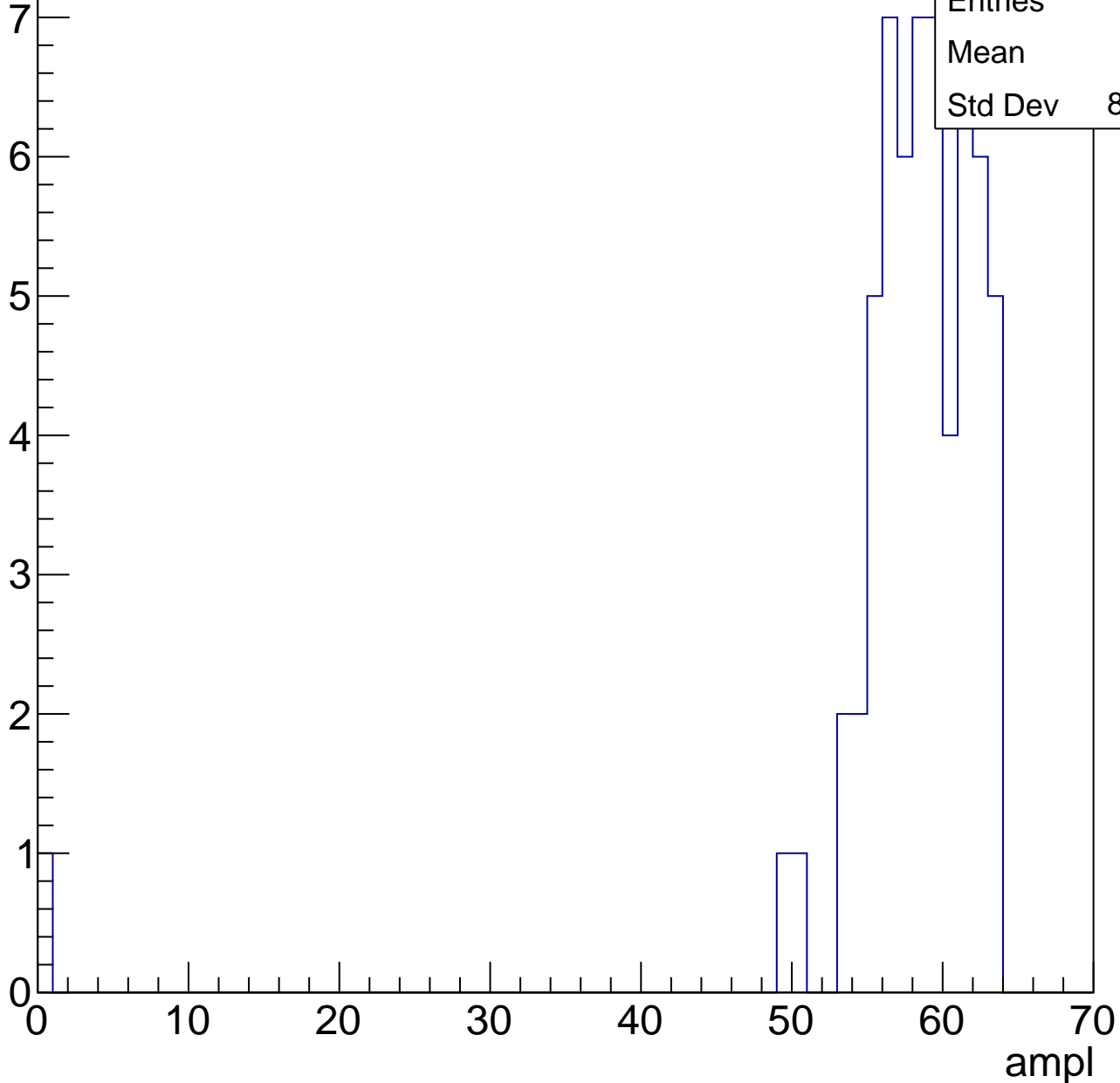


B1L100S, U1-ch102, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

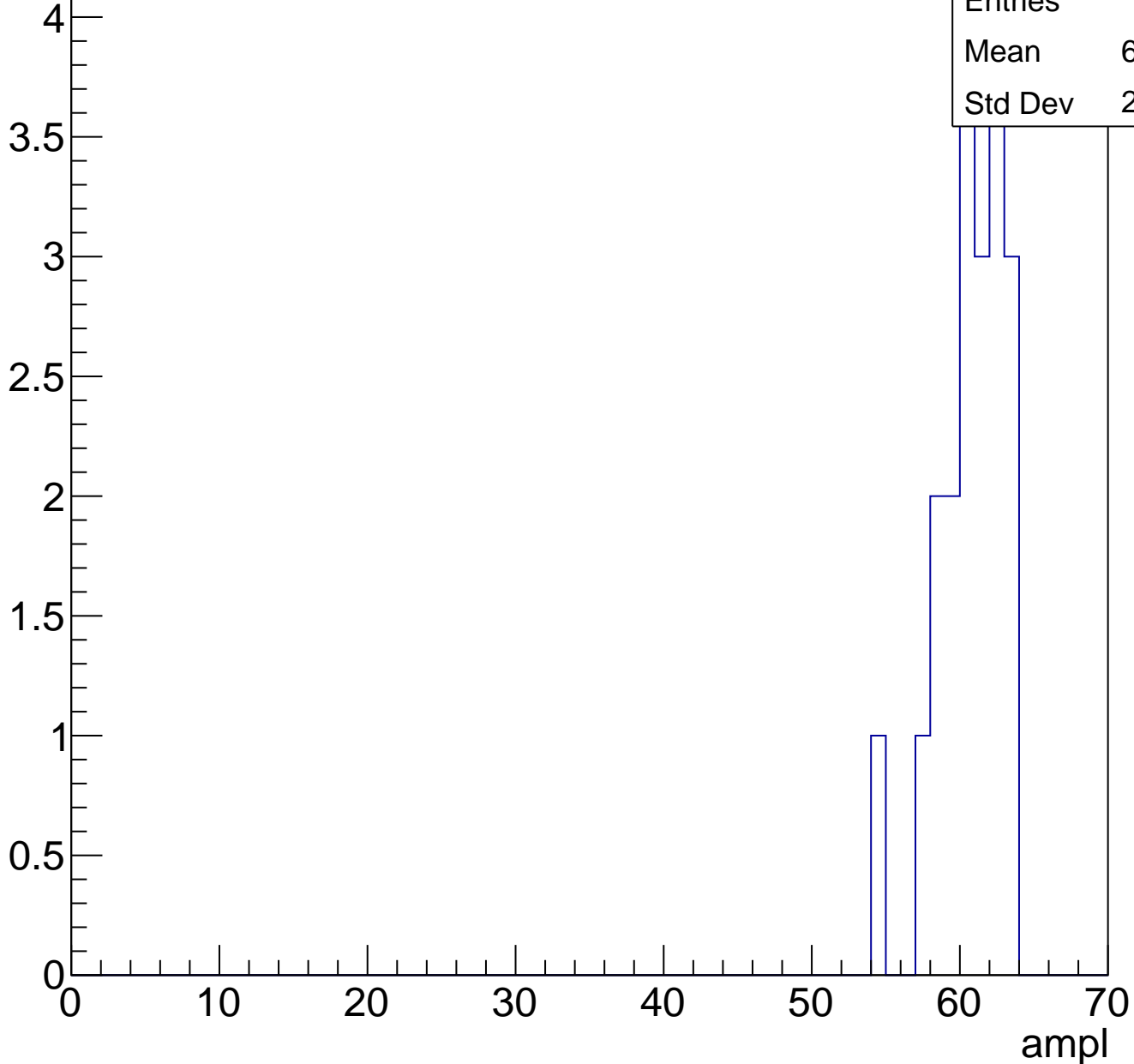
Entries	61
Mean	57.3
Std Dev	8.044



B1L100S, U1-ch102, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch102, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch103, adc0

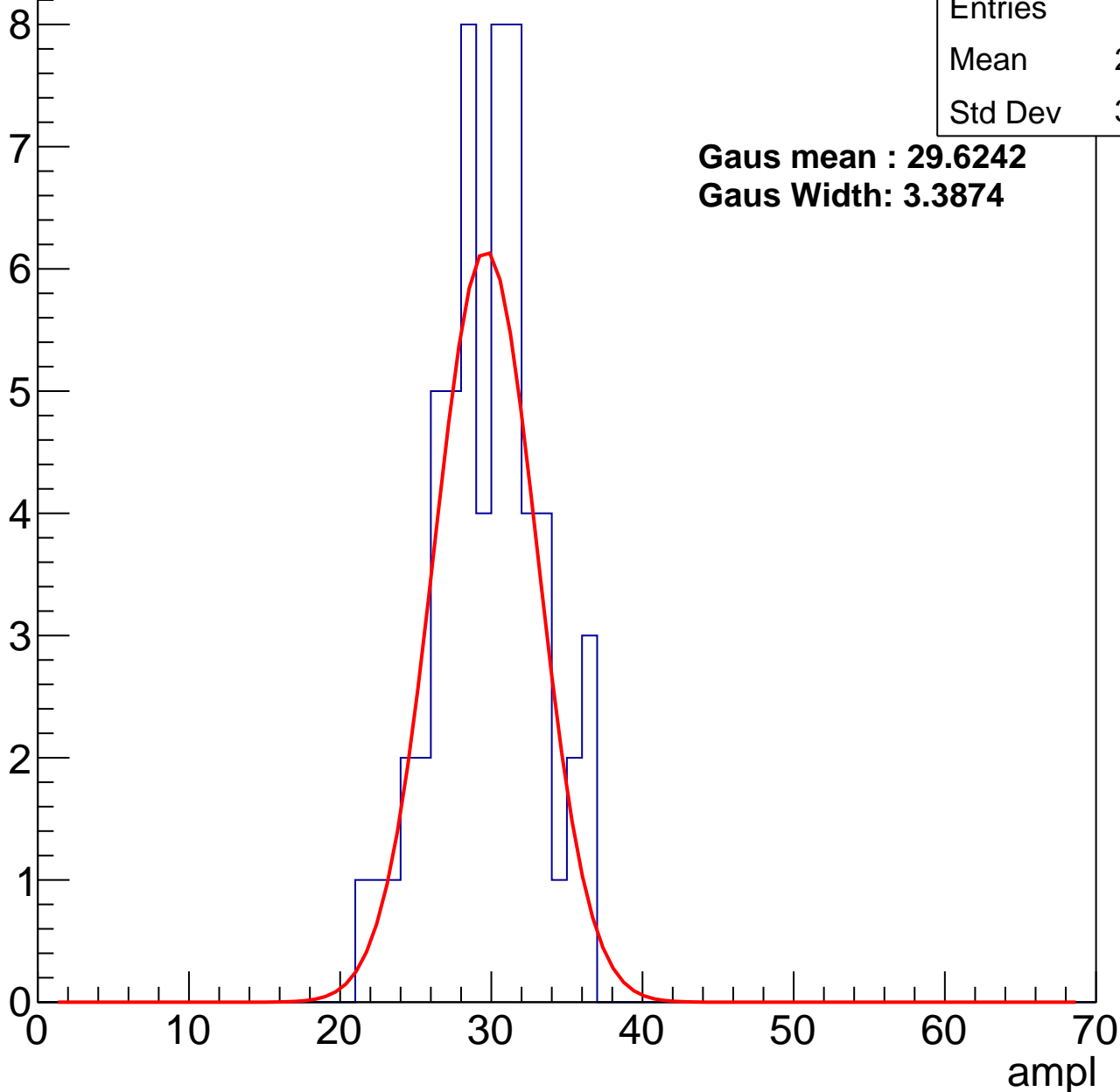
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	29.31
Std Dev	3.401

Gaus mean : 29.6242

Gaus Width: 3.3874



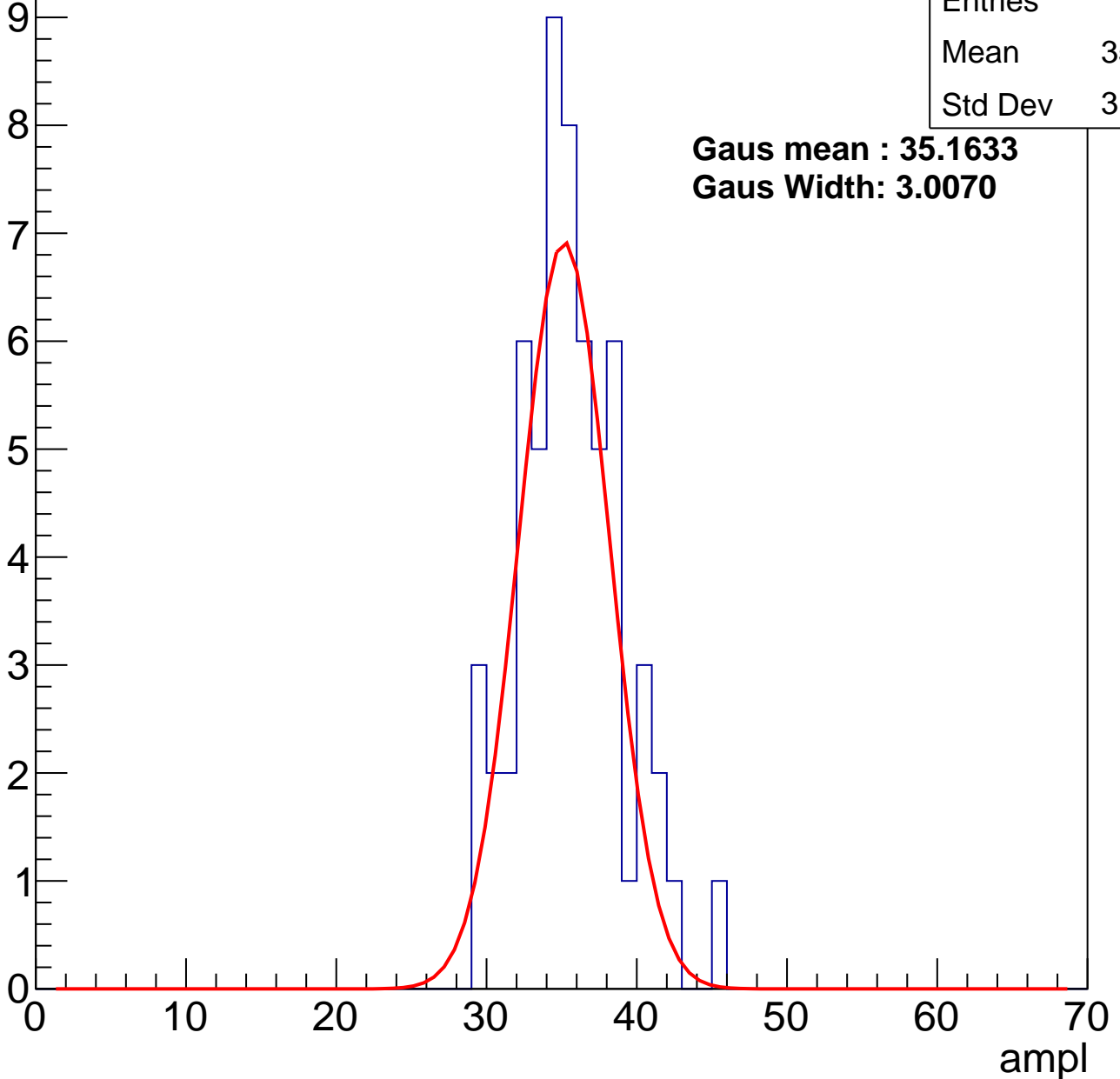
B1L100S, U1-ch103, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	35.15
Std Dev	3.326

Gaus mean : 35.1633
Gaus Width: 3.0070



B1L100S, U1-ch103, adc2

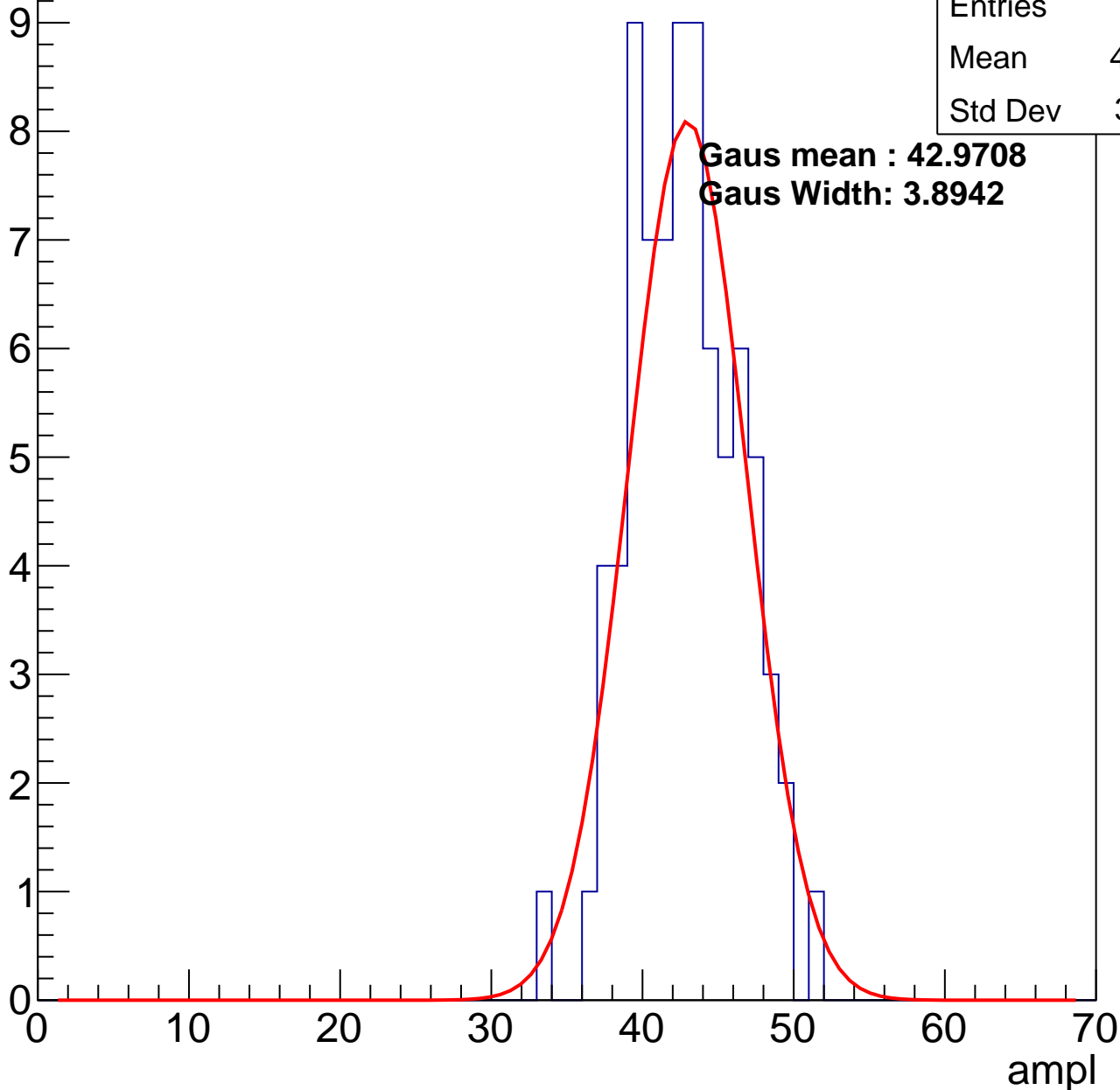
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	42.34
Std Dev	3.511

Gaus mean : 42.9708

Gaus Width: 3.8942

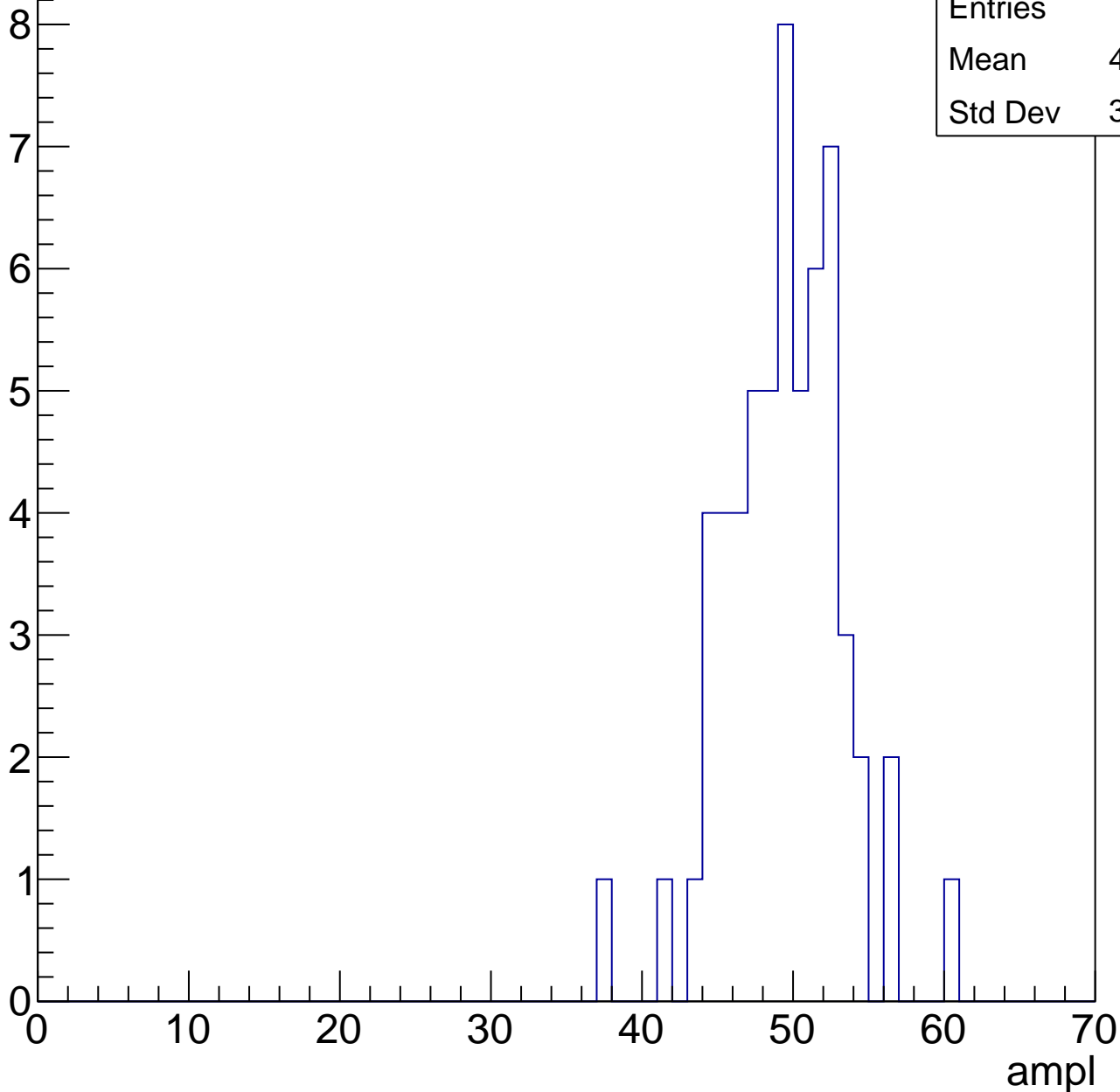


B1L100S, U1-ch103, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	48.93
Std Dev	3.857



B1L100S, U1-ch103, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	72
Mean	55.6
Std Dev	3.435

Entry

10

8

6

4

2

0

0

10

20

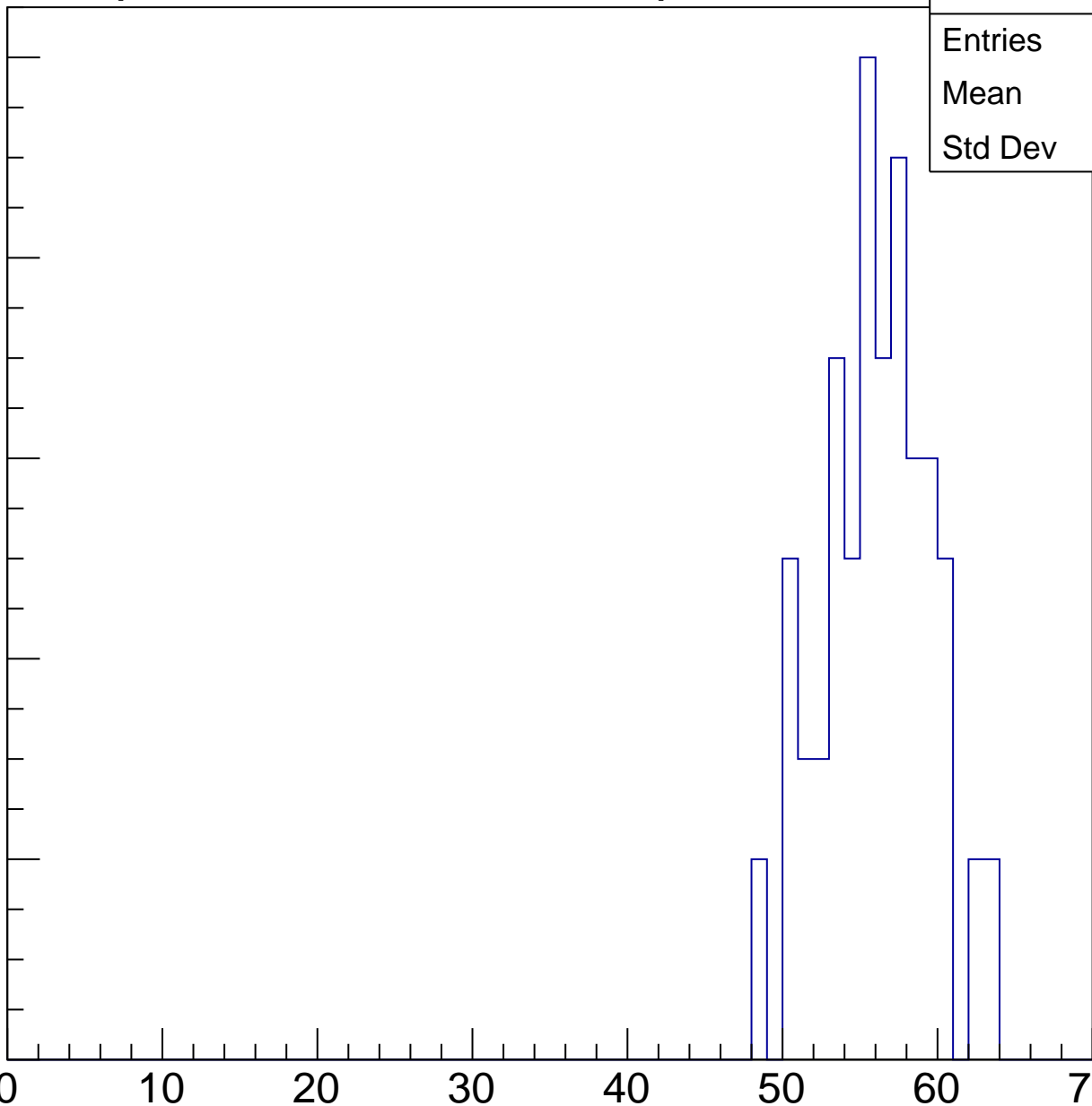
30

40

50

60

ampl

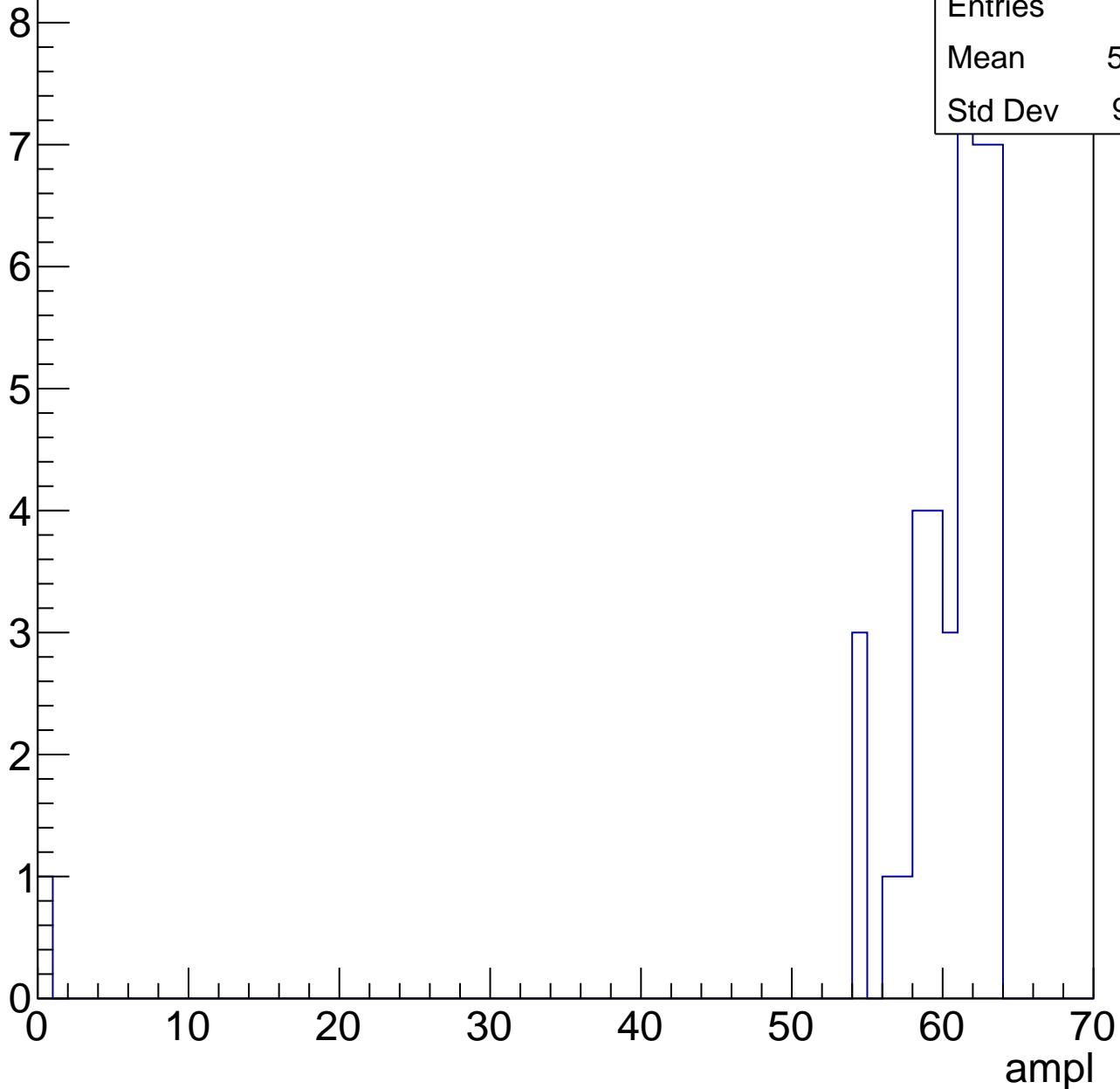


B1L100S, U1-ch103, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

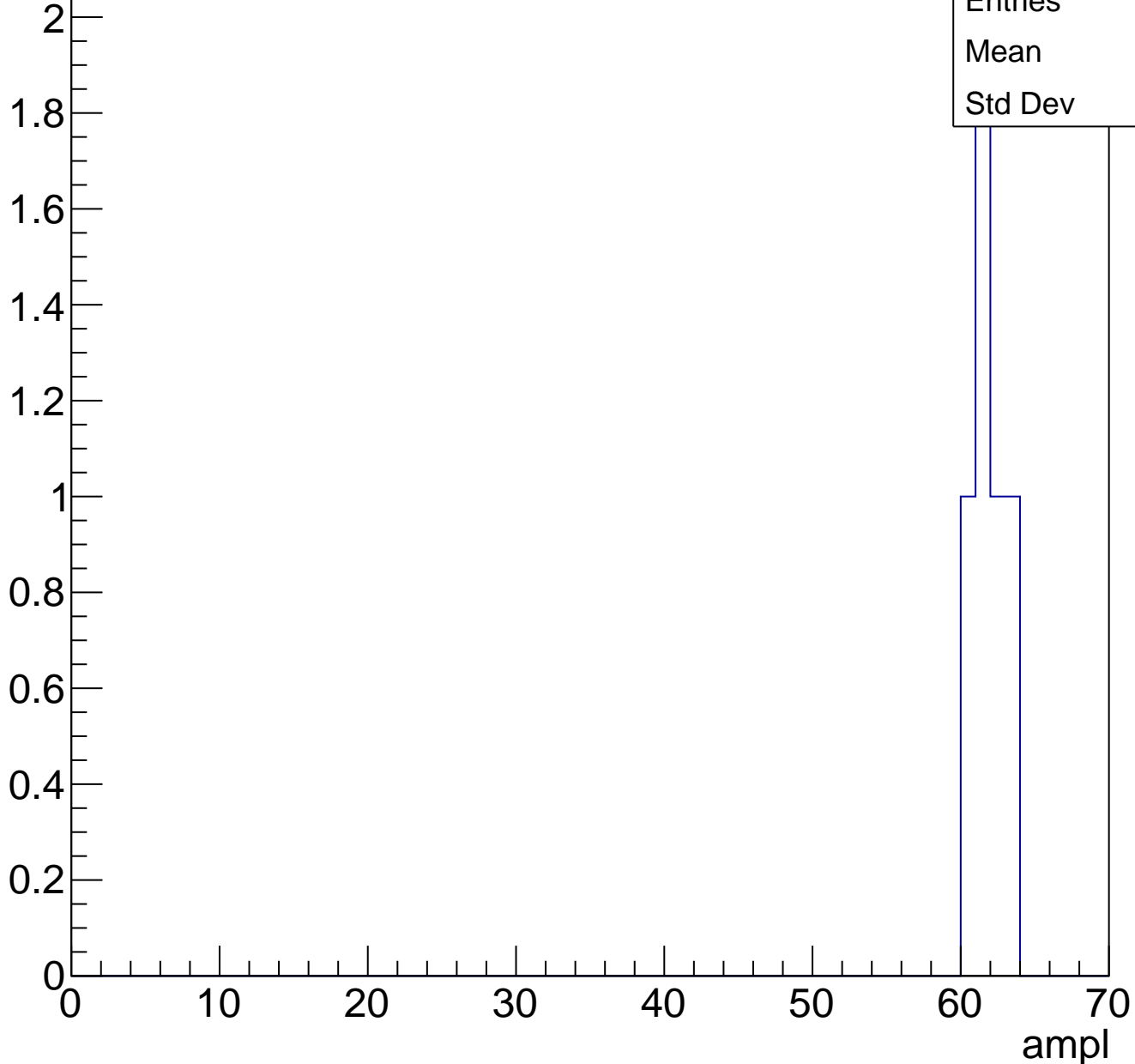
Entries	39
Mean	58.62
Std Dev	9.841



B1L100S, U1-ch103, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch103, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch104, adc0

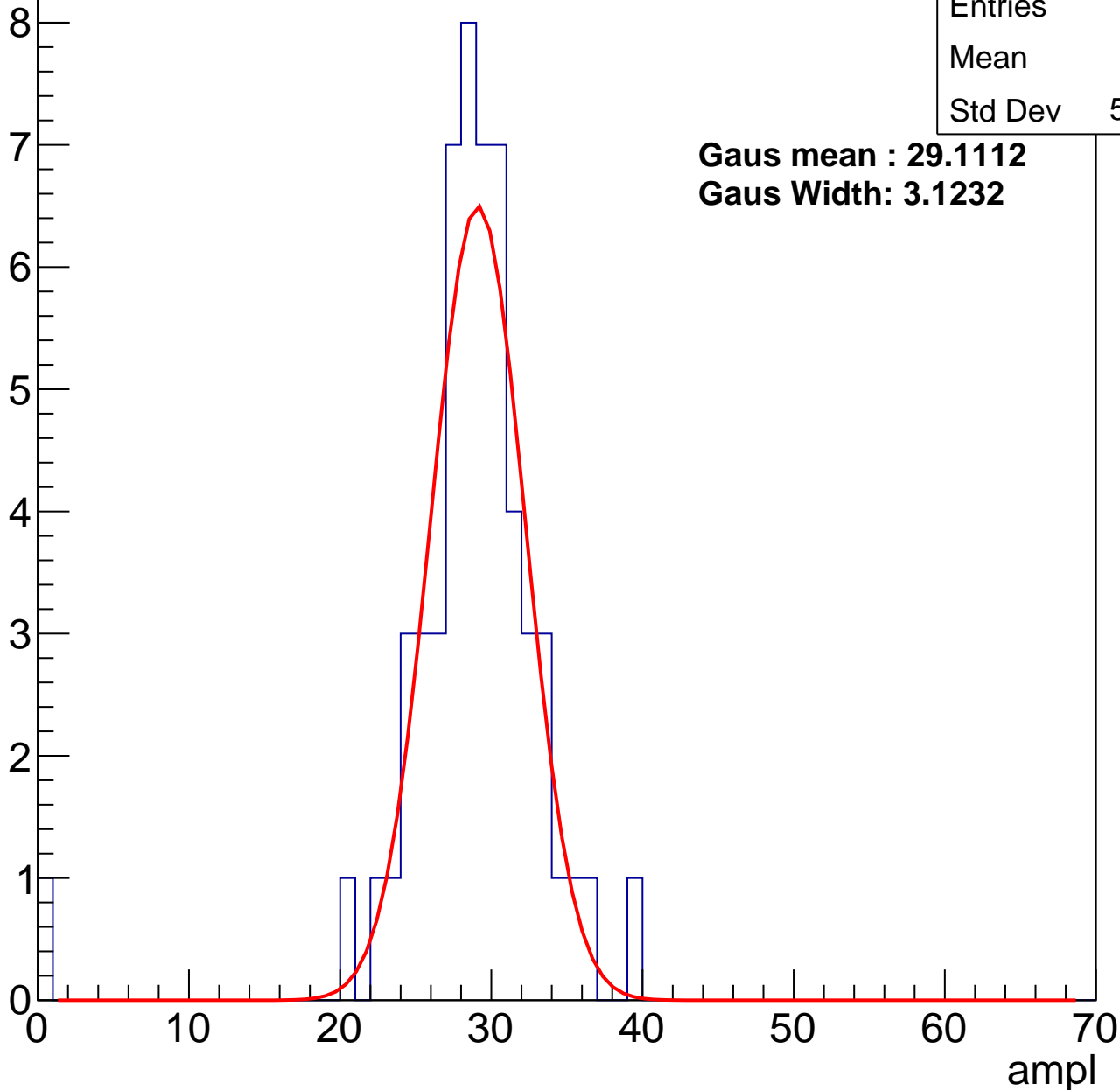
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	28.2
Std Dev	5.114

Gaus mean : 29.1112

Gaus Width: 3.1232



B1L100S, U1-ch104, adc1

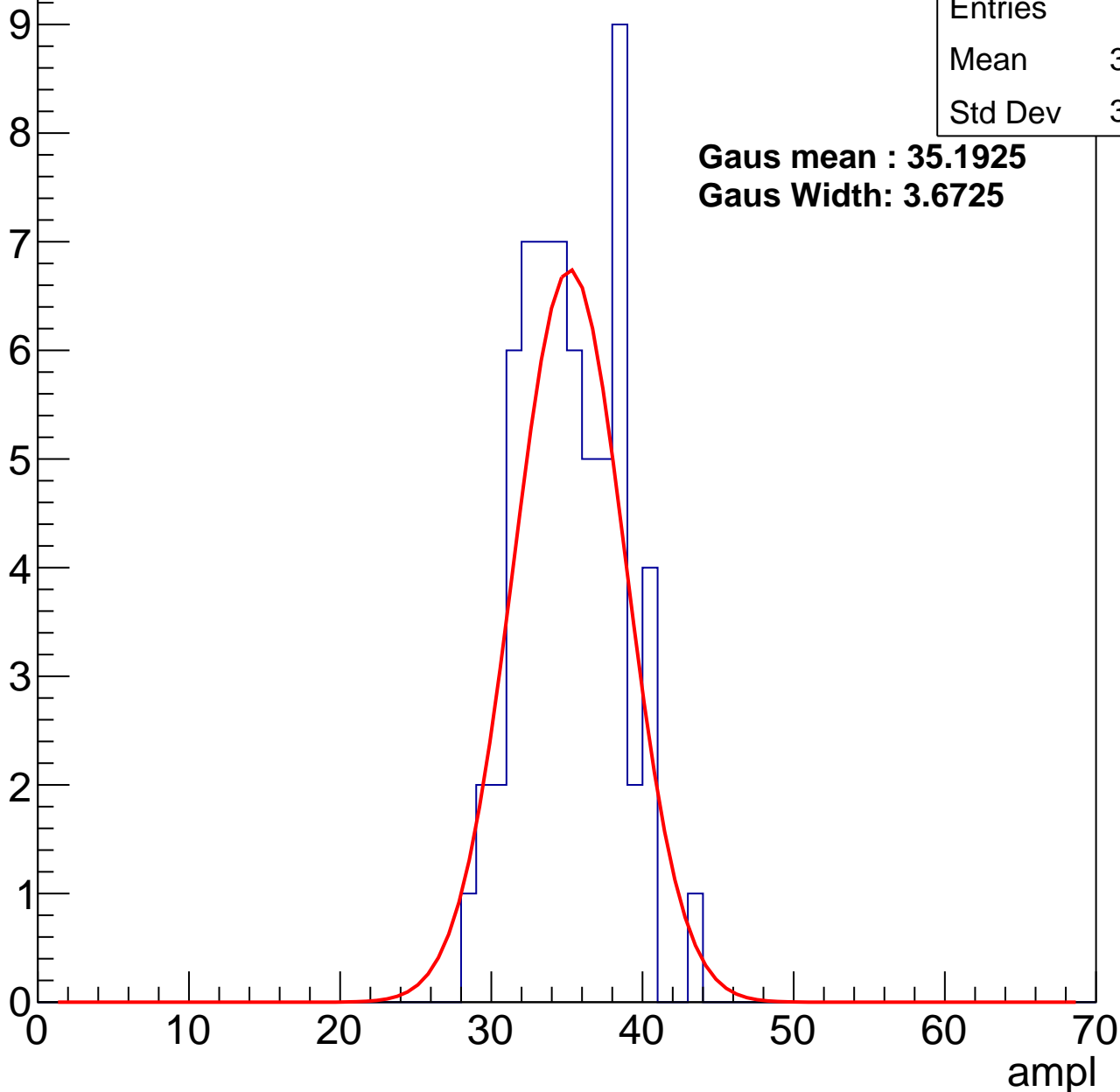
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	34.73
Std Dev	3.217

Gaus mean : 35.1925

Gaus Width: 3.6725



B1L100S, U1-ch104, adc2

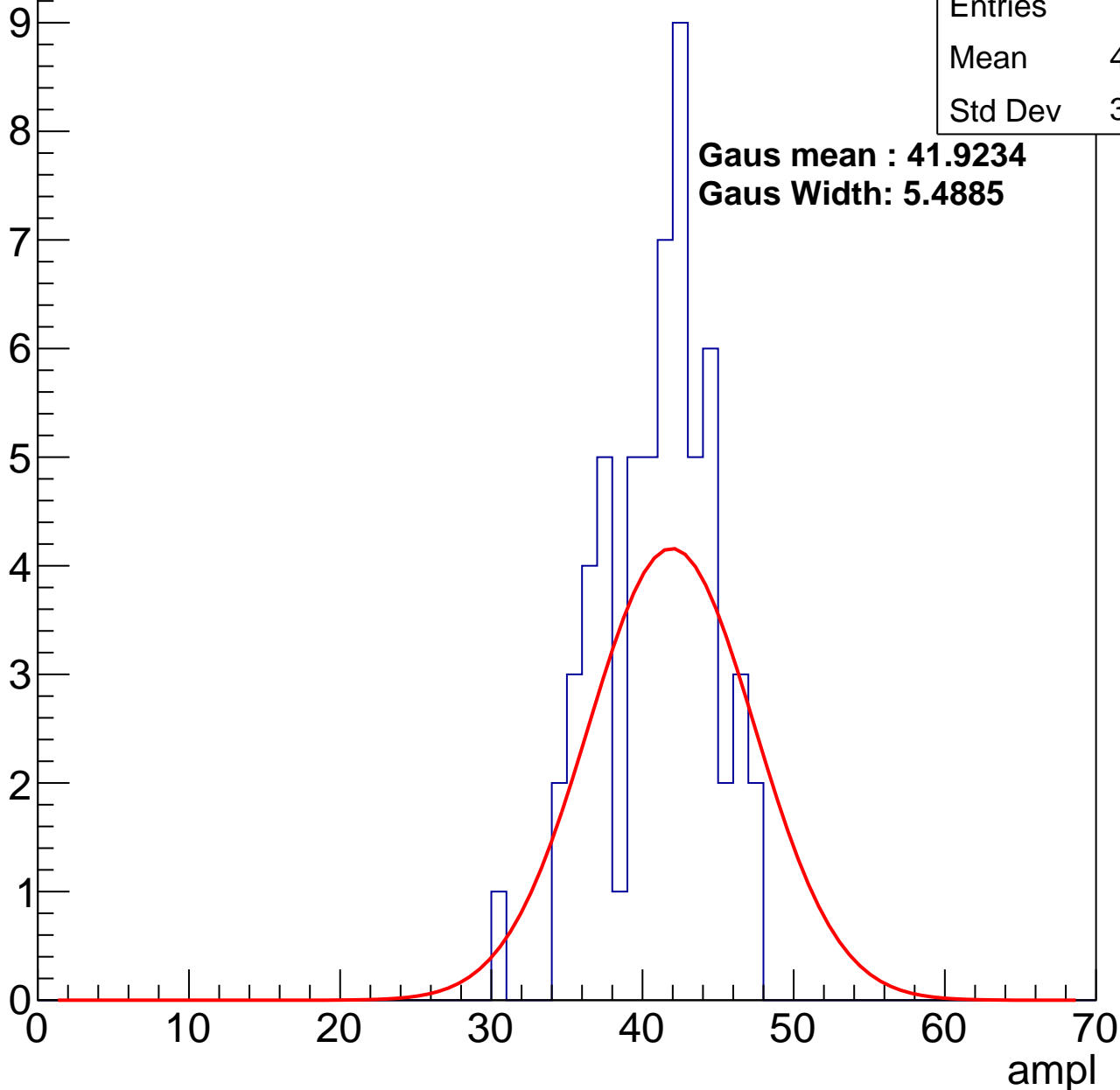
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	40.52
Std Dev	3.626

Gaus mean : 41.9234

Gaus Width: 5.4885

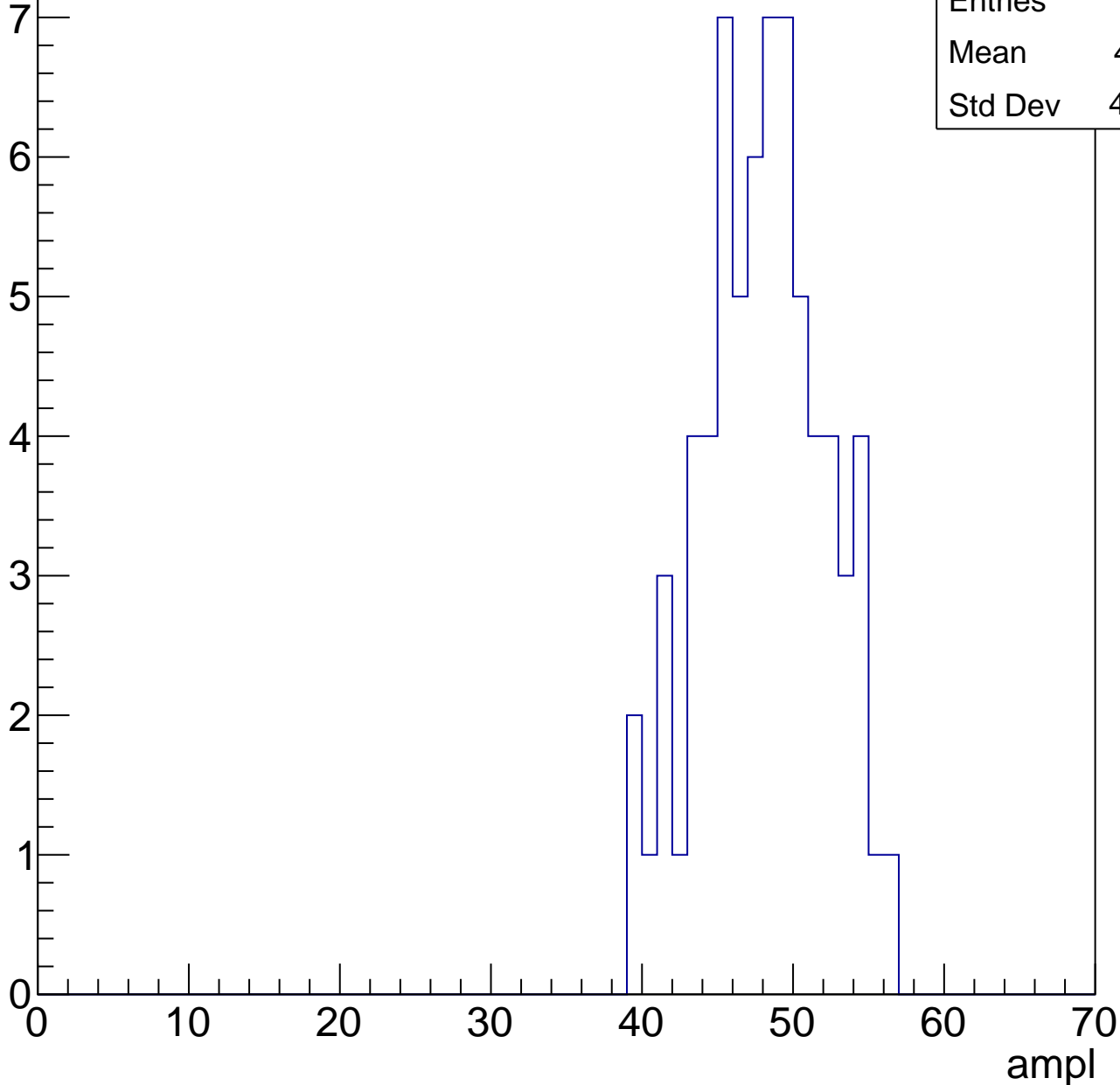


B1L100S, U1-ch104, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

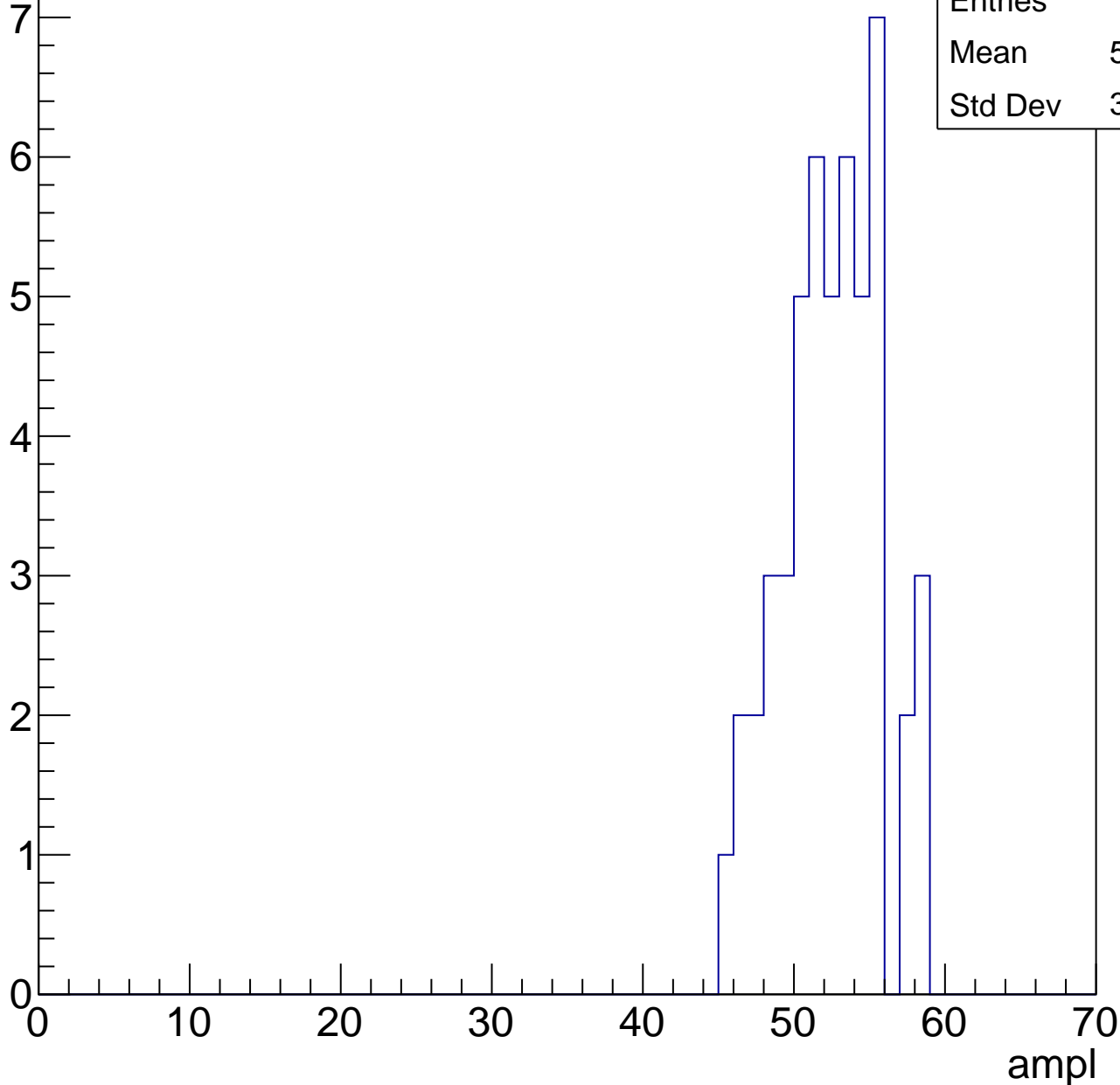
Entries	69
Mean	47.61
Std Dev	4.012



B1L100S, U1-ch104, adc4

calib_packv5_042523_0143.root, FC#4, port A2

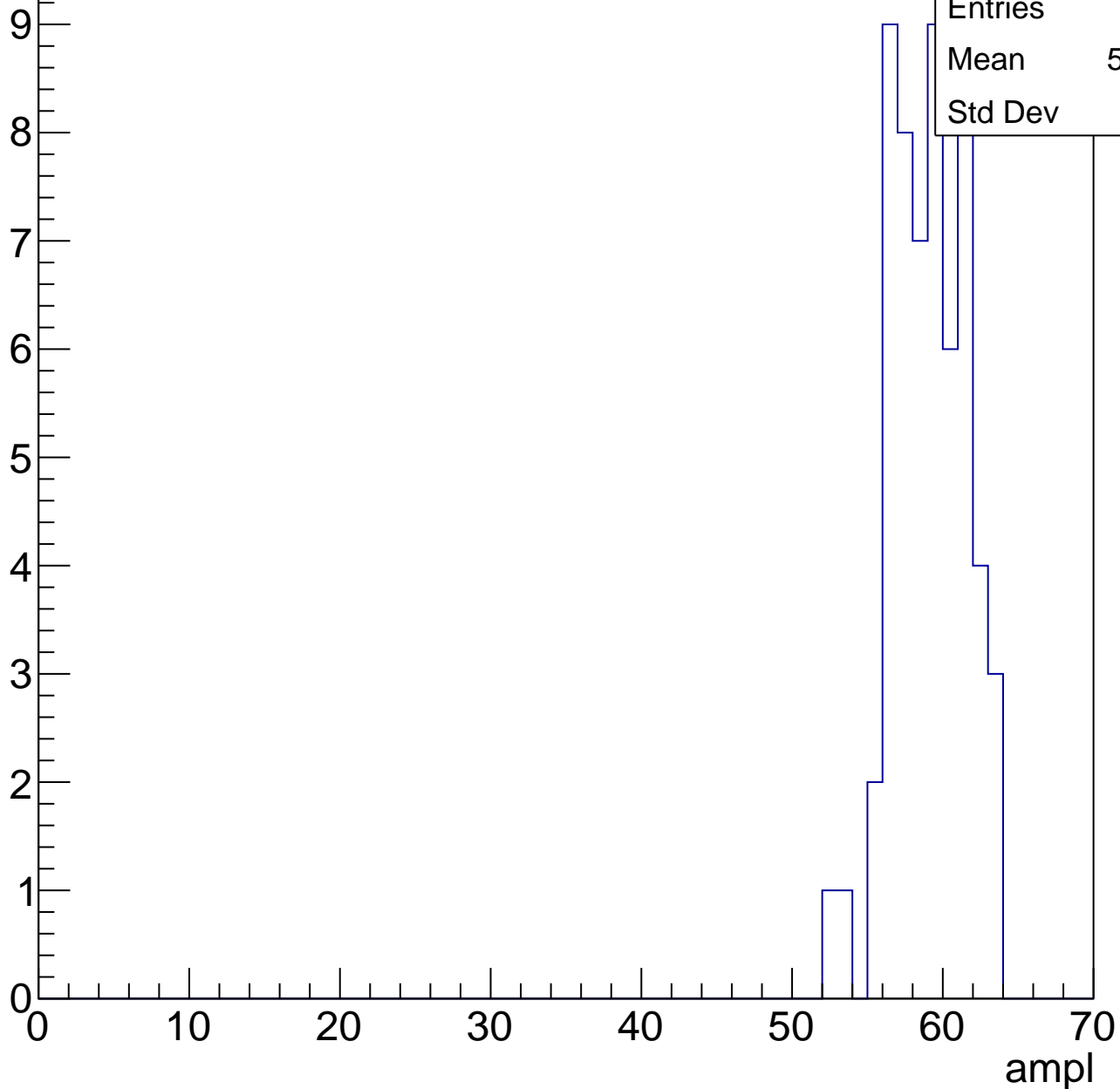
Entry



B1L100S, U1-ch104, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

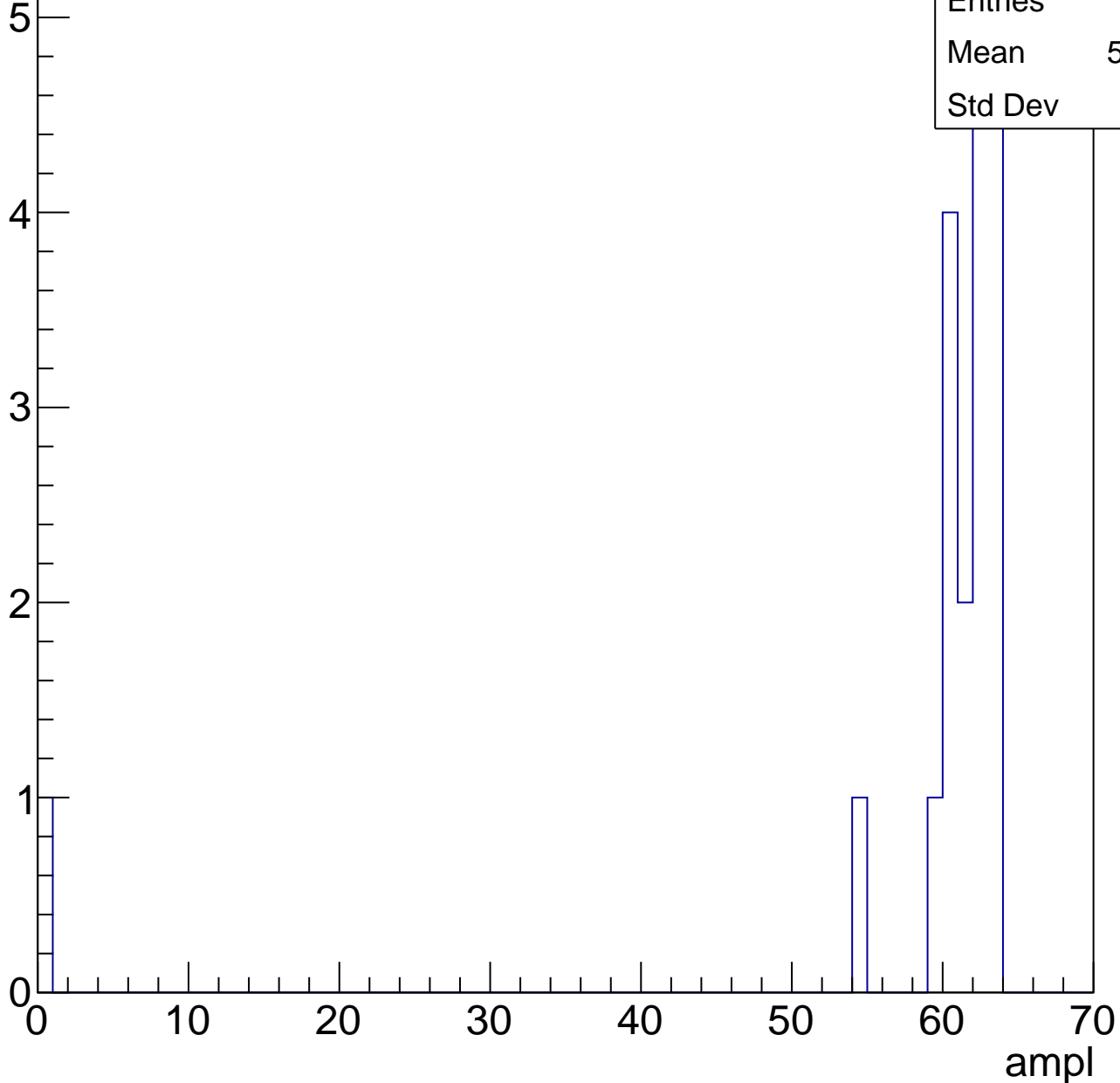


B1L100S, U1-ch104, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	19
Mean	57.89
Std Dev	13.8



B1L100S, U1-ch104, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch105, adc0

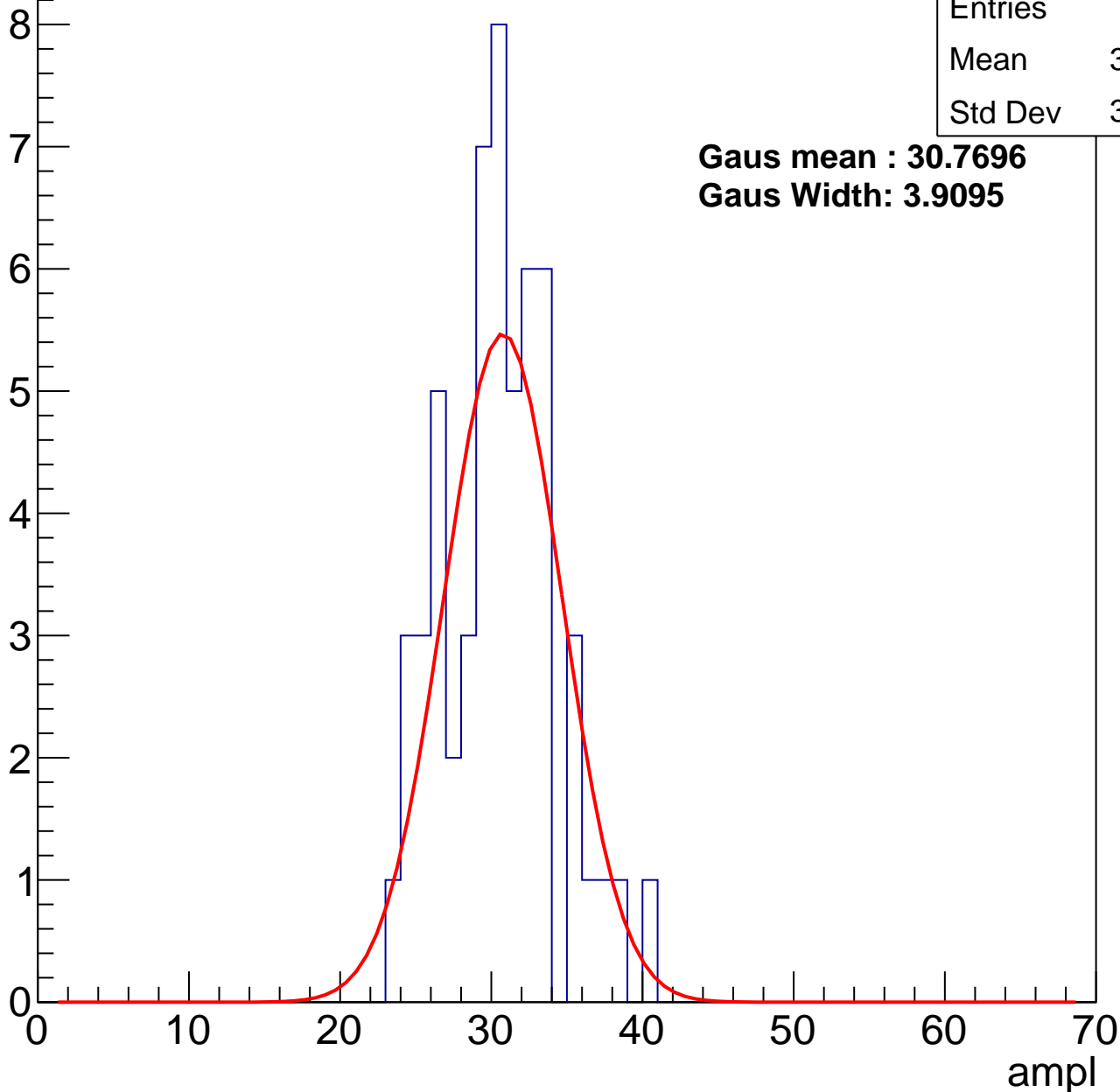
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	30.04
Std Dev	3.674

Gaus mean : 30.7696

Gaus Width: 3.9095



B1L100S, U1-ch105, adc1

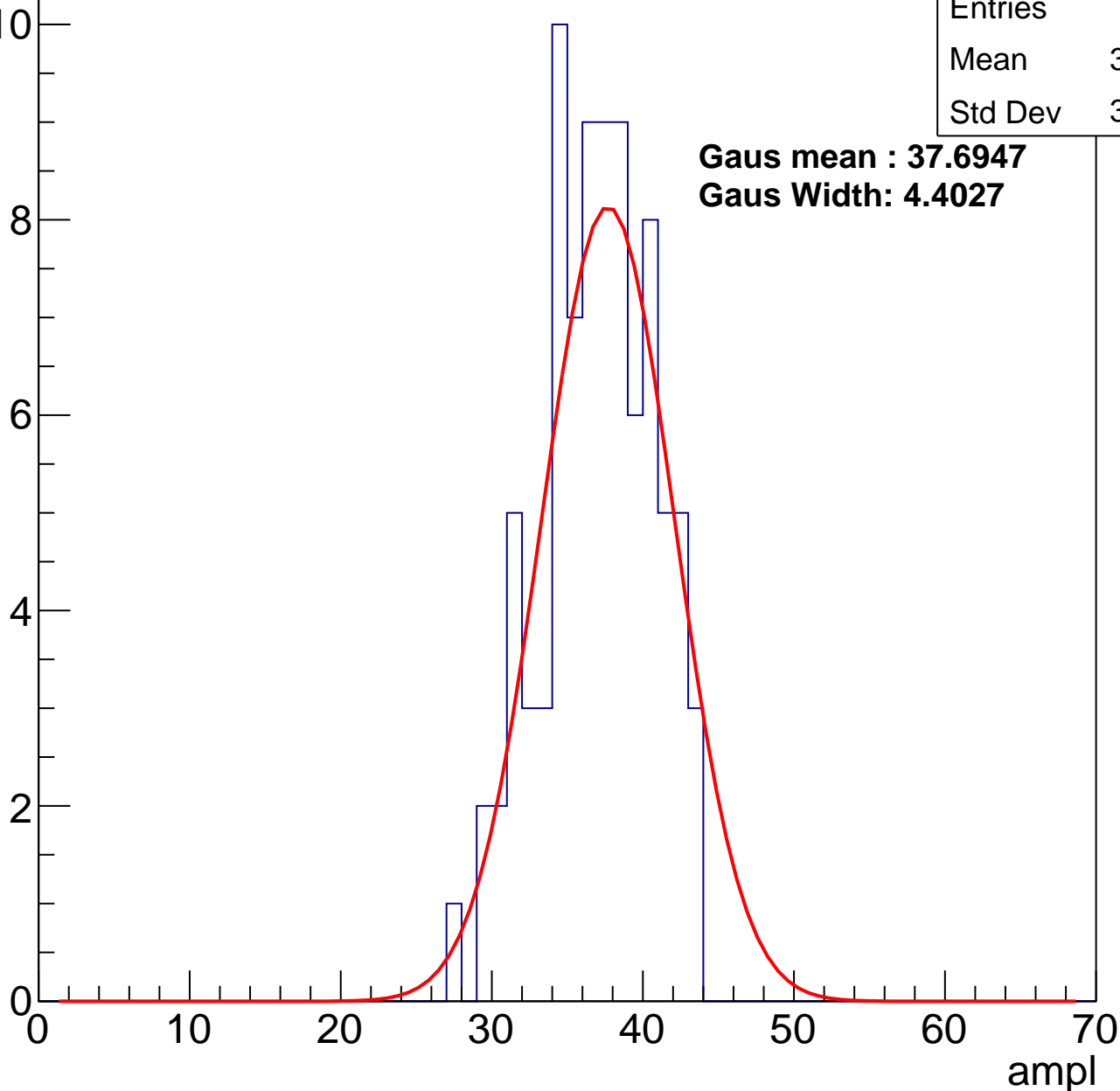
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	87
Mean	36.52
Std Dev	3.642

Gaus mean : 37.6947

Gaus Width: 4.4027



B1L100S, U1-ch105, adc2

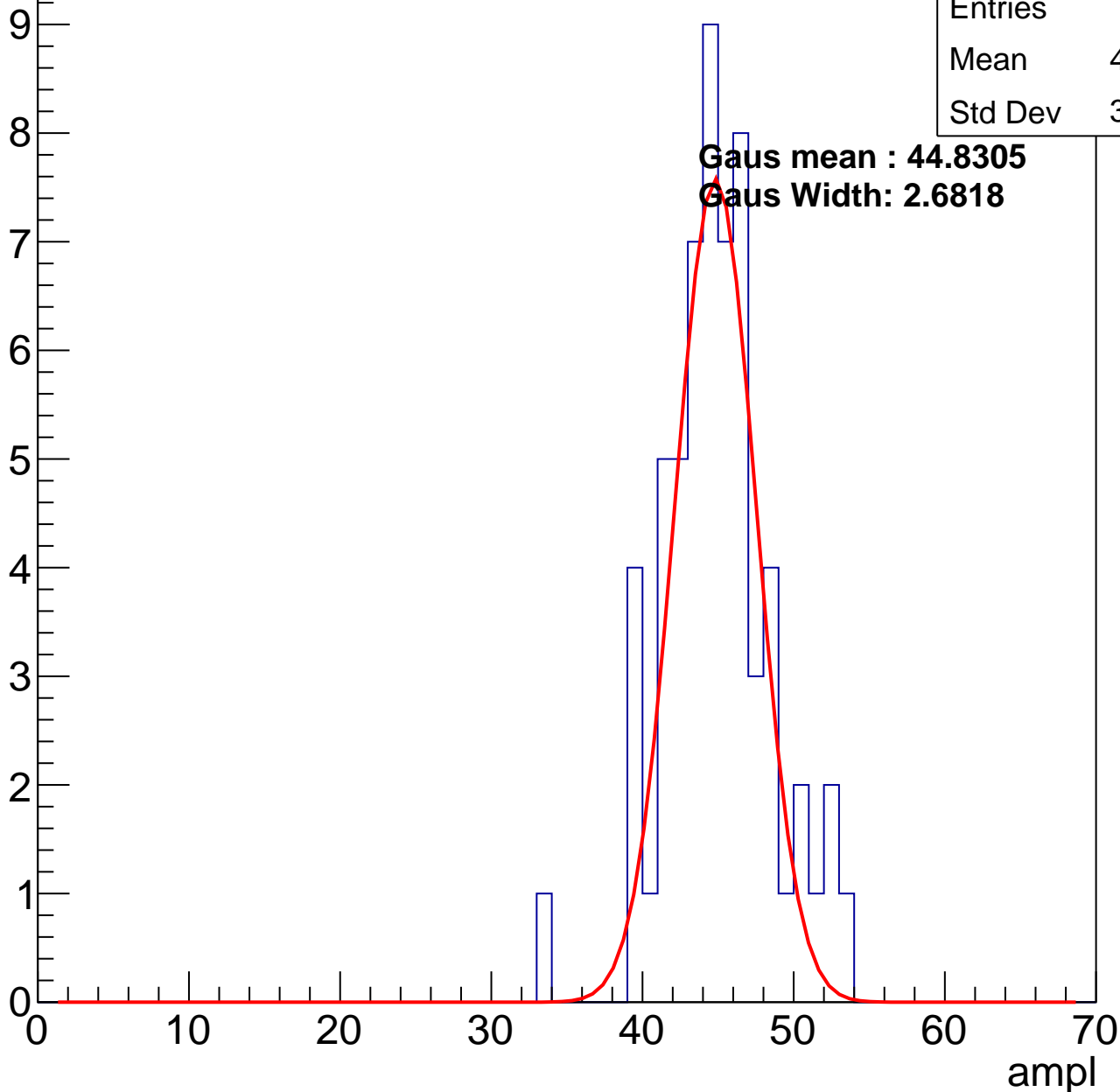
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	44.49
Std Dev	3.574

Gaus mean : 44.8305

Gaus Width: 2.6818

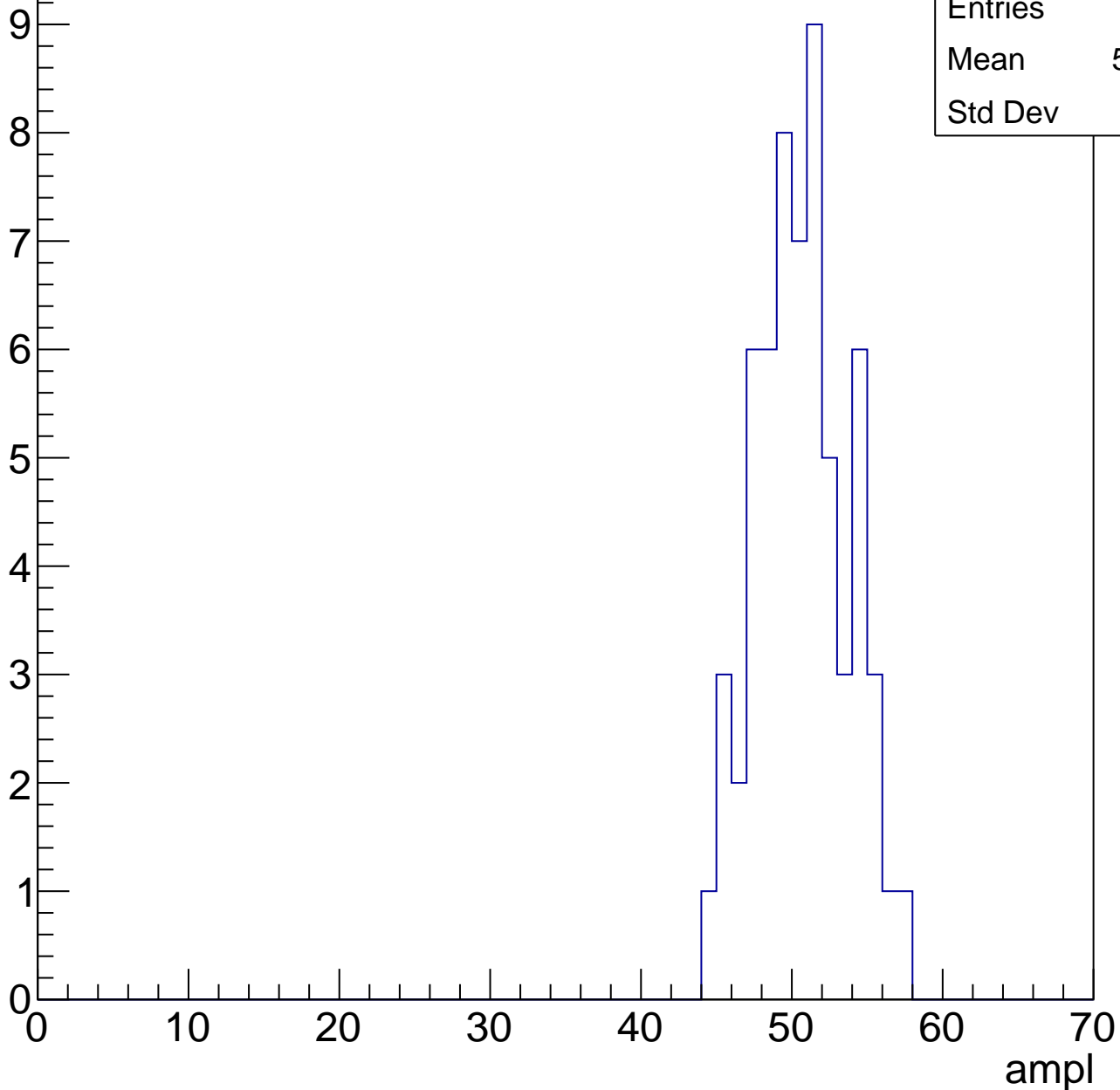


B1L100S, U1-ch105, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	50.21
Std Dev	2.97

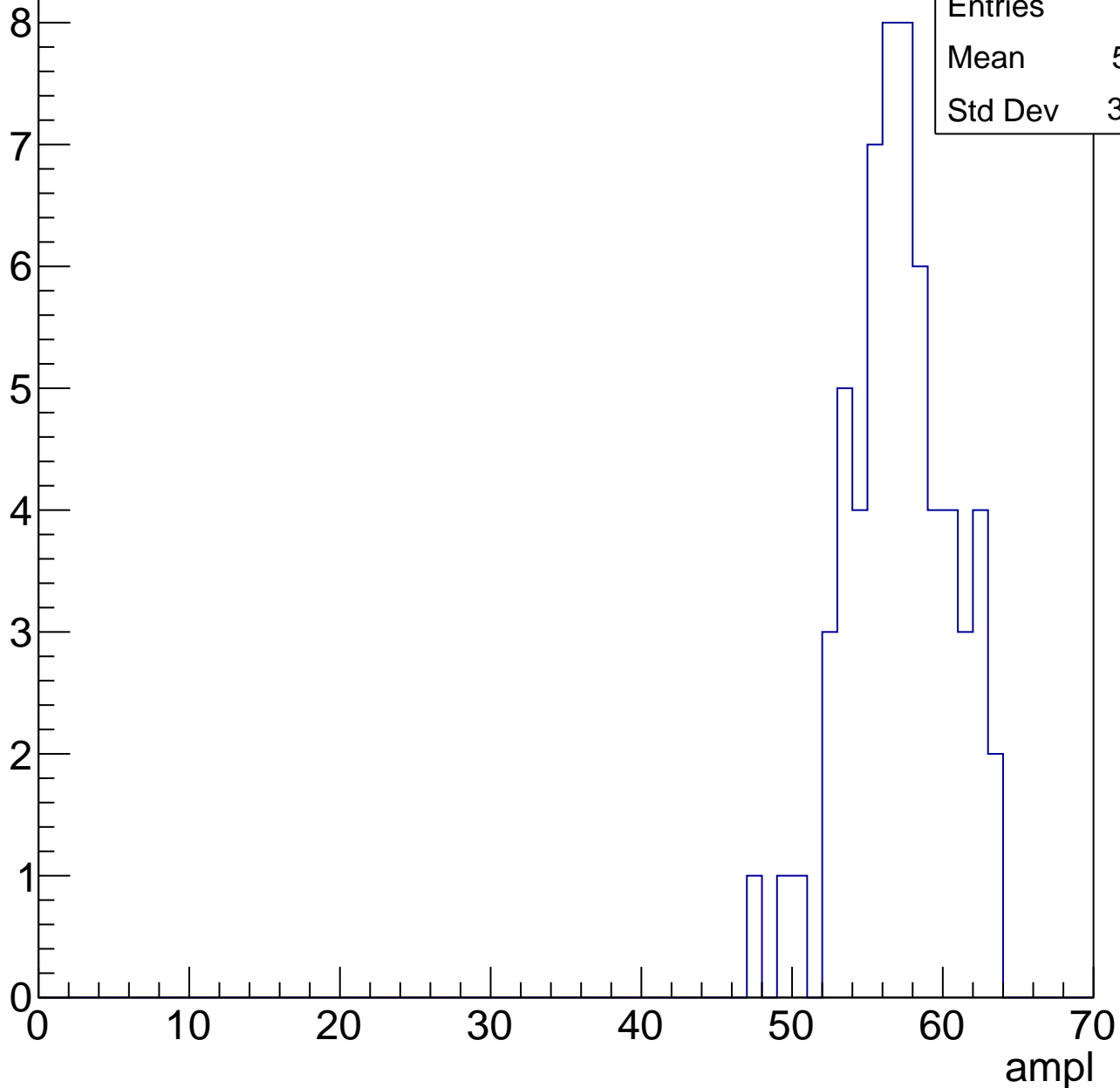


B1L100S, U1-ch105, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	61
Mean	56.61
Std Dev	3.408

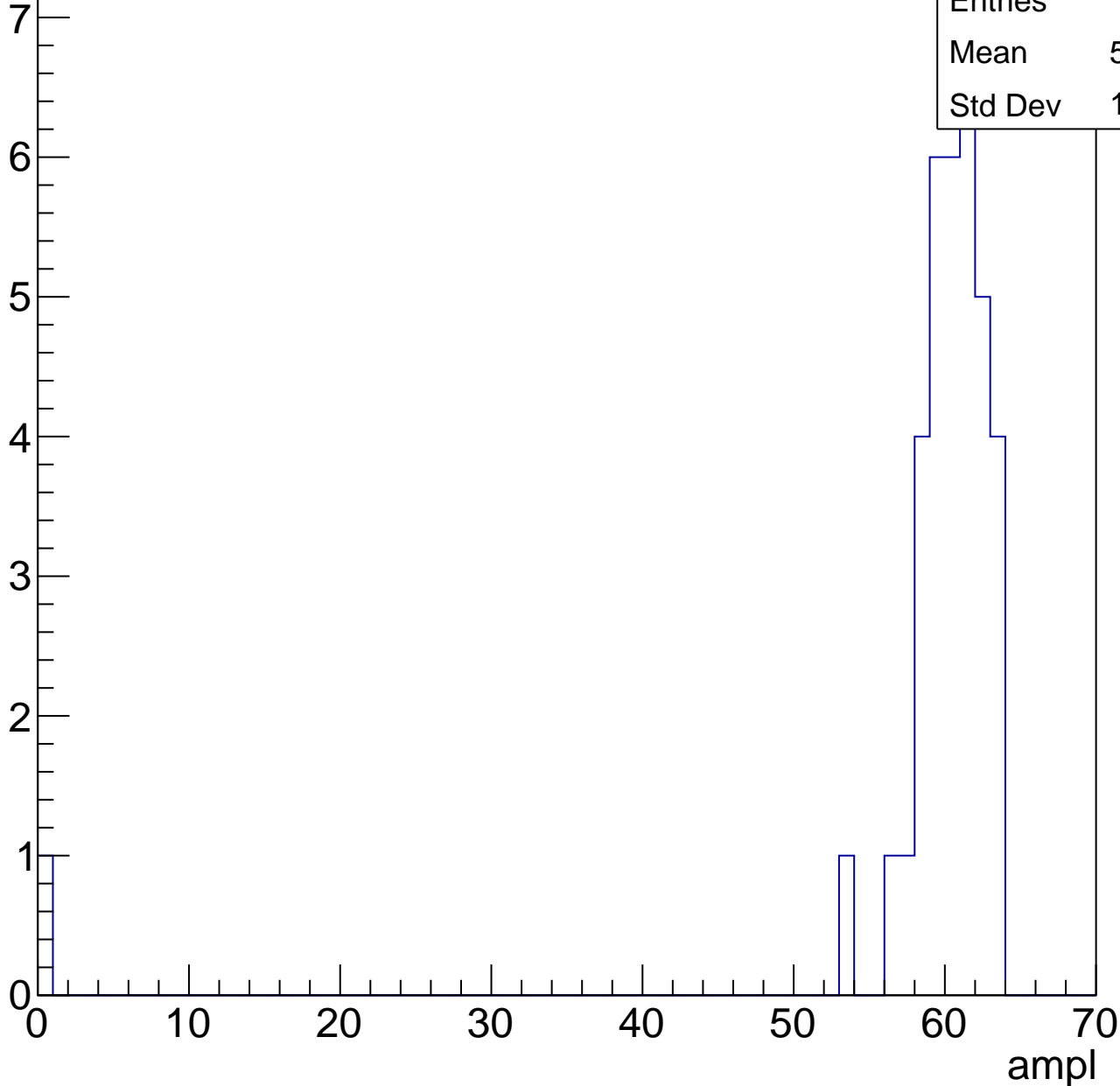


B1L100S, U1-ch105, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	36
Mean	58.36
Std Dev	10.09



B1L100S, U1-ch105, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch105, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch106, adc0

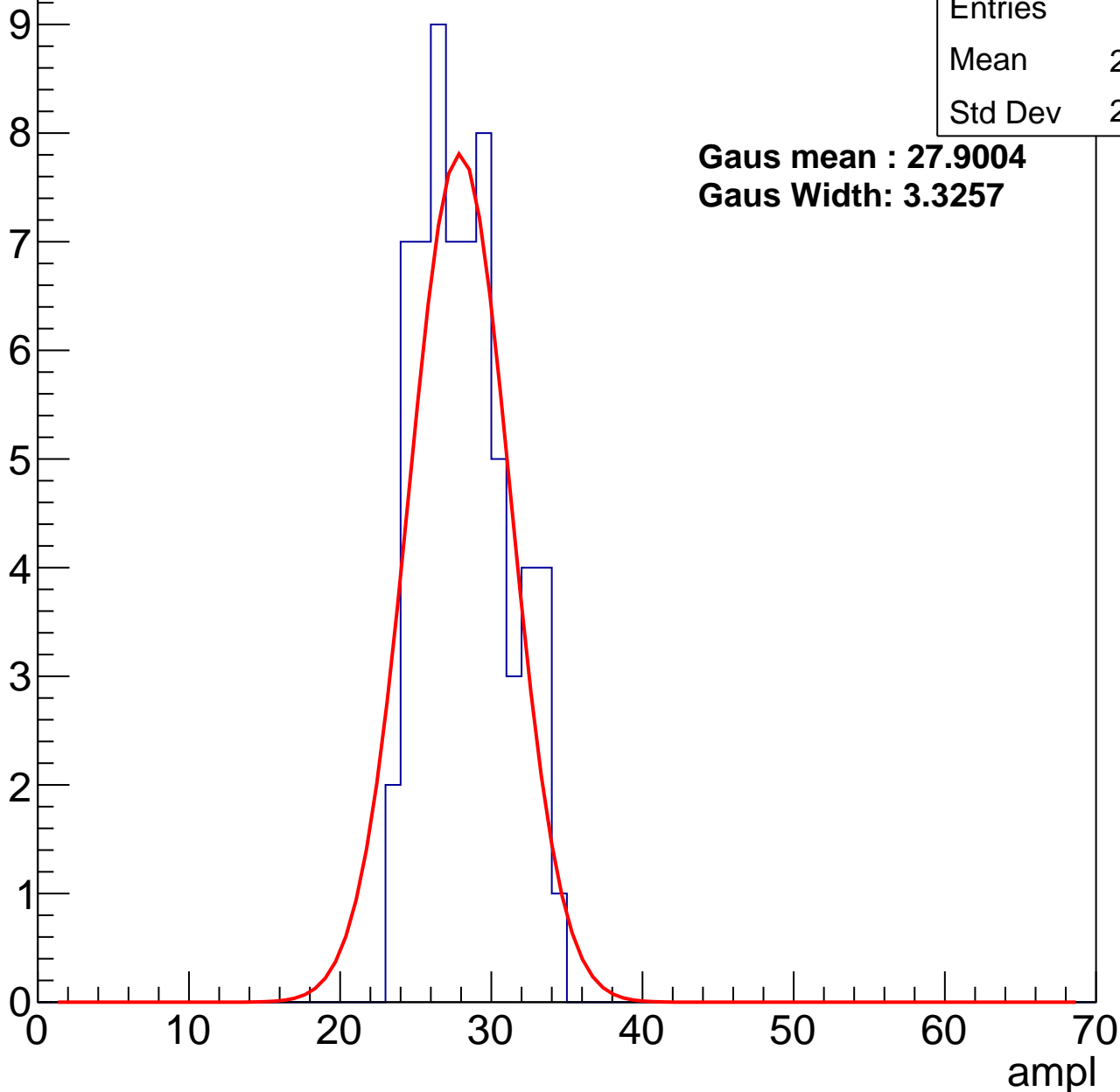
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	27.77
Std Dev	2.849

Gaus mean : 27.9004

Gaus Width: 3.3257



B1L100S, U1-ch106, adc1

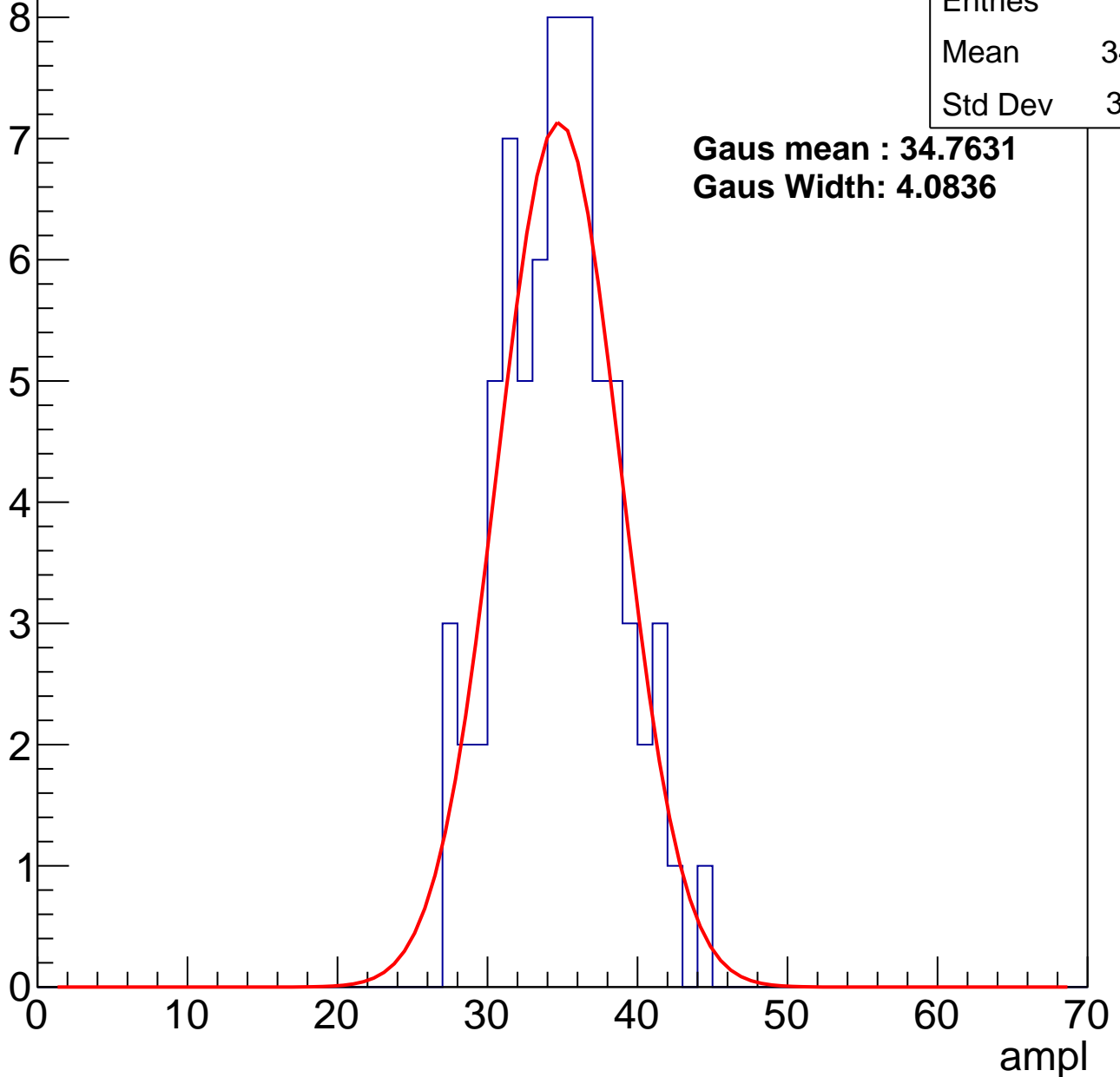
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	34.34
Std Dev	3.771

Gaus mean : 34.7631

Gaus Width: 4.0836



B1L100S, U1-ch106, adc2

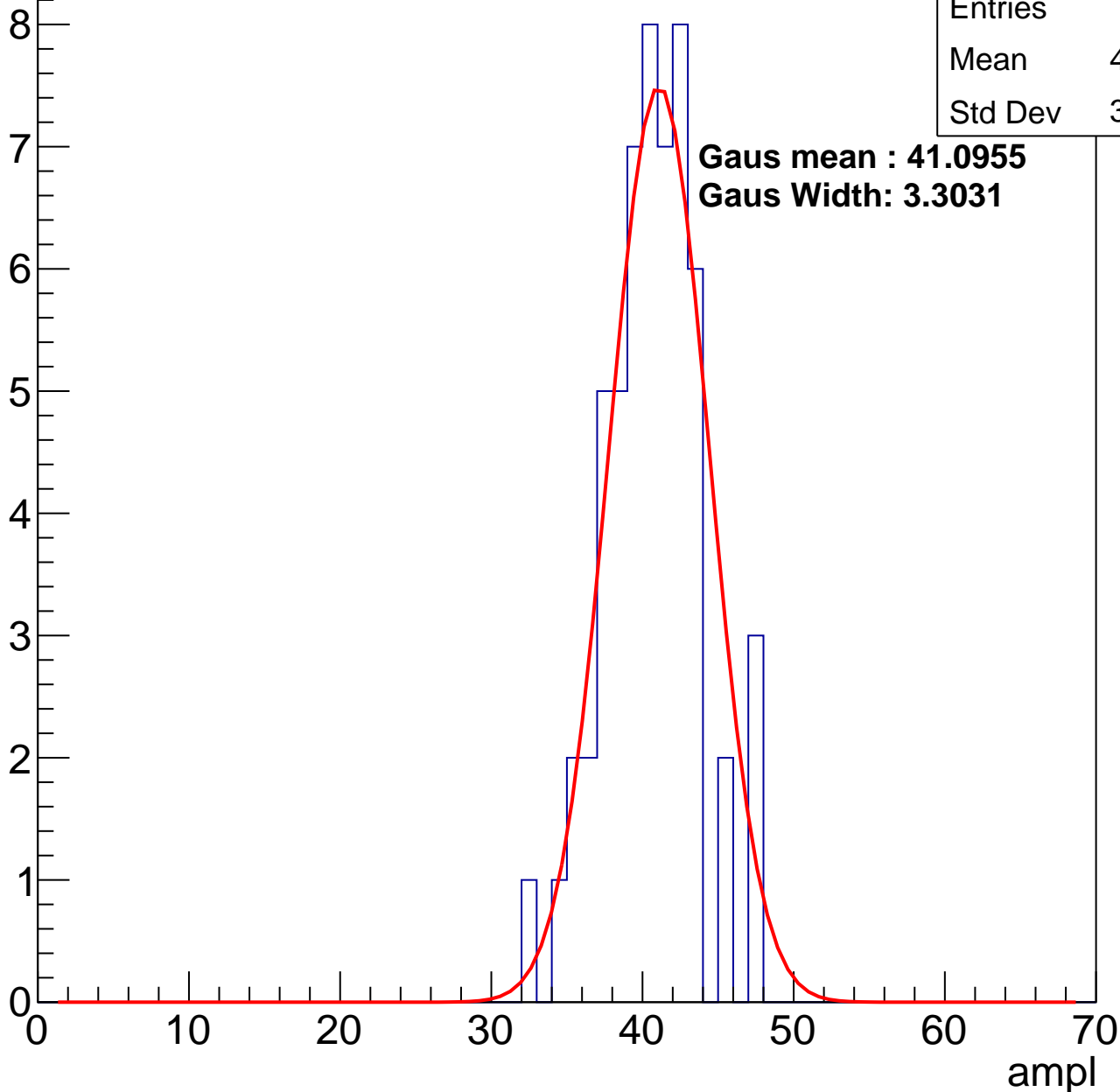
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	40.14
Std Dev	3.086

Gaus mean : 41.0955

Gaus Width: 3.3031

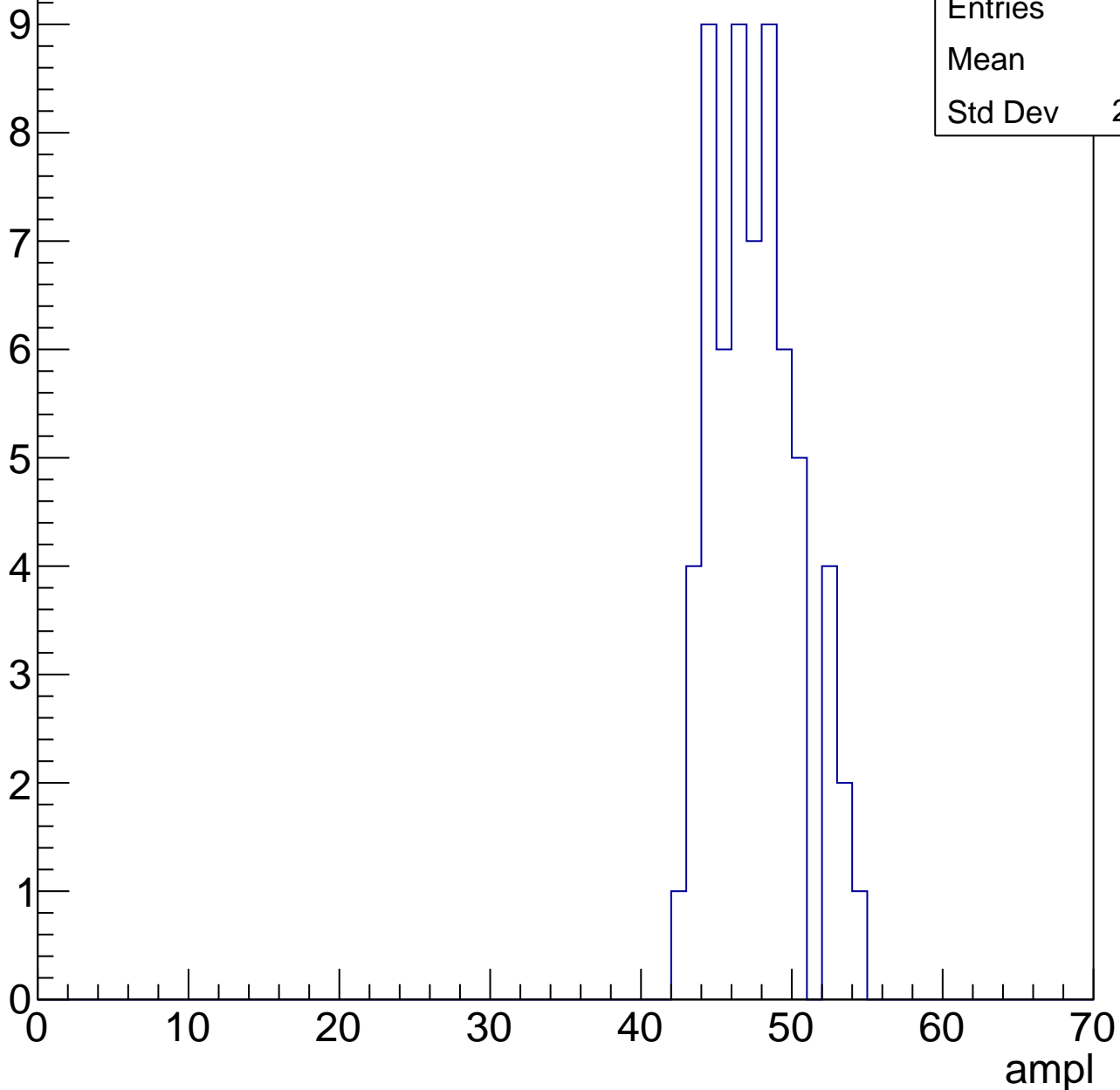


B1L100S, U1-ch106, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

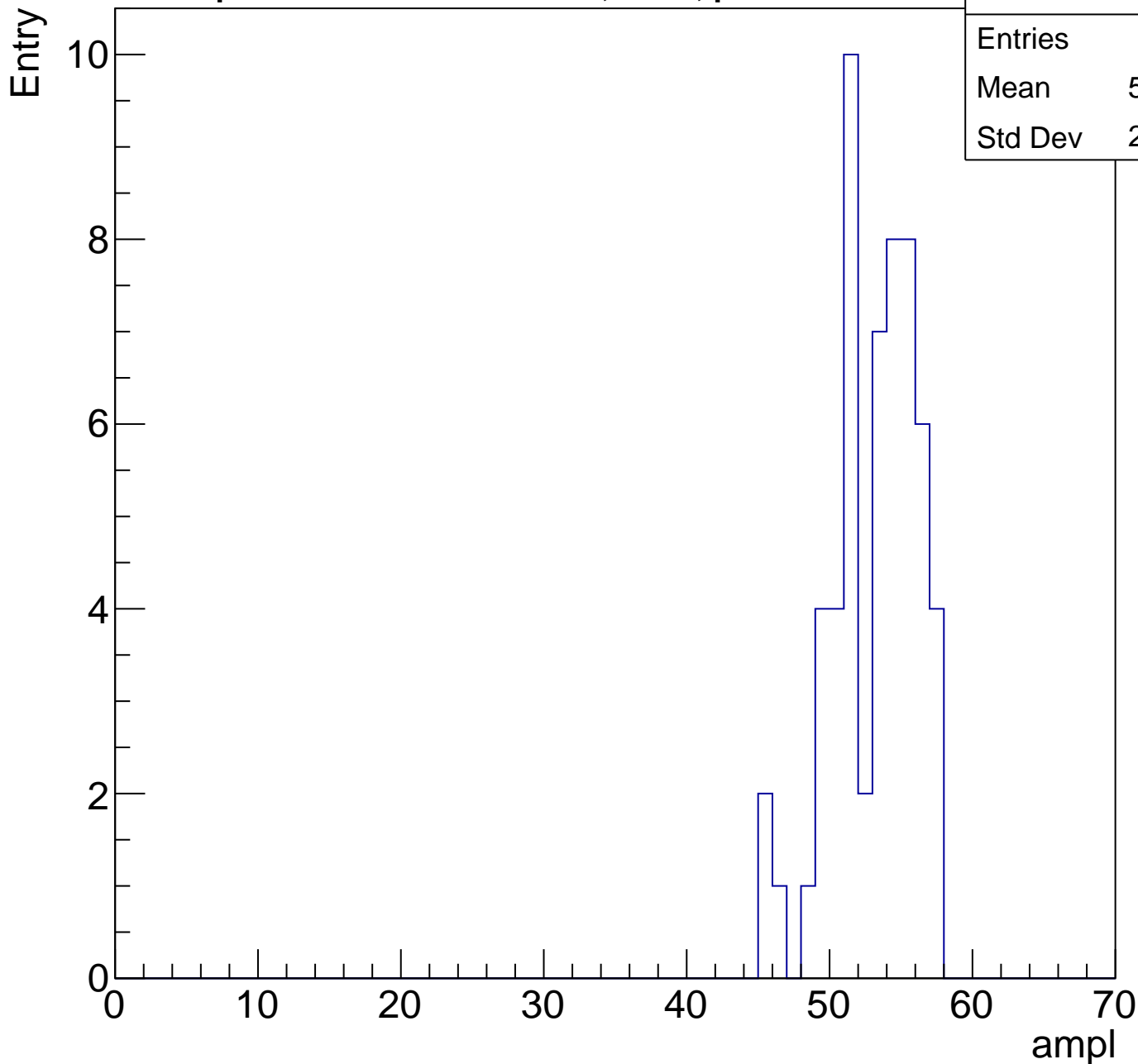
Entries	63
Mean	47.1
Std Dev	2.821



B1L100S, U1-ch106, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	57
Mean	52.65
Std Dev	2.947

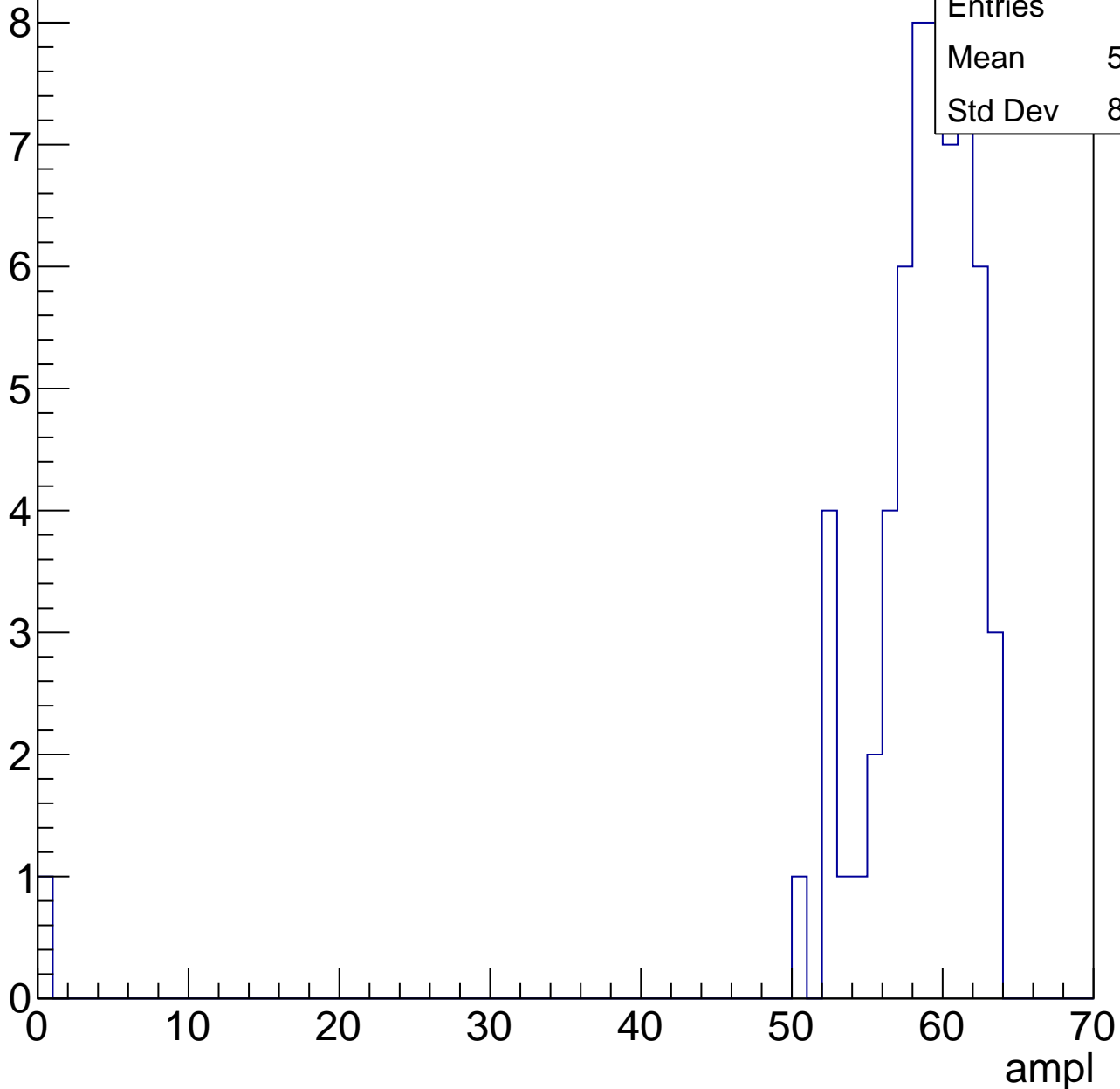


B1L100S, U1-ch106, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	57.43
Std Dev	8.075

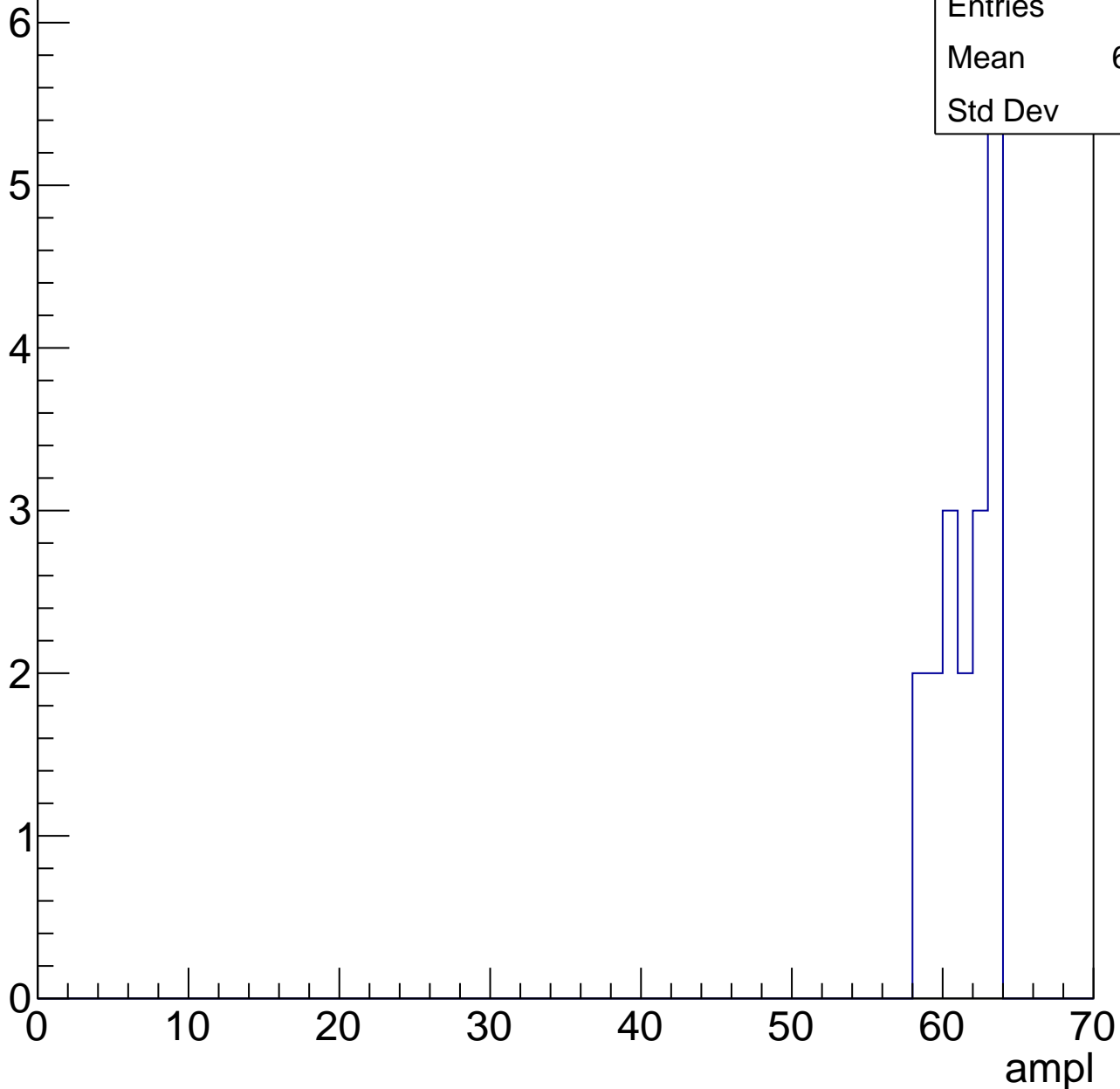


B1L100S, U1-ch106, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	18
Mean	61.11
Std Dev	1.76



B1L100S, U1-ch106, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	4
Mean	31.25
Std Dev	31.25

B1L100S, U1-ch107, adc0

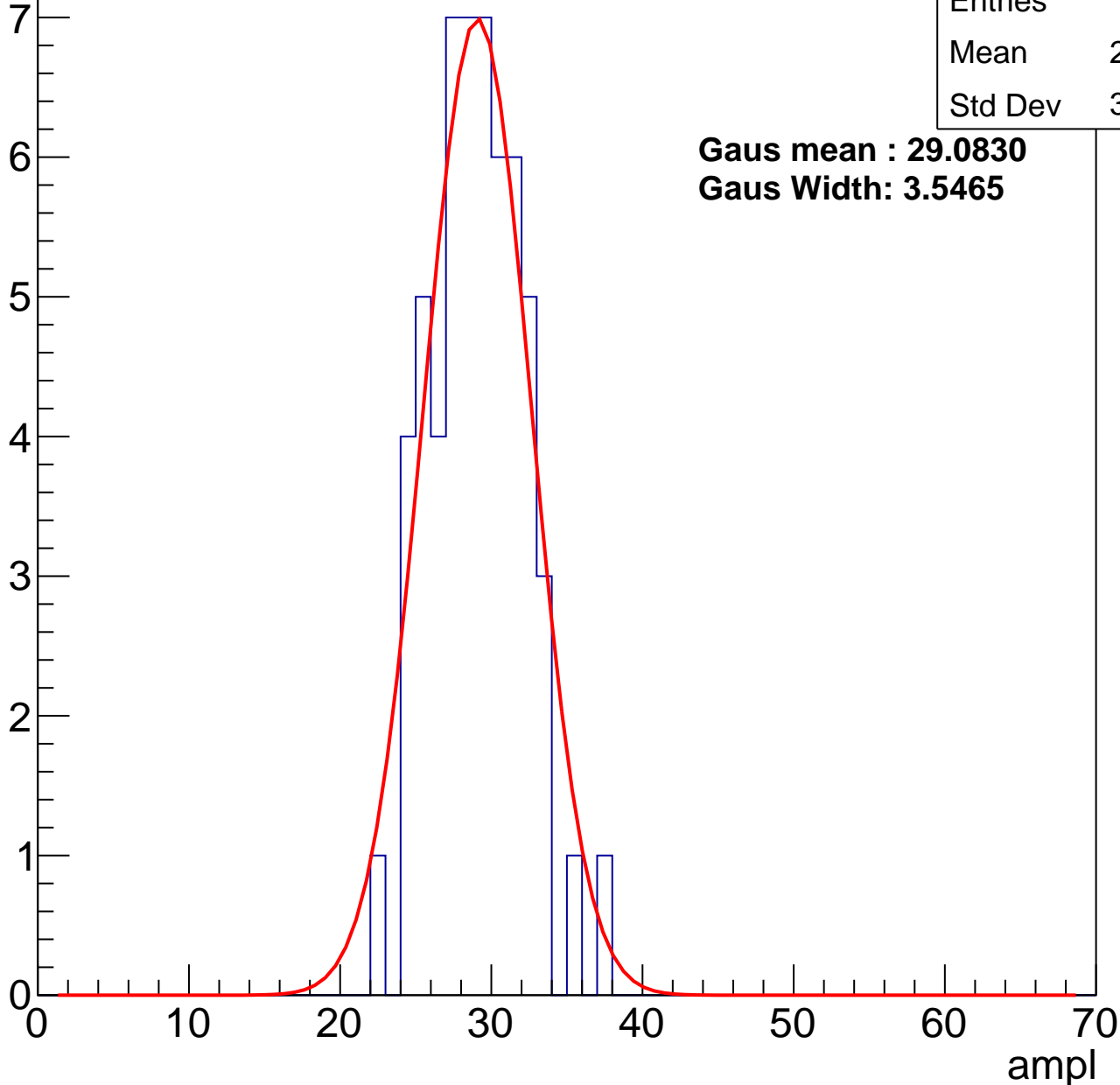
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	57
Mean	28.63
Std Dev	3.007

Gaus mean : 29.0830

Gaus Width: 3.5465



B1L100S, U1-ch107, adc1

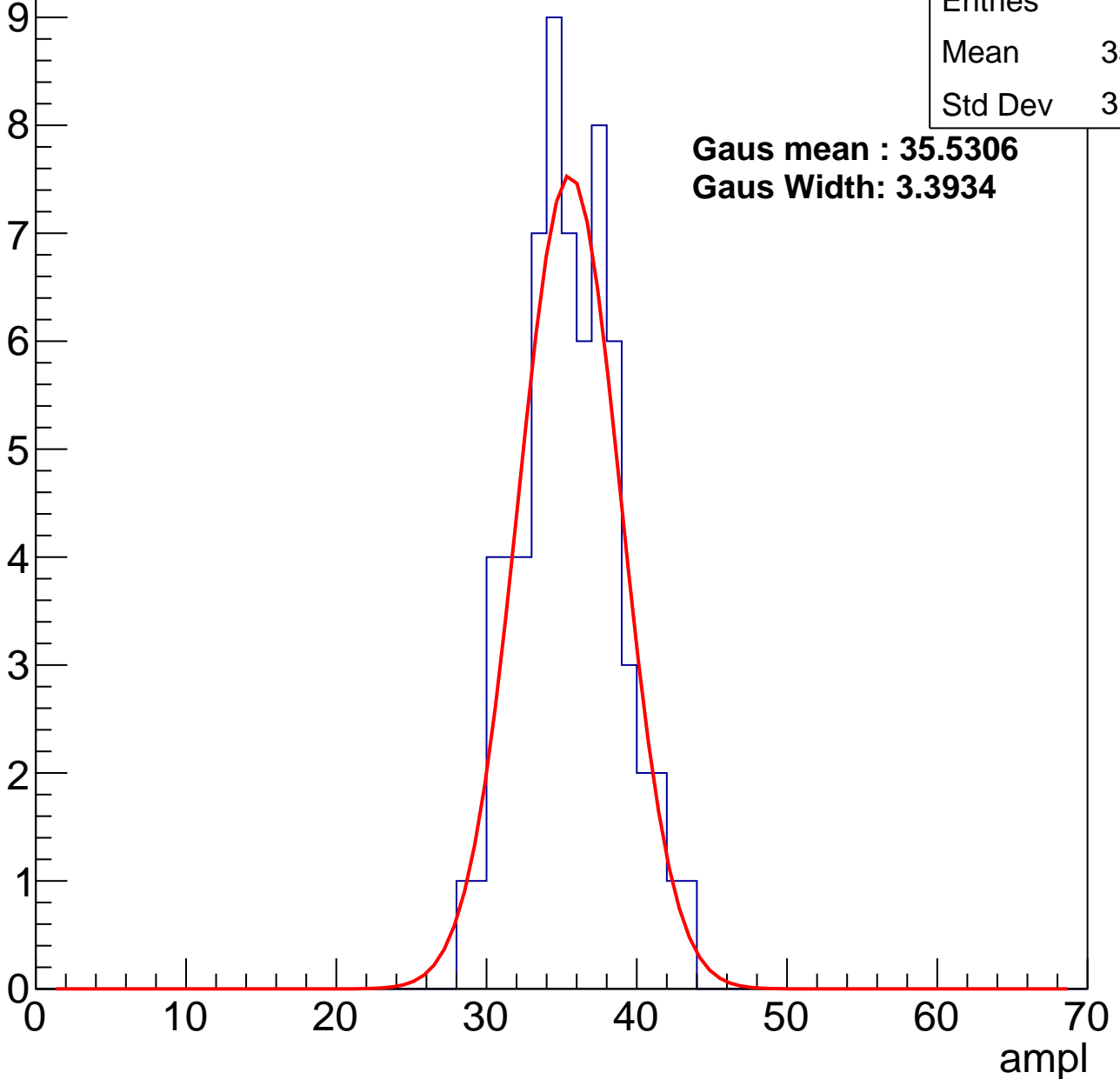
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	35.08
Std Dev	3.249

Gaus mean : 35.5306

Gaus Width: 3.3934



B1L100S, U1-ch107, adc2

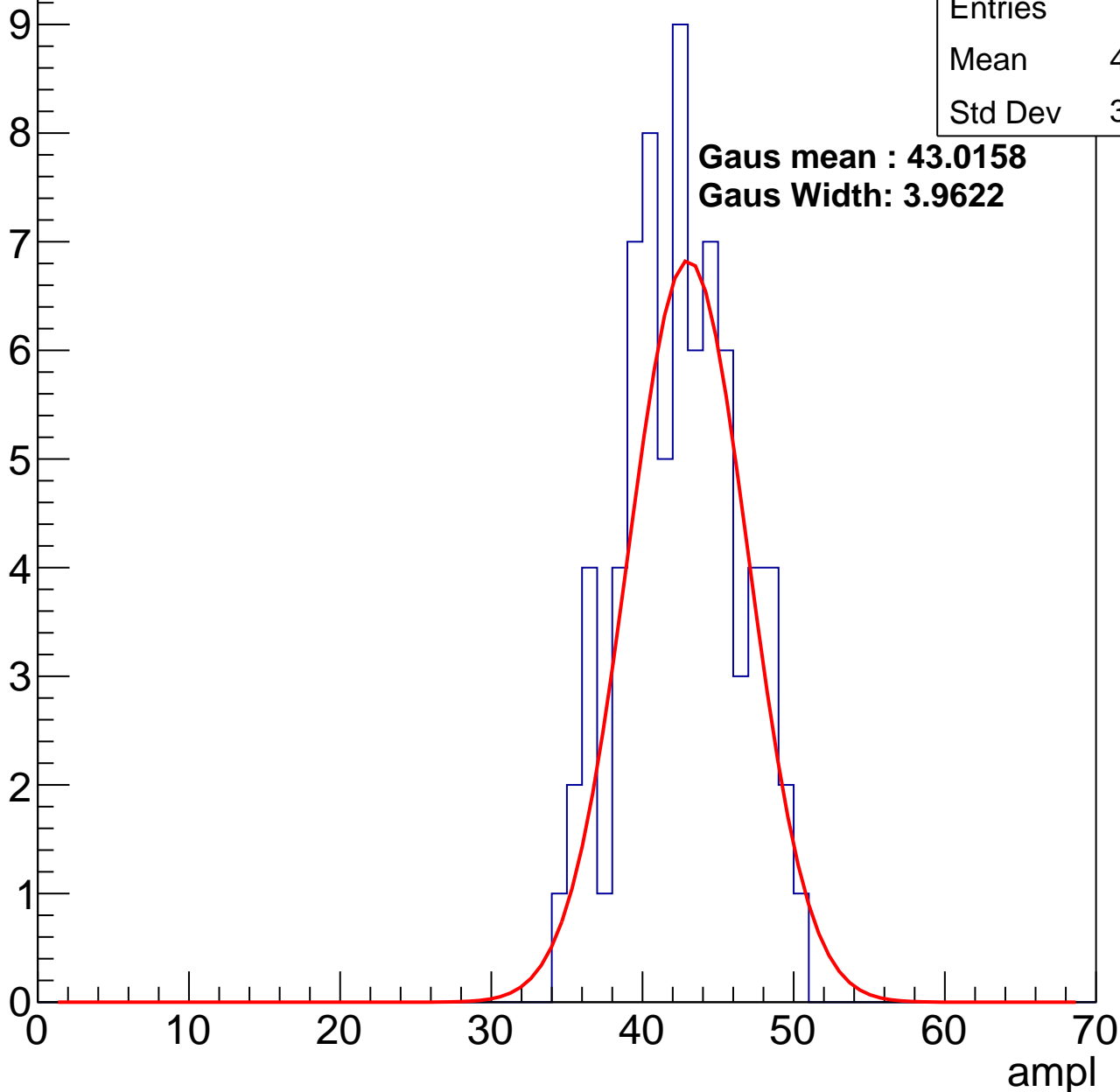
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	42.09
Std Dev	3.742

Gaus mean : 43.0158

Gaus Width: 3.9622

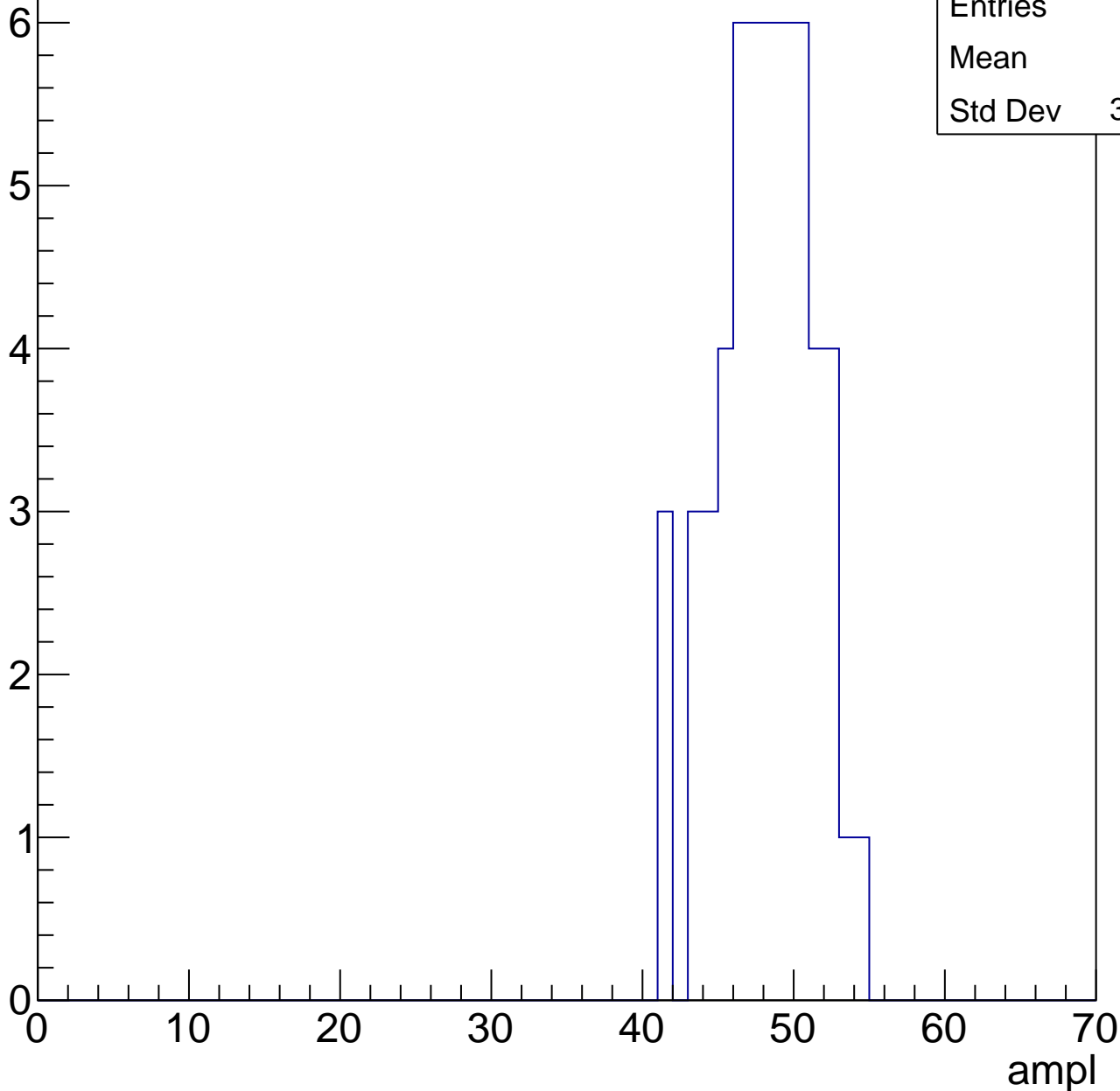


B1L100S, U1-ch107, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	47.6
Std Dev	3.128

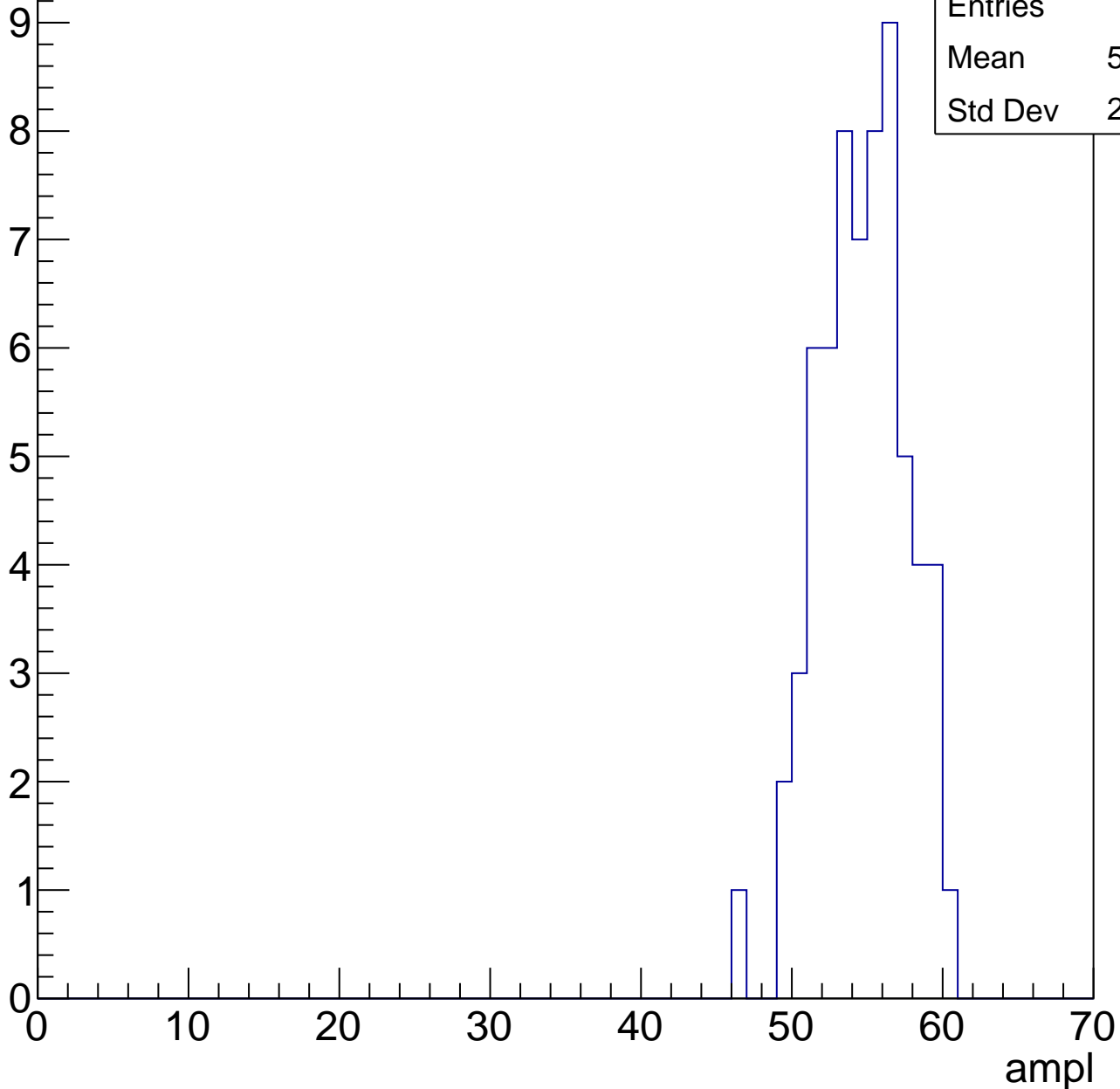


B1L100S, U1-ch107, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	54.23
Std Dev	2.887

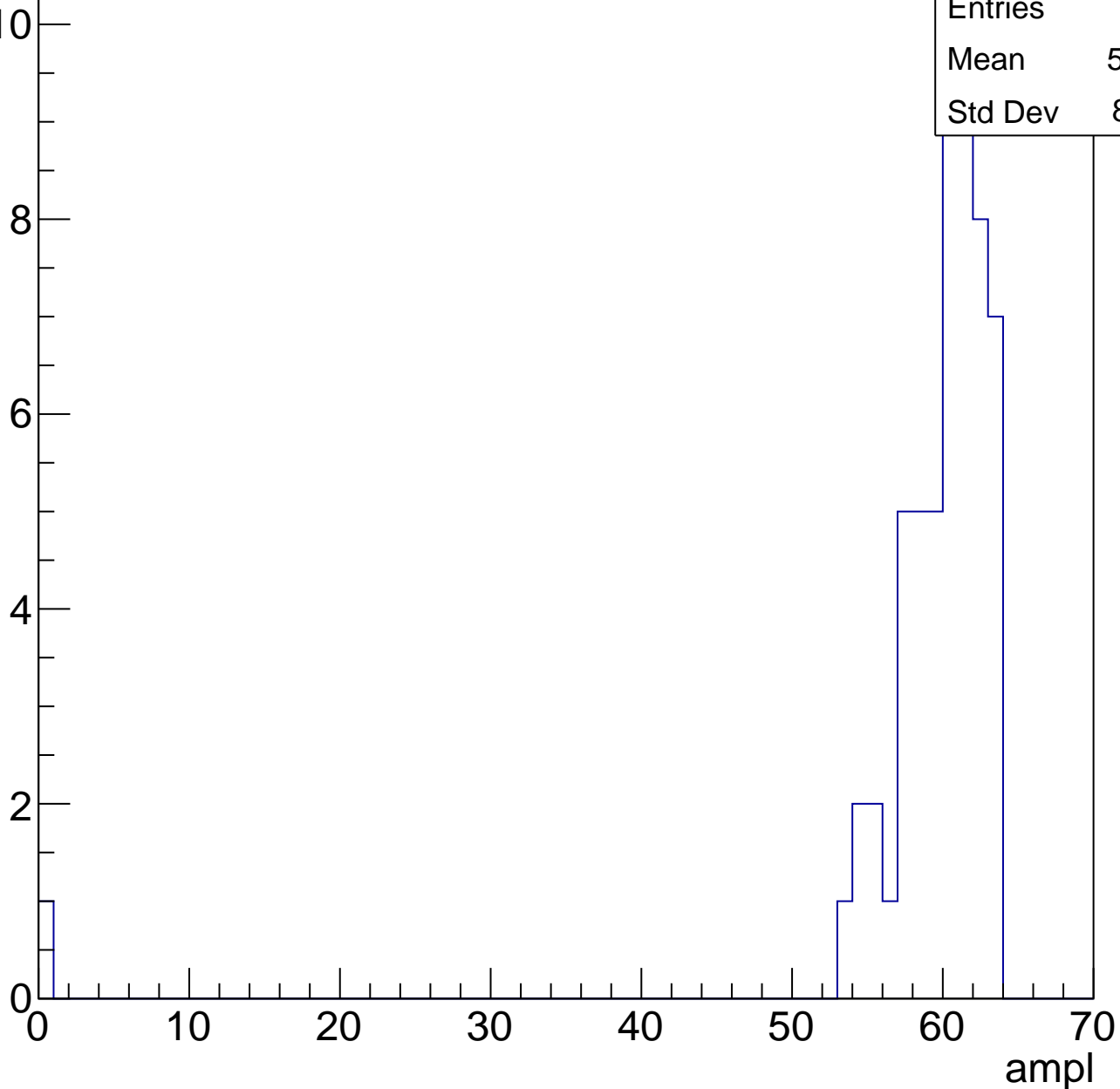


B1L100S, U1-ch107, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

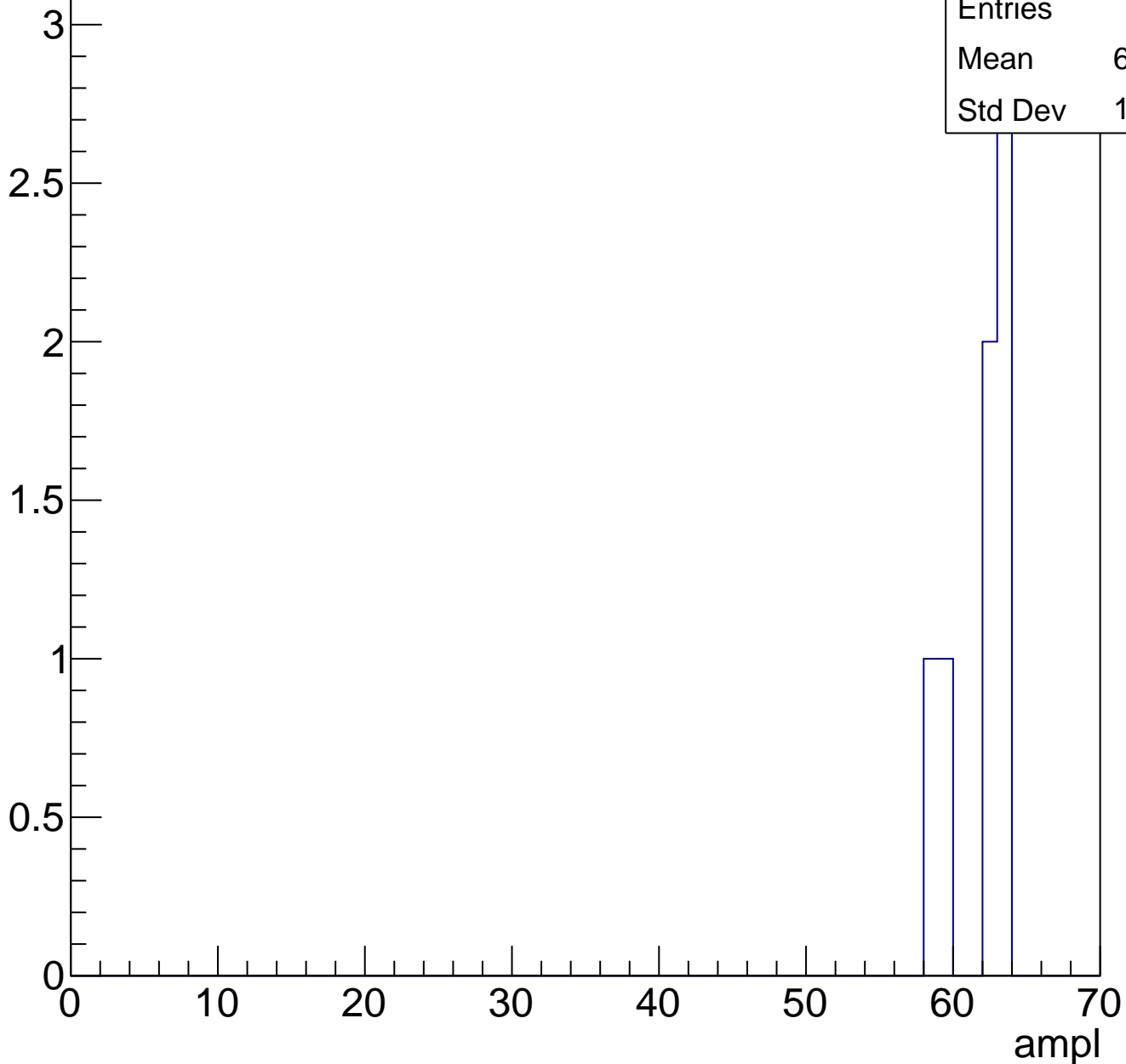
Entries	56
Mean	58.64
Std Dev	8.301



B1L100S, U1-ch107, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch107, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch108, adc0

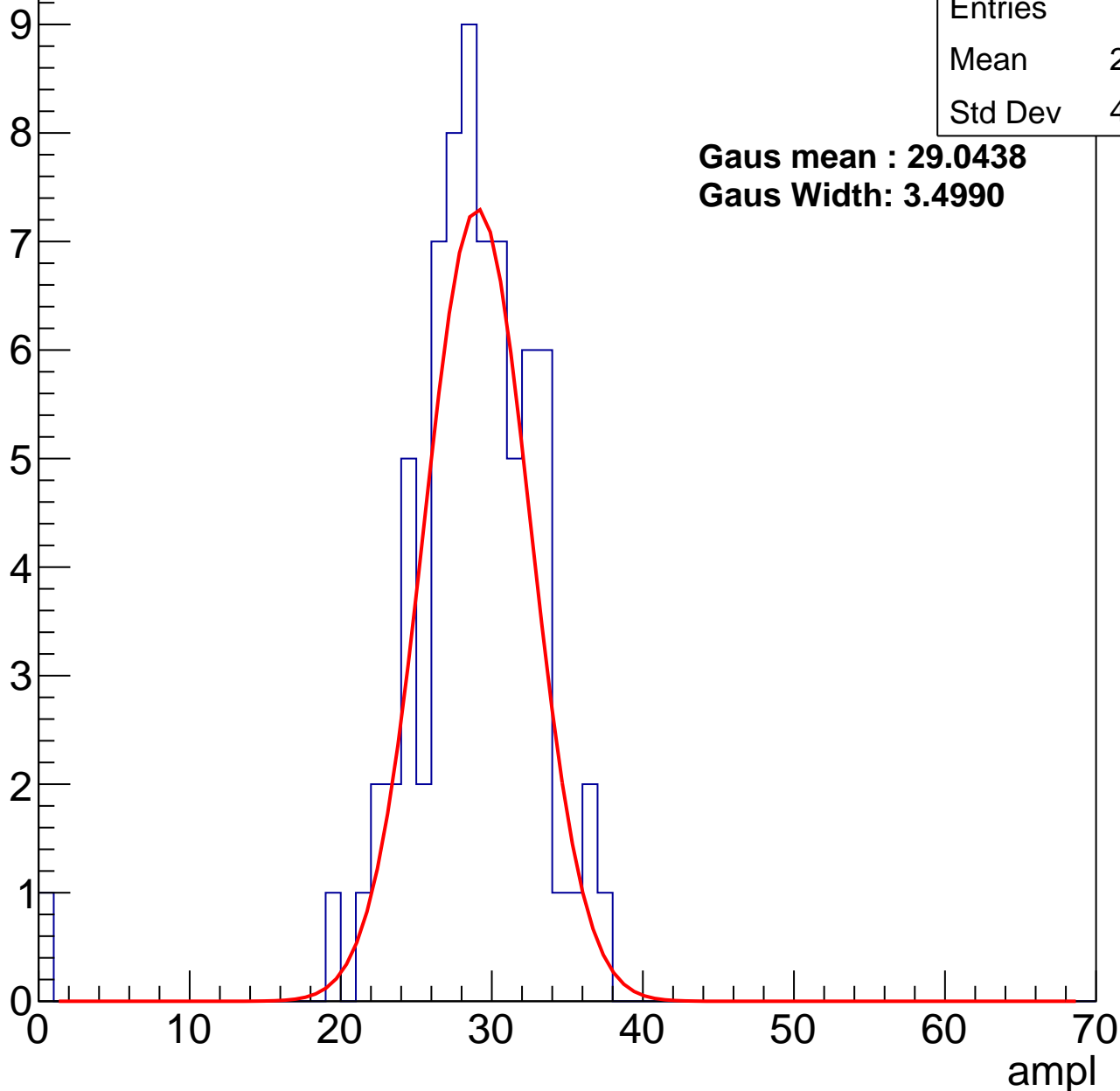
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	28.19
Std Dev	4.915

Gaus mean : 29.0438

Gaus Width: 3.4990



B1L100S, U1-ch108, adc1

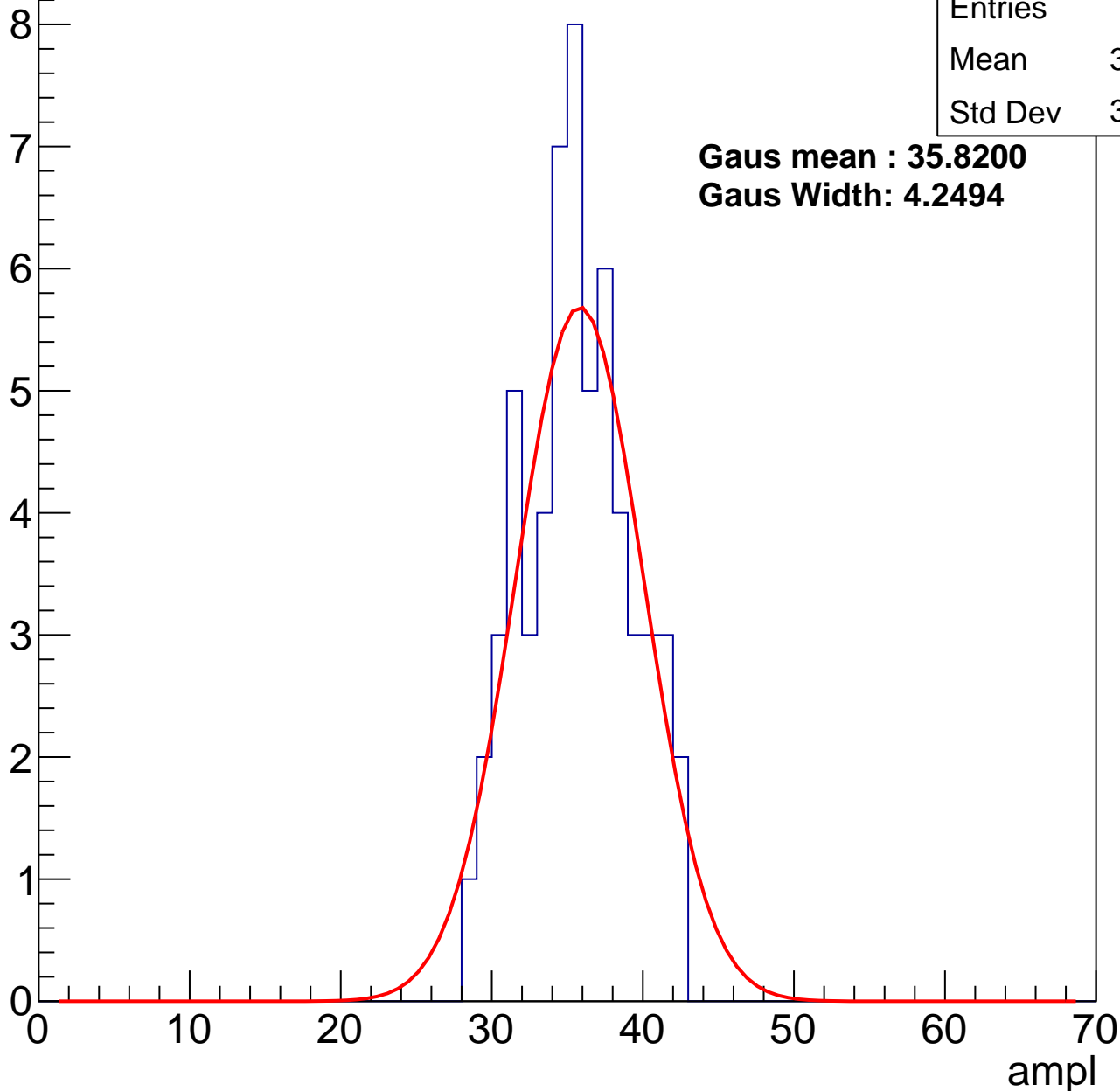
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	35.17
Std Dev	3.489

Gaus mean : 35.8200

Gaus Width: 4.2494



B1L100S, U1-ch108, adc2

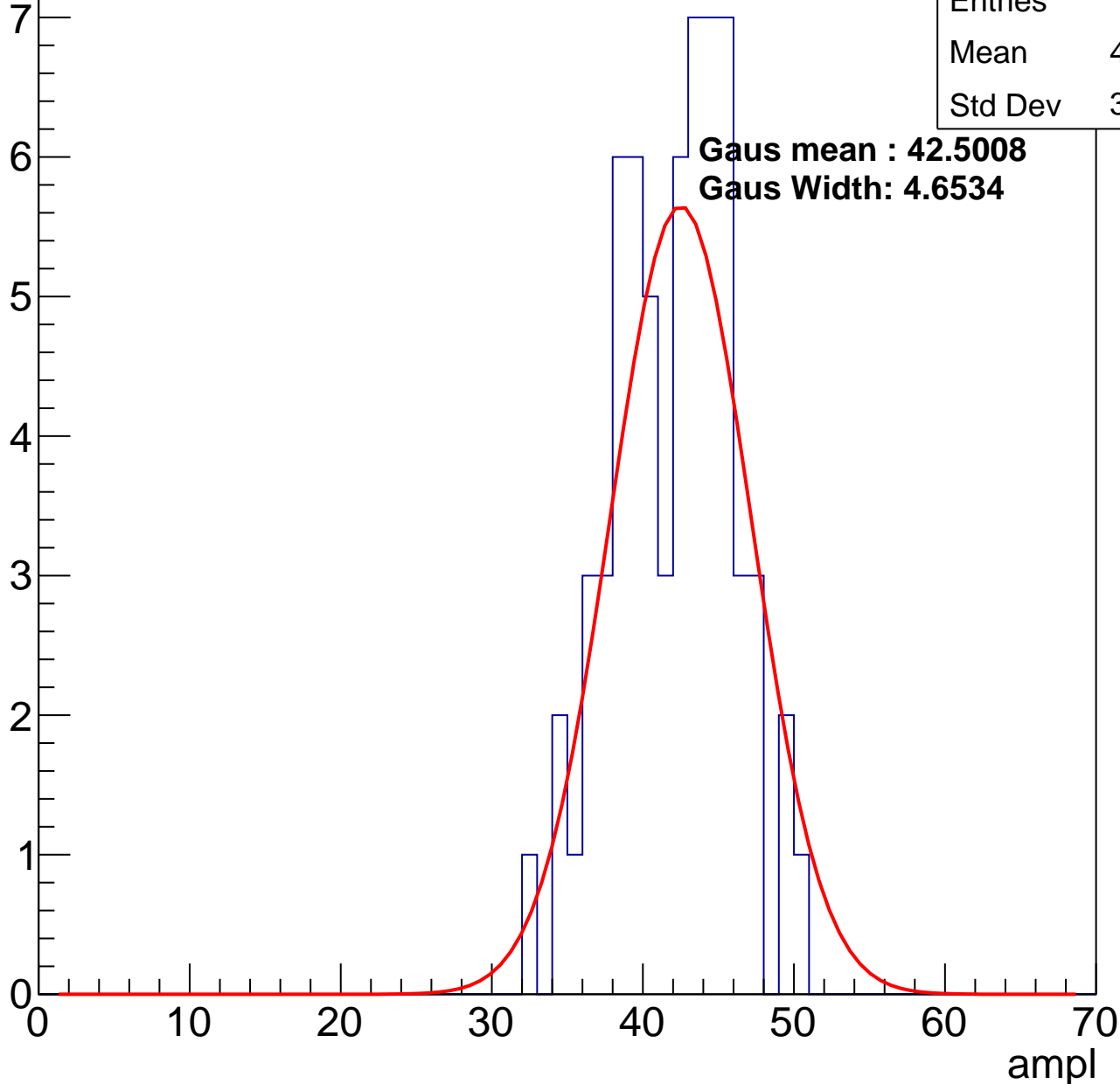
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	41.55
Std Dev	3.897

Gaus mean : 42.5008

Gaus Width: 4.6534

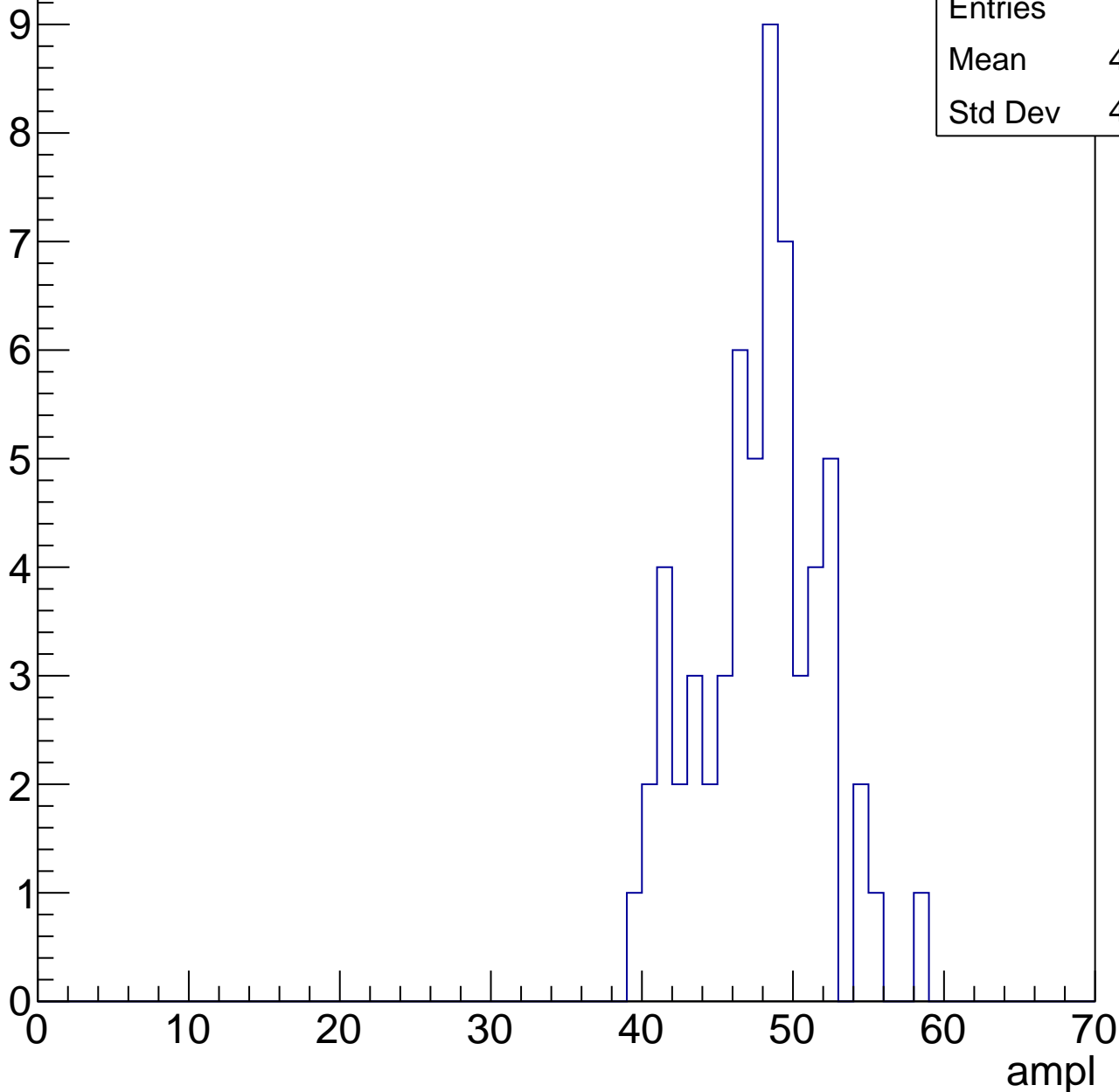


B1L100S, U1-ch108, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	47.33
Std Dev	4.019



B1L100S, U1-ch108, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	65
Mean	53.77
Std Dev	3.786

Entry

10

8

6

4

2

0

0

10

20

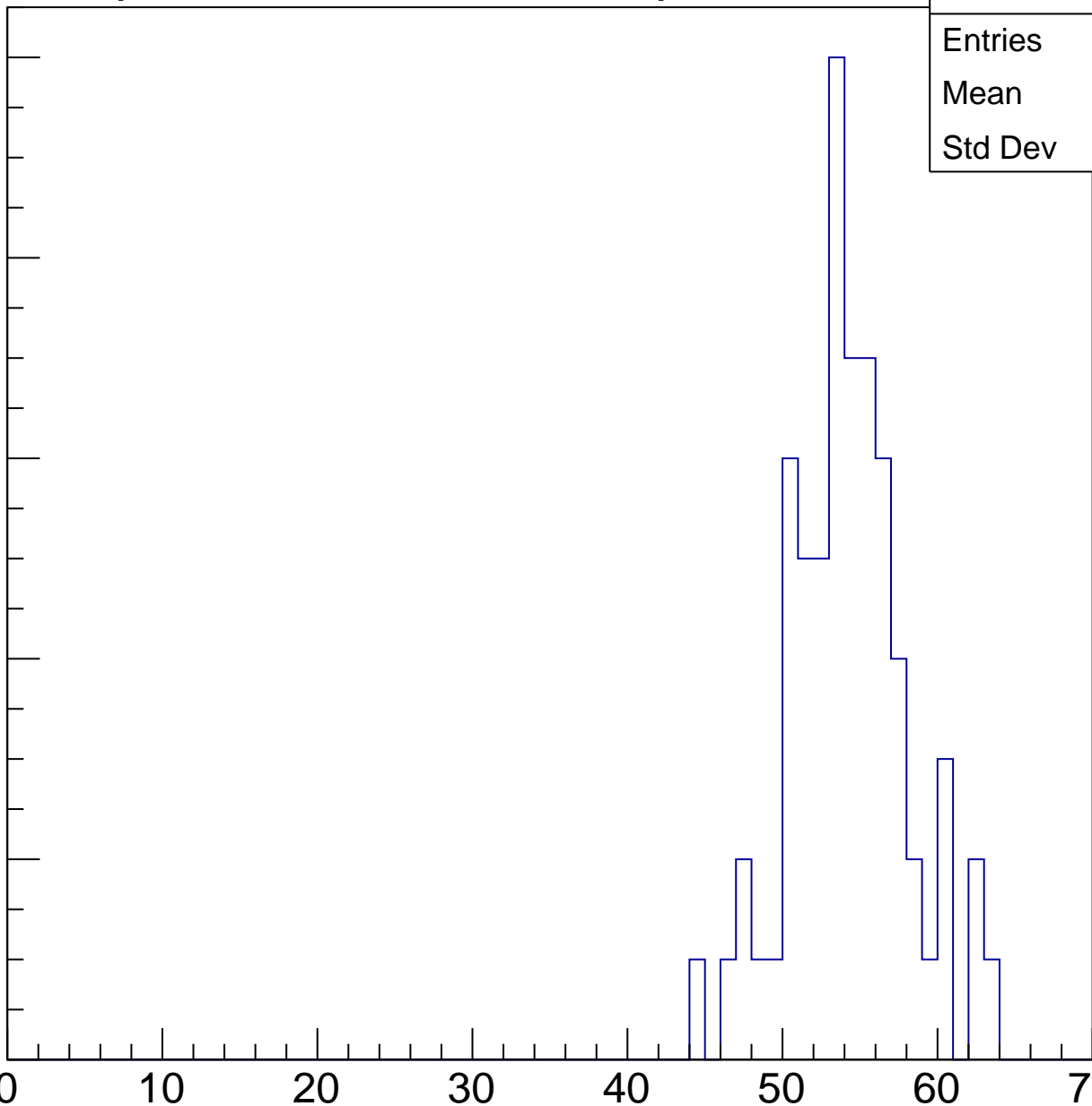
30

40

50

60

ampl

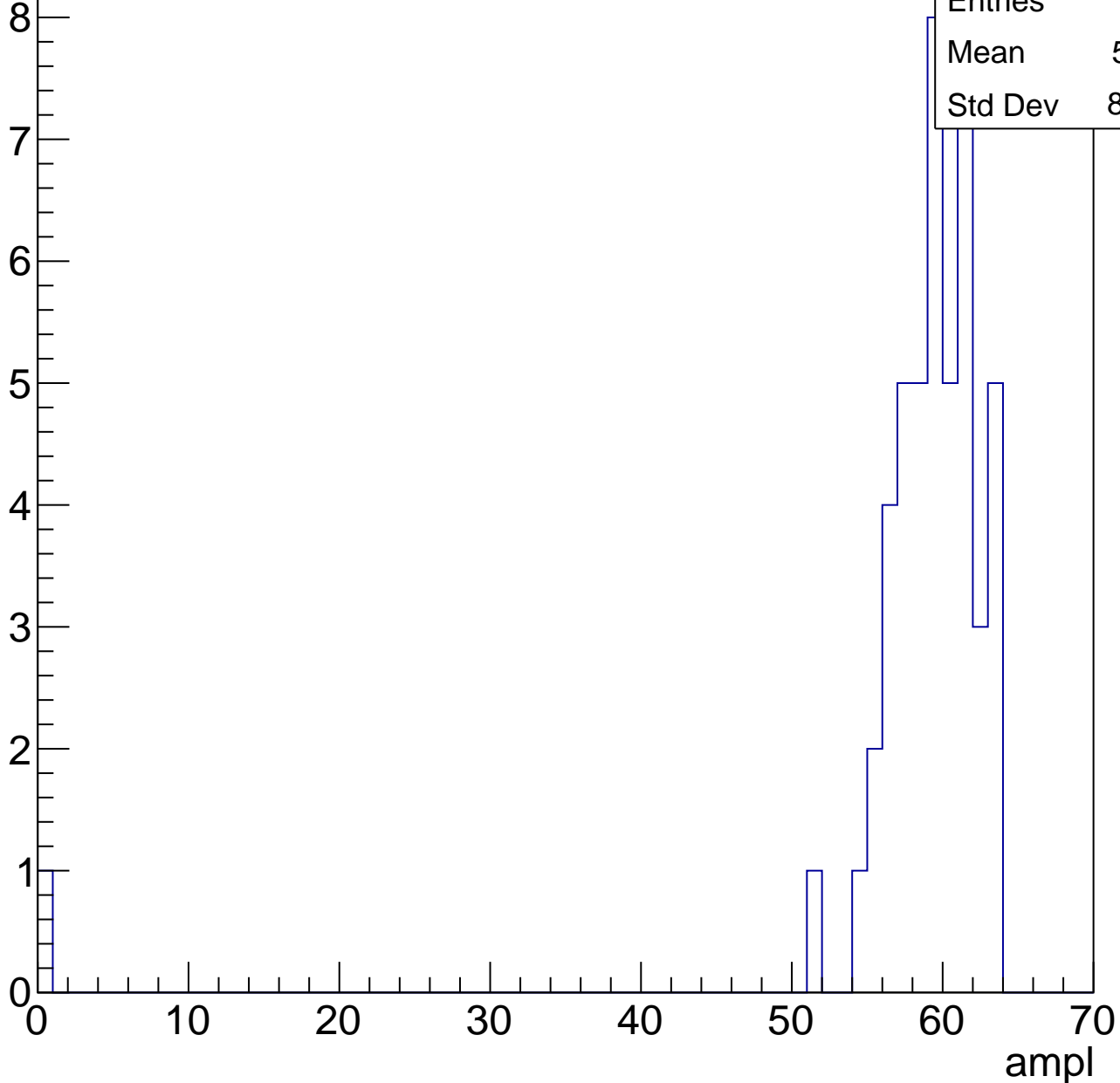


B1L100S, U1-ch108, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	57.81
Std Dev	8.826

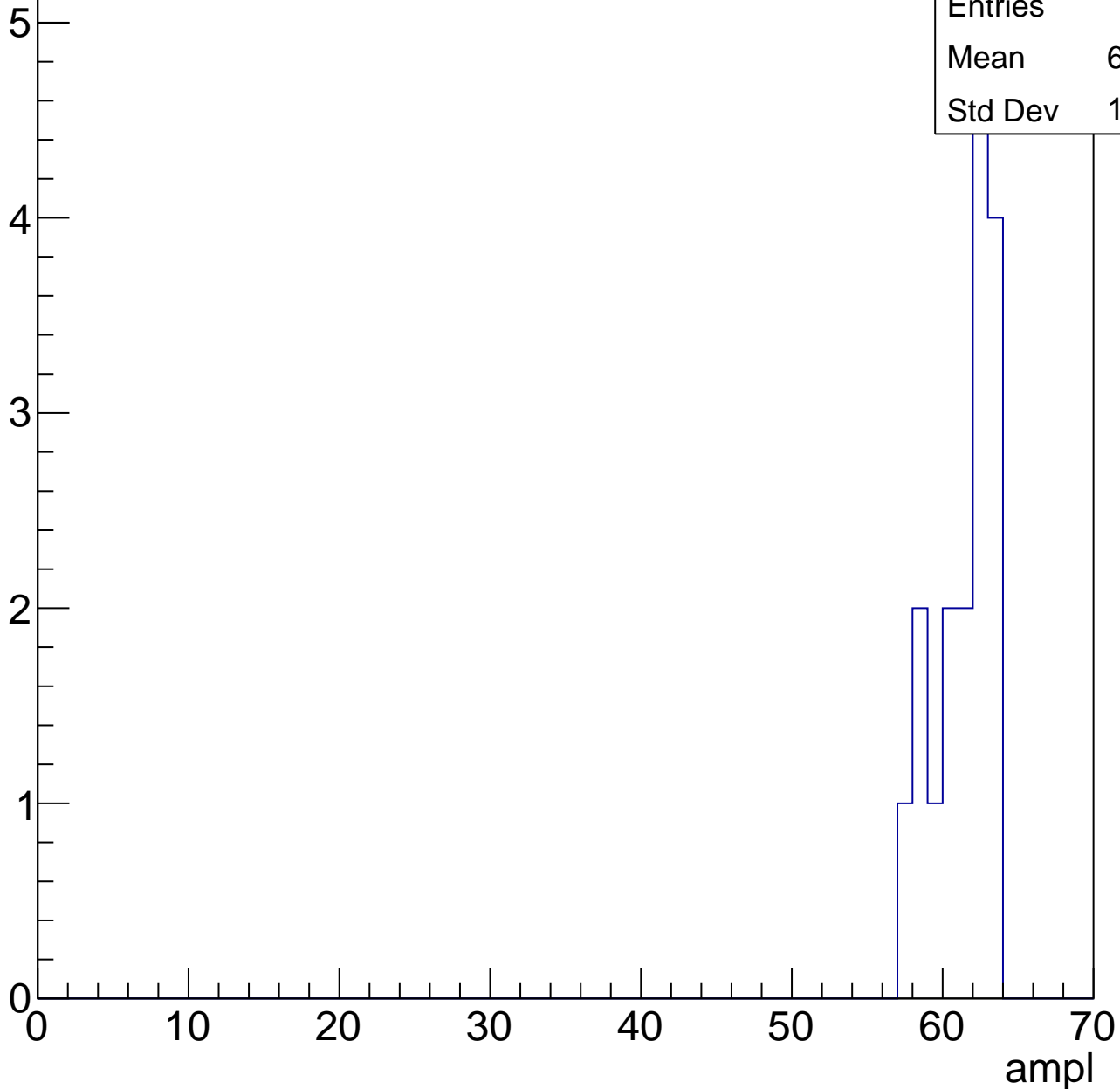


B1L100S, U1-ch108, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	60.94
Std Dev	1.893



B1L100S, U1-ch108, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch109, adc0

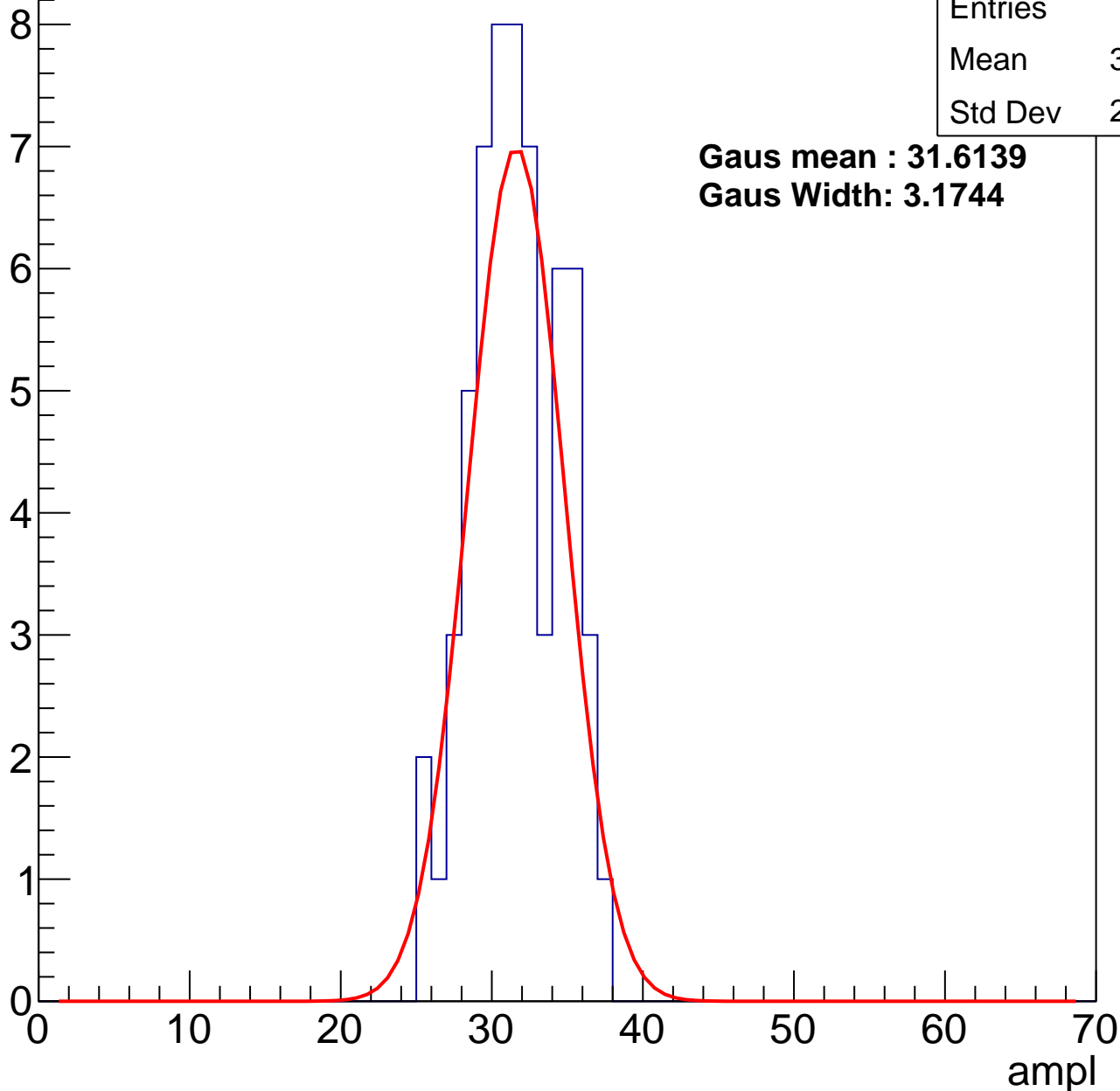
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	31.17
Std Dev	2.899

Gaus mean : 31.6139

Gaus Width: 3.1744



B1L100S, U1-ch109, adc1

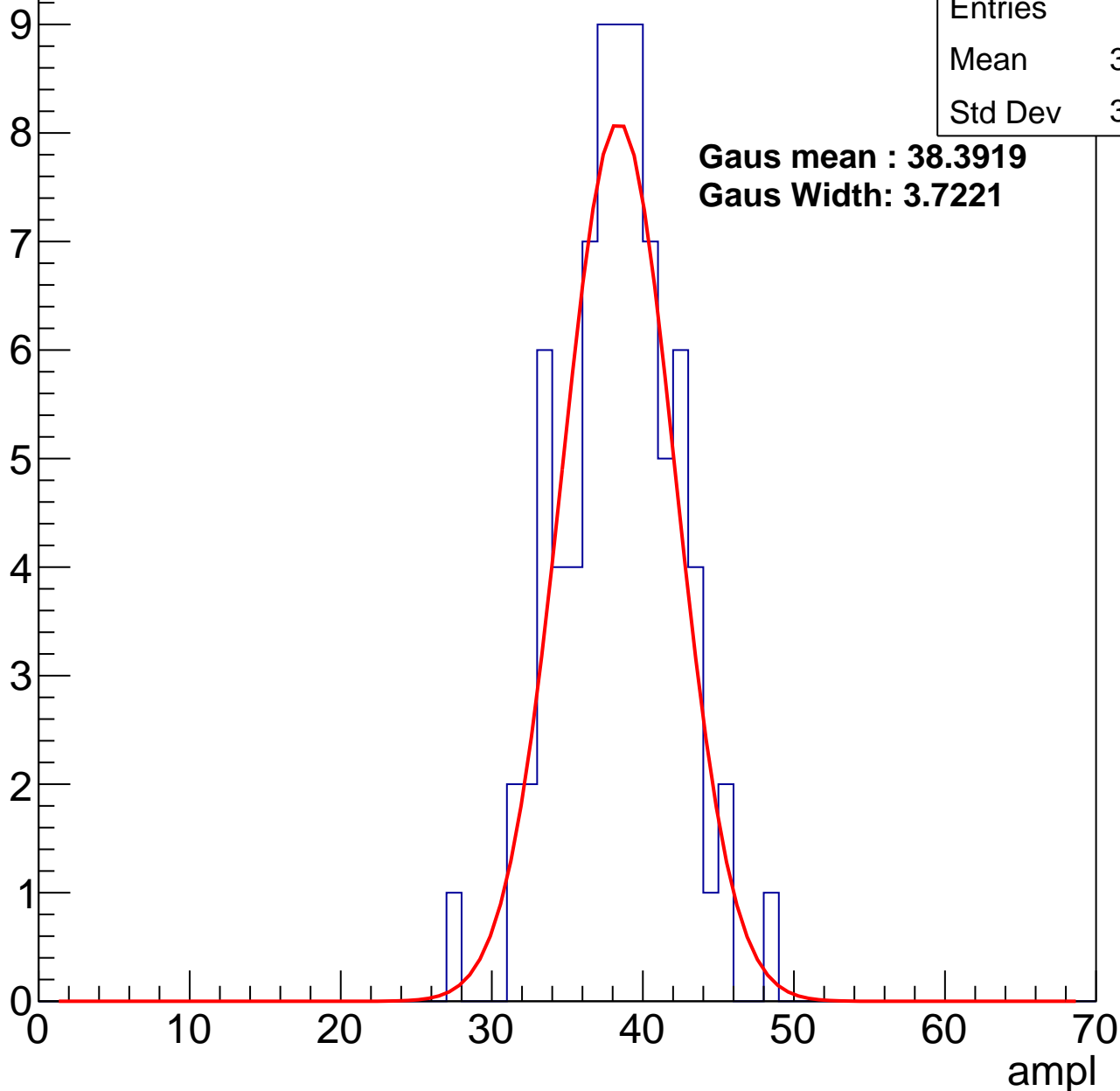
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	37.92
Std Dev	3.714

Gaus mean : 38.3919

Gaus Width: 3.7221



B1L100S, U1-ch109, adc2

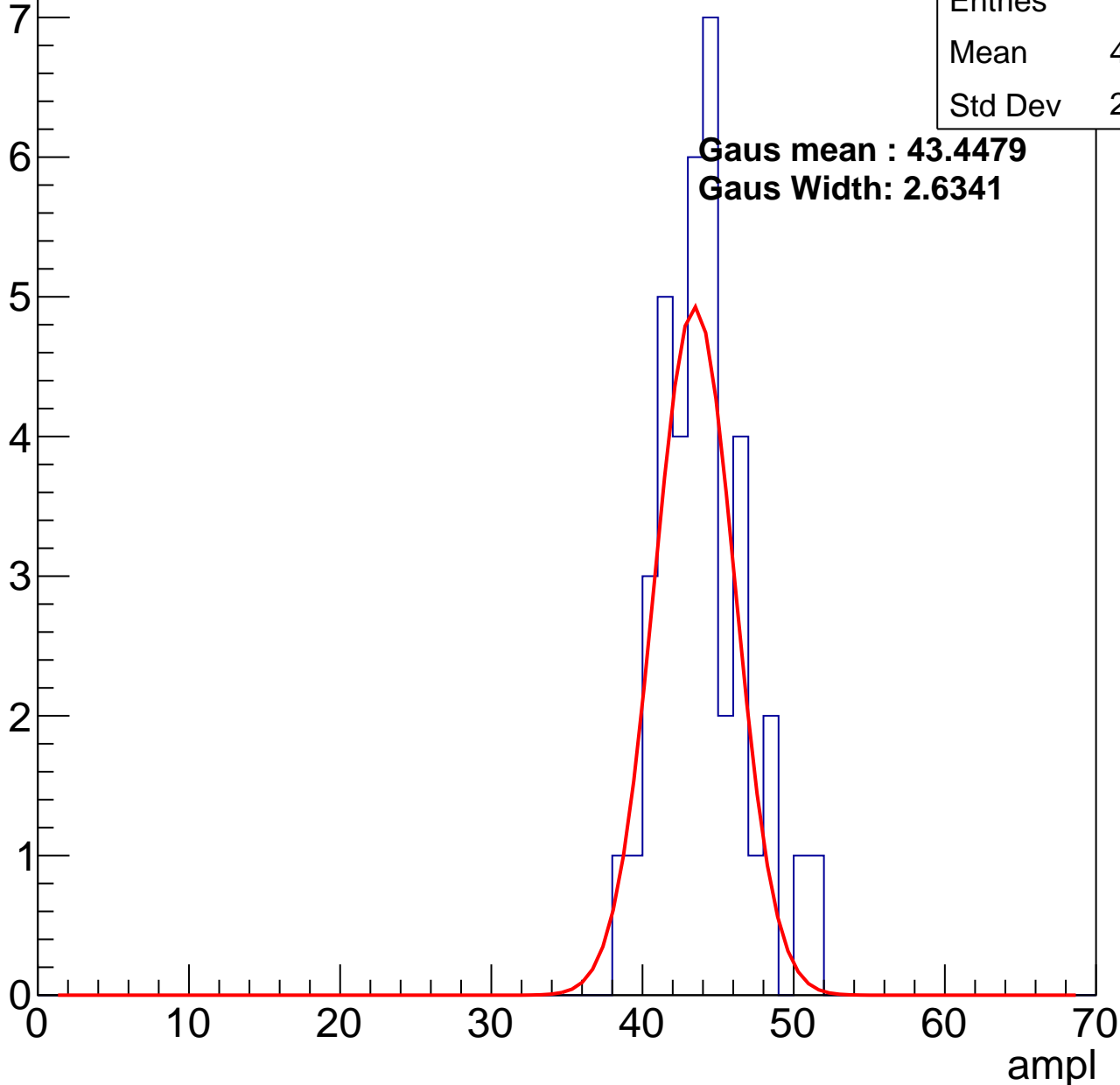
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	43.53
Std Dev	2.863

Gaus mean : 43.4479

Gaus Width: 2.6341



B1L100S, U1-ch109, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	84
Mean	50.13
Std Dev	3.501

Entry

10

8

6

4

2

0

0

10

20

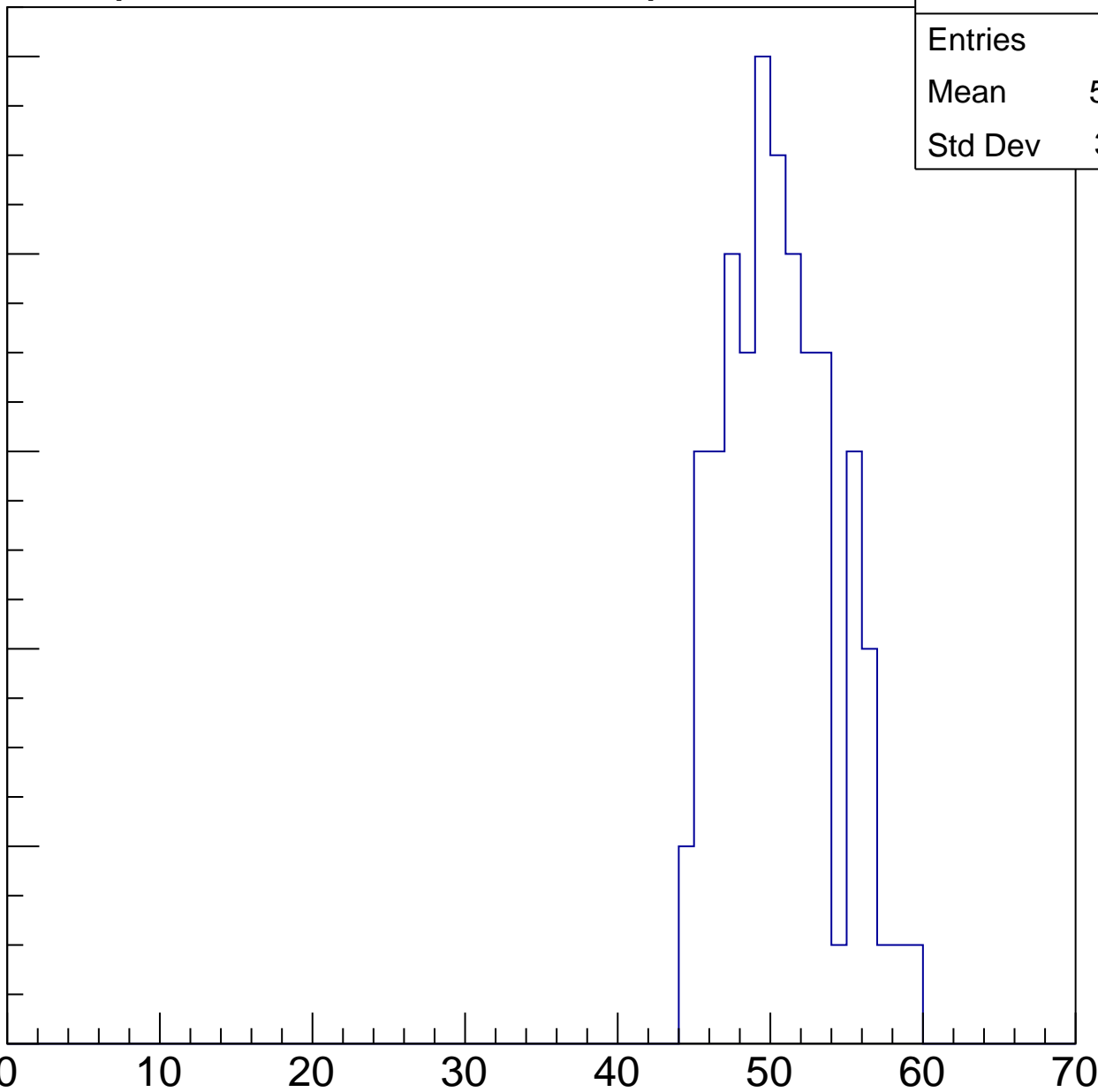
30

40

50

60

ampl

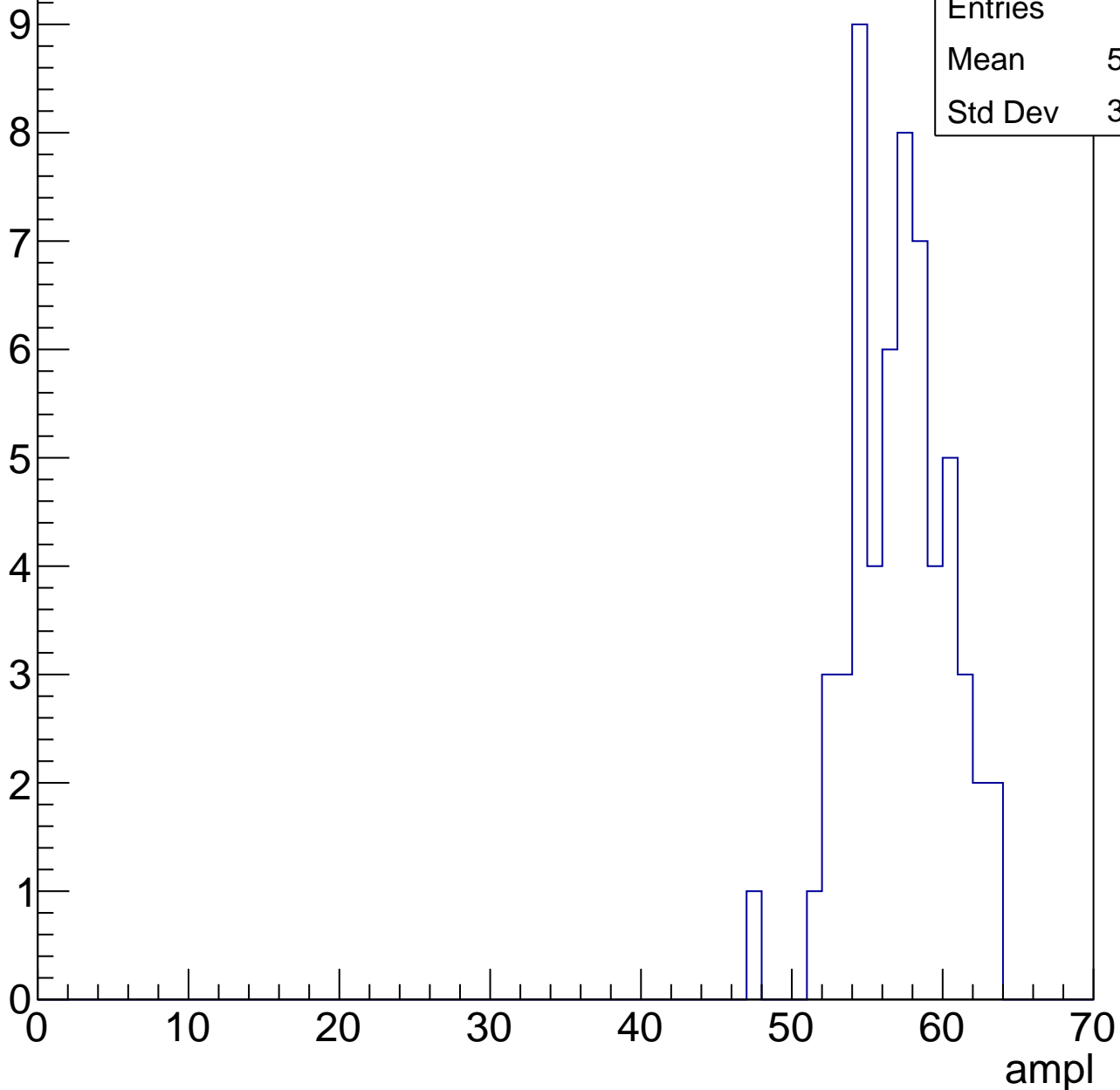


B1L100S, U1-ch109, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	56.66
Std Dev	3.198

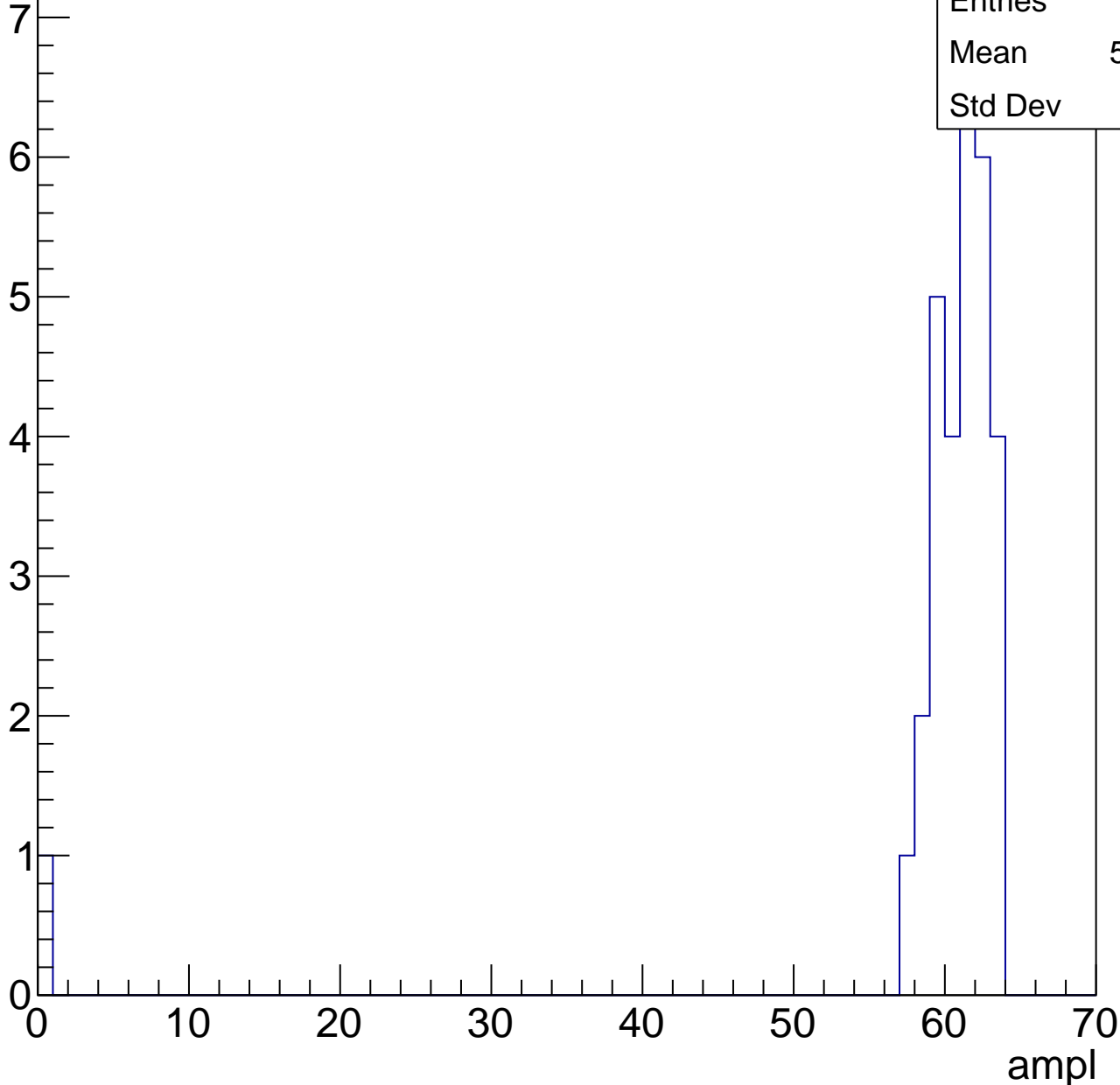


B1L100S, U1-ch109, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	30
Mean	58.63
Std Dev	11



B1L100S, U1-ch109, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch109, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch110, adc0

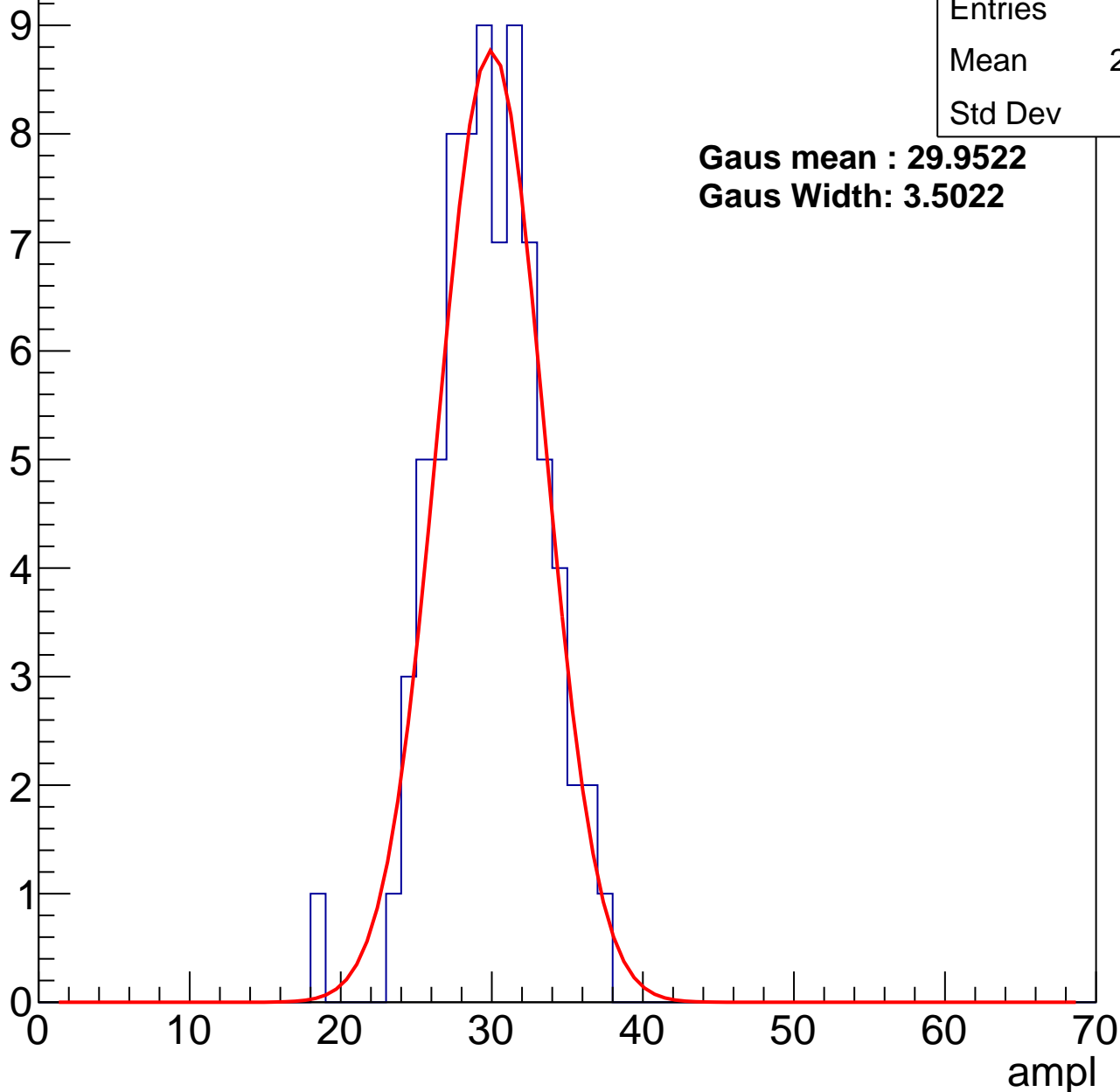
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	77
Mean	29.38
Std Dev	3.43

Gaus mean : 29.9522

Gaus Width: 3.5022



B1L100S, U1-ch110, adc1

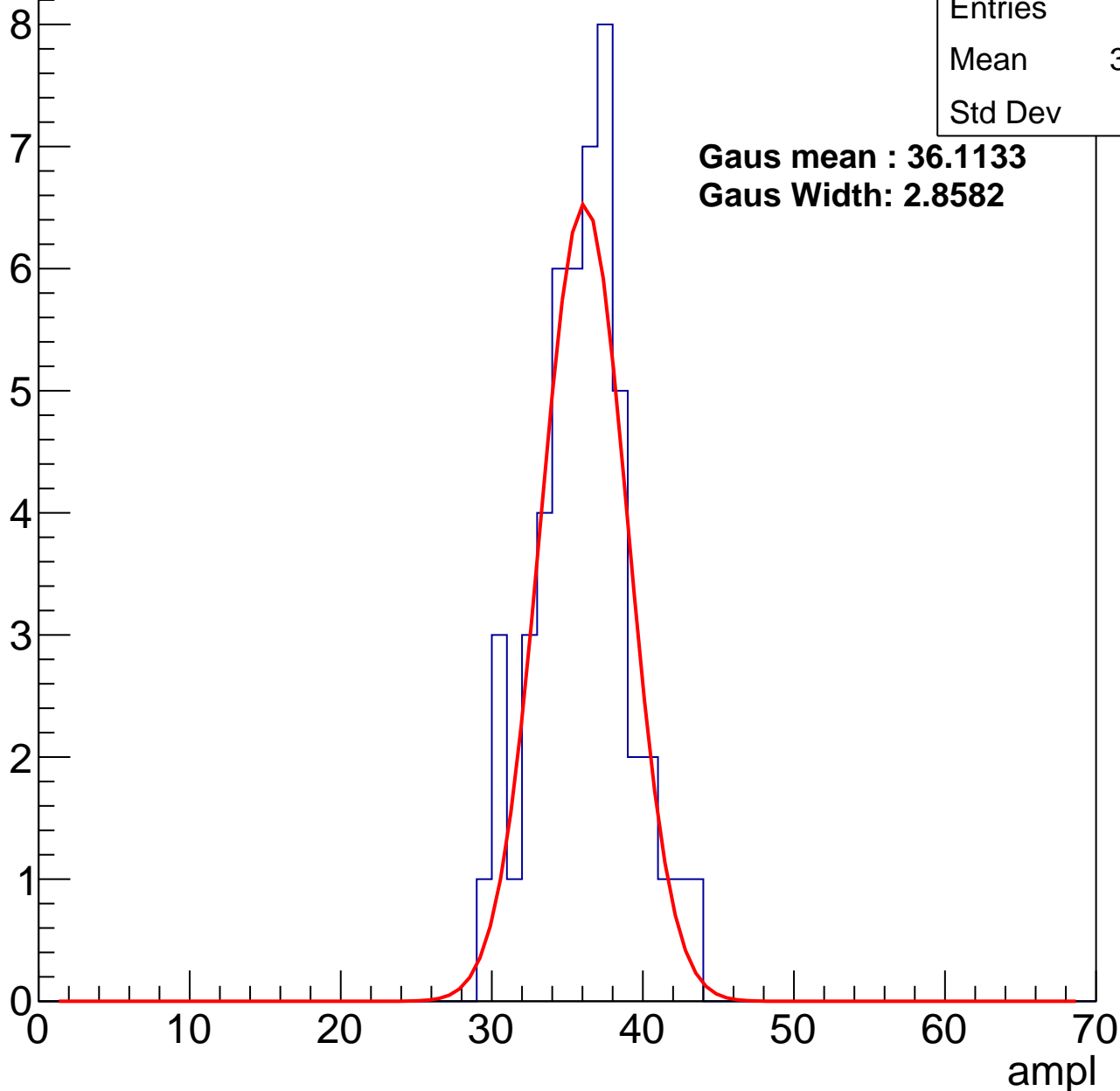
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	35.57
Std Dev	3.05

Gaus mean : 36.1133

Gaus Width: 2.8582



B1L100S, U1-ch110, adc2

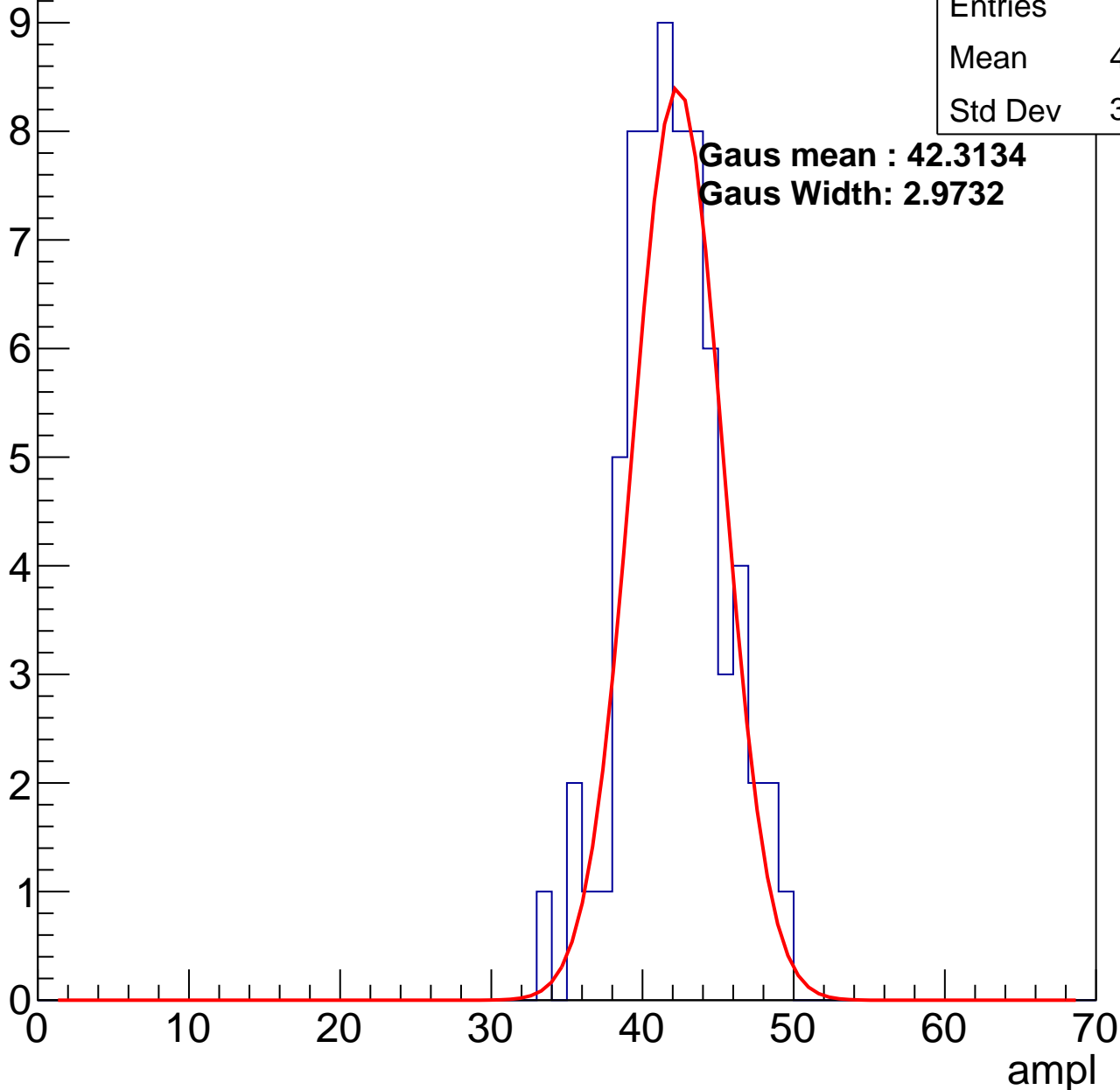
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	41.58
Std Dev	3.219

Gaus mean : 42.3134

Gaus Width: 2.9732

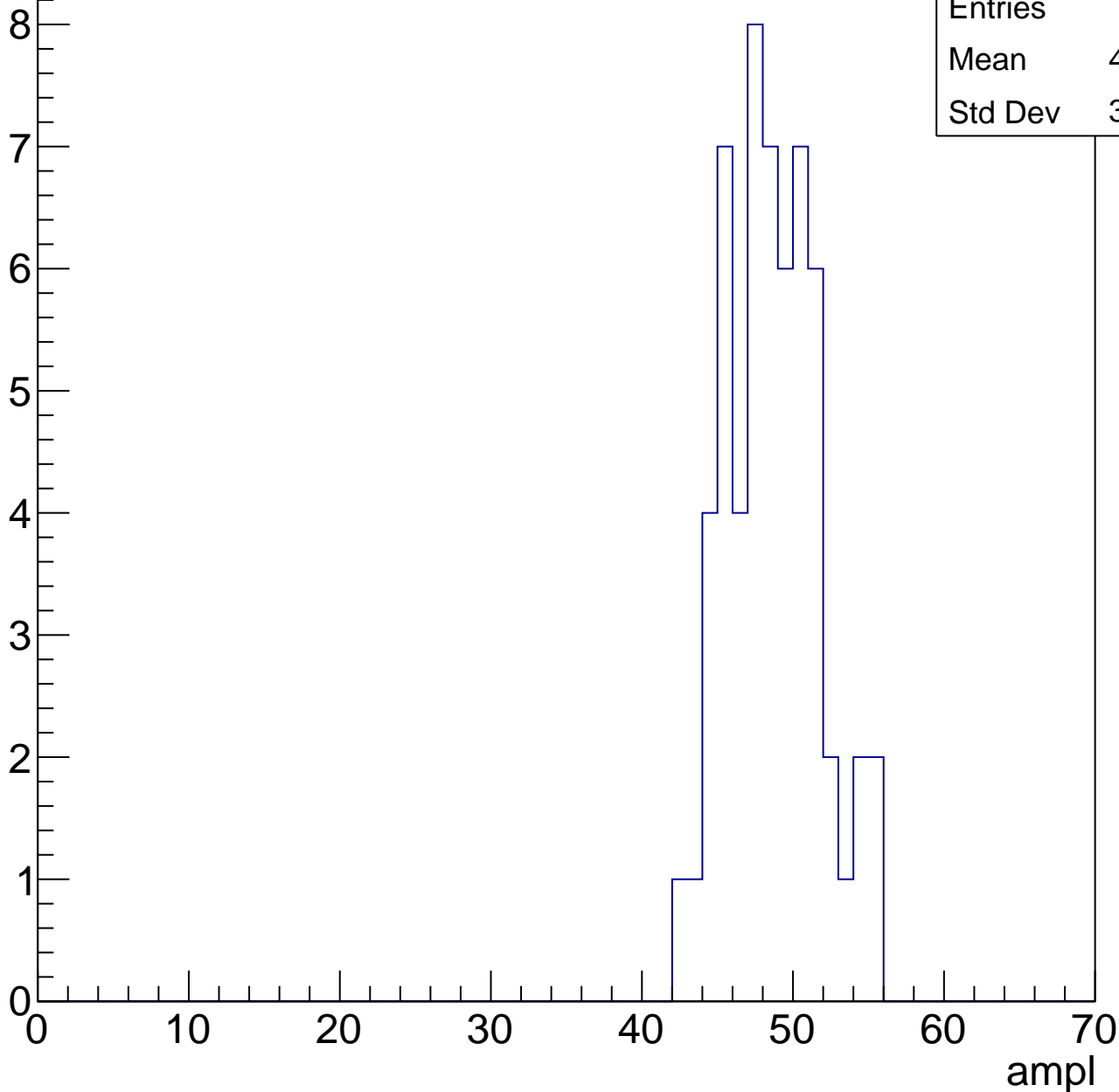


B1L100S, U1-ch110, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	48.22
Std Dev	3.006

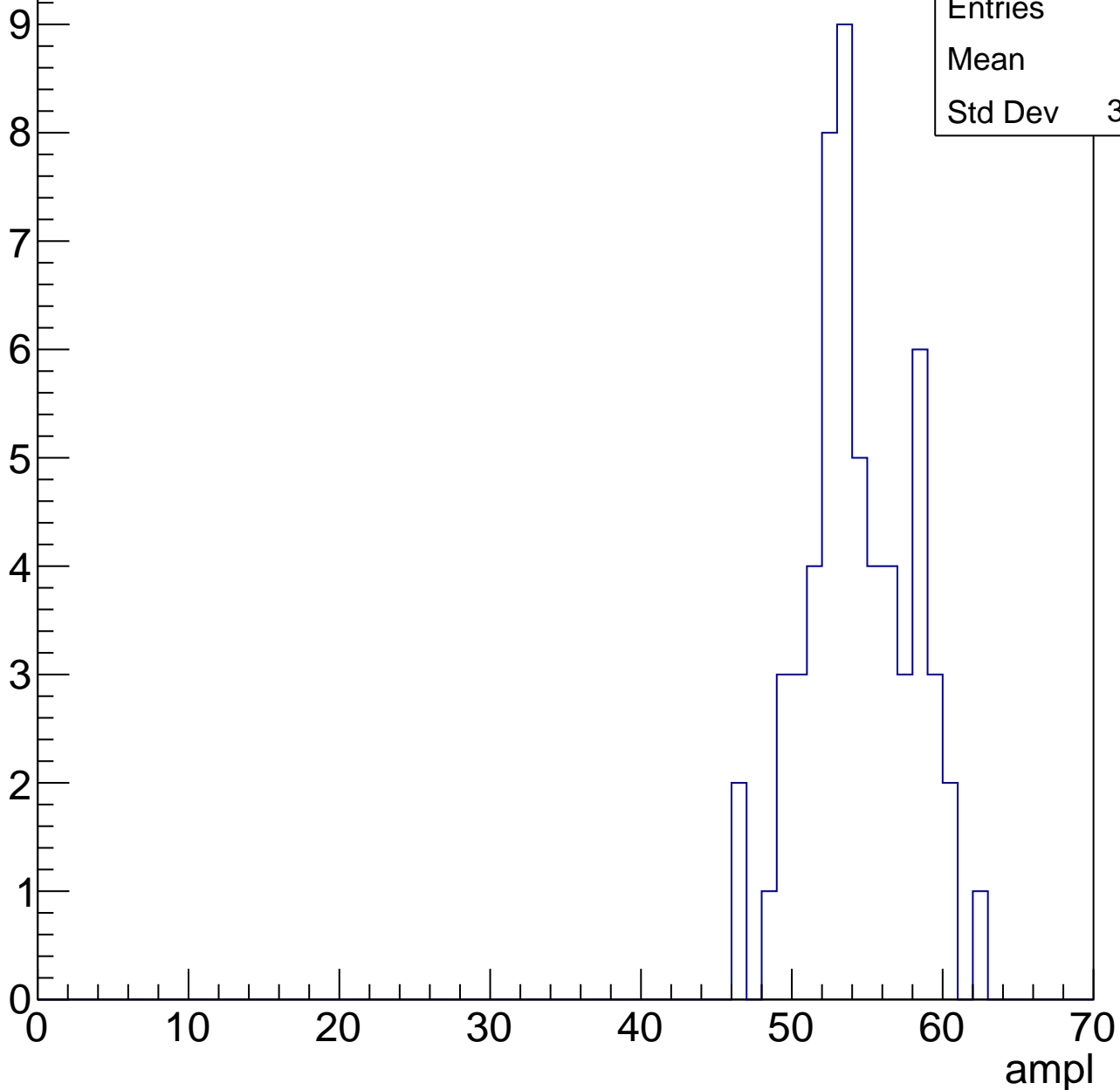


B1L100S, U1-ch110, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	58
Mean	53.9
Std Dev	3.517

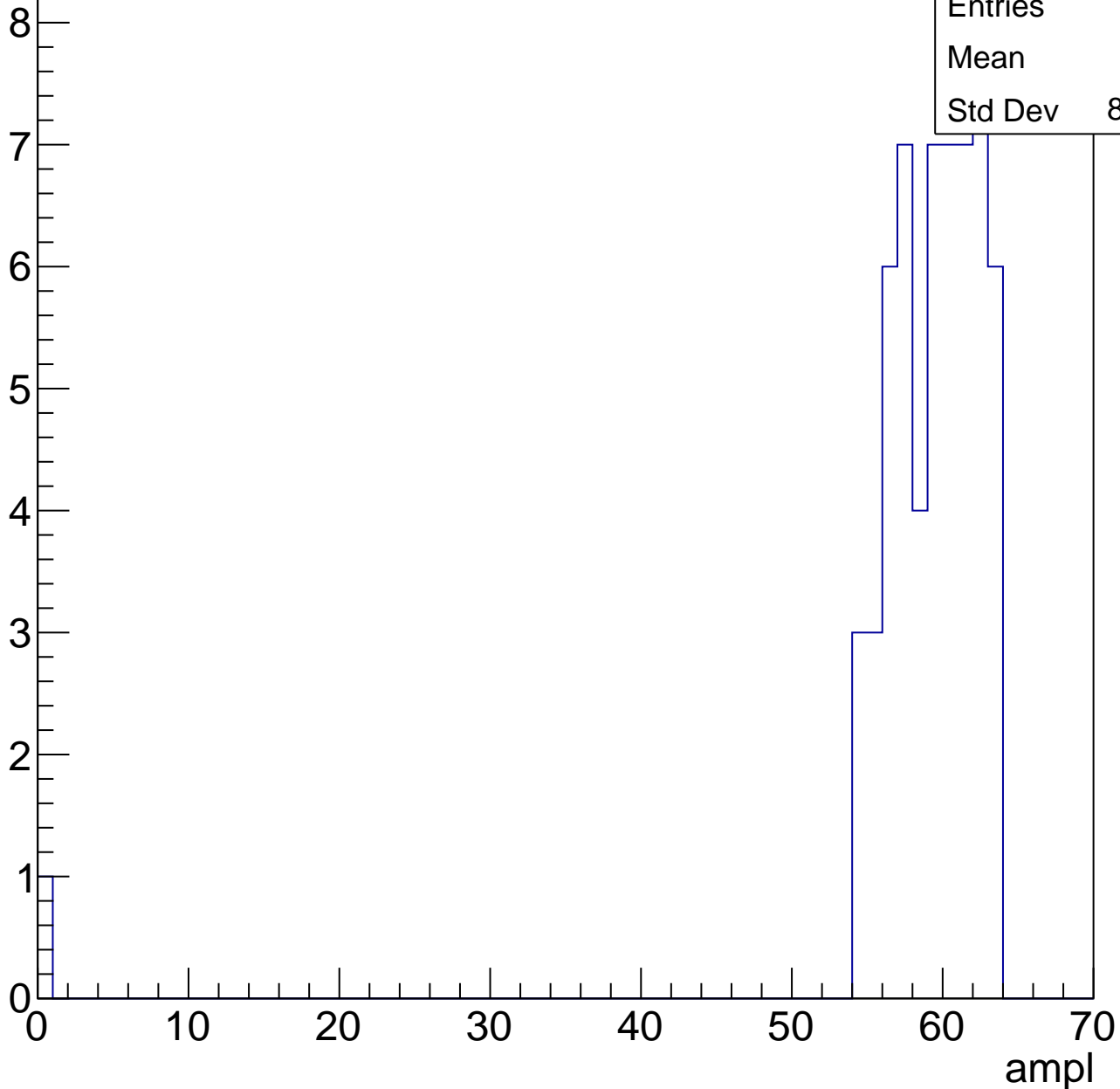


B1L100S, U1-ch110, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

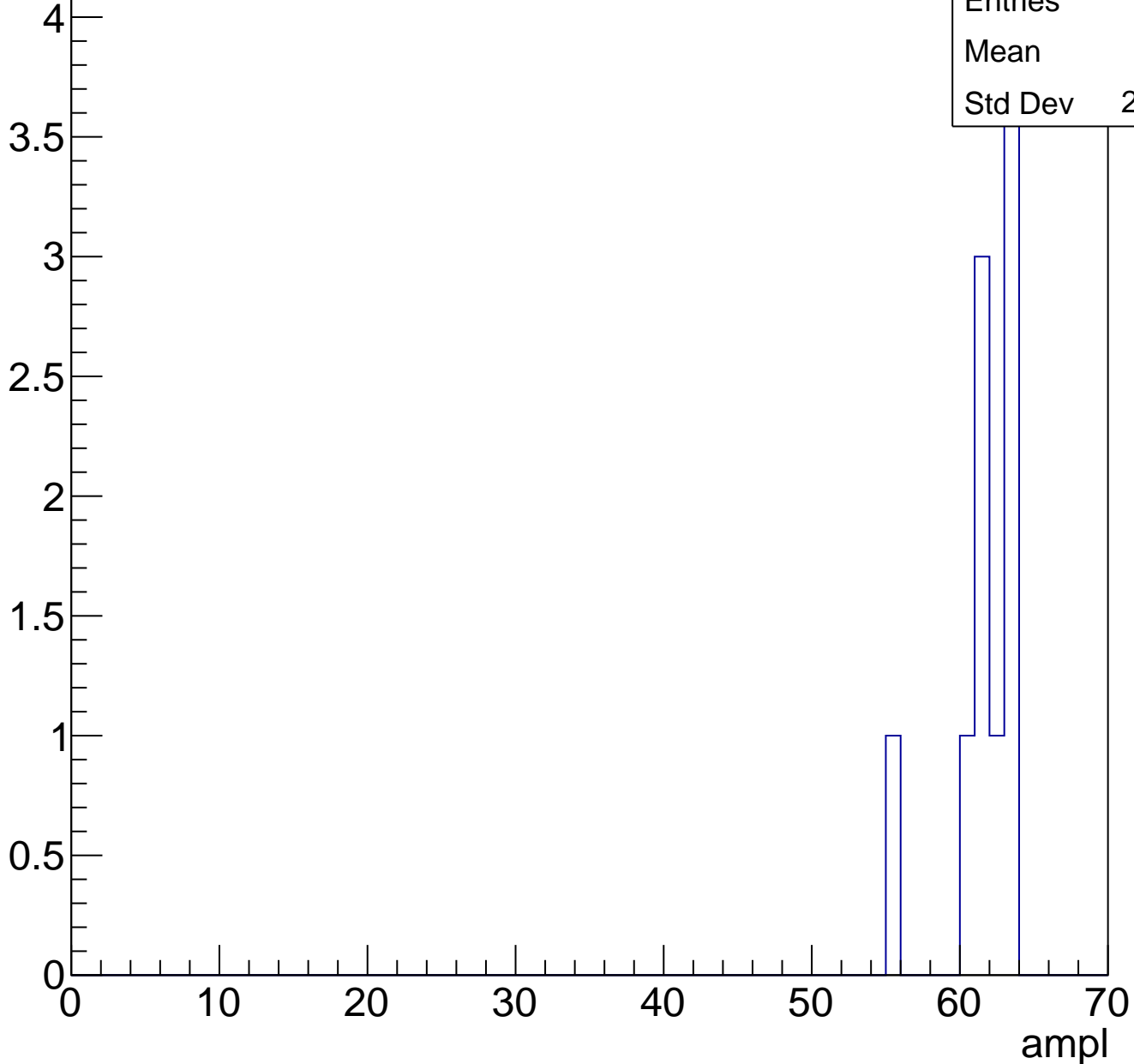
Entries	59
Mean	58.1
Std Dev	8.073



B1L100S, U1-ch110, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch110, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch111, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	88
Mean	29.16
Std Dev	3.726

Gaus mean : 29.6731

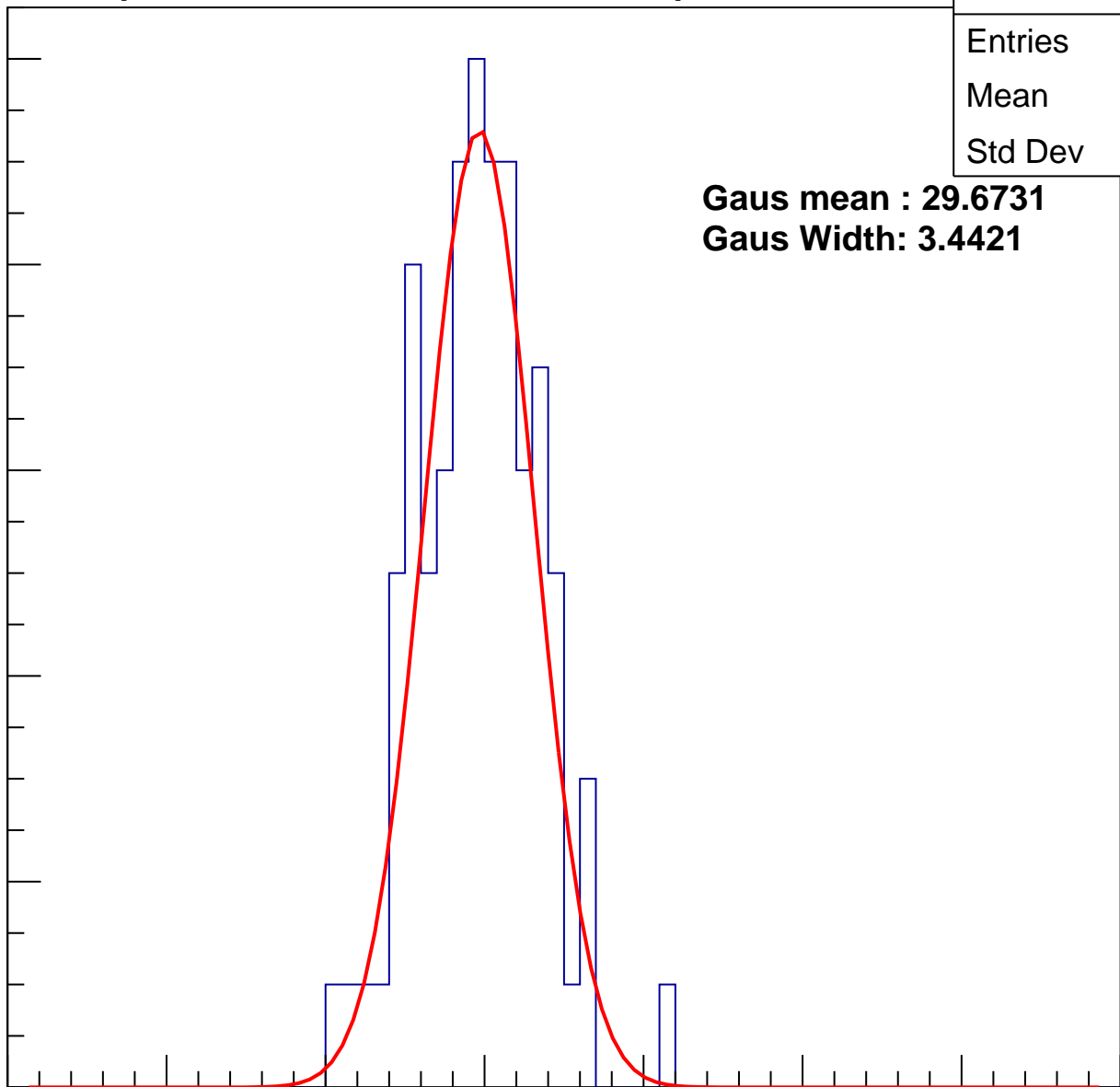
Gaus Width: 3.4421

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch111, adc1

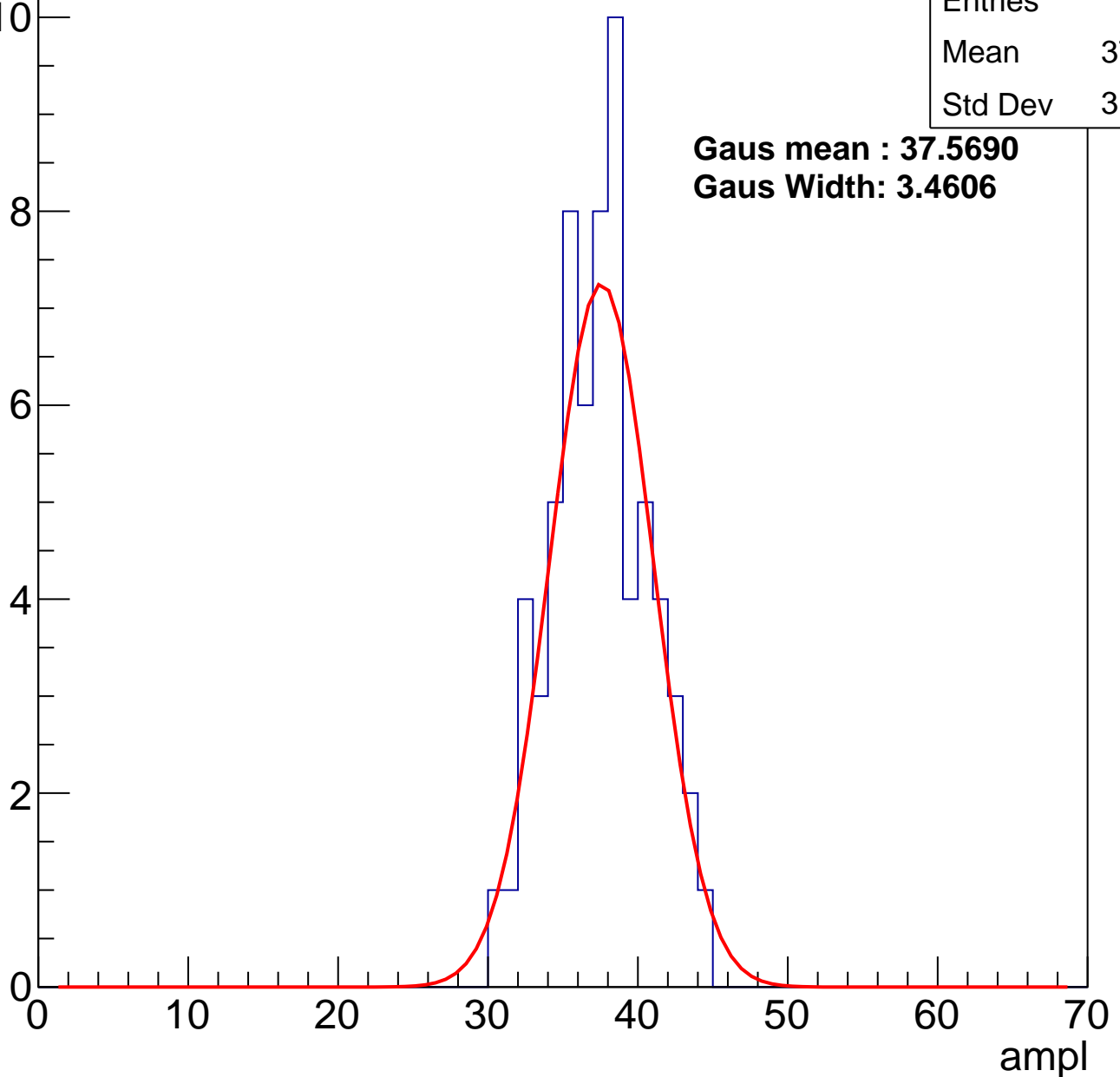
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	37.02
Std Dev	3.155

Gaus mean : 37.5690

Gaus Width: 3.4606



B1L100S, U1-ch111, adc2

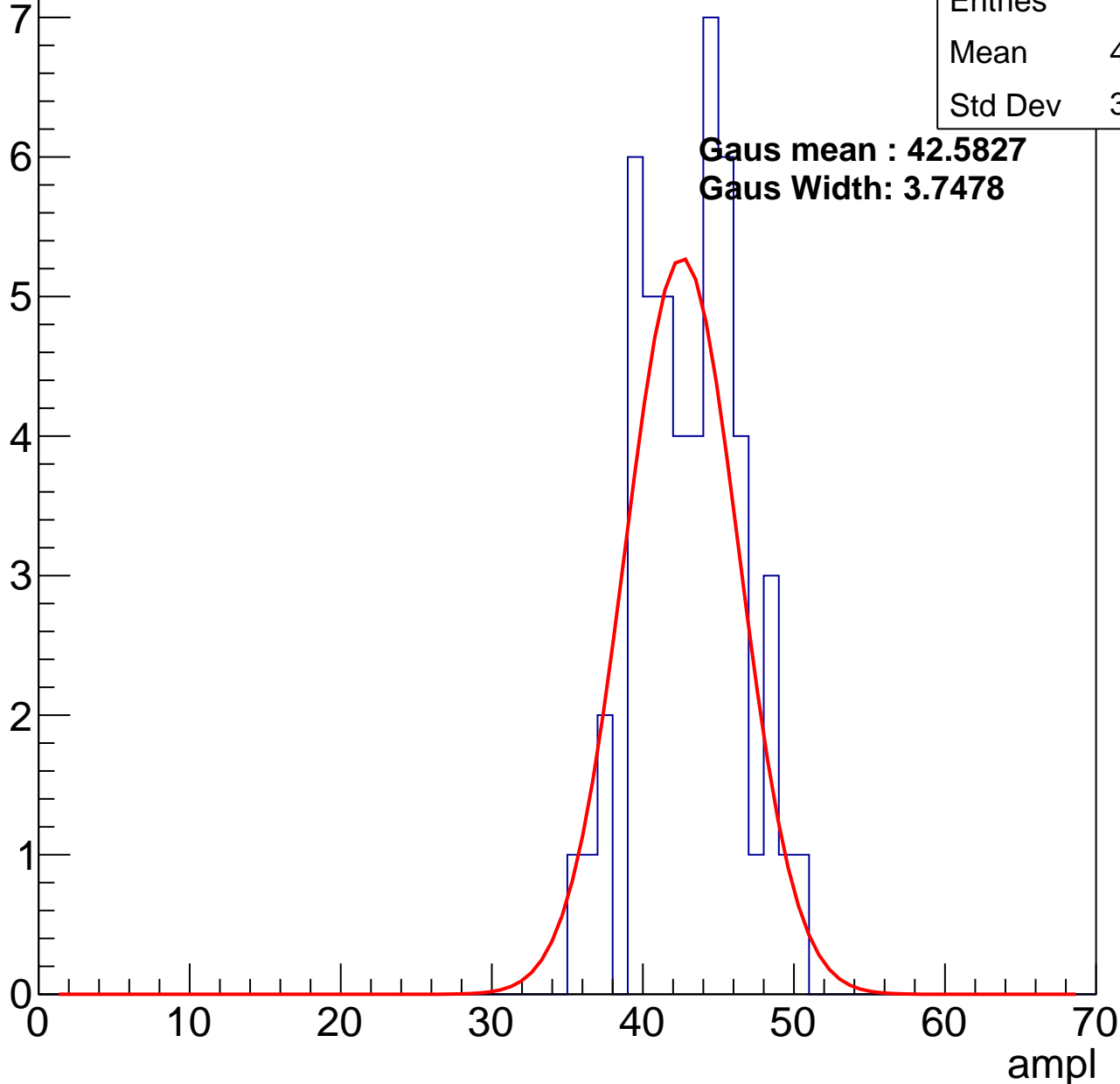
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	42.67
Std Dev	3.394

Gaus mean : 42.5827

Gaus Width: 3.7478

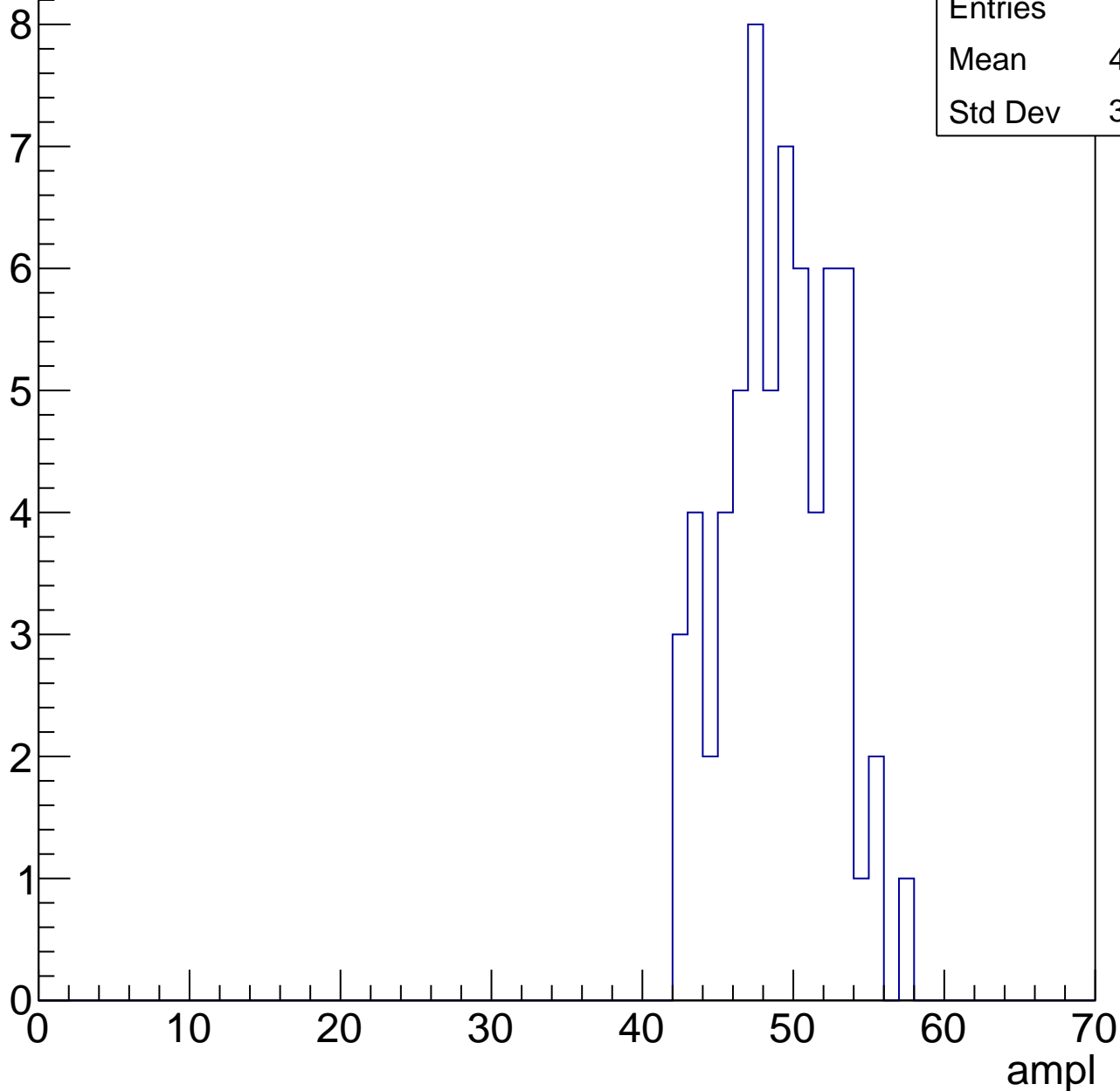


B1L100S, U1-ch111, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	64
Mean	48.59
Std Dev	3.556

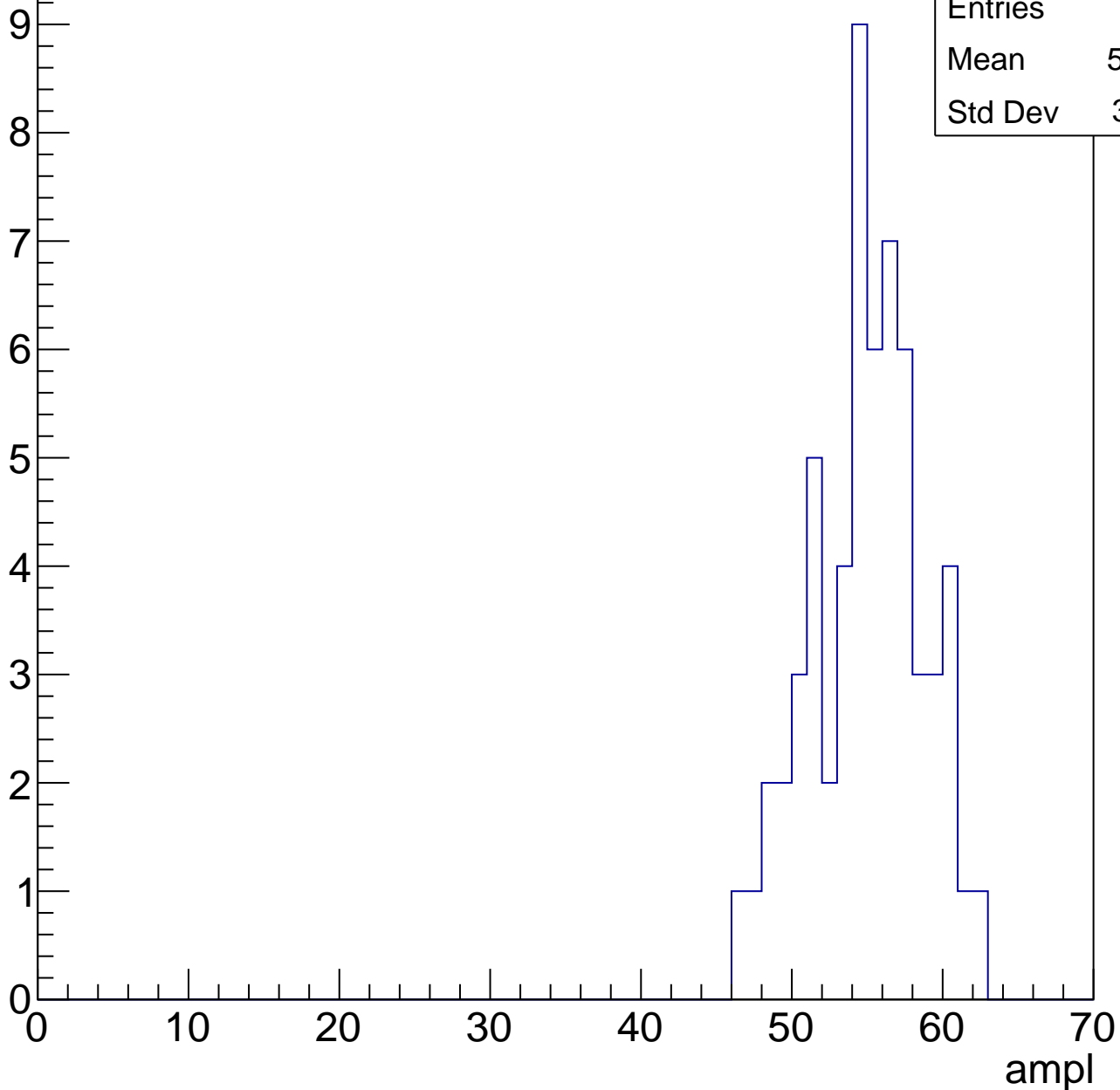


B1L100S, U1-ch111, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	54.53
Std Dev	3.631

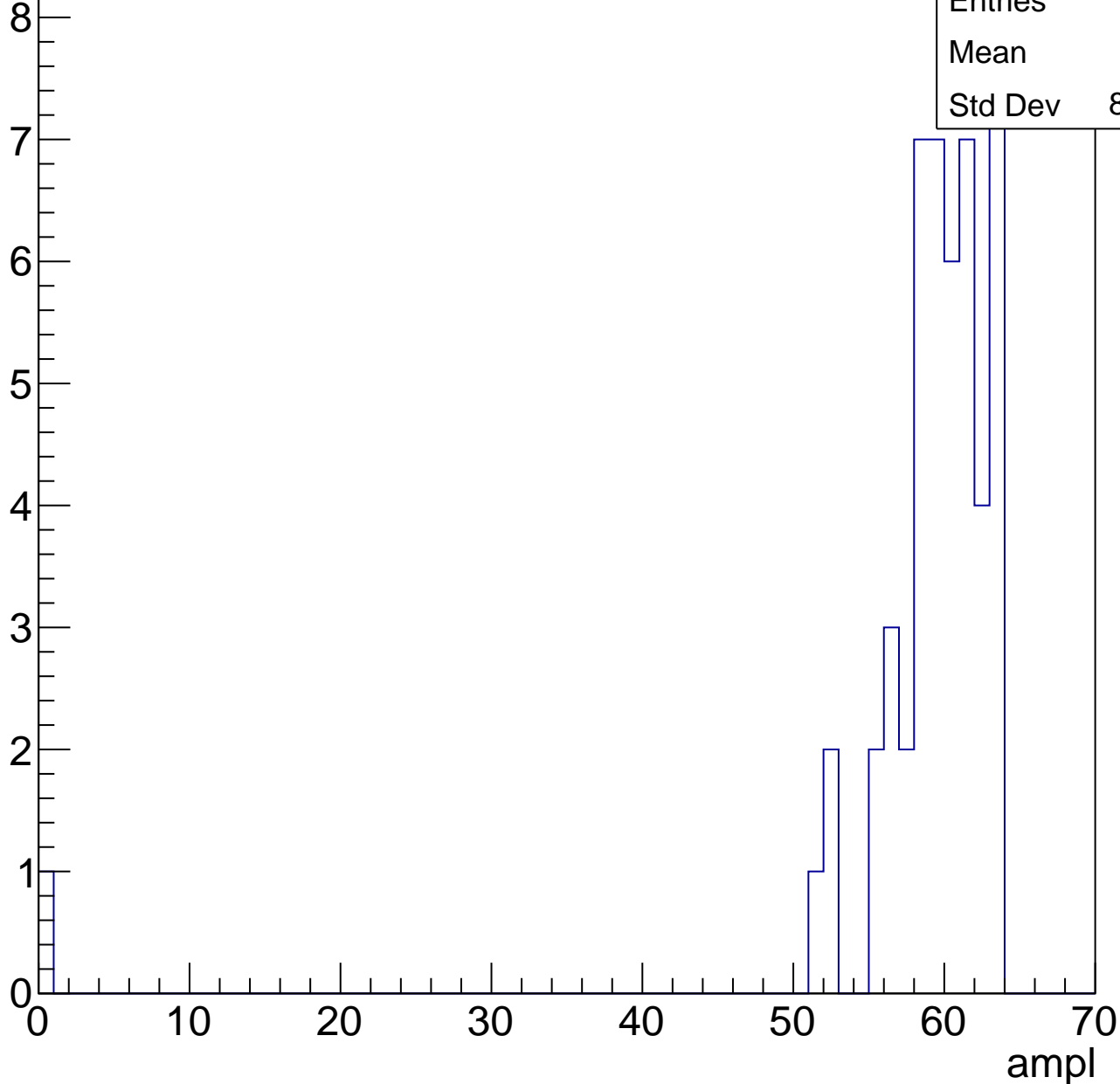


B1L100S, U1-ch111, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	58.1
Std Dev	8.803

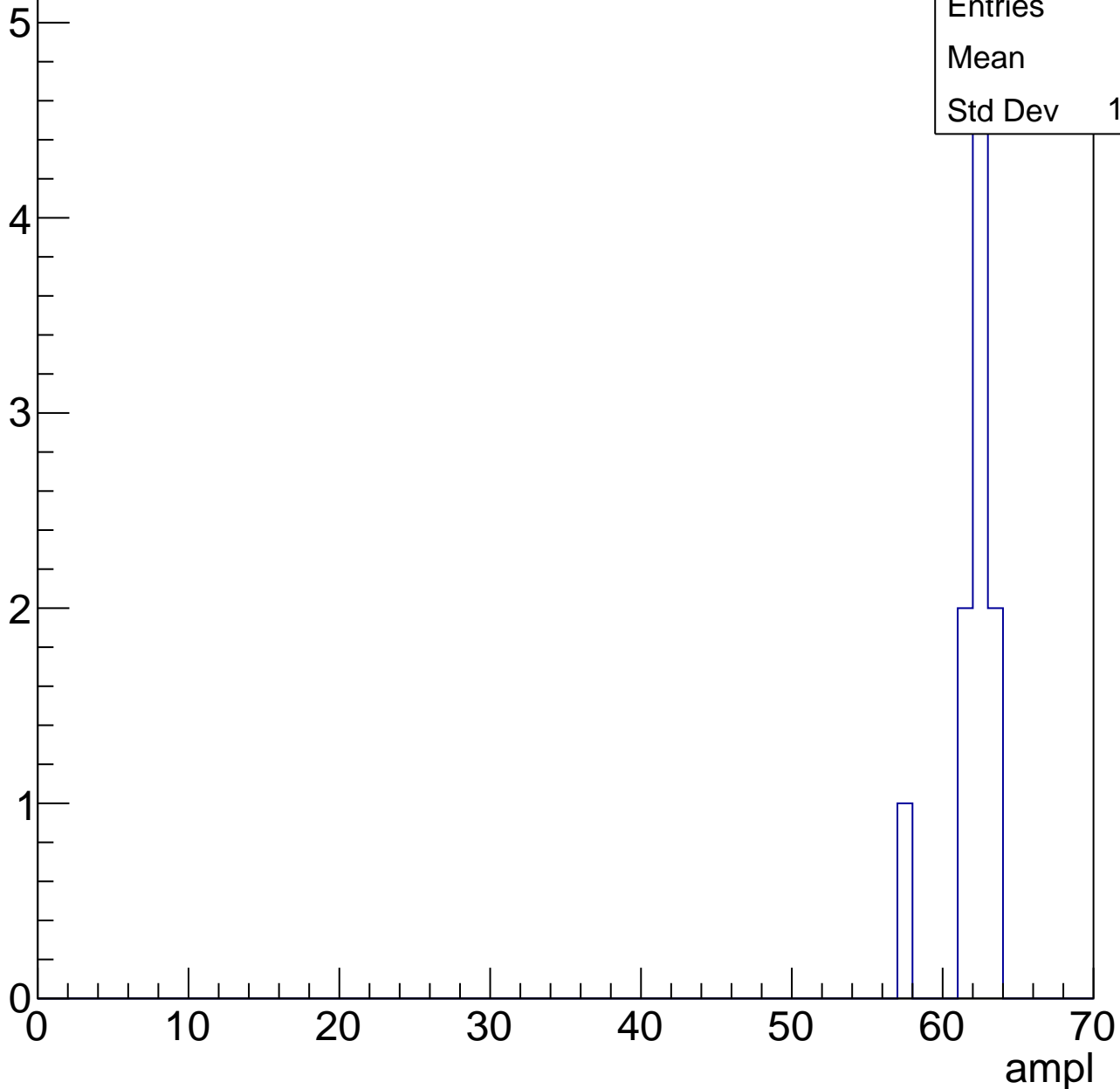


B1L100S, U1-ch111, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	10
Mean	61.5
Std Dev	1.628



B1L100S, U1-ch111, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch112, adc0

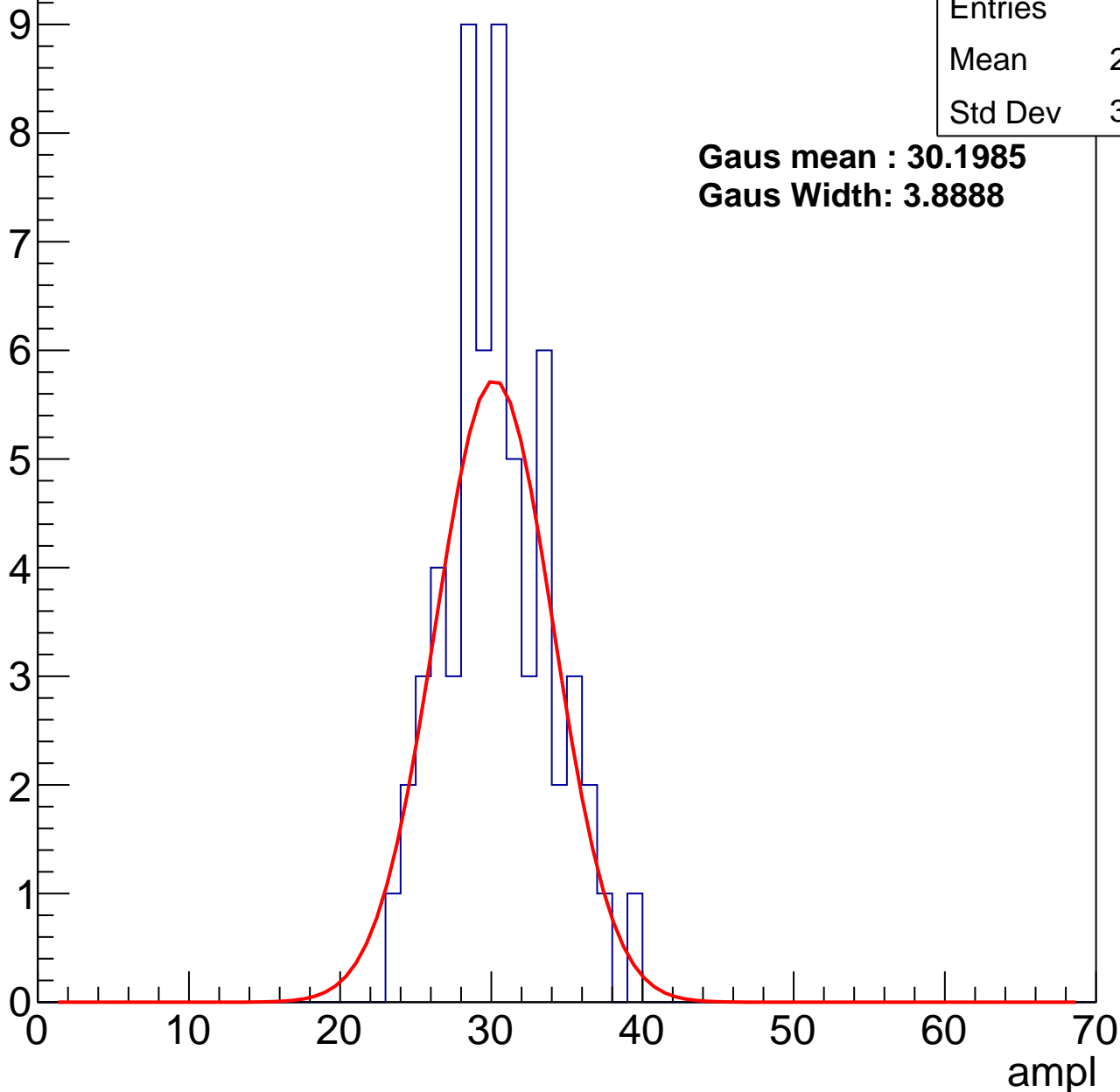
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	29.95
Std Dev	3.437

Gaus mean : 30.1985

Gaus Width: 3.8888



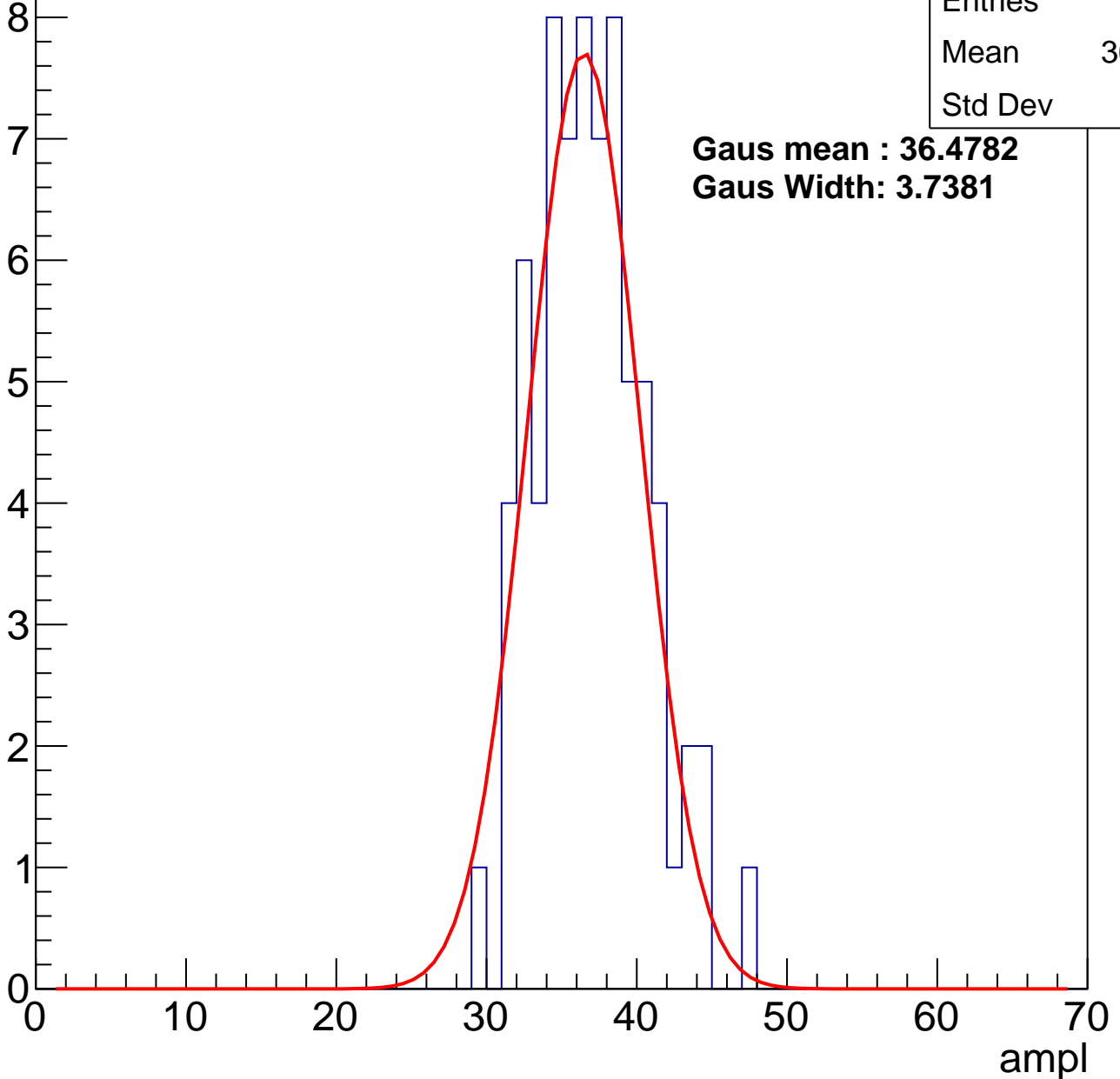
B1L100S, U1-ch112, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	73
Mean	36.53
Std Dev	3.6

Gaus mean : 36.4782
Gaus Width: 3.7381



B1L100S, U1-ch112, adc2

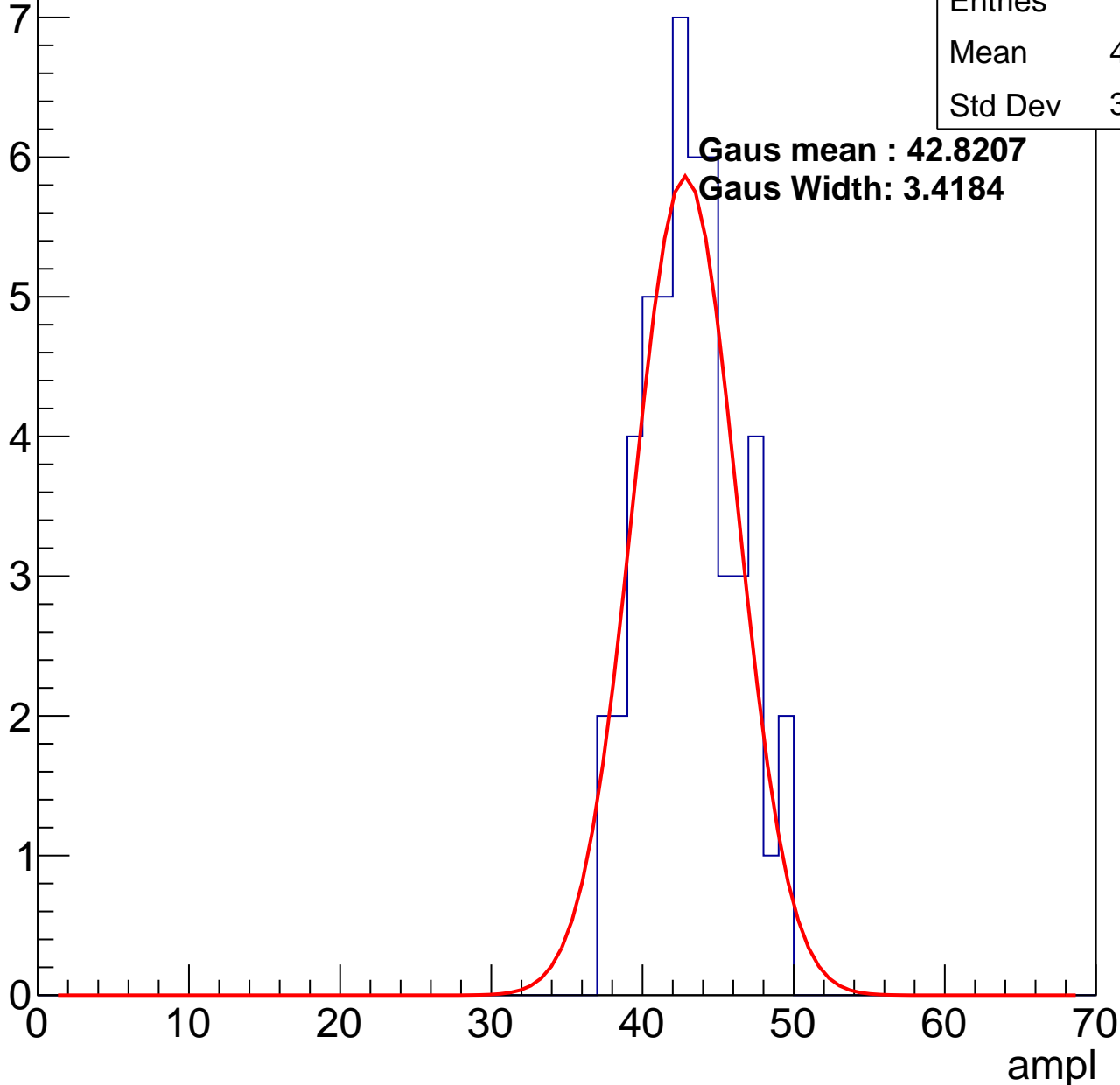
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	42.68
Std Dev	3.029

Gaus mean : 42.8207

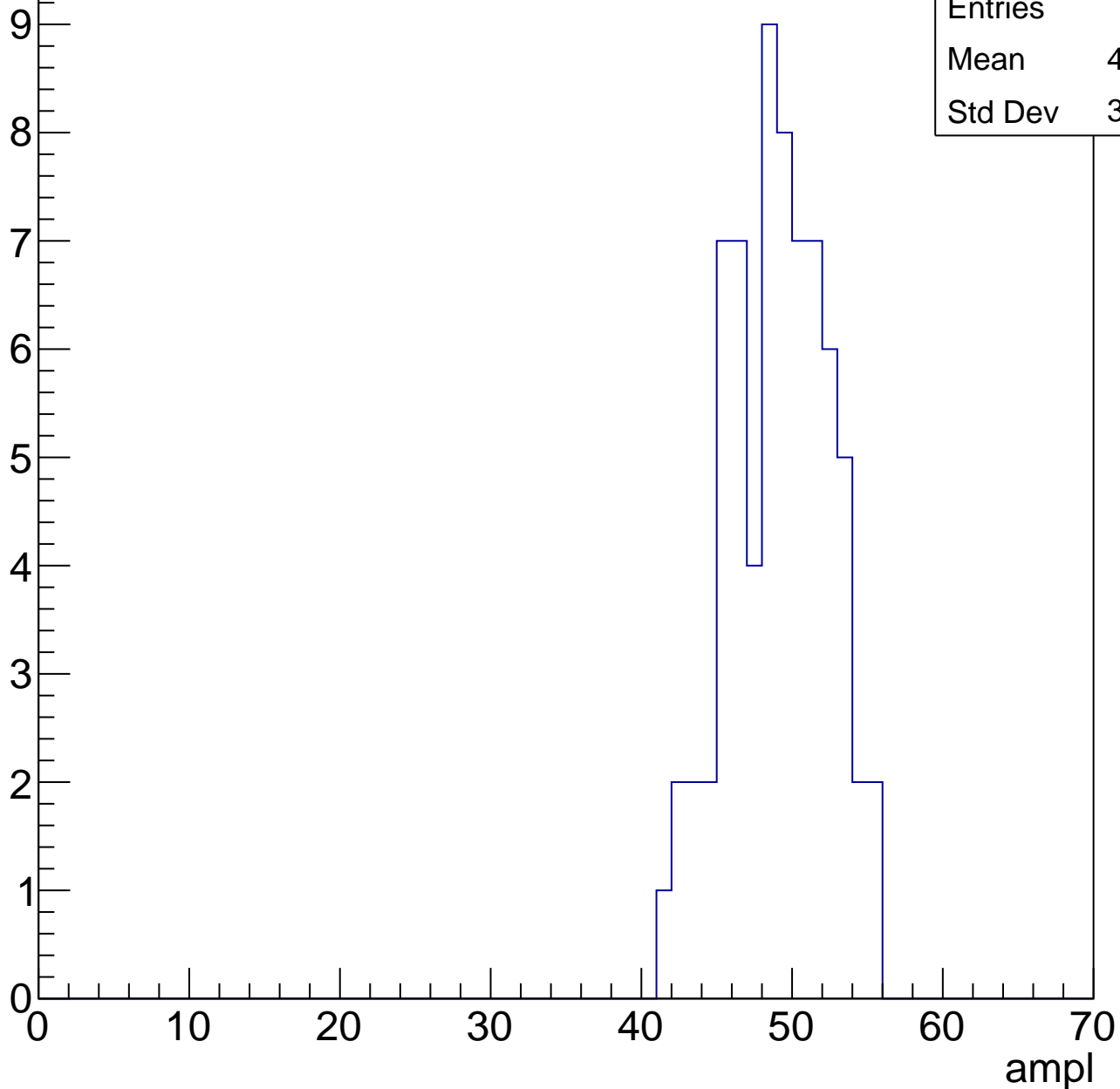
Gaus Width: 3.4184



B1L100S, U1-ch112, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry



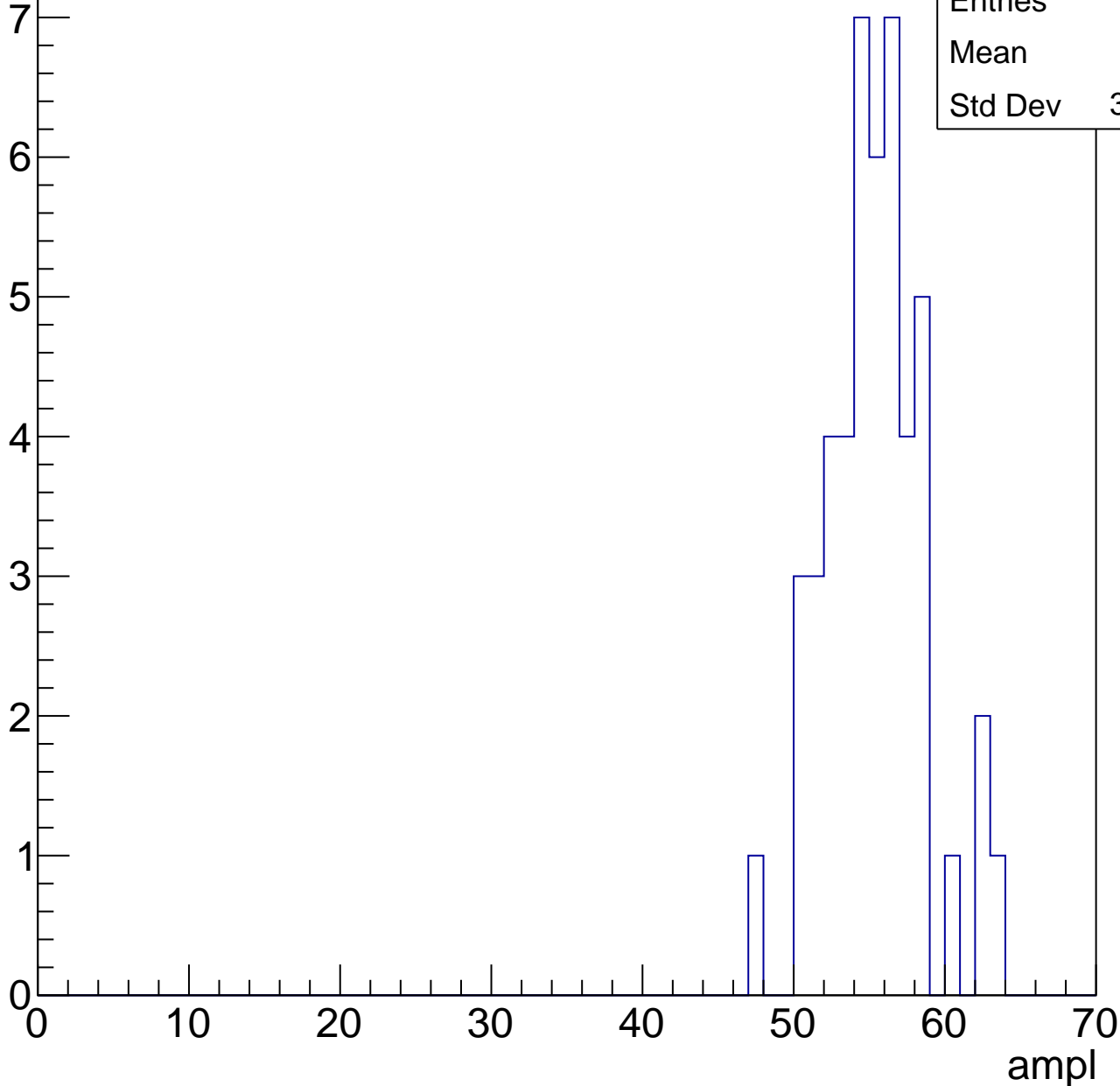
Entries	71
Mean	48.59
Std Dev	3.279

B1L100S, U1-ch112, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

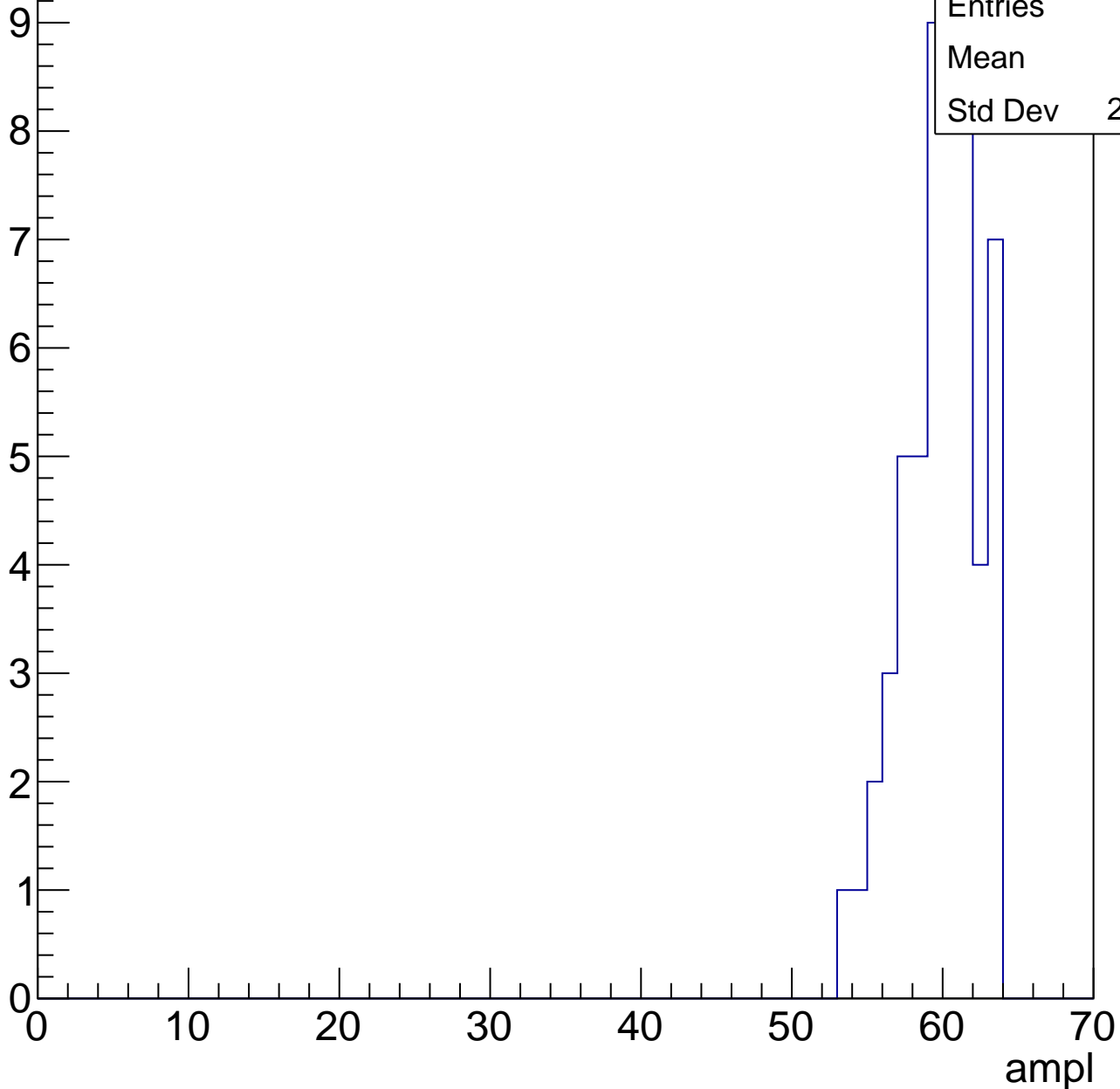
Entries	48
Mean	54.9
Std Dev	3.229



B1L100S, U1-ch112, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

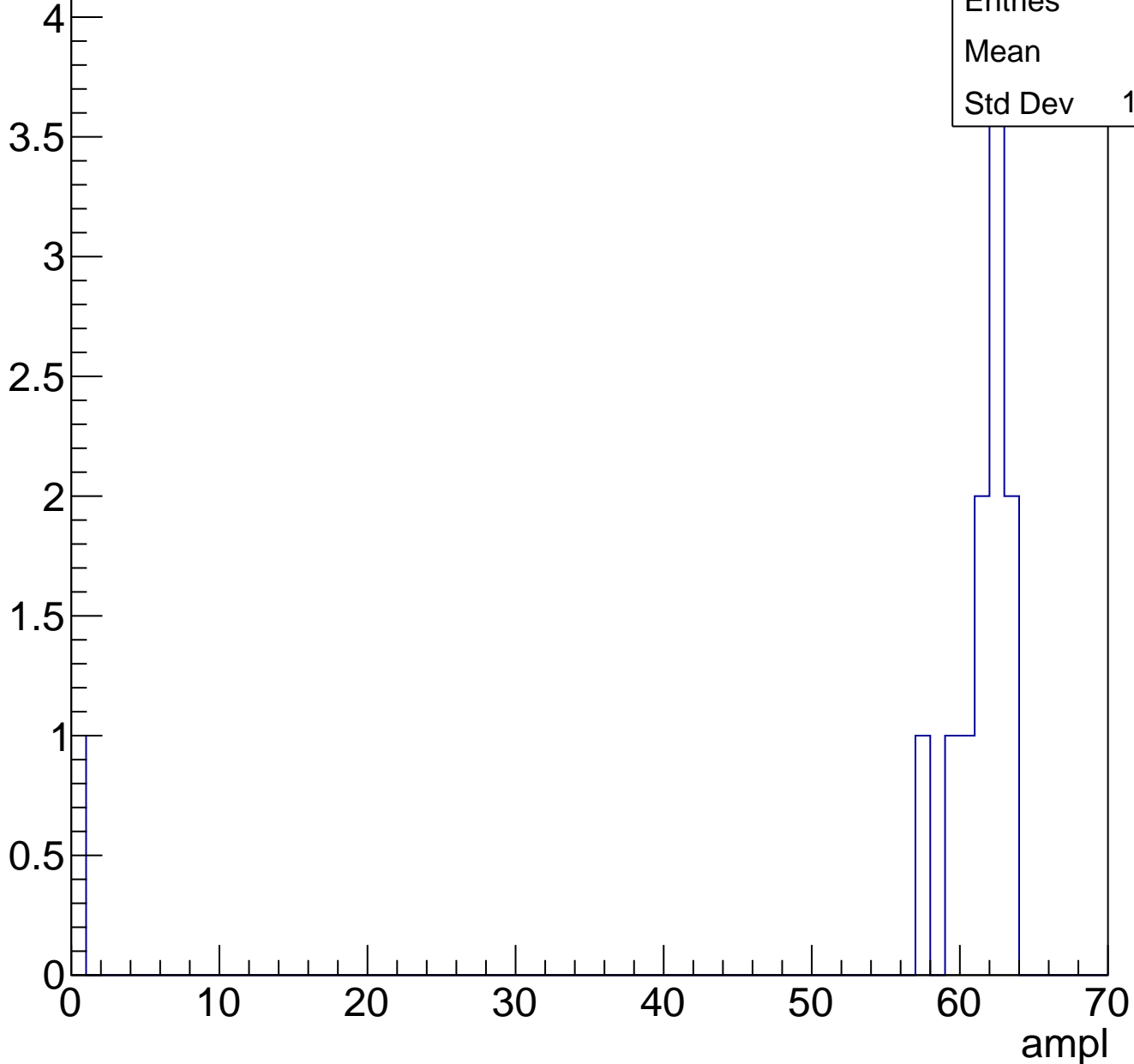


Entries	53
Mean	59.4
Std Dev	2.475

B1L100S, U1-ch112, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch112, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch113, adc0

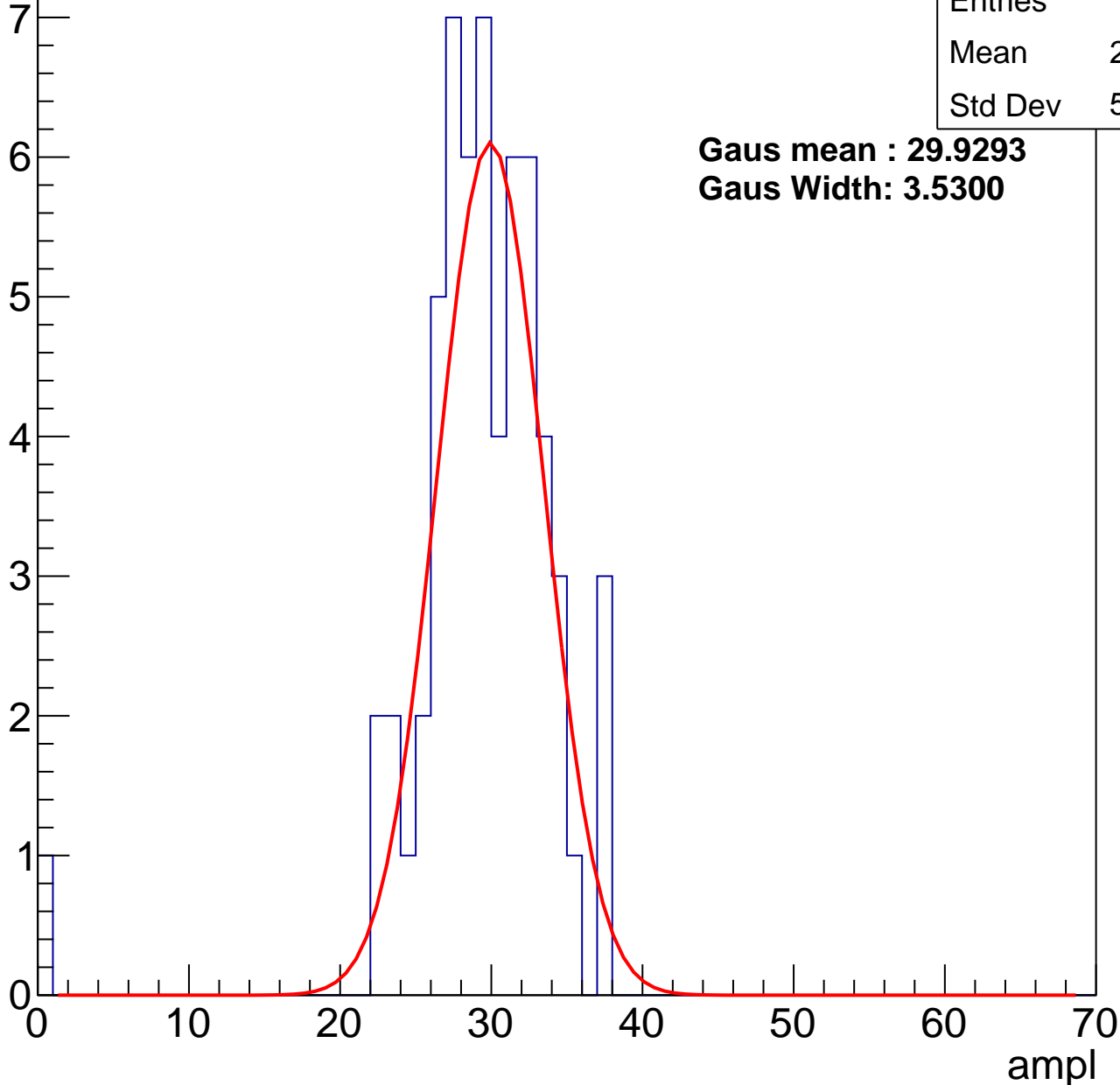
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	28.87
Std Dev	5.156

Gaus mean : 29.9293

Gaus Width: 3.5300



B1L100S, U1-ch113, adc1

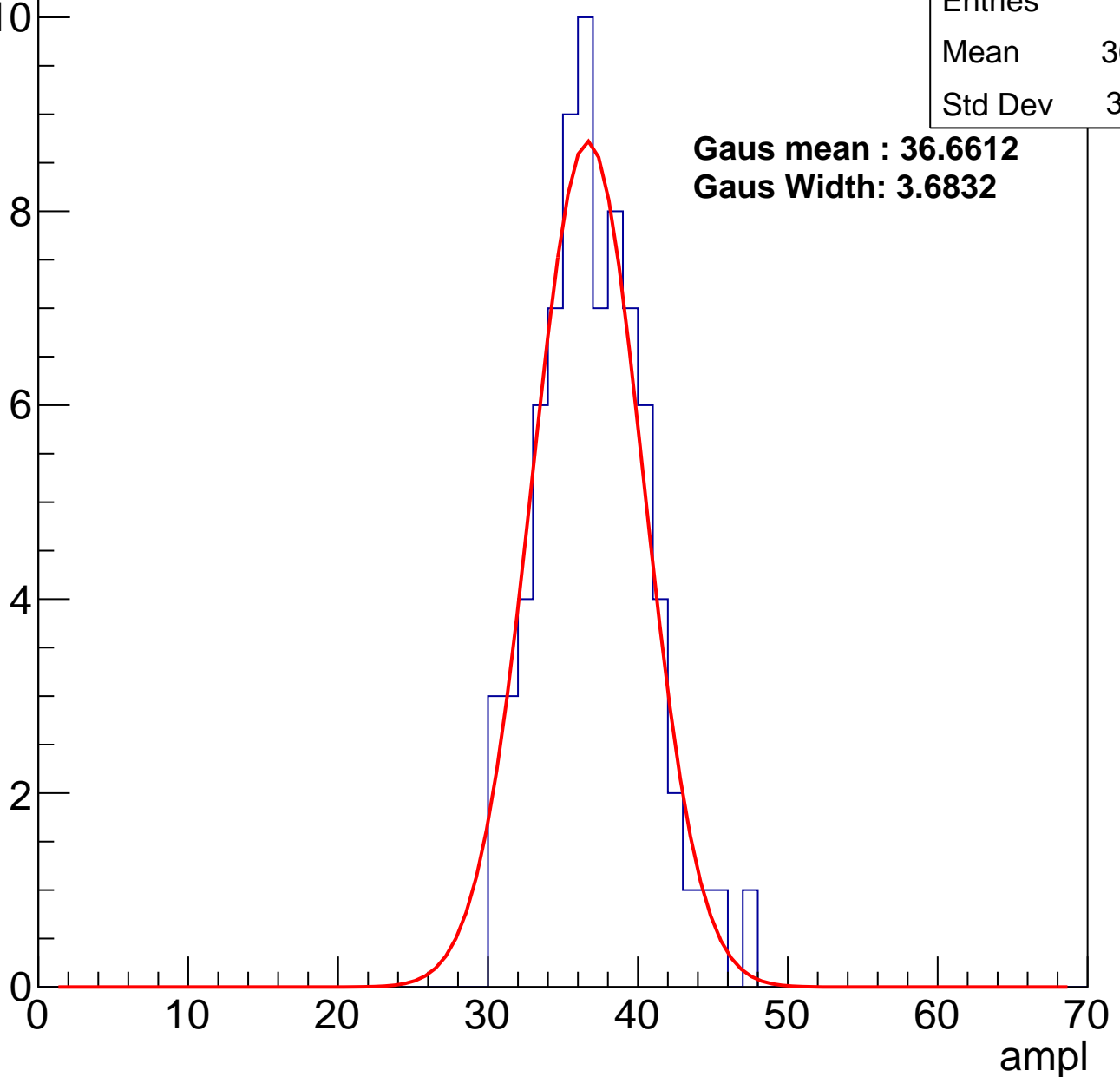
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	80
Mean	36.56
Std Dev	3.531

Gaus mean : 36.6612

Gaus Width: 3.6832

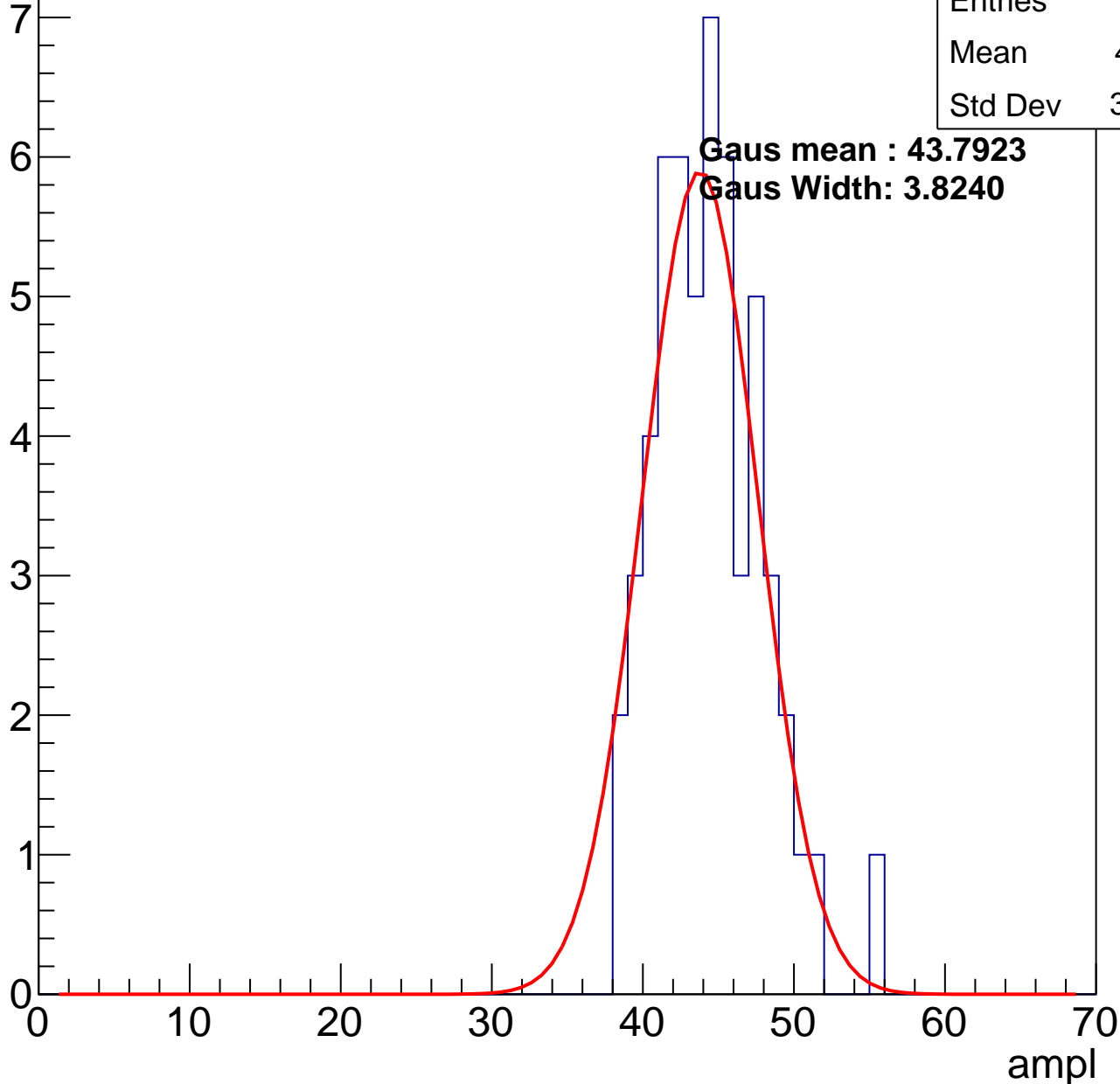


B1L100S, U1-ch113, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	43.91
Std Dev	3.466

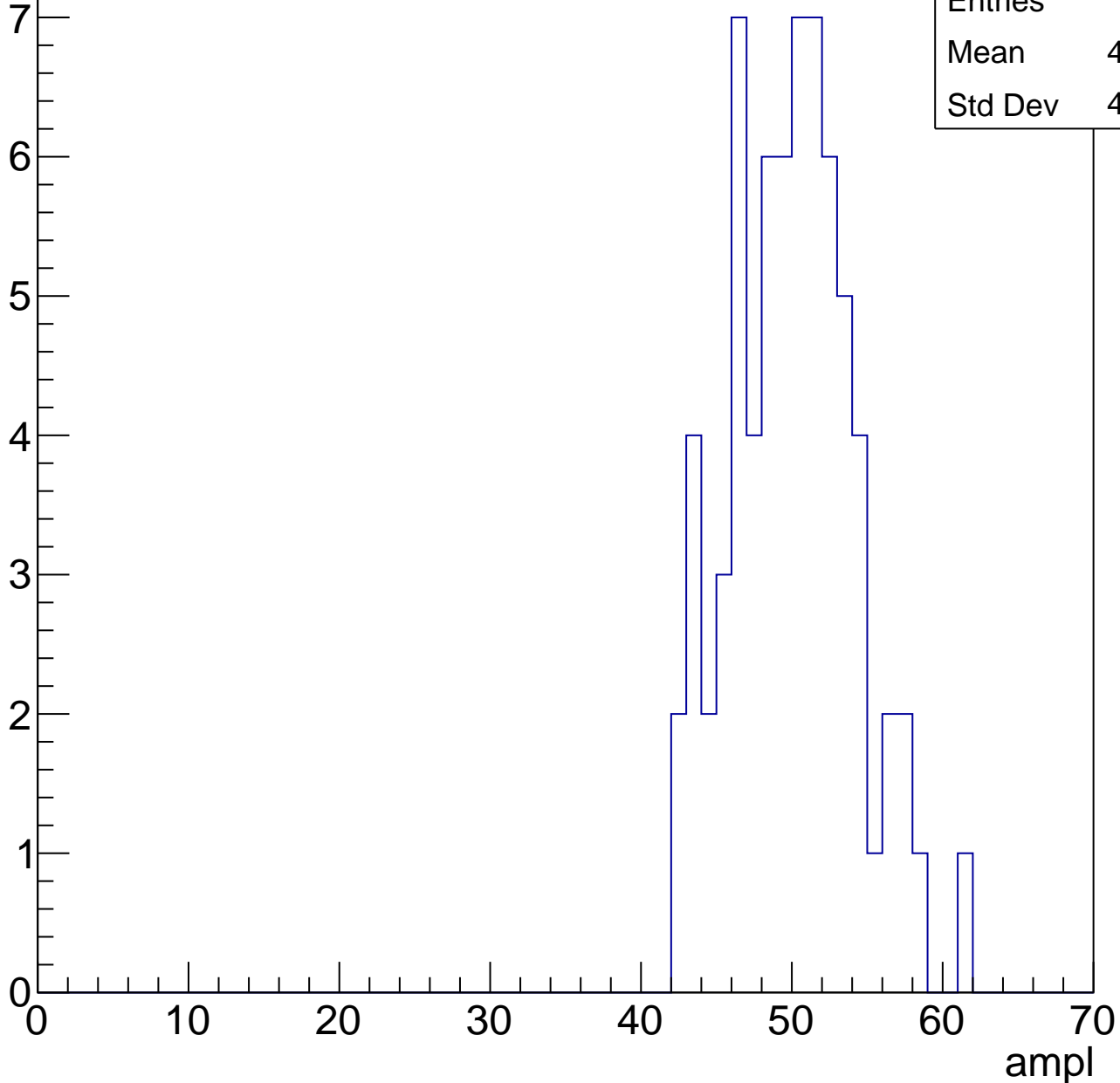


B1L100S, U1-ch113, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	49.59
Std Dev	4.066

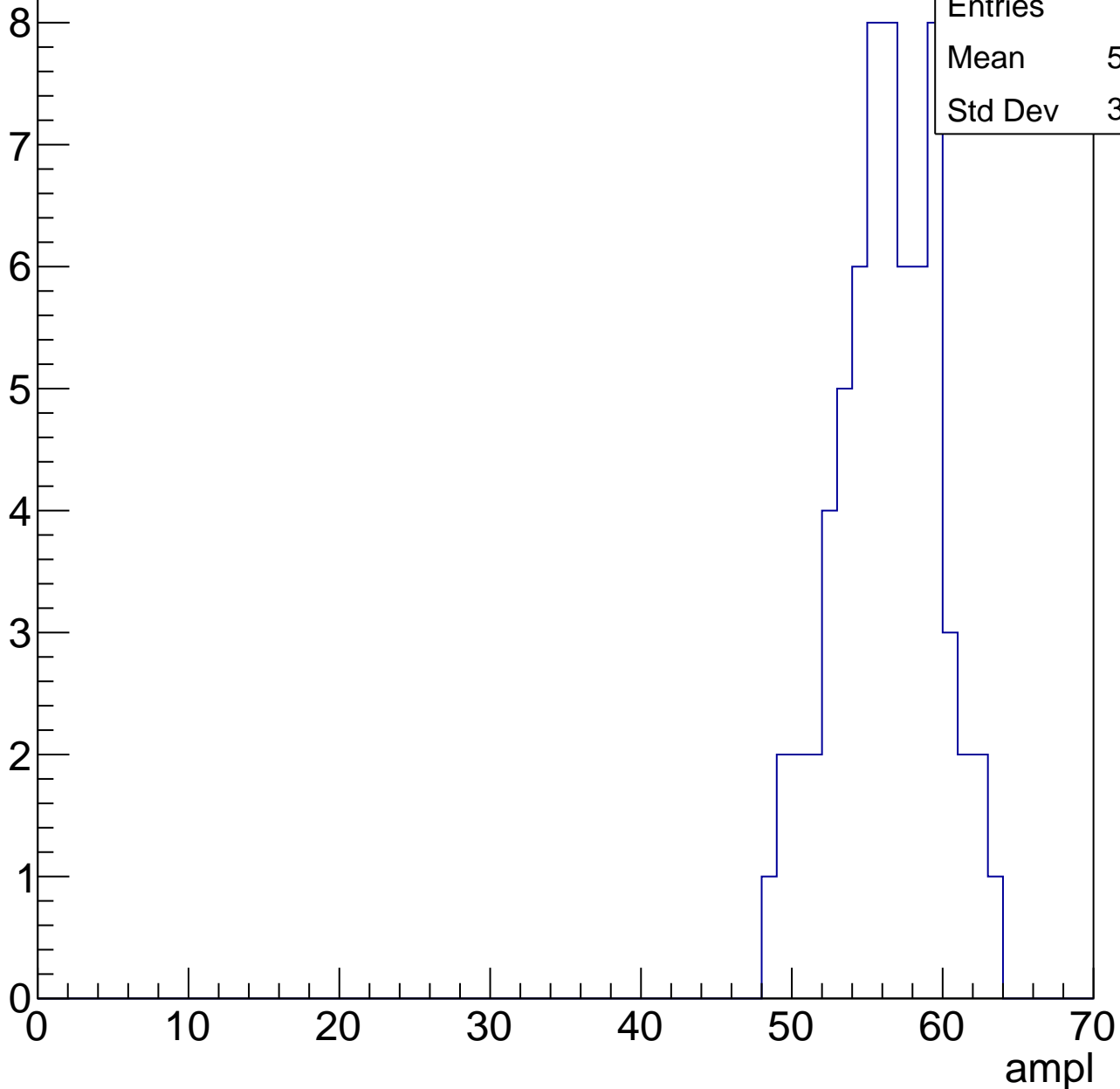


B1L100S, U1-ch113, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

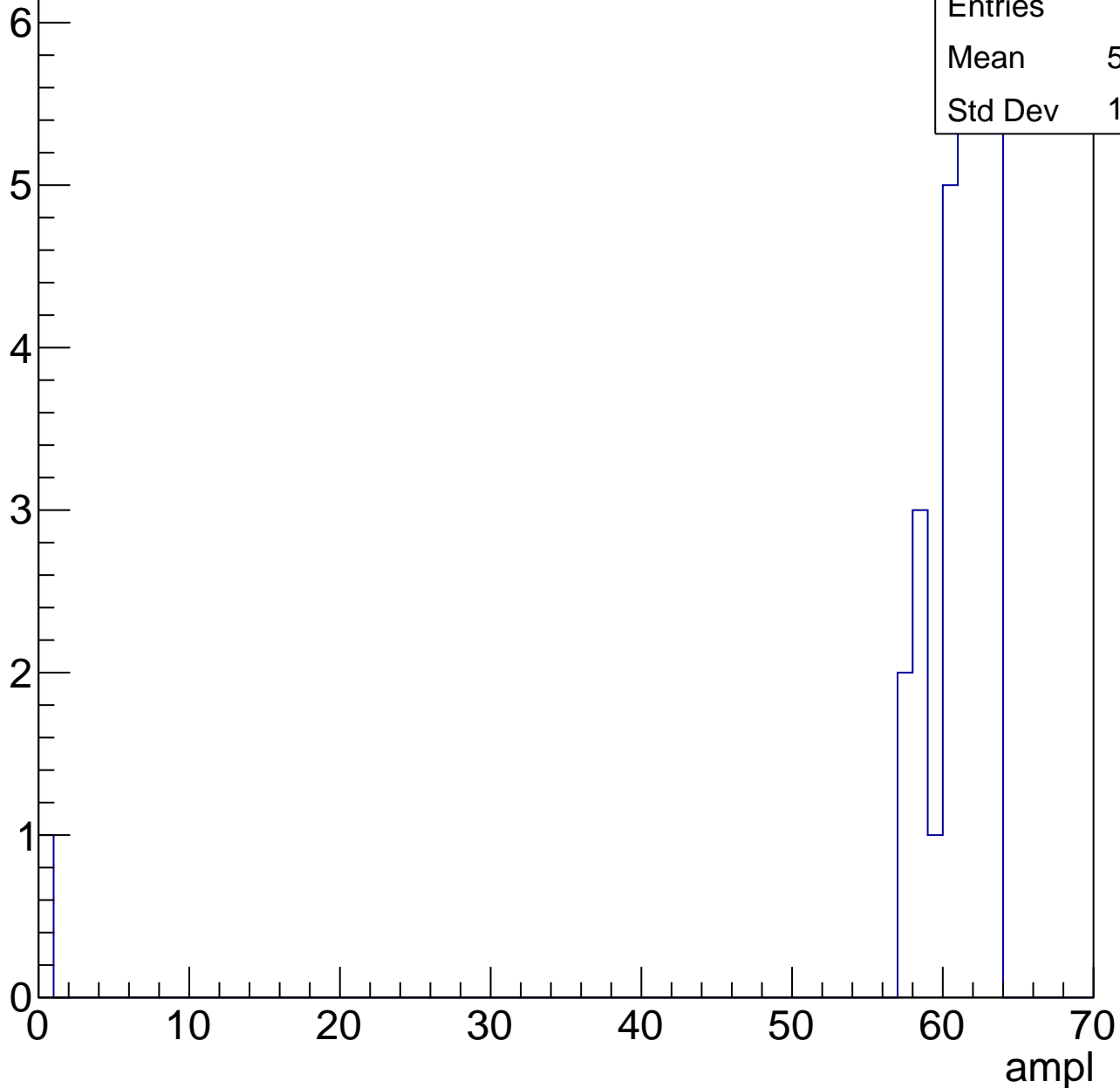
Entries	66
Mean	55.82
Std Dev	3.357



B1L100S, U1-ch113, adc5

calib_packv5_042523_0143.root, FC#4, port A2

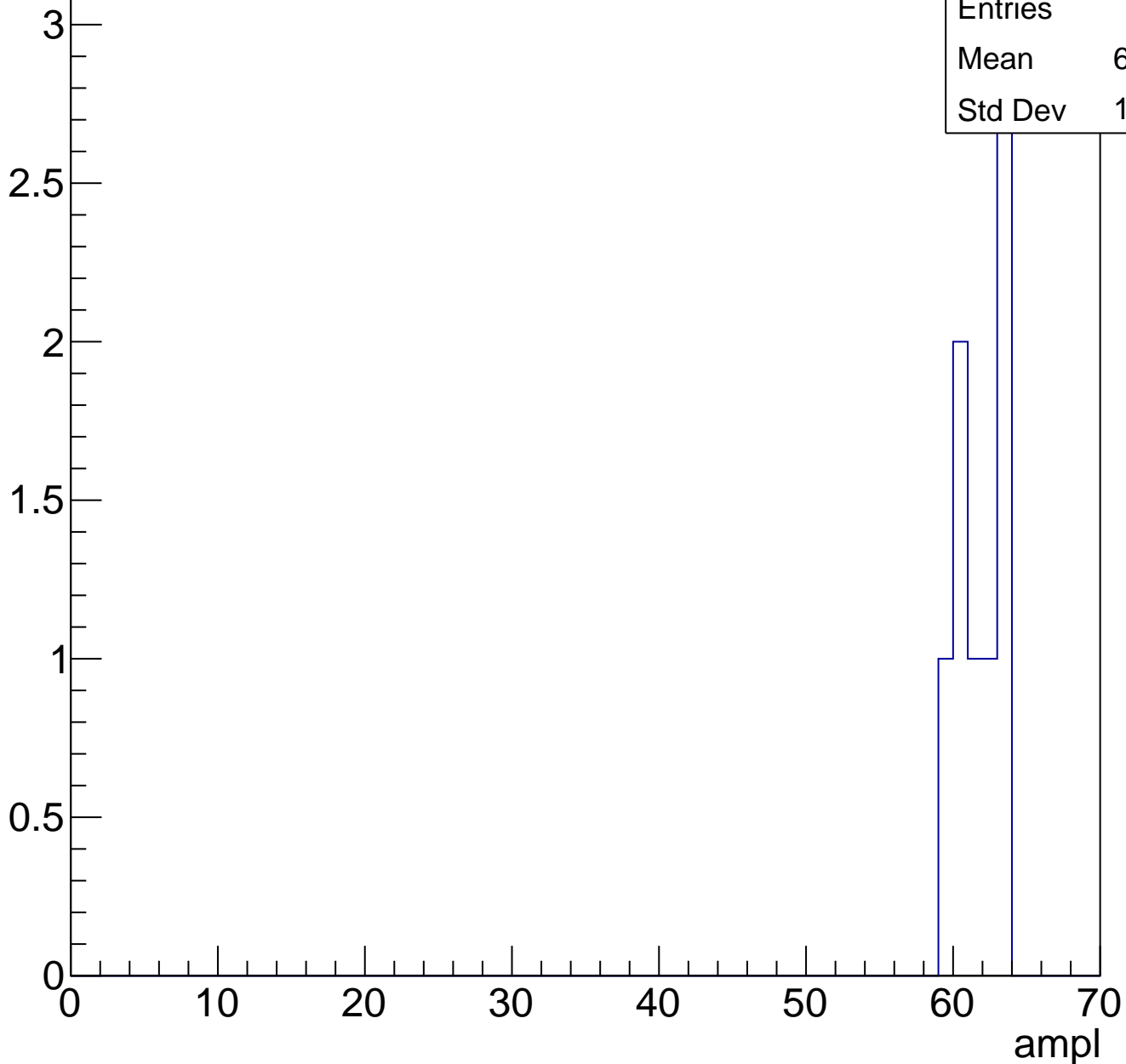
Entry



B1L100S, U1-ch113, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch113, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	3
Mean	20.67
Std Dev	29.23

B1L100S, U1-ch114, adc0

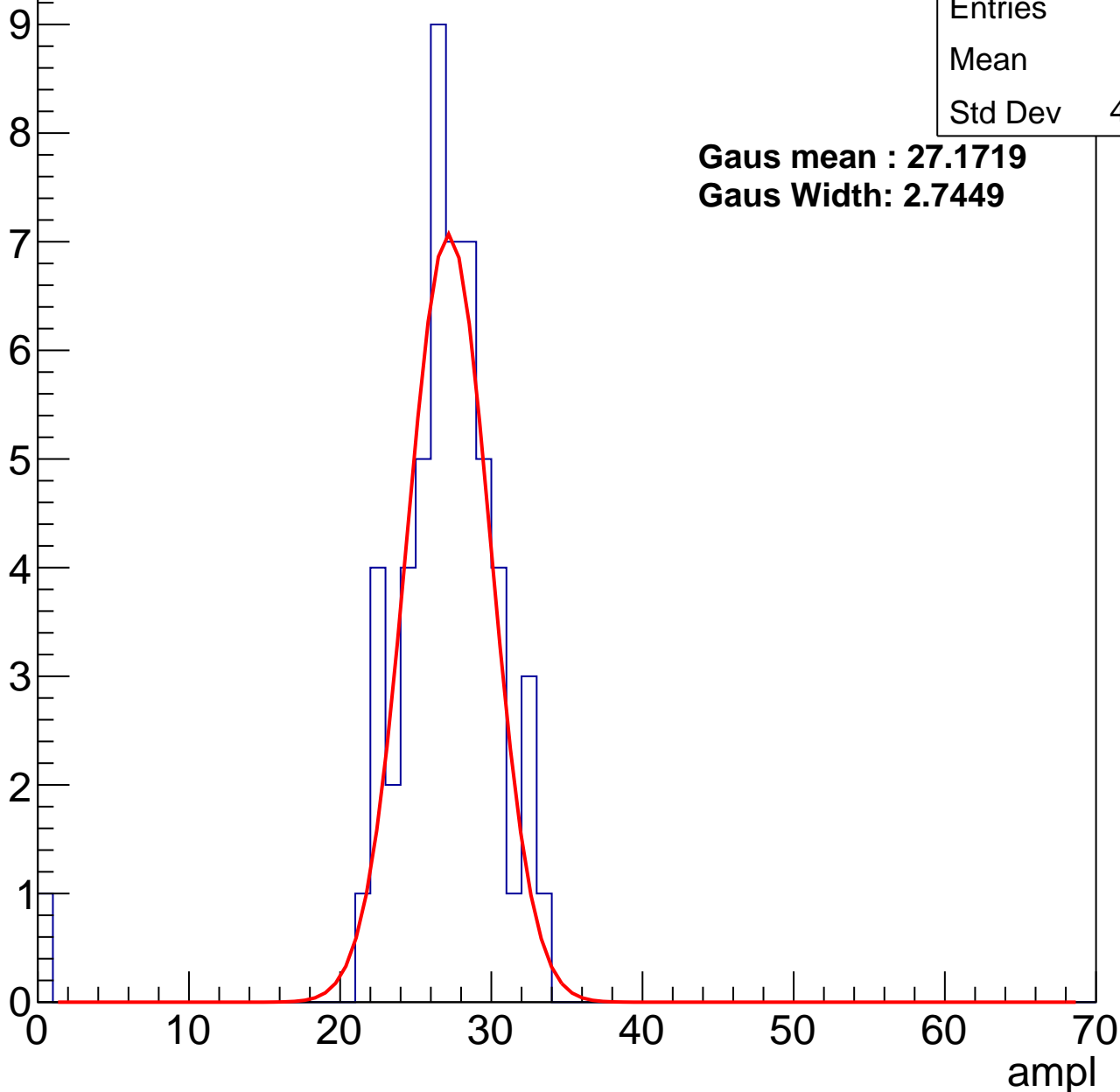
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	26.3
Std Dev	4.565

Gaus mean : 27.1719

Gaus Width: 2.7449



B1L100S, U1-ch114, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	34.42
Std Dev	3.546

Gaus mean : 34.7964

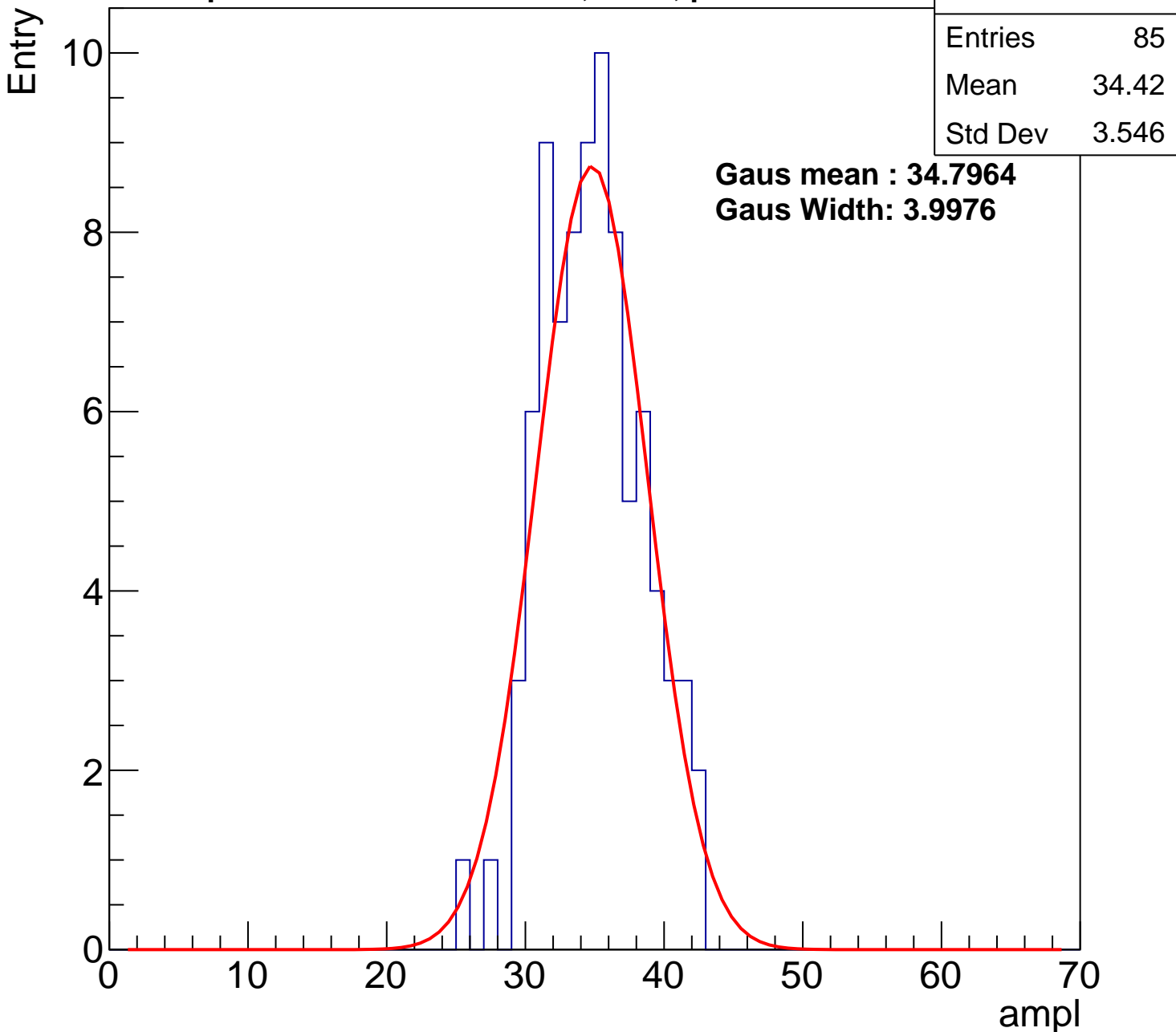
Gaus Width: 3.9976

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl

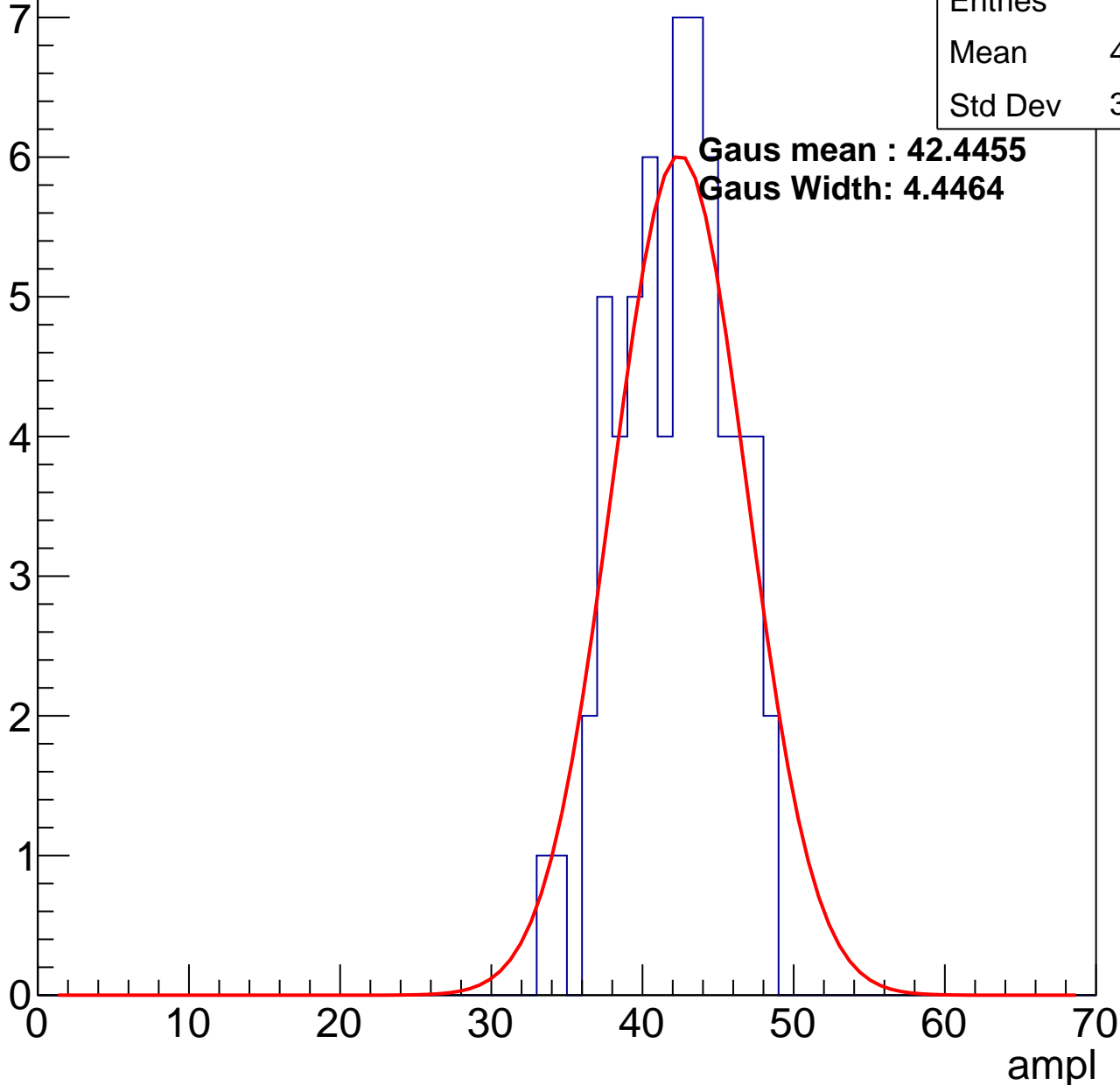


B1L100S, U1-ch114, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	41.65
Std Dev	3.534

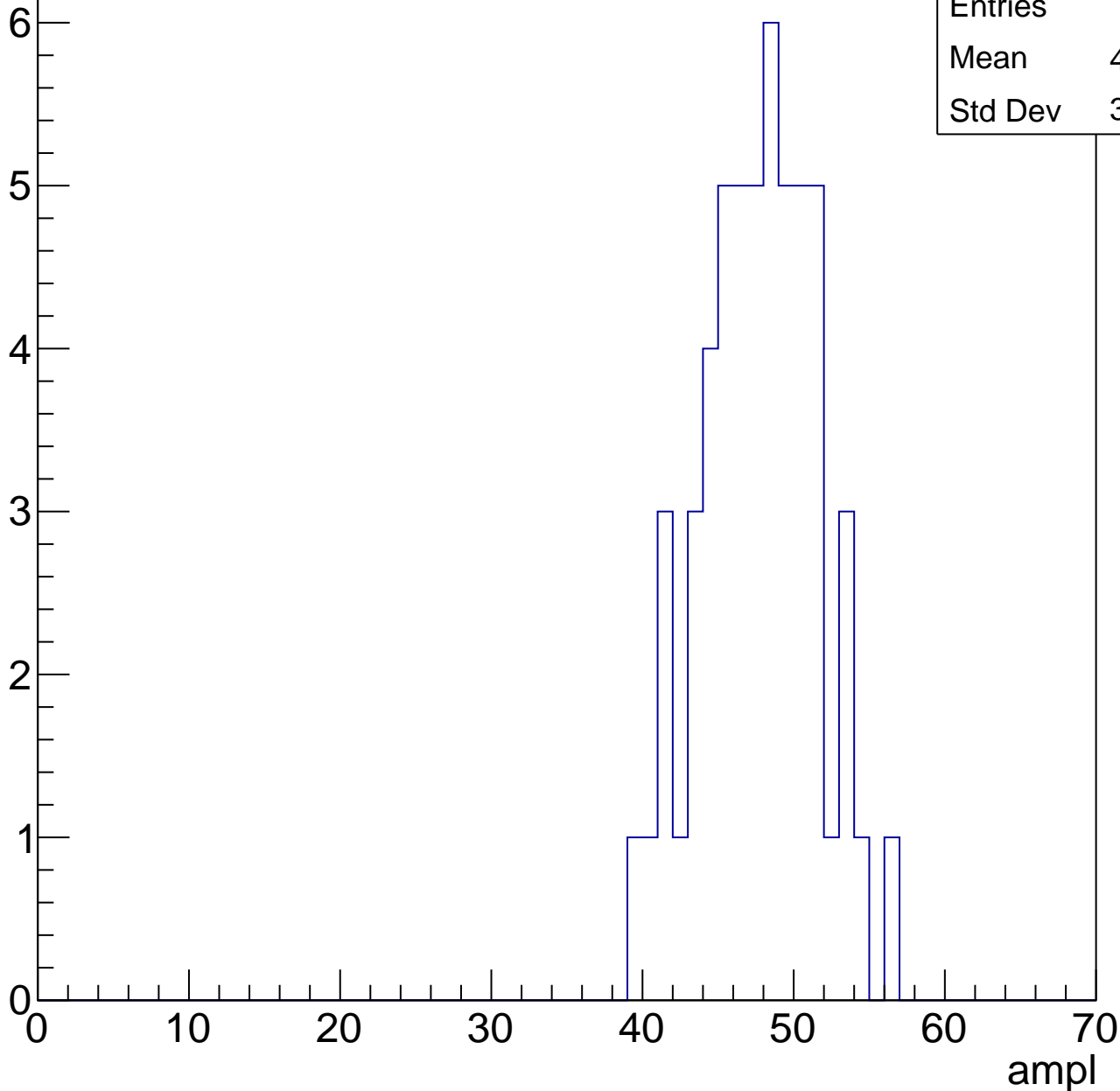


B1L100S, U1-ch114, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	47.24
Std Dev	3.732

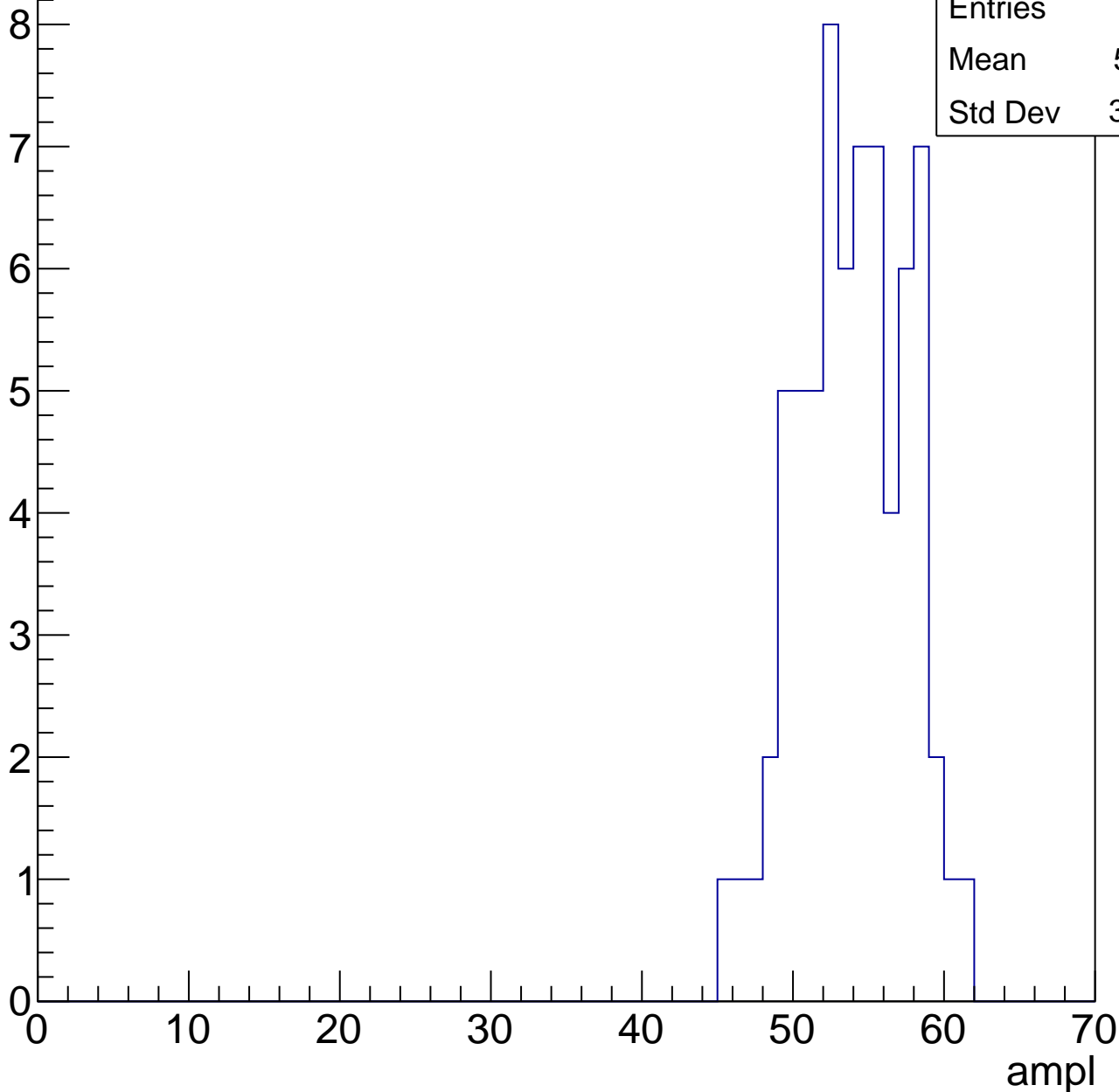


B1L100S, U1-ch114, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	69
Mean	53.51
Std Dev	3.533

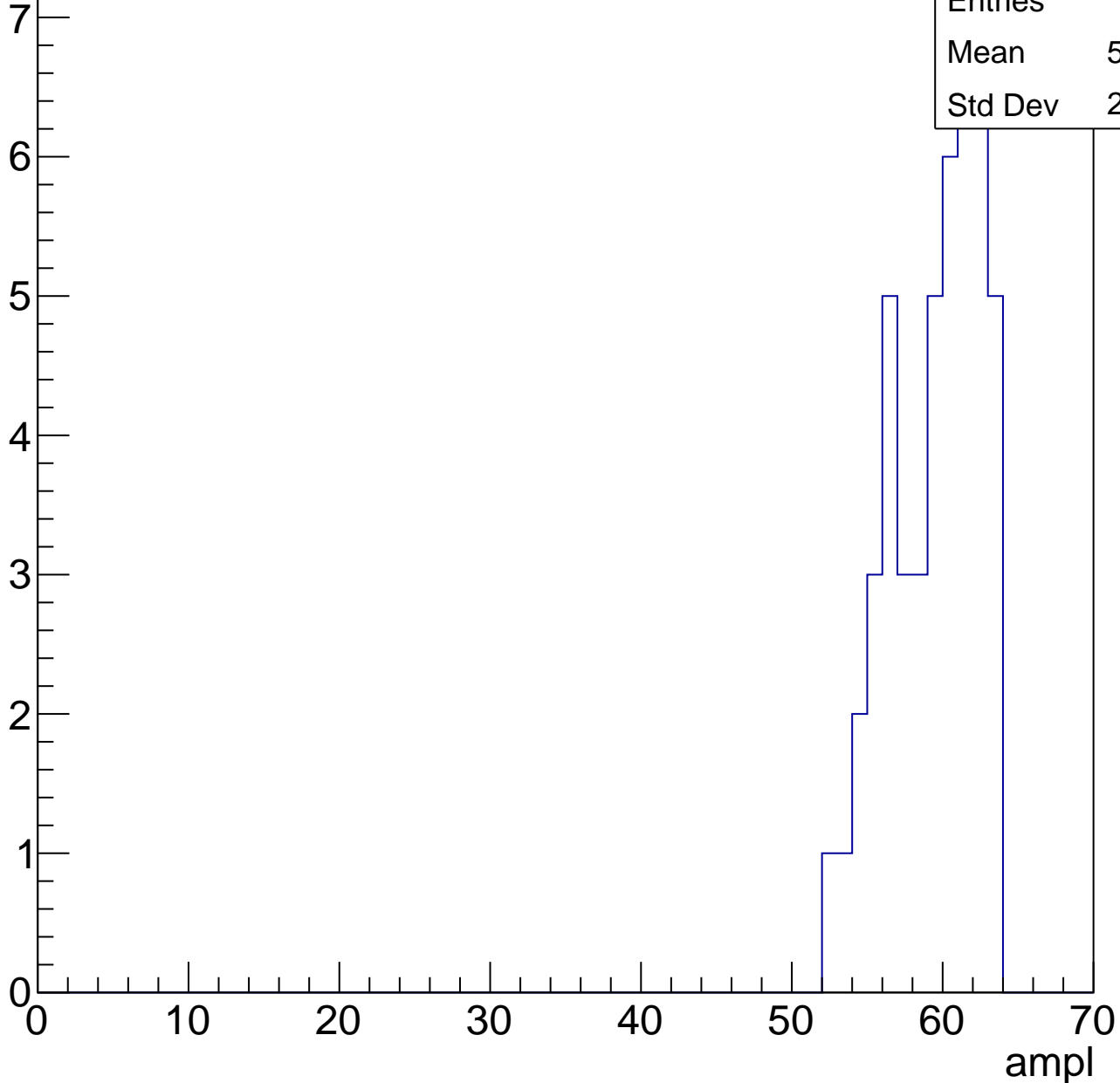


B1L100S, U1-ch114, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	48
Mean	59.04
Std Dev	2.958

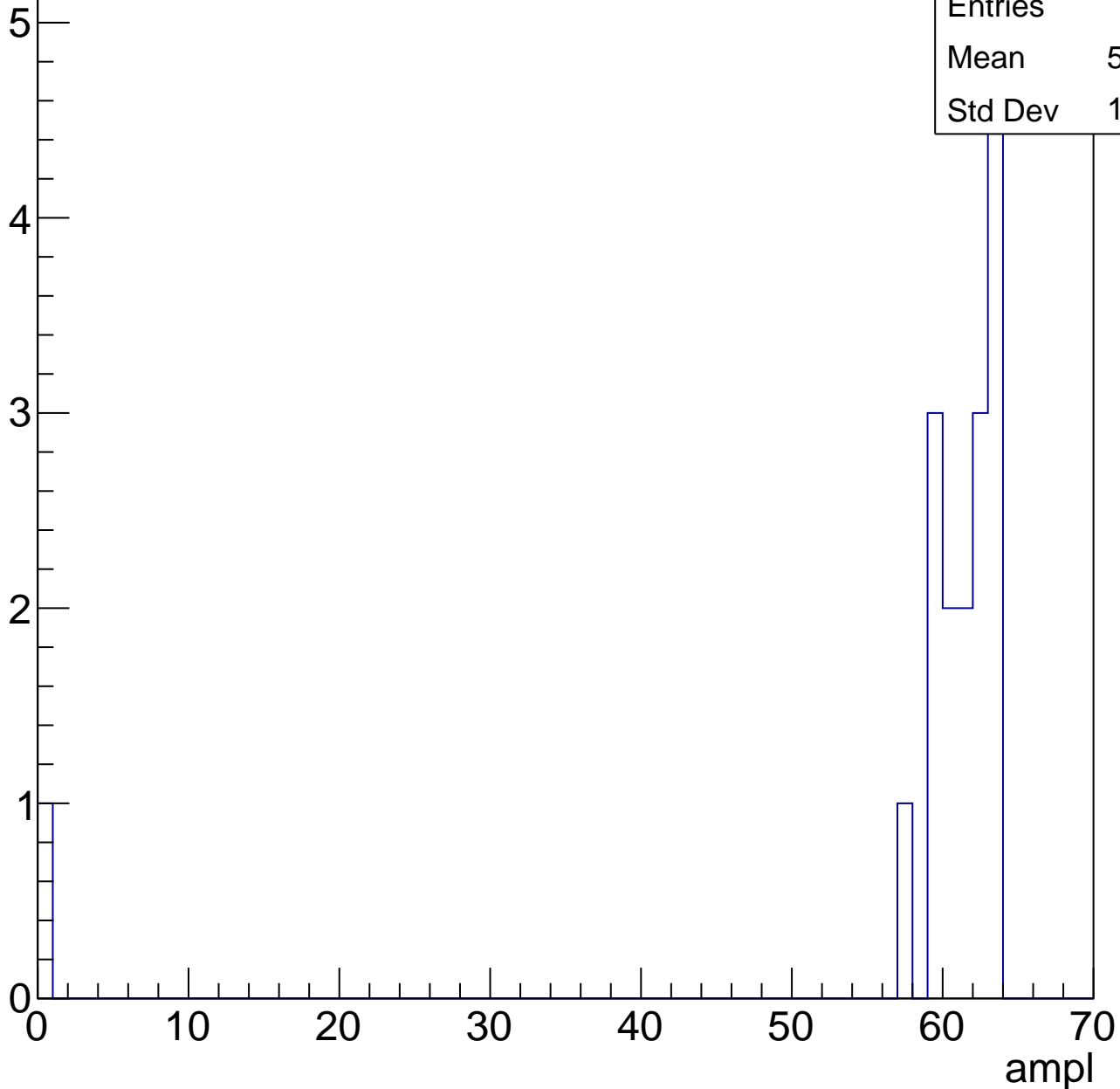


B1L100S, U1-ch114, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	17
Mean	57.47
Std Dev	14.48



B1L100S, U1-ch114, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch115, adc0

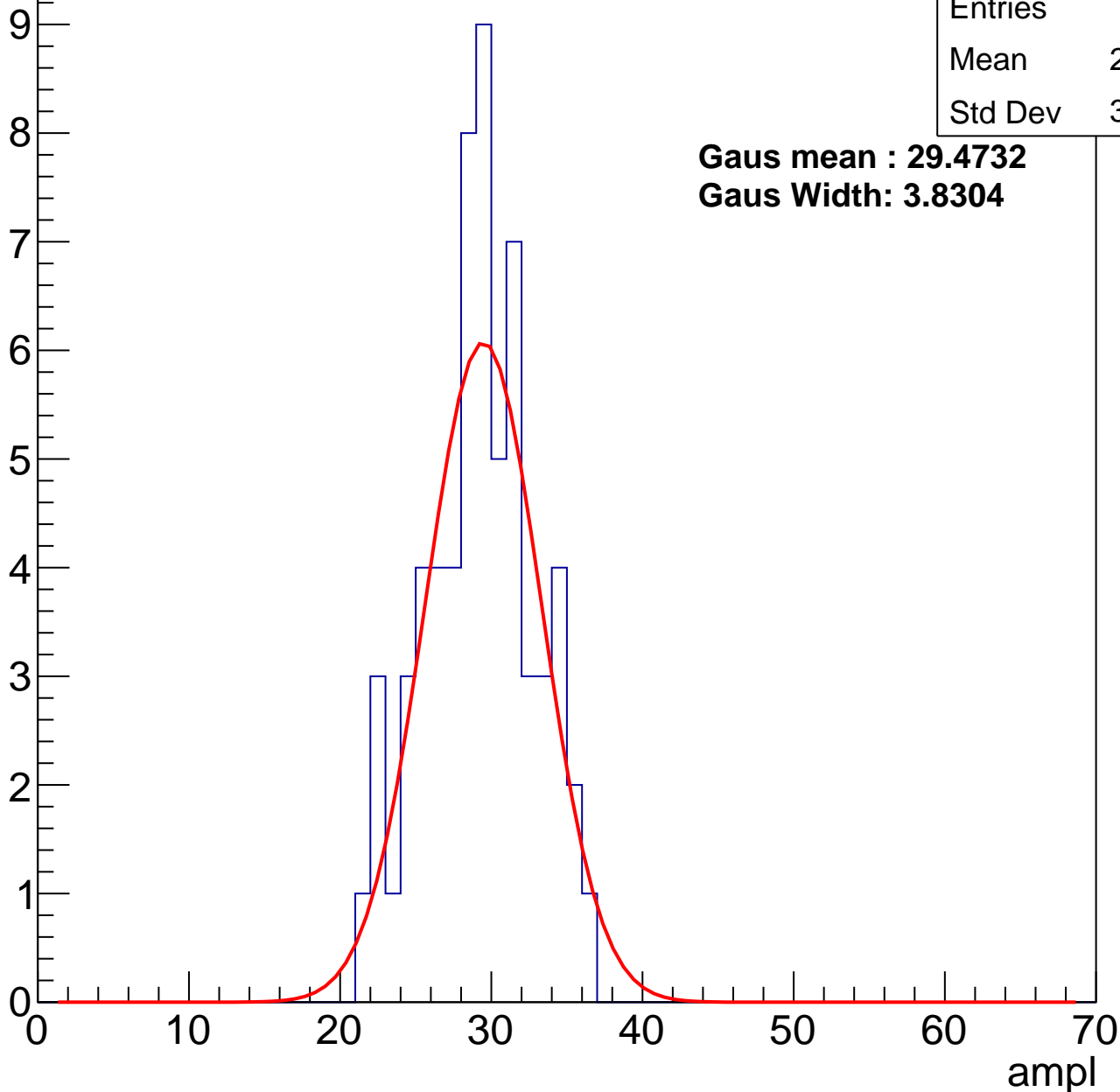
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	28.76
Std Dev	3.527

Gaus mean : 29.4732

Gaus Width: 3.8304



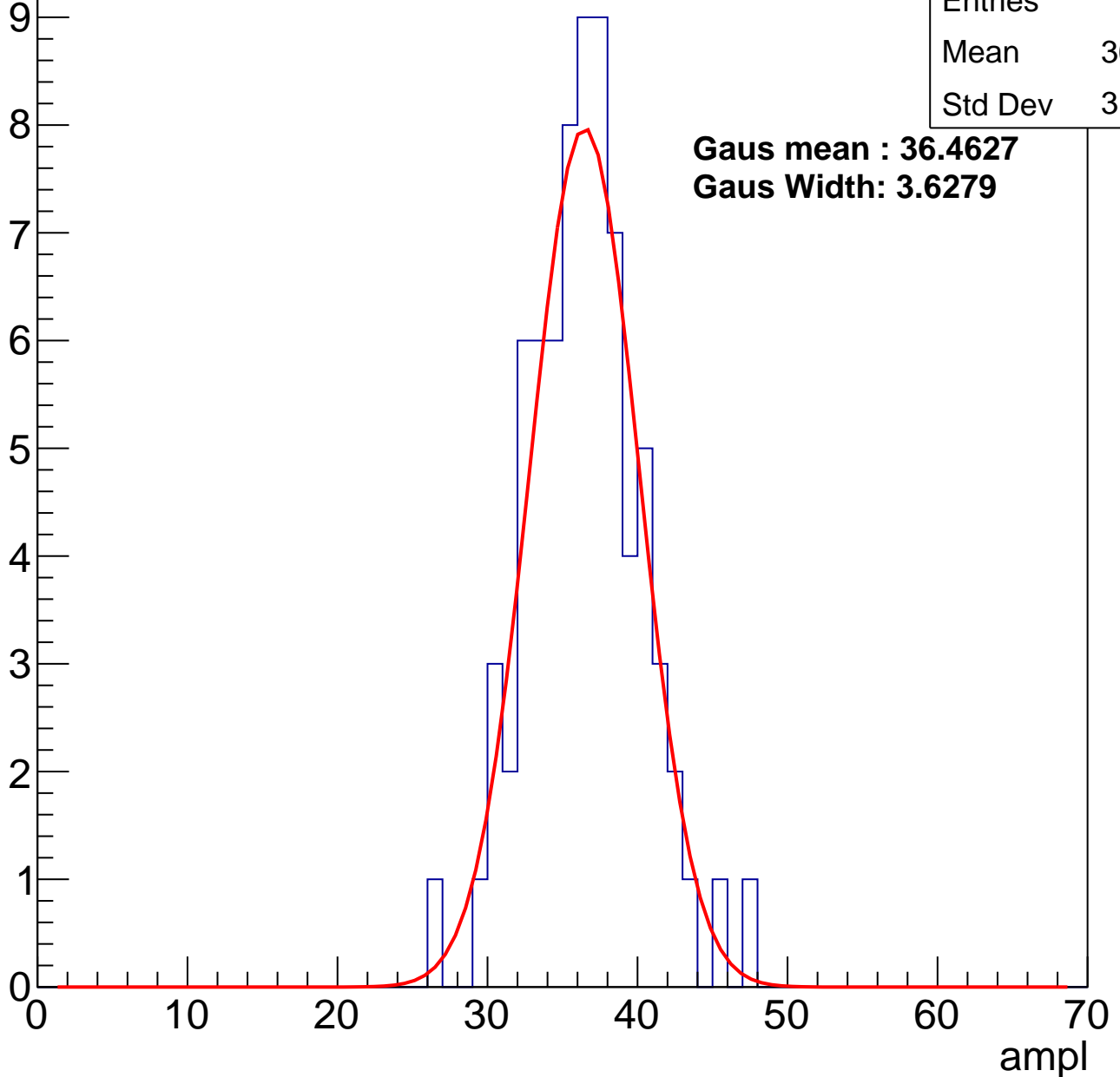
B1L100S, U1-ch115, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	36.03
Std Dev	3.724

Gaus mean : 36.4627
Gaus Width: 3.6279



B1L100S, U1-ch115, adc2

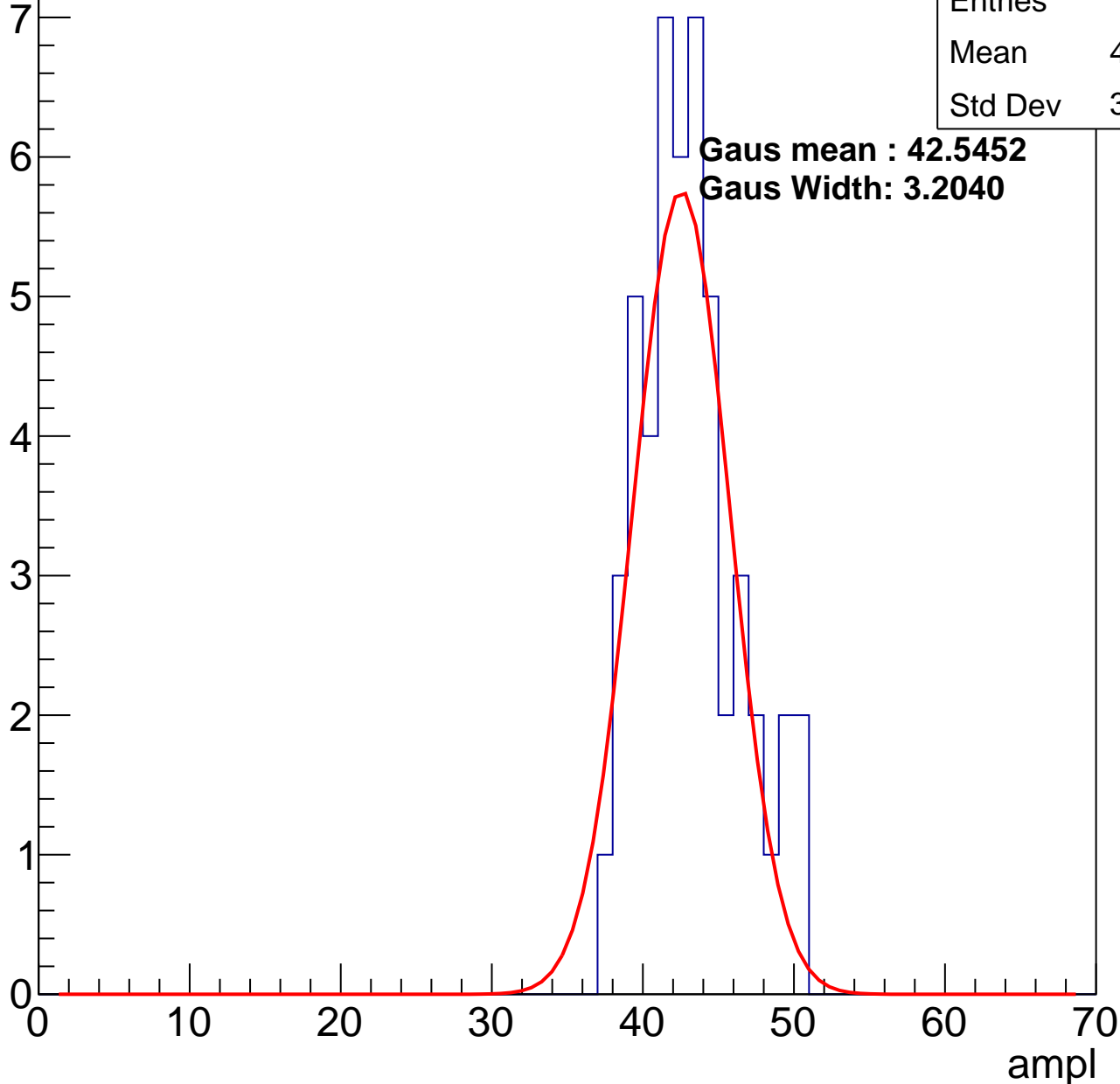
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	42.68
Std Dev	3.234

Gaus mean : 42.5452

Gaus Width: 3.2040

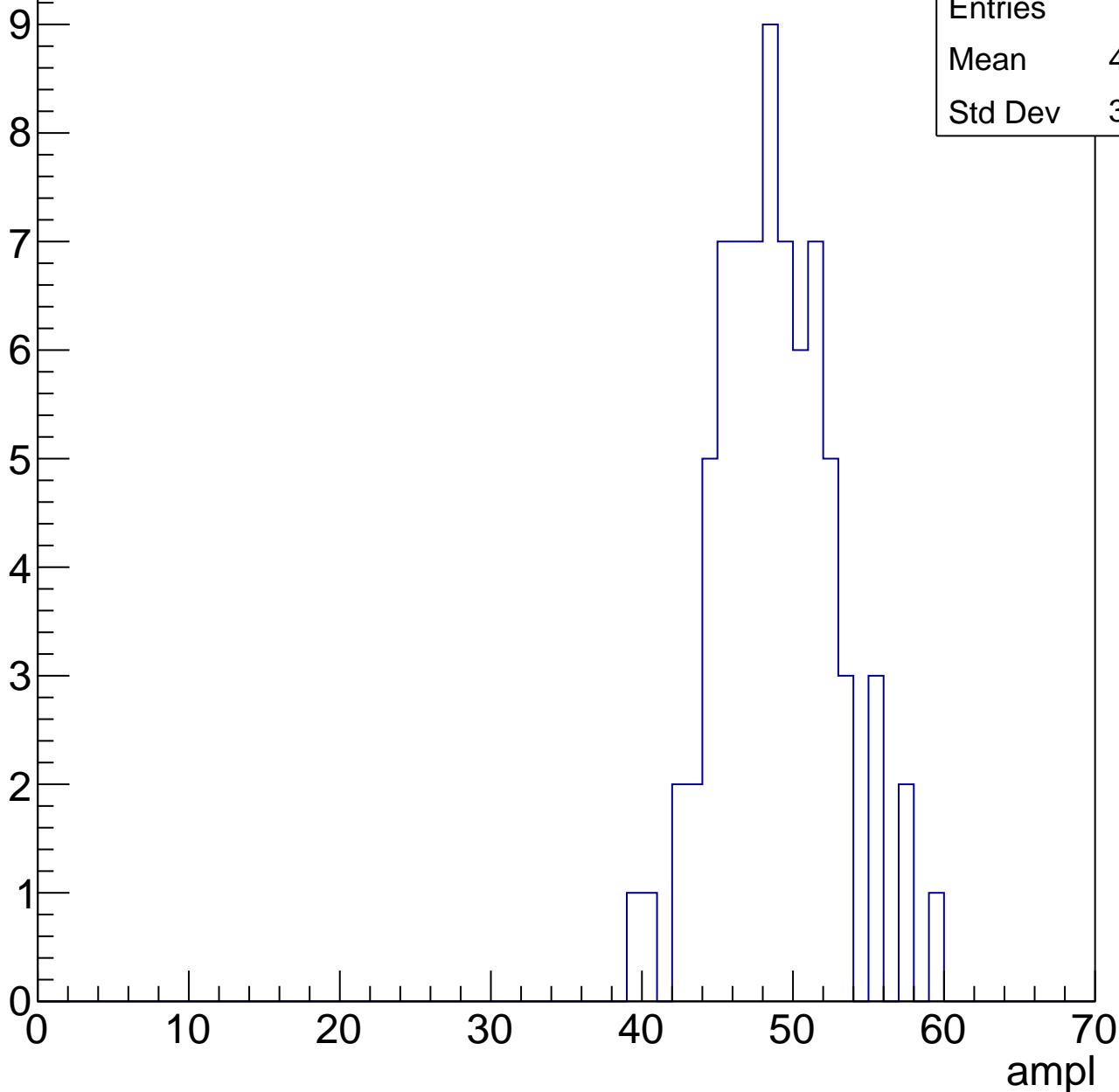


B1L100S, U1-ch115, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	48.32
Std Dev	3.858



B1L100S, U1-ch115, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entries	68
Mean	55.5
Std Dev	2.988

Entry

10

8

6

4

2

0

0

10

20

30

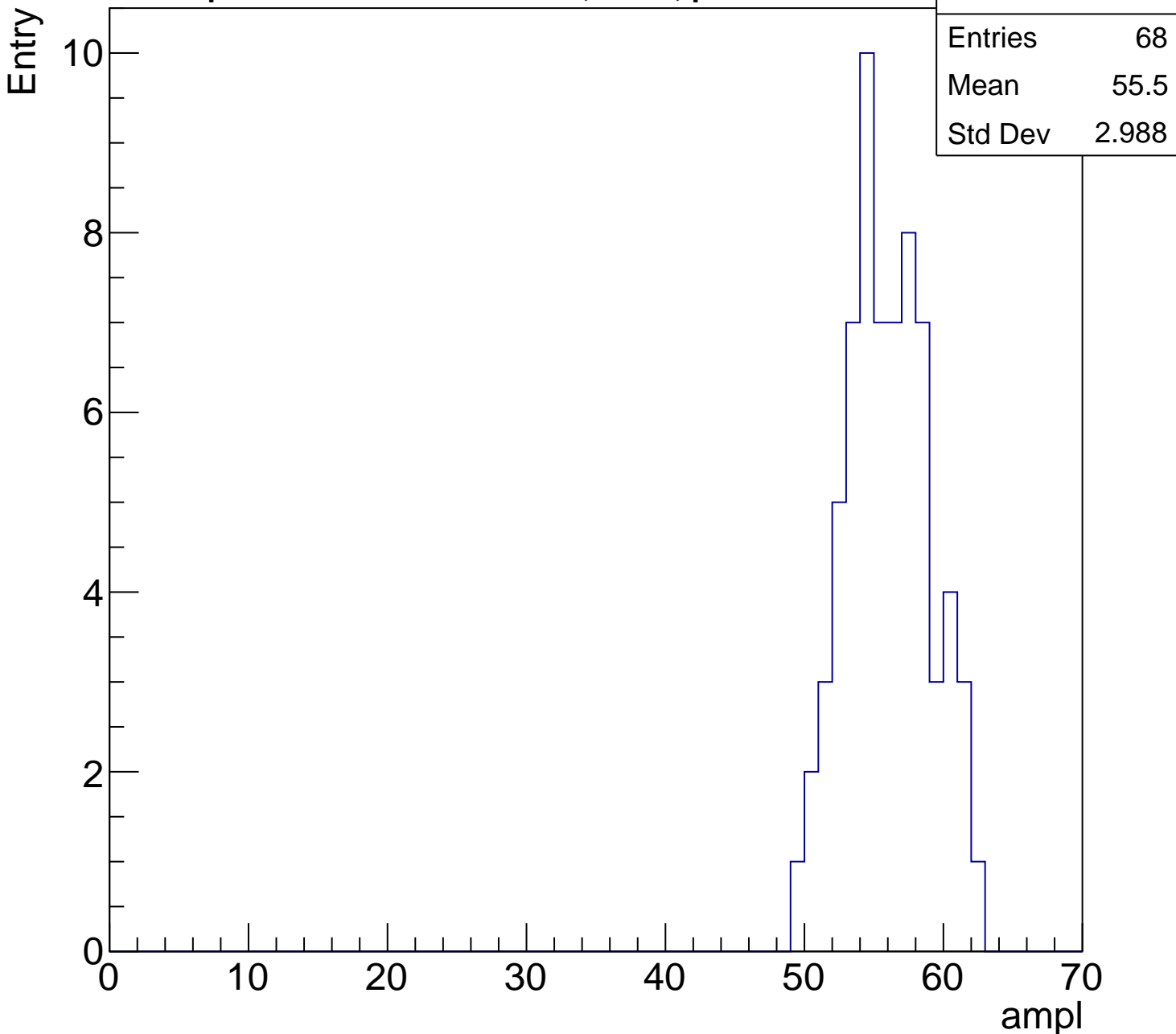
40

50

60

ampl

70

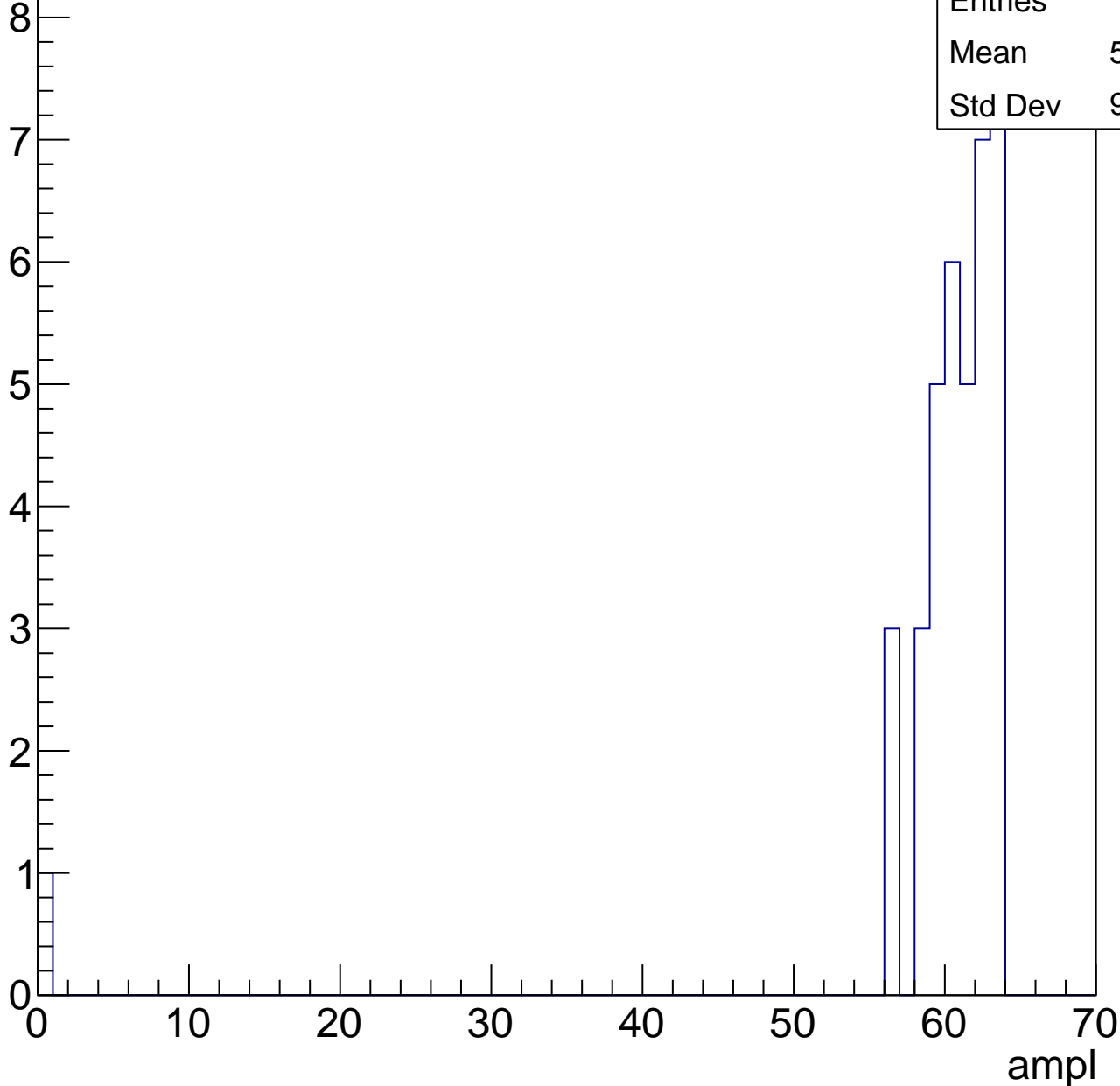


B1L100S, U1-ch115, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

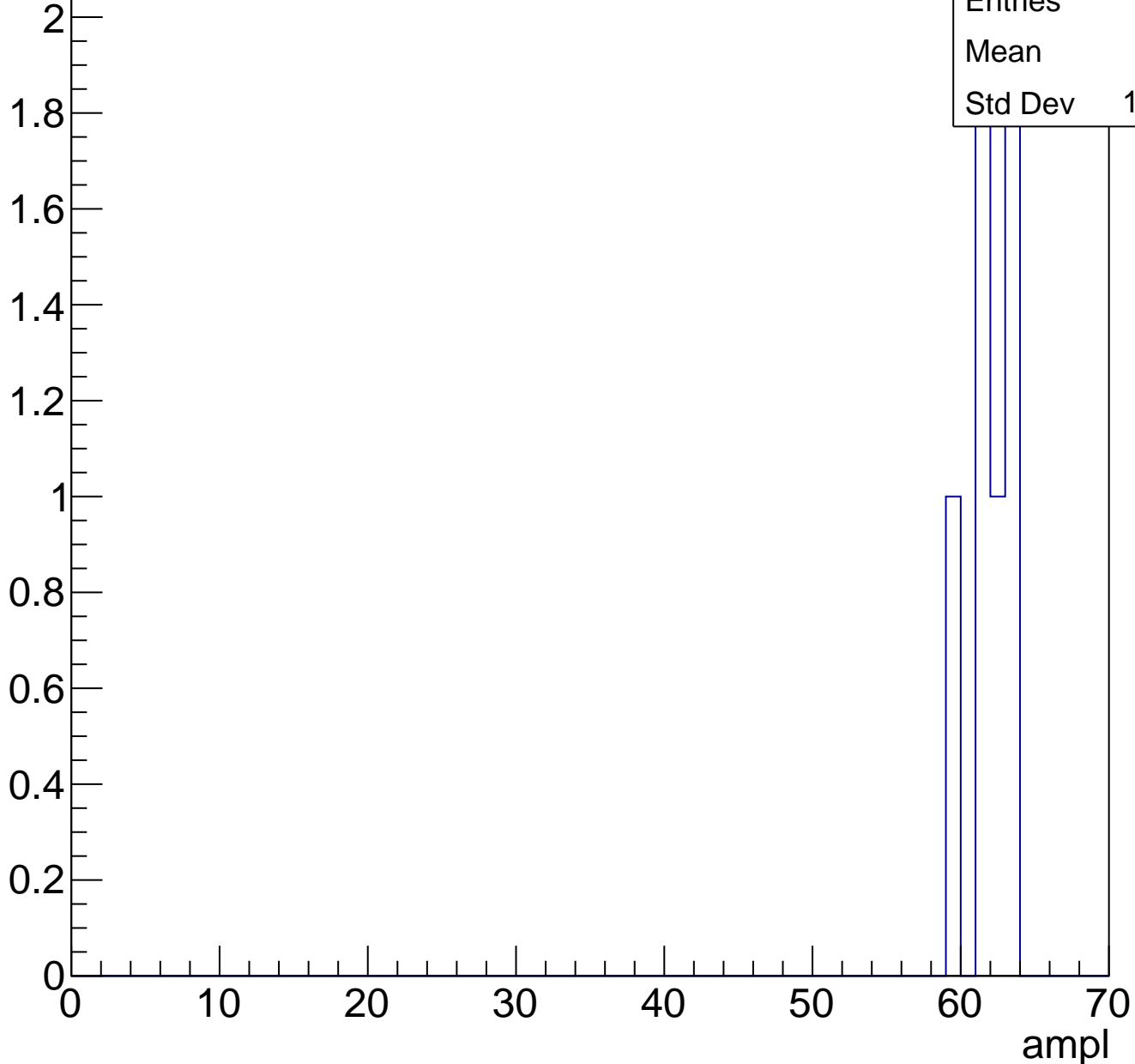
Entries	38
Mean	58.95
Std Dev	9.905



B1L100S, U1-ch115, adc6

calib_packv5_042523_0143.root, FC#4, port A2

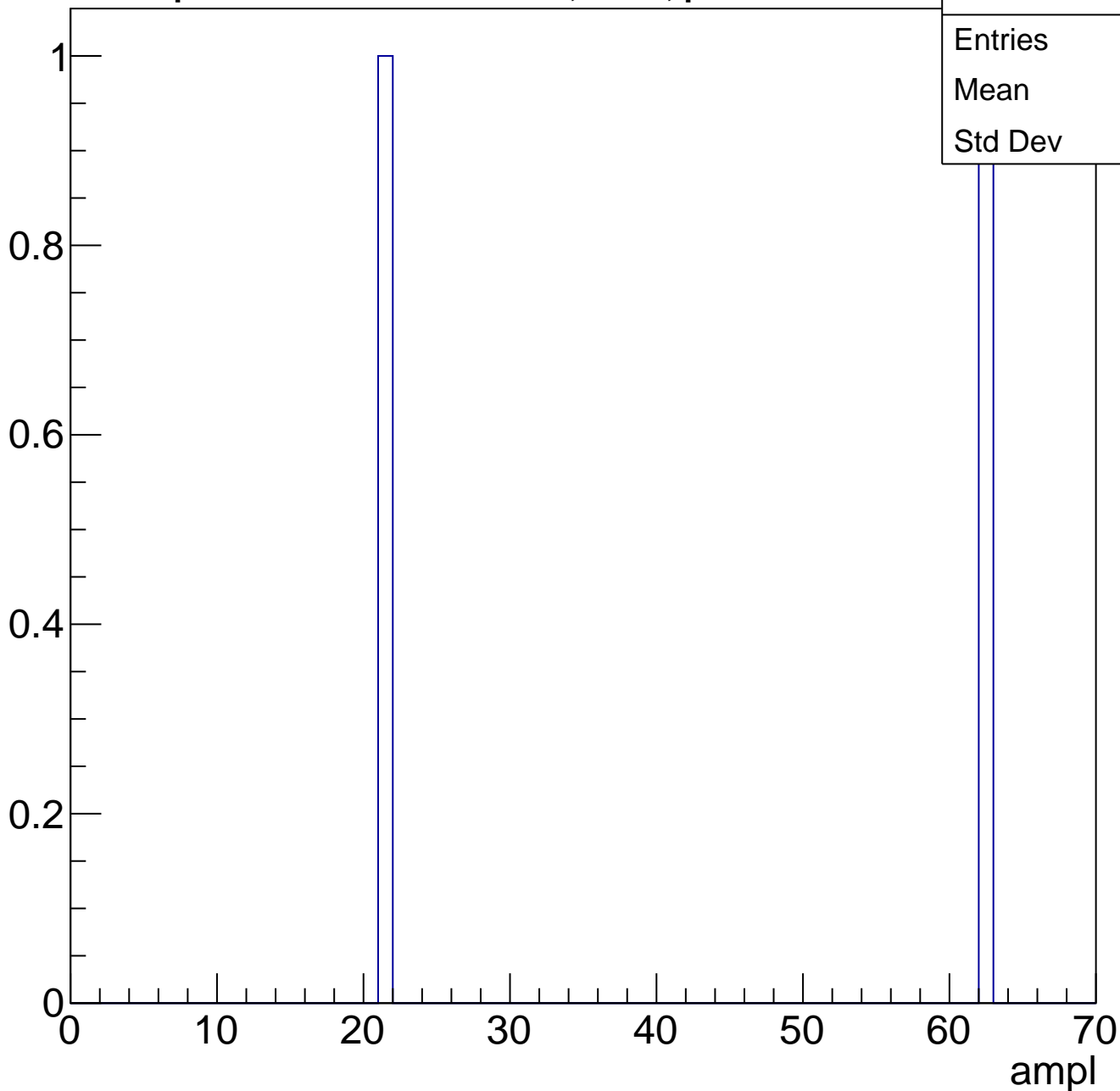
Entry



B1L100S, U1-ch115, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	41.5
Std Dev	20.5

B1L100S, U1-ch116, adc0

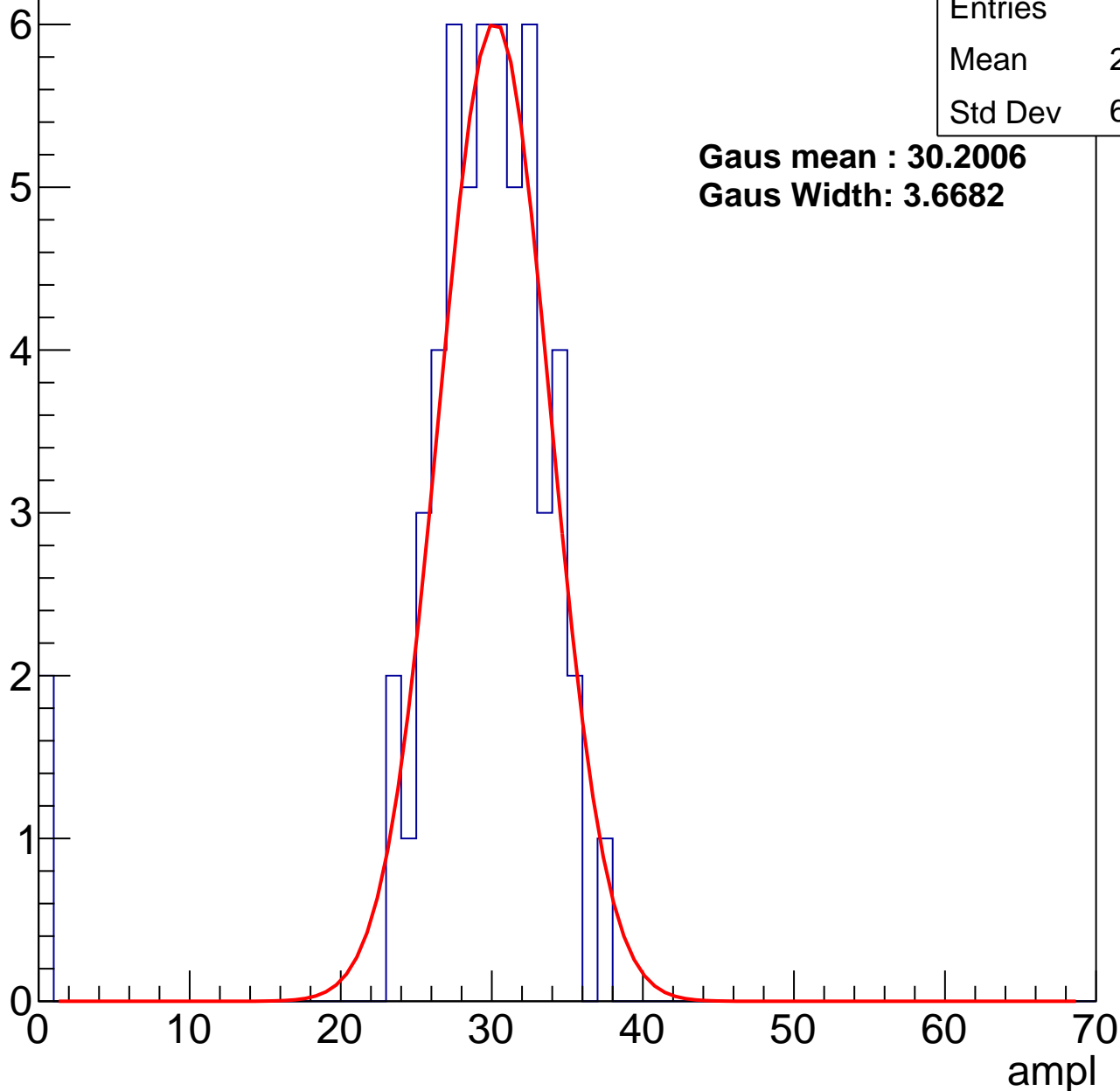
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	28.46
Std Dev	6.324

Gaus mean : 30.2006

Gaus Width: 3.6682



B1L100S, U1-ch116, adc1

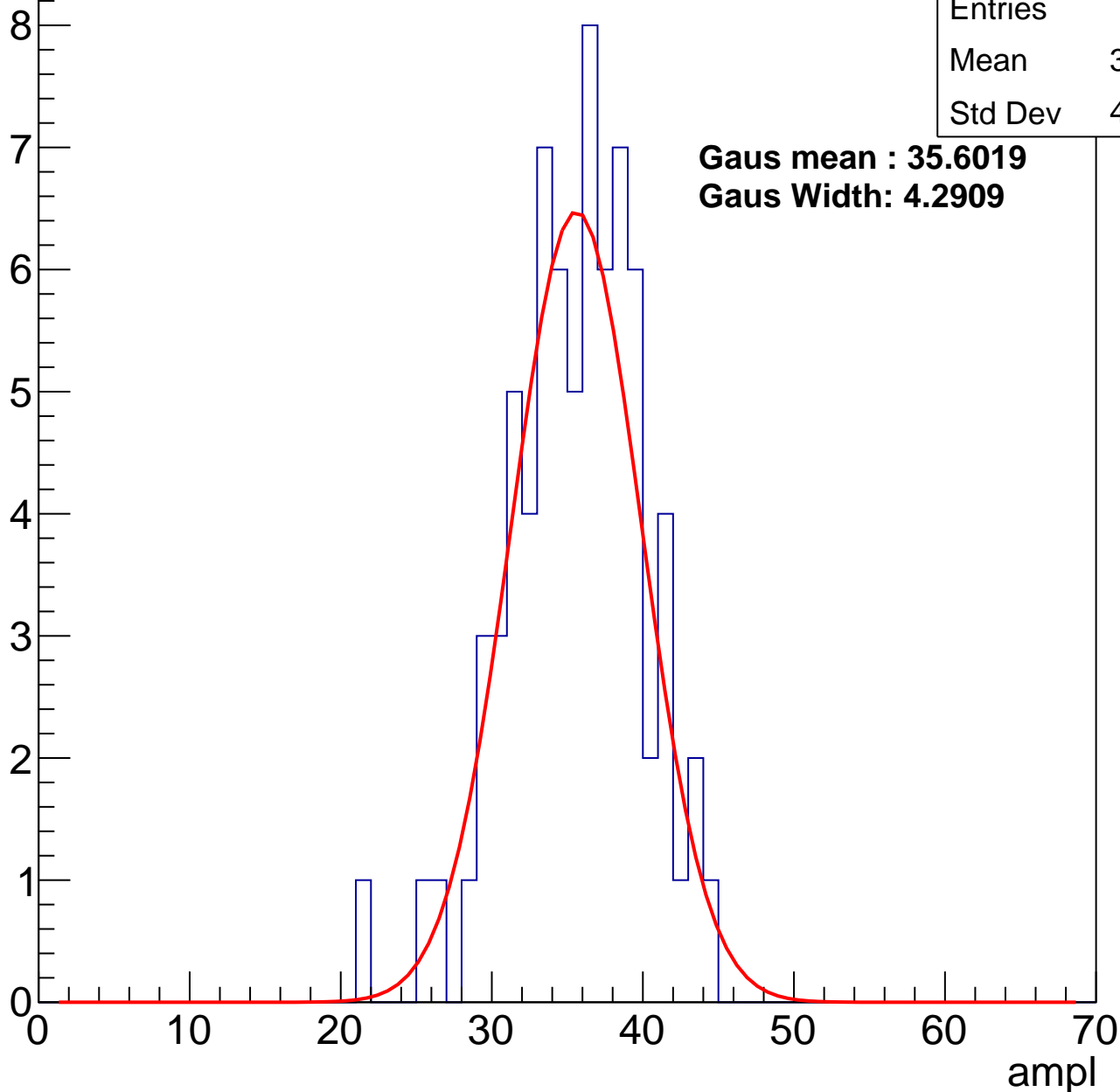
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	35.08
Std Dev	4.352

Gaus mean : 35.6019

Gaus Width: 4.2909



B1L100S, U1-ch116, adc2

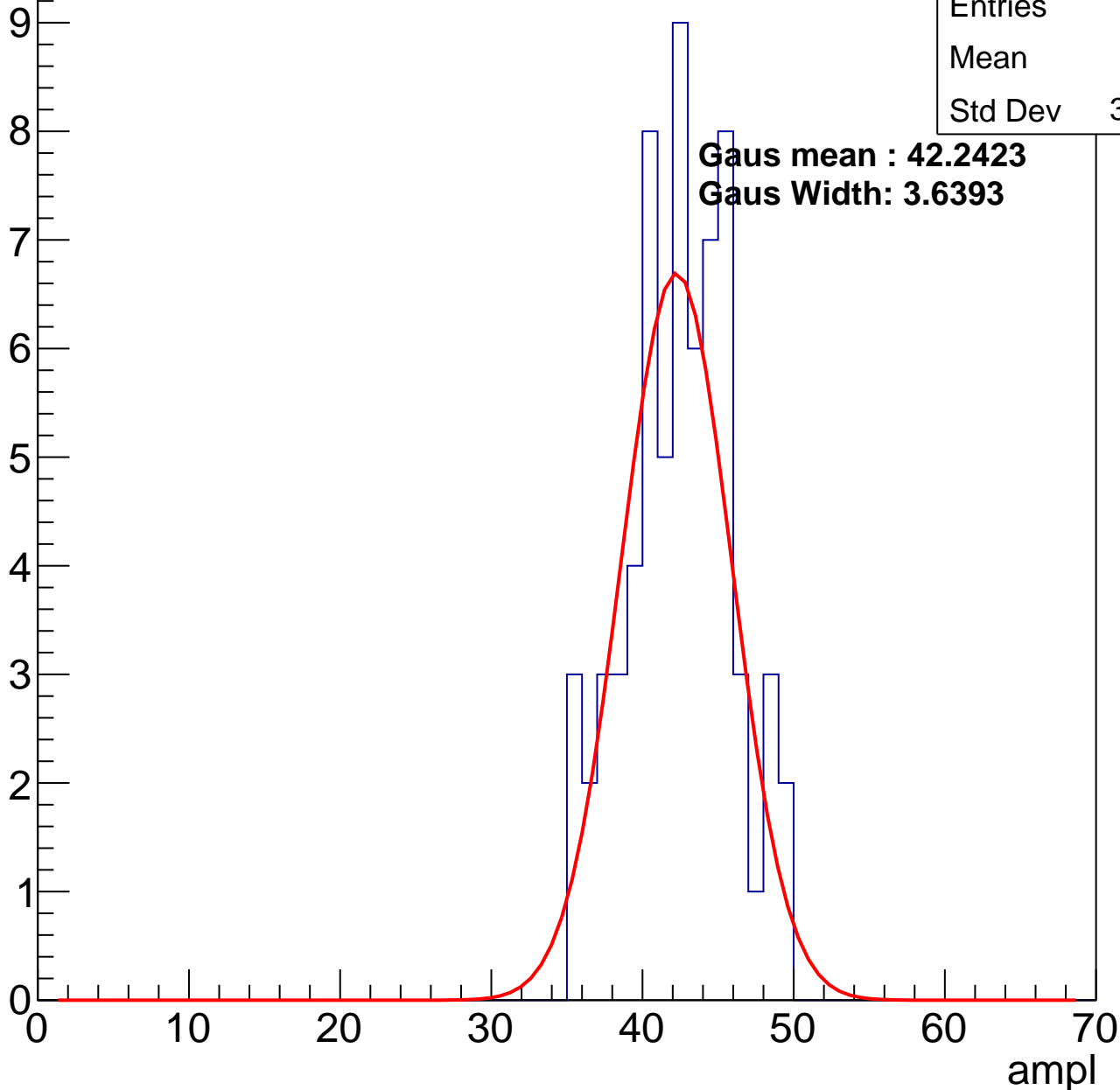
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42
Std Dev	3.455

Gaus mean : 42.2423

Gaus Width: 3.6393

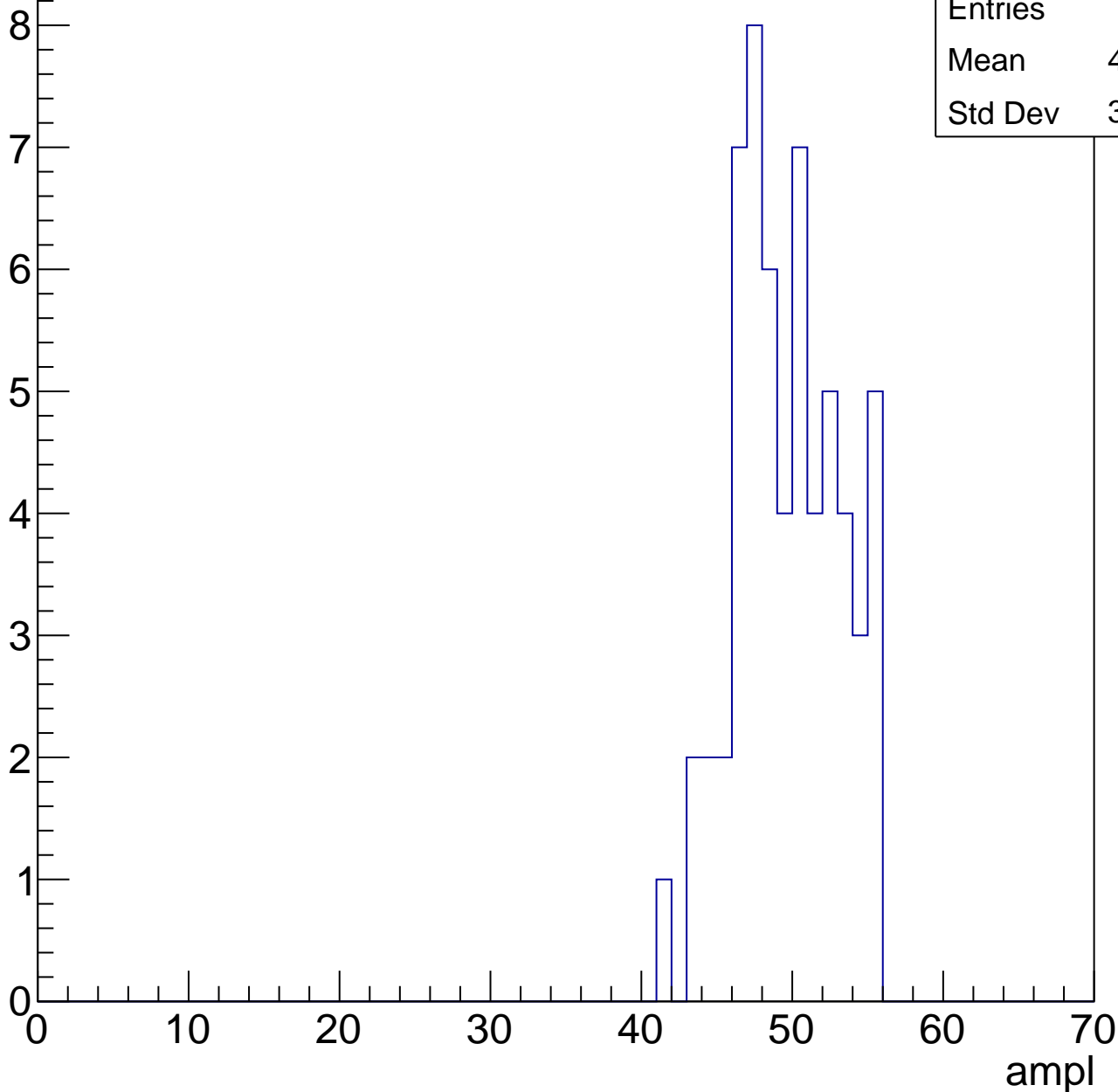


B1L100S, U1-ch116, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	60
Mean	49.17
Std Dev	3.426

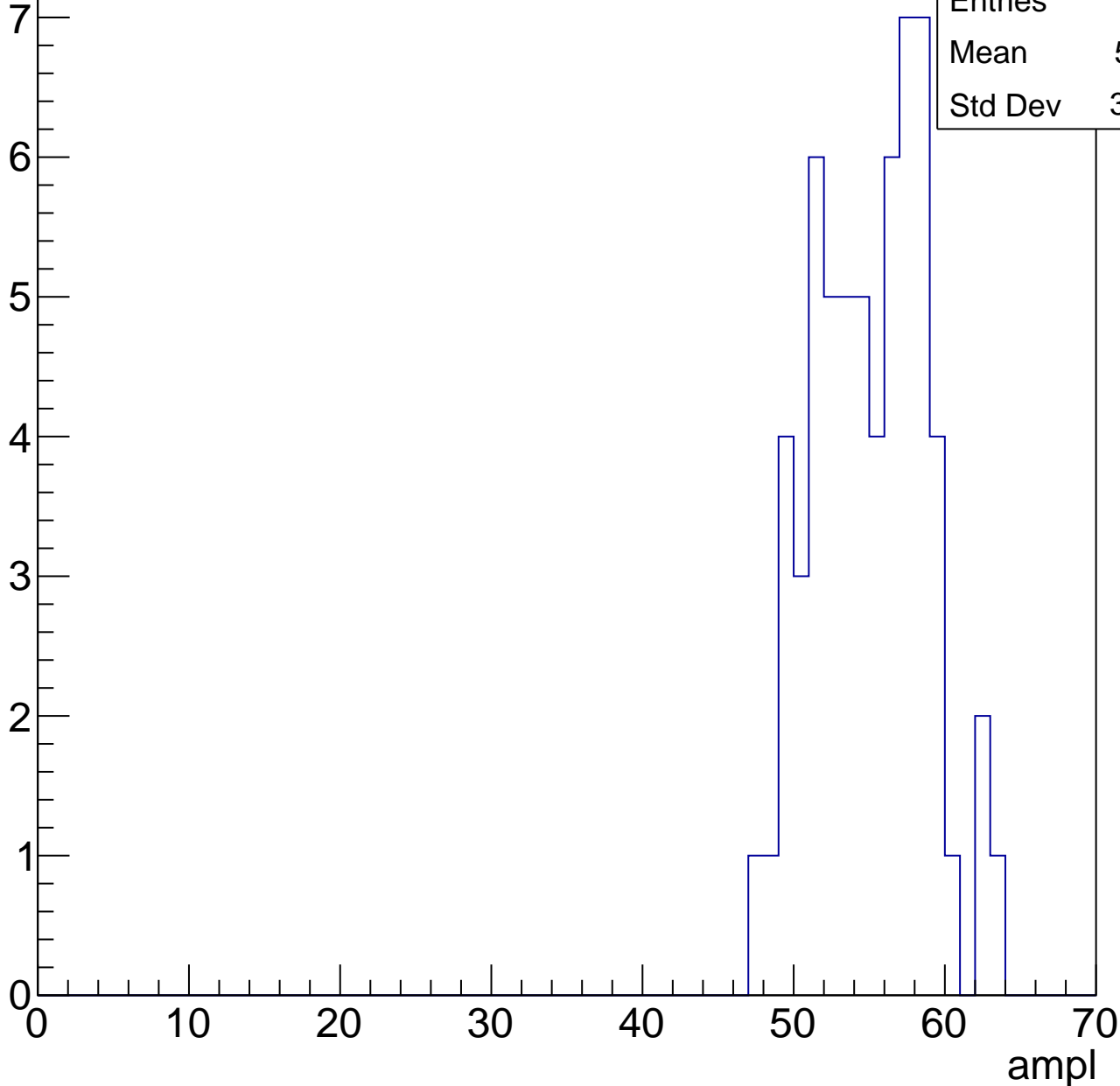


B1L100S, U1-ch116, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

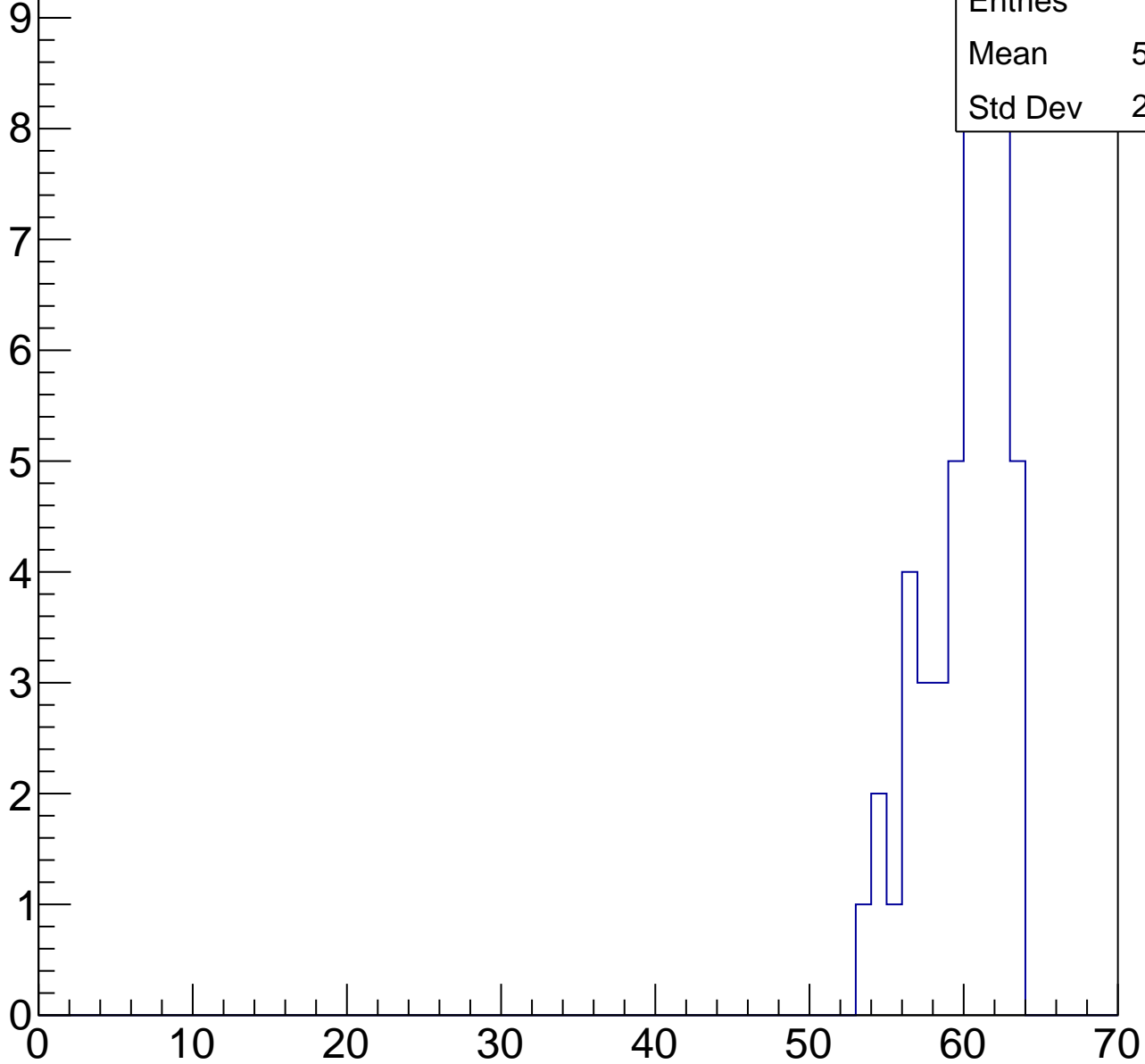
Entries	62
Mean	54.61
Std Dev	3.674



B1L100S, U1-ch116, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



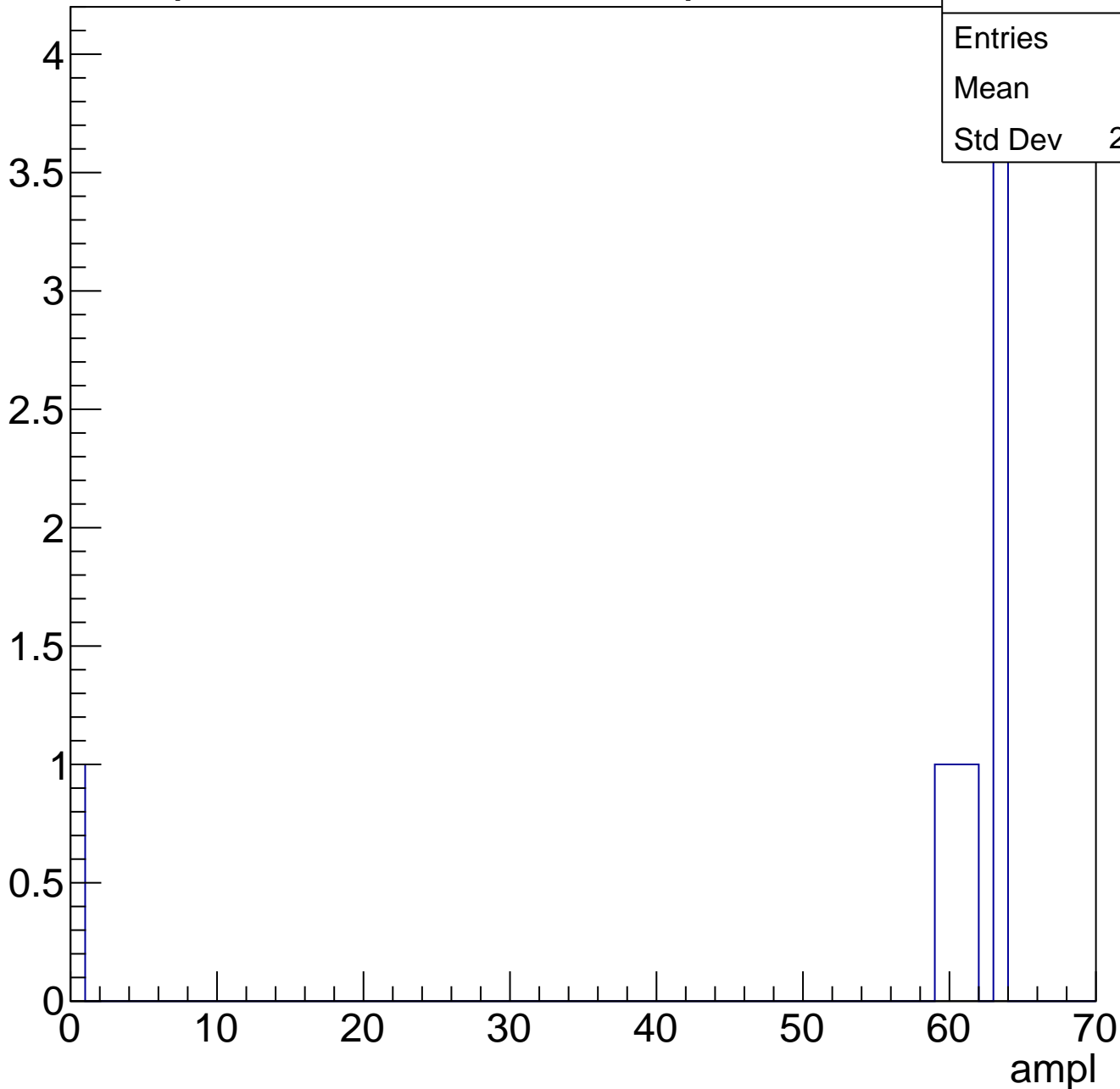
Entries	49
Mean	59.59
Std Dev	2.602

ampl

B1L100S, U1-ch116, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch116, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch117, adc0

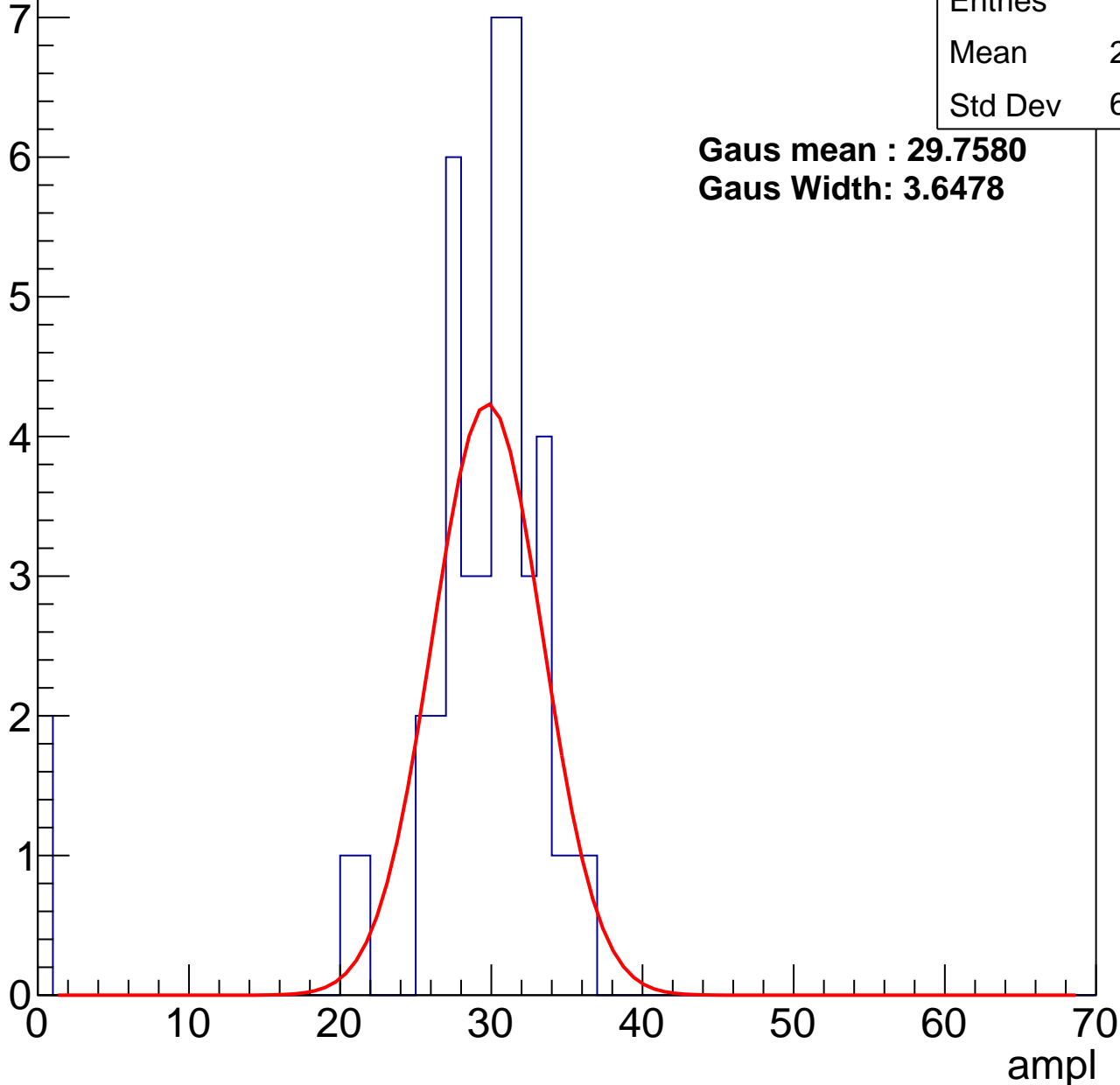
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	28.09
Std Dev	6.914

Gaus mean : 29.7580

Gaus Width: 3.6478



B1L100S, U1-ch117, adc1

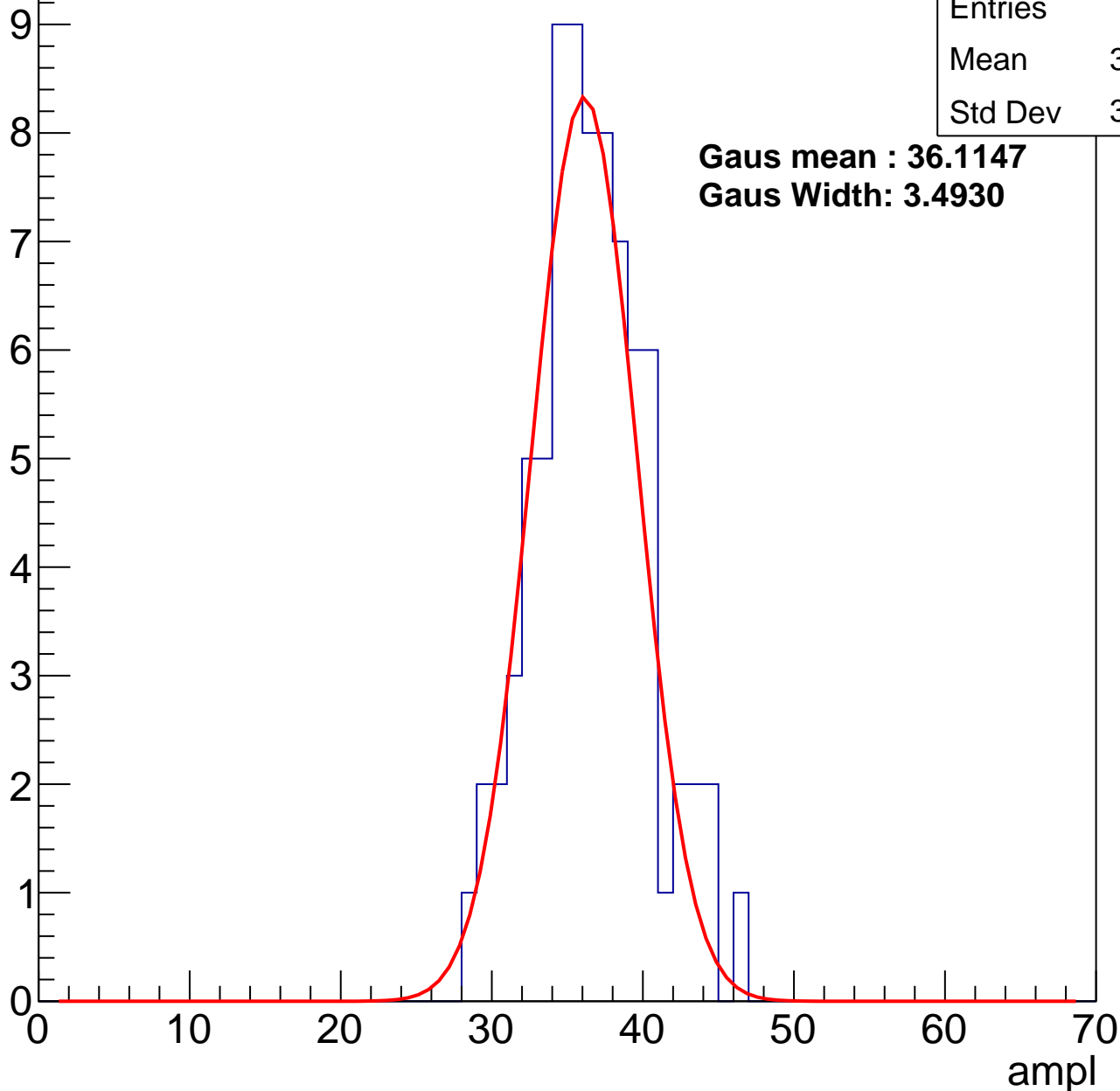
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	36.13
Std Dev	3.709

Gaus mean : 36.1147

Gaus Width: 3.4930



B1L100S, U1-ch117, adc2

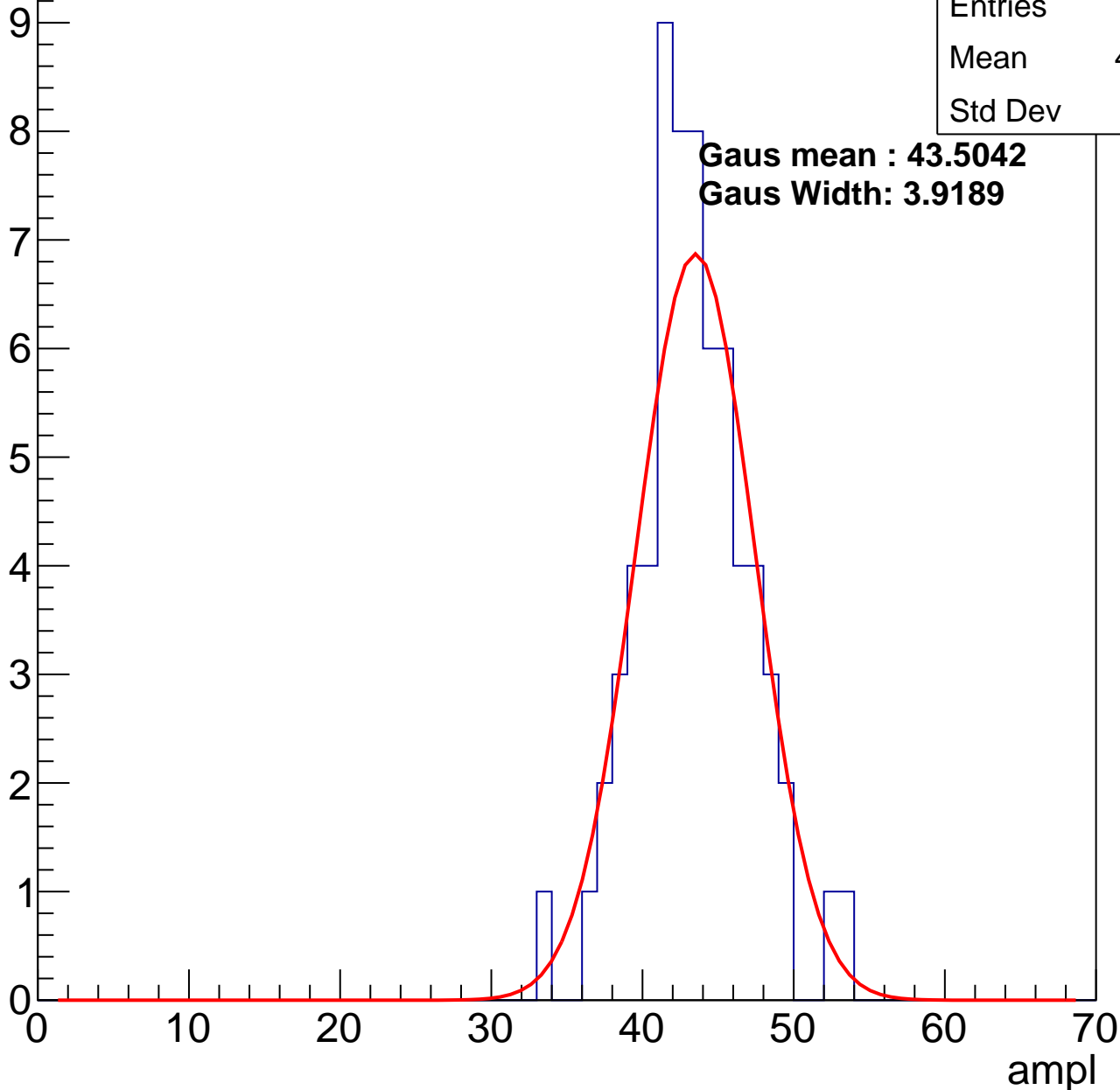
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	67
Mean	42.91
Std Dev	3.66

Gaus mean : 43.5042

Gaus Width: 3.9189



B1L100S, U1-ch117, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entries	76
Mean	50.67
Std Dev	3.795

Entry

10

8

6

4

2

0

0

10

20

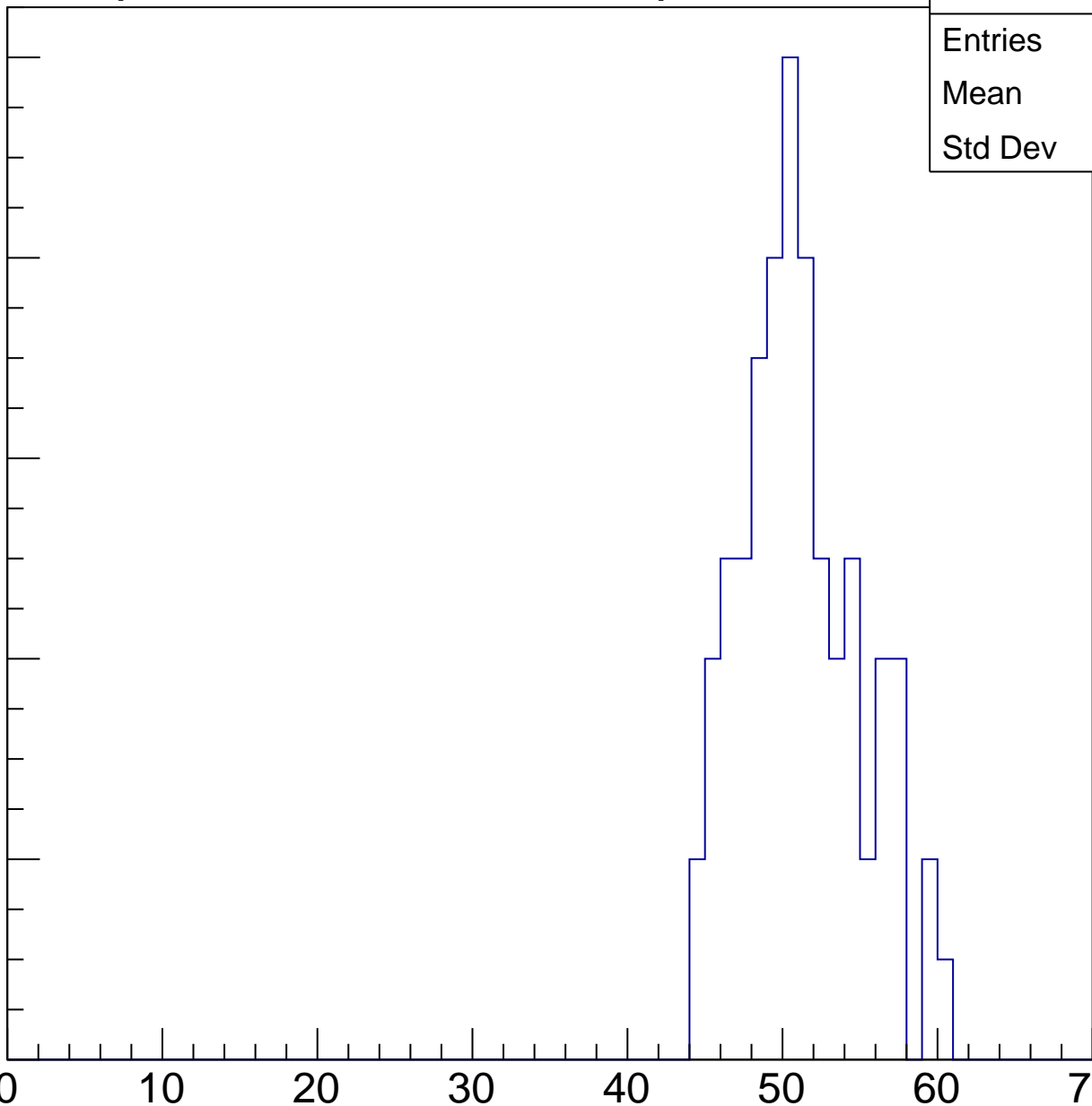
30

40

50

60

ampl

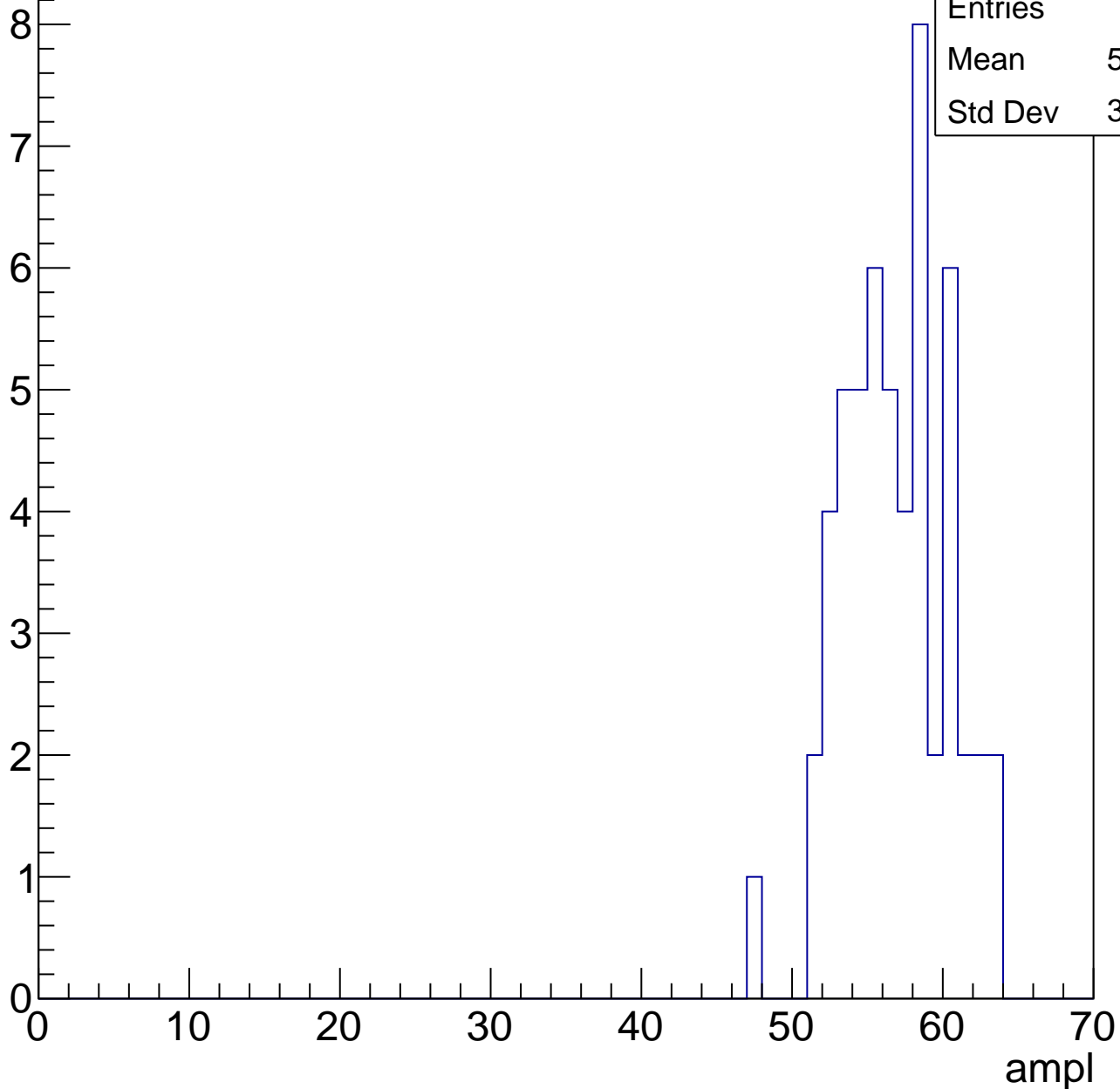


B1L100S, U1-ch117, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	54
Mean	56.37
Std Dev	3.412

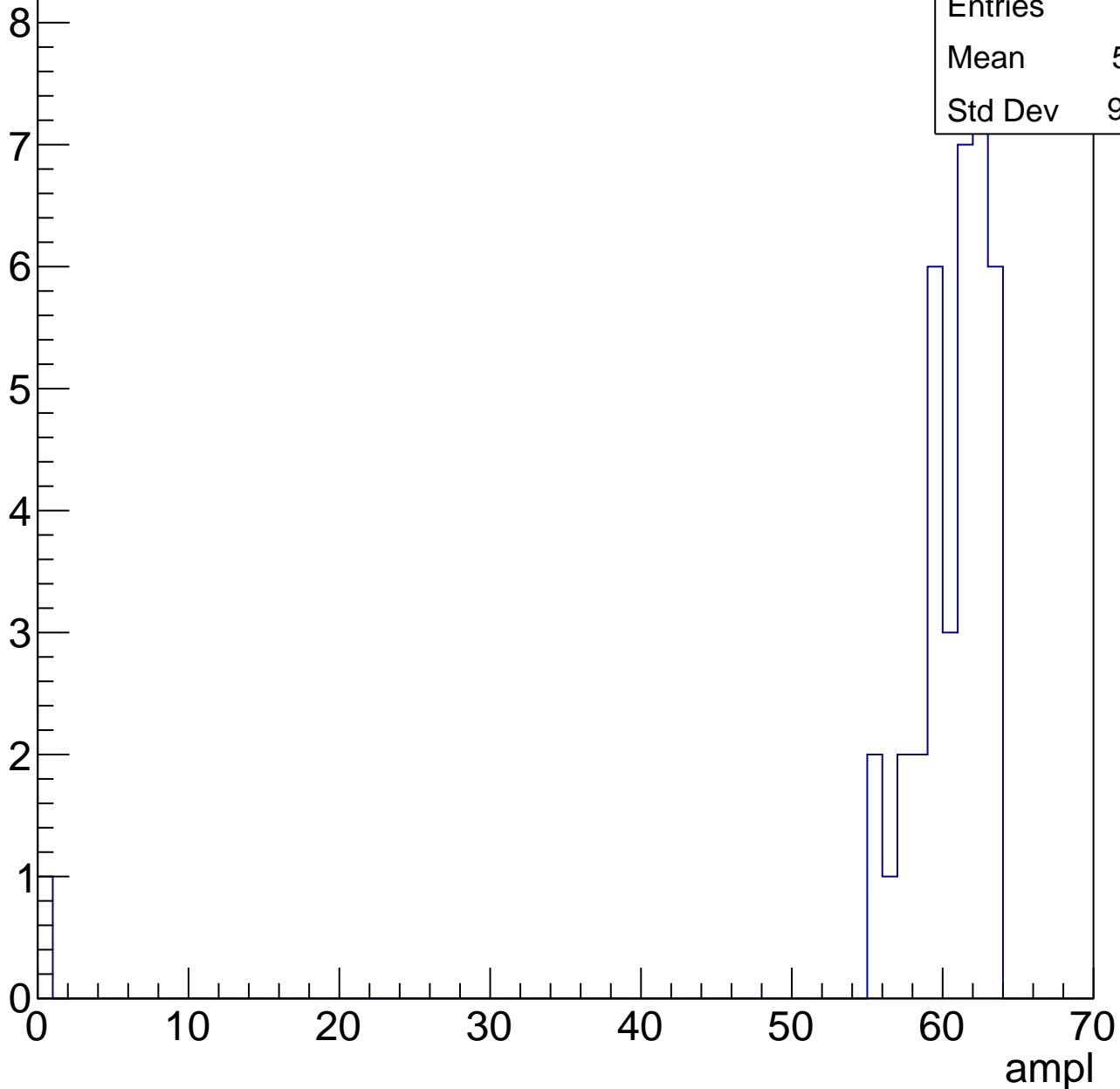


B1L100S, U1-ch117, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	38
Mean	58.71
Std Dev	9.905



B1L100S, U1-ch117, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch117, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch118, adc0

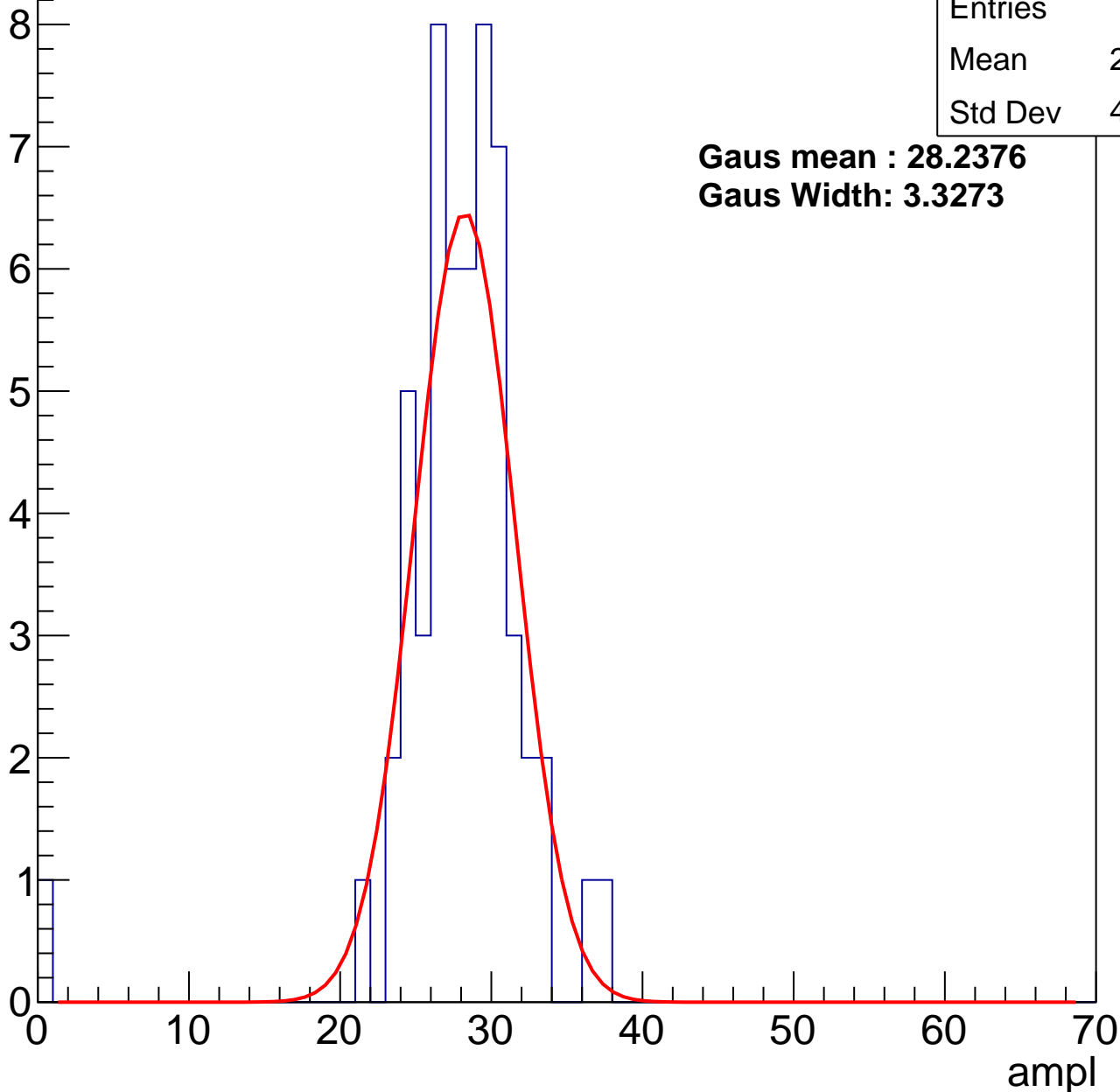
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	27.46
Std Dev	4.822

Gaus mean : 28.2376

Gaus Width: 3.3273



B1L100S, U1-ch118, adc1

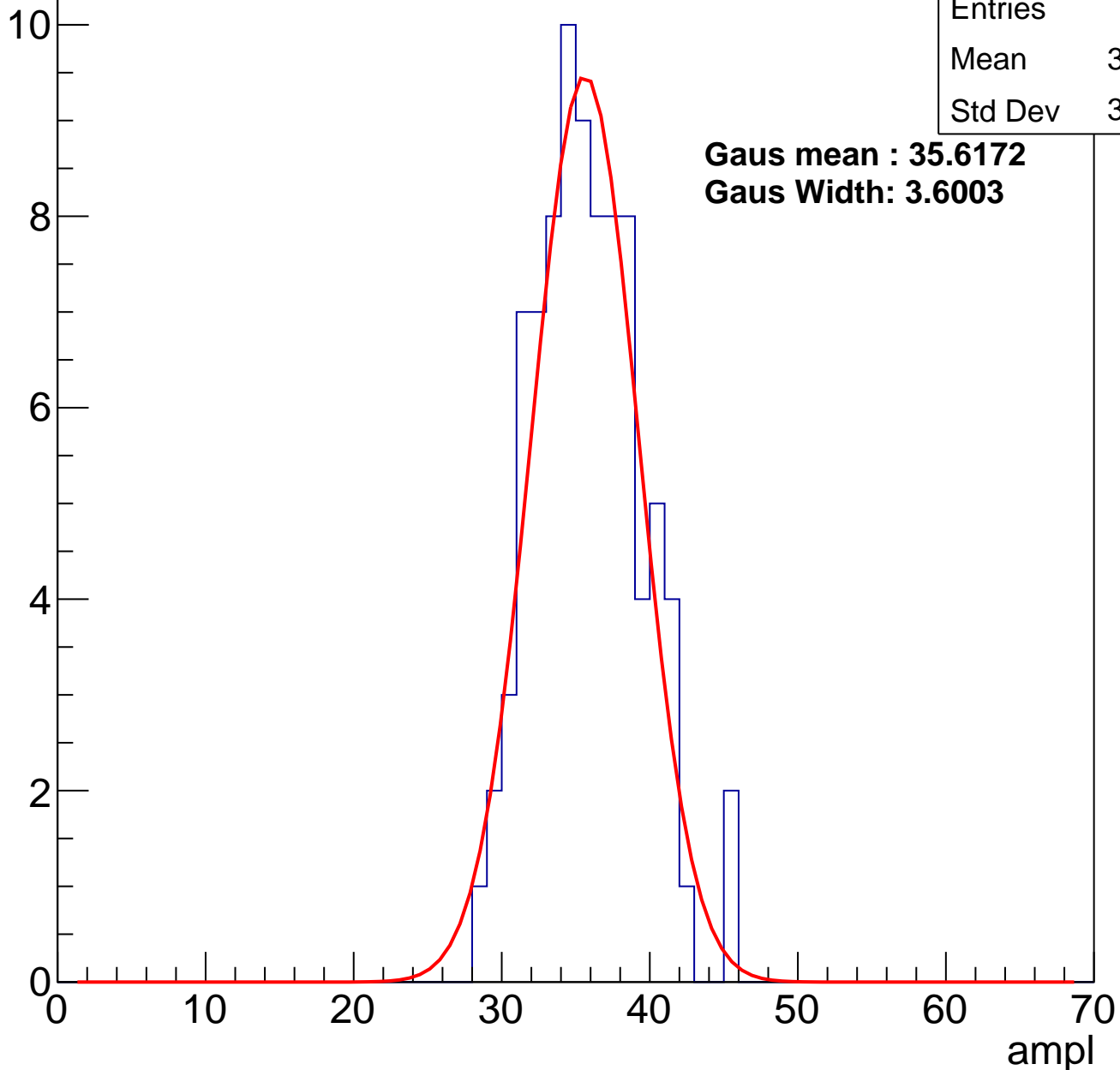
calib_packv5_042523_0143.root, FC#4, port A2

Entries	87
Mean	35.36
Std Dev	3.549

Gaus mean : 35.6172

Gaus Width: 3.6003

Entry



B1L100S, U1-ch118, adc2

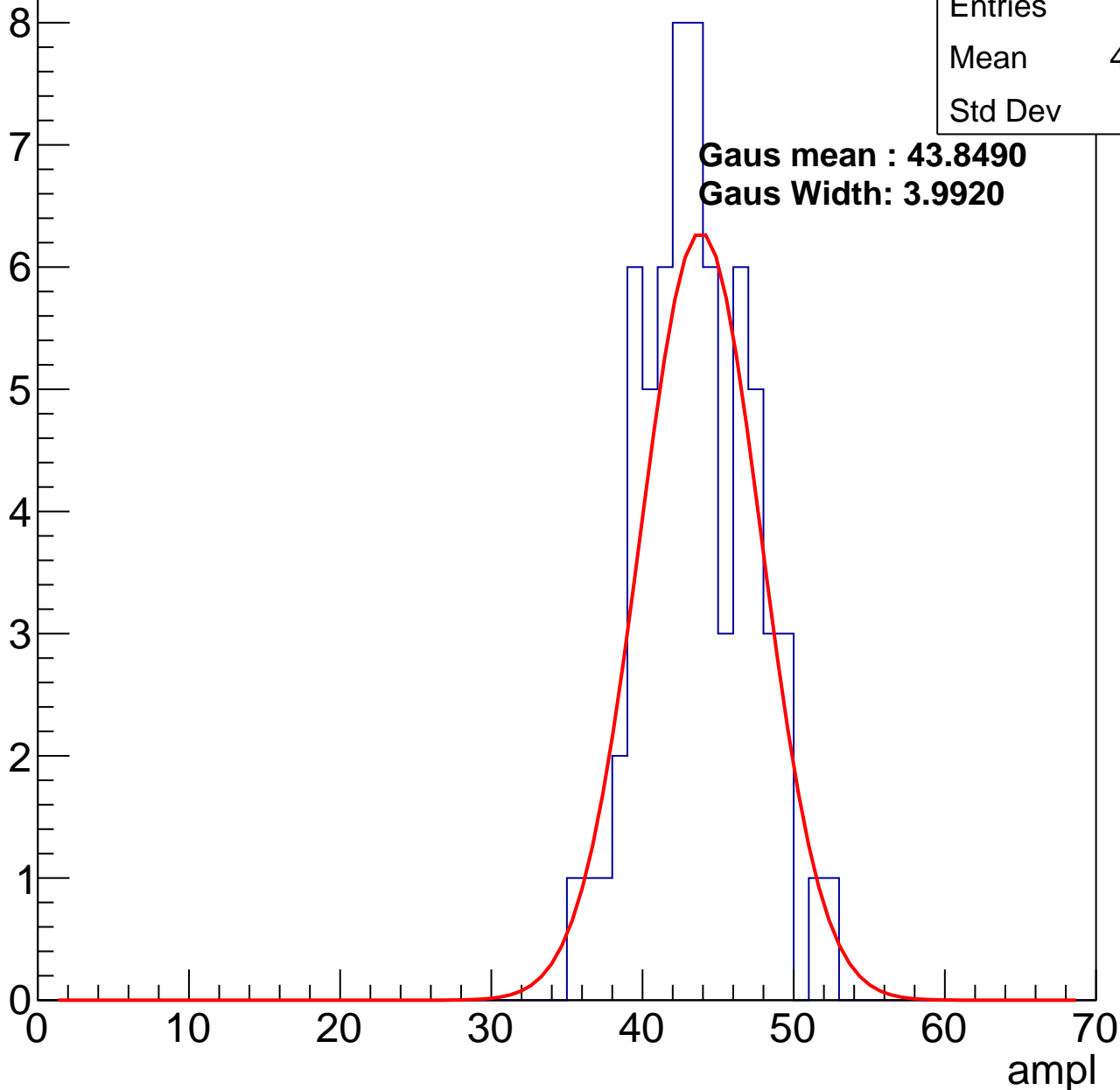
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	66
Mean	43.15
Std Dev	3.59

Gaus mean : 43.8490

Gaus Width: 3.9920

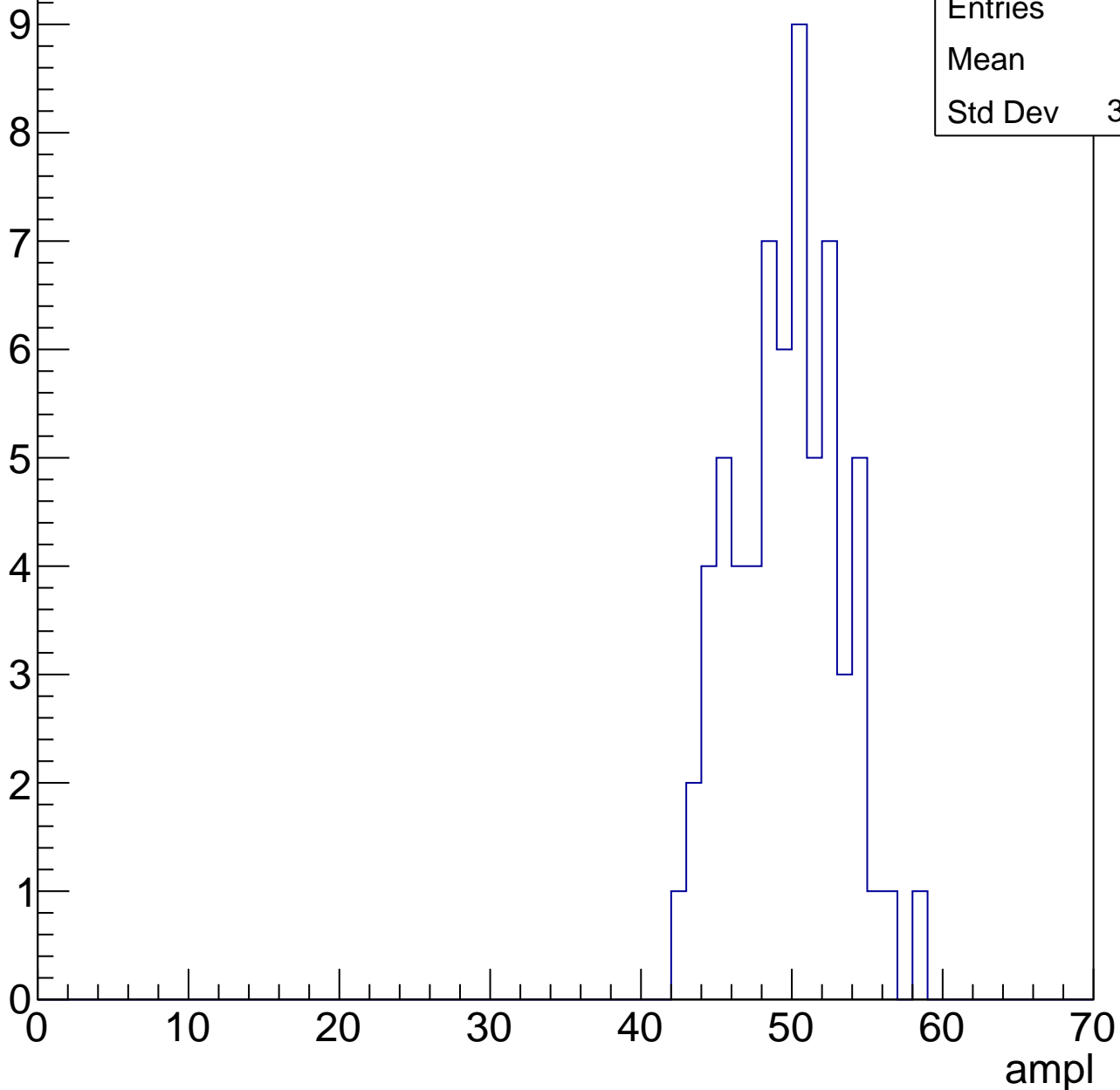


B1L100S, U1-ch118, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	65
Mean	49.2
Std Dev	3.483



B1L100S, U1-ch118, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

6

5

4

3

2

1

0

Entries	47
Mean	55.34
Std Dev	3.441

ampl

0

10

20

30

40

50

60

70

0

1

2

3

4

5

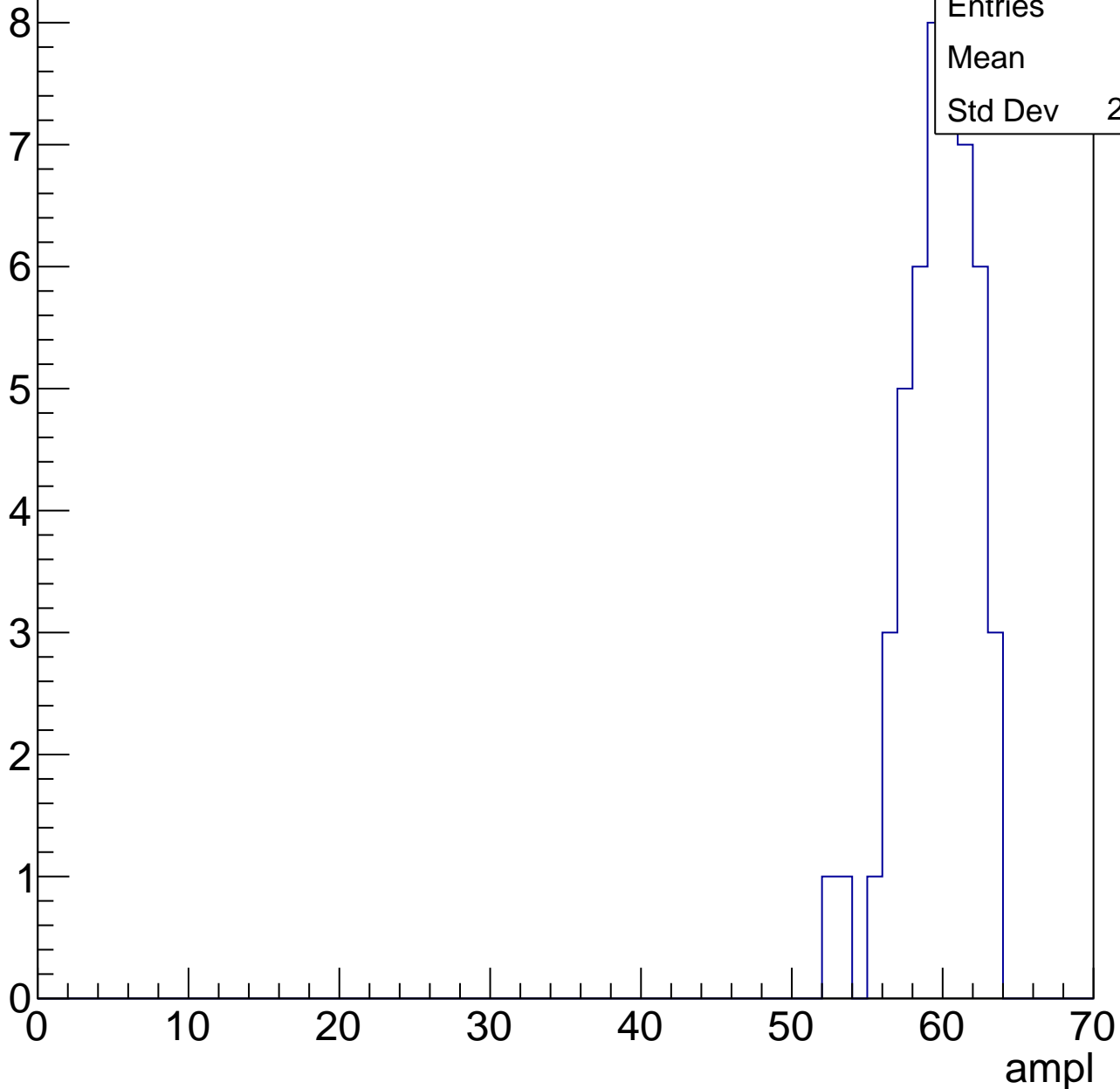
6

B1L100S, U1-ch118, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	59.2
Std Dev	2.433

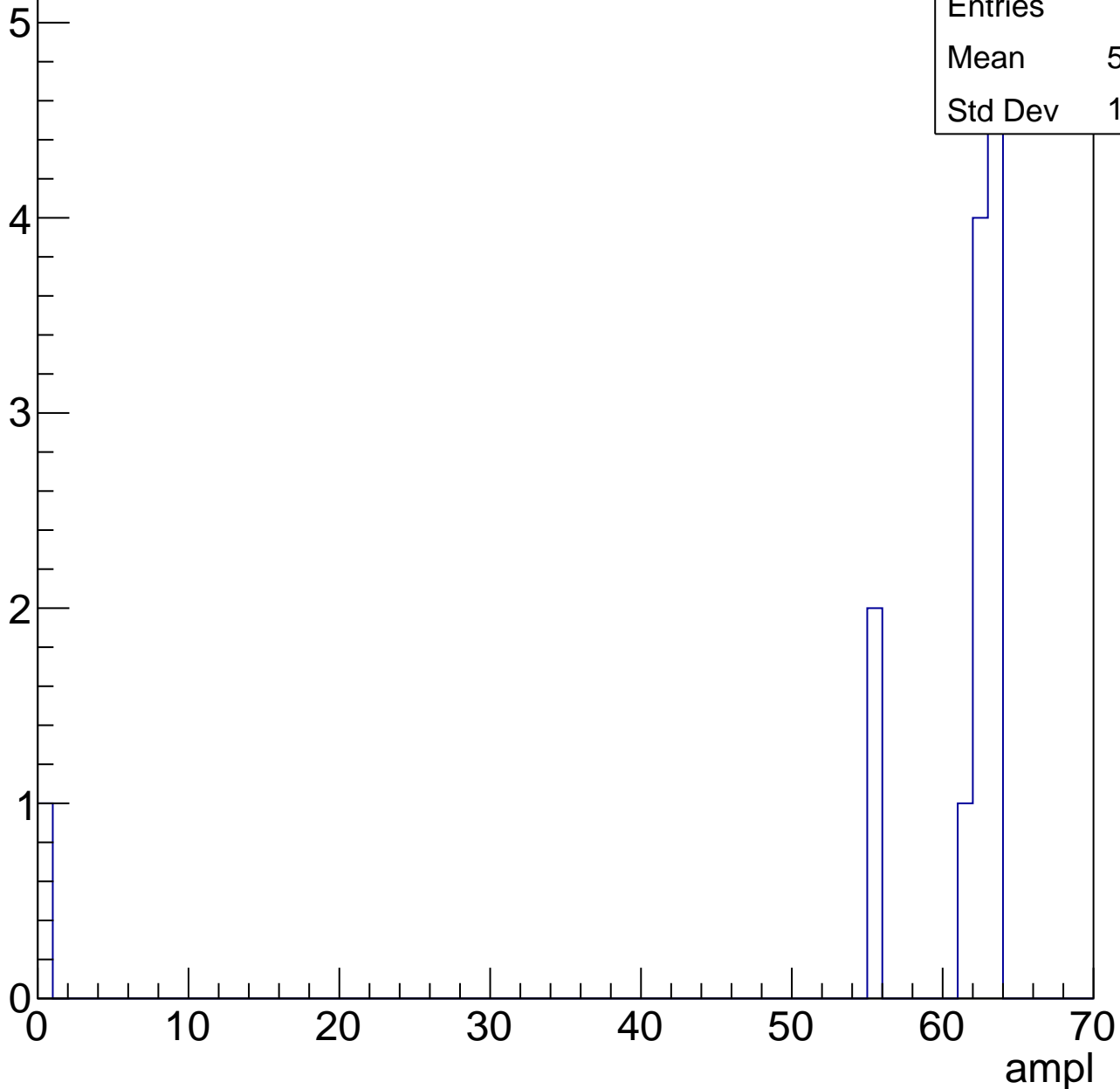


B1L100S, U1-ch118, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	13
Mean	56.46
Std Dev	16.52



B1L100S, U1-ch118, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch119, adc0

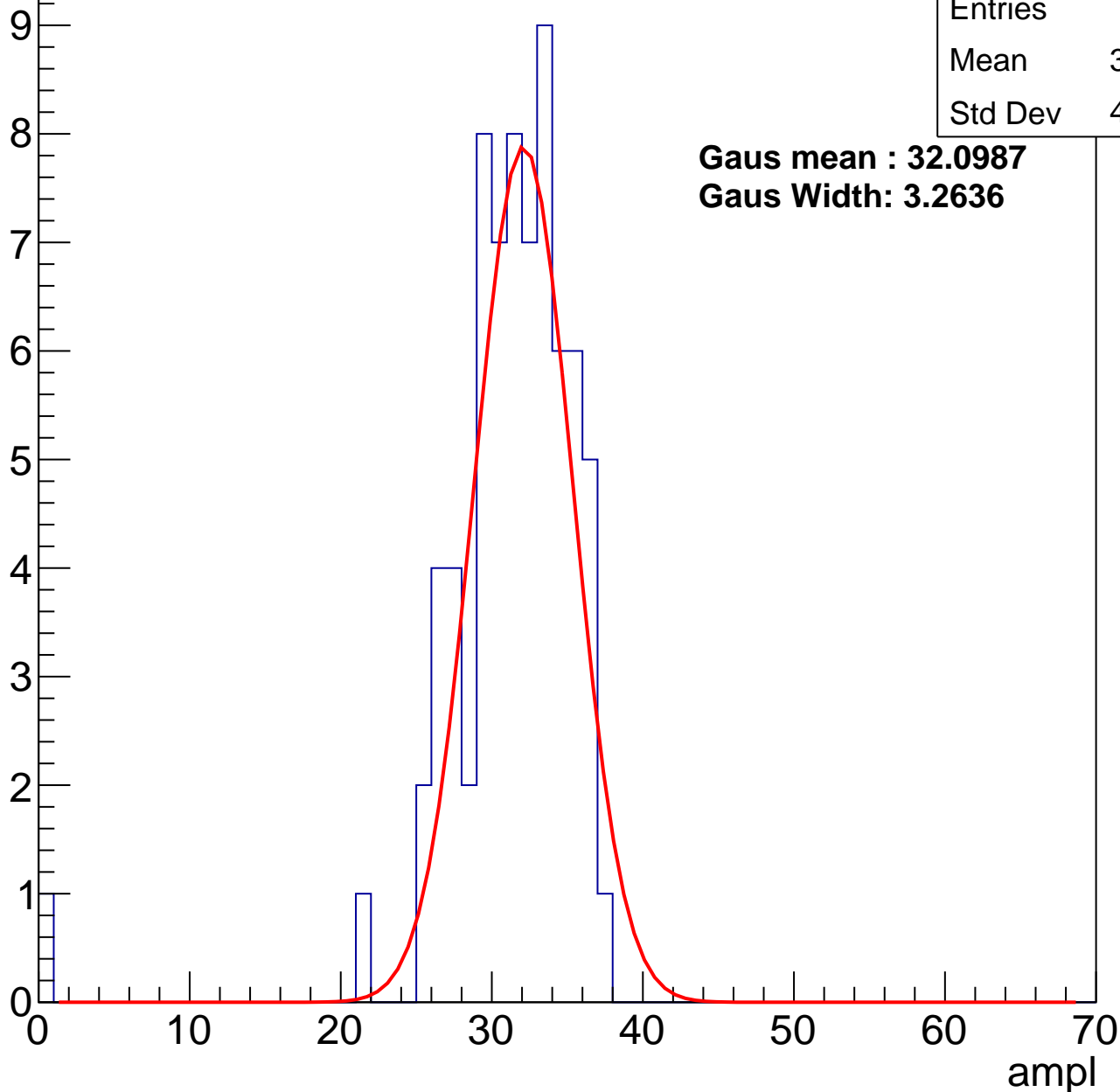
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	71
Mean	30.72
Std Dev	4.897

Gaus mean : 32.0987

Gaus Width: 3.2636



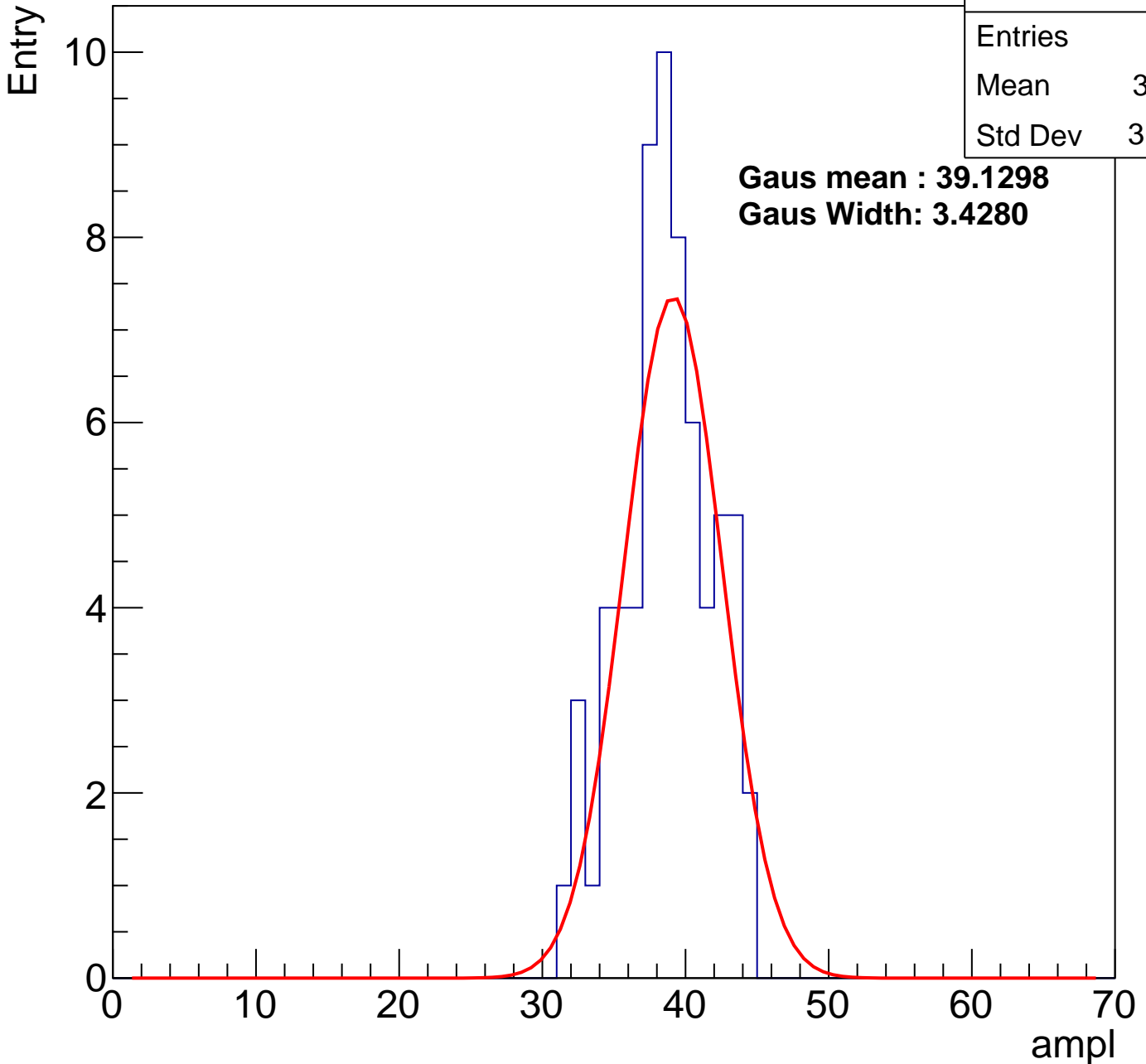
B1L100S, U1-ch119, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	66
Mean	38.21
Std Dev	3.136

Gaus mean : 39.1298

Gaus Width: 3.4280



B1L100S, U1-ch119, adc2

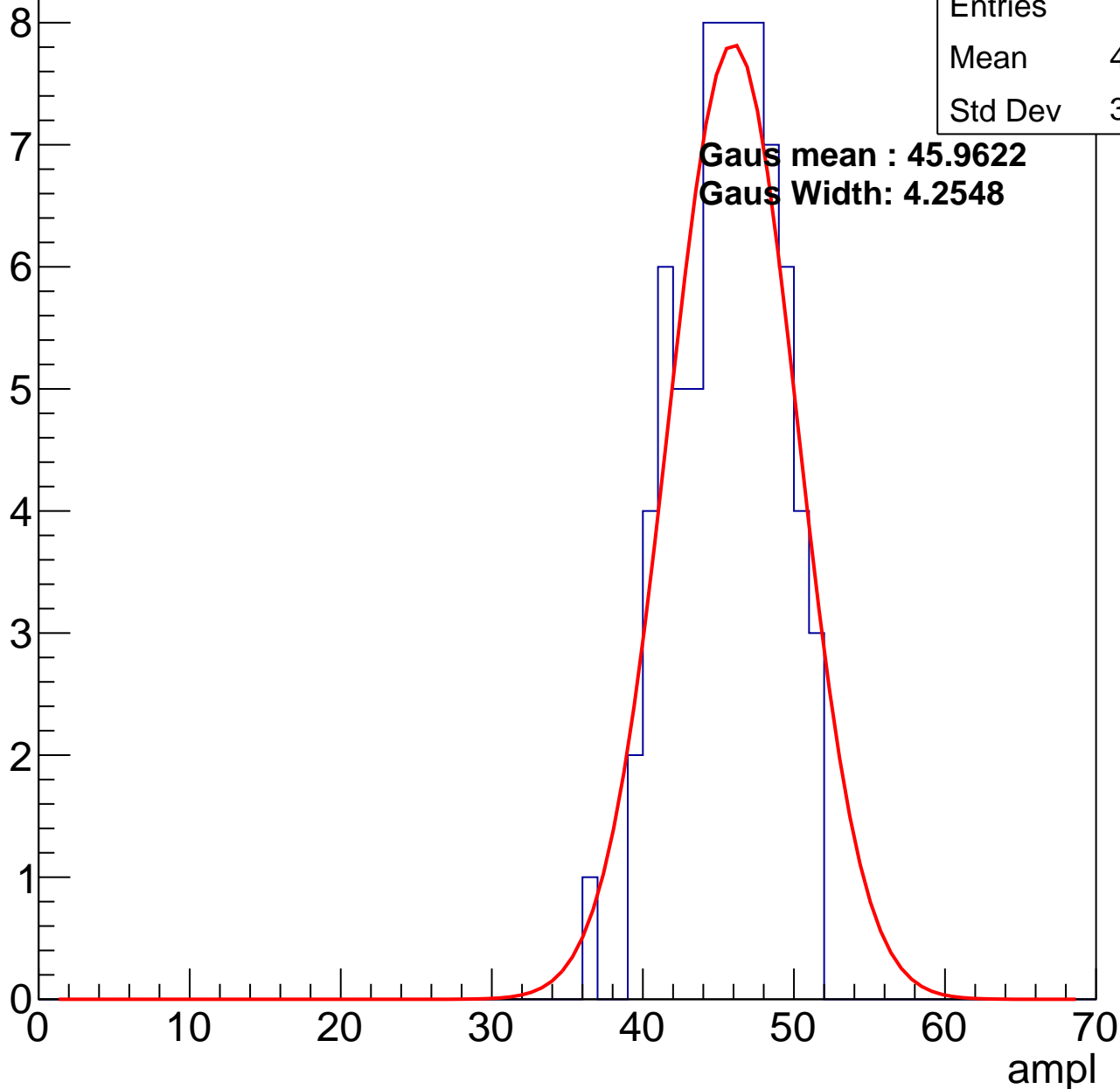
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	75
Mean	45.12
Std Dev	3.322

Gaus mean : 45.9622

Gaus Width: 4.2548

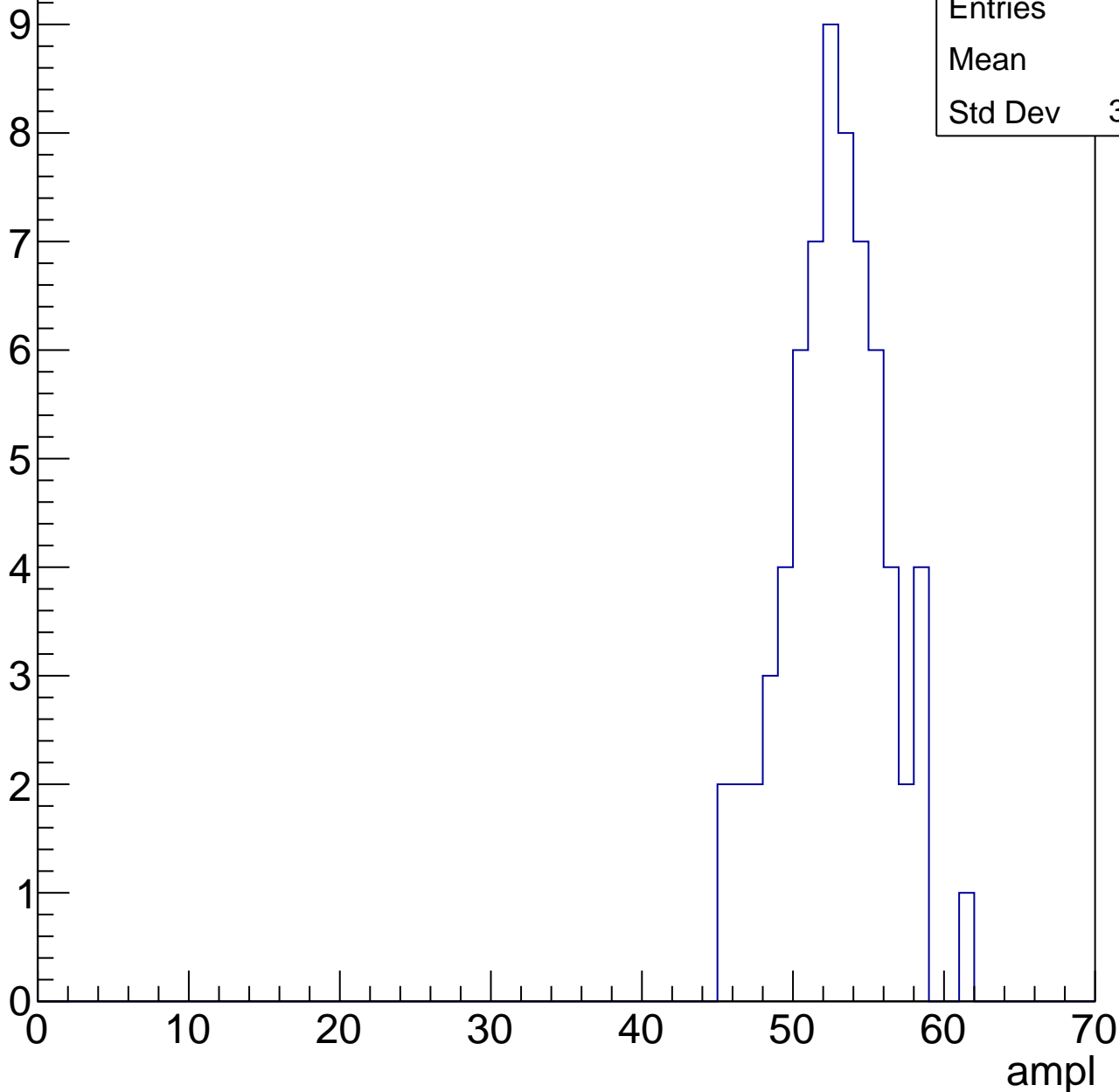


B1L100S, U1-ch119, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

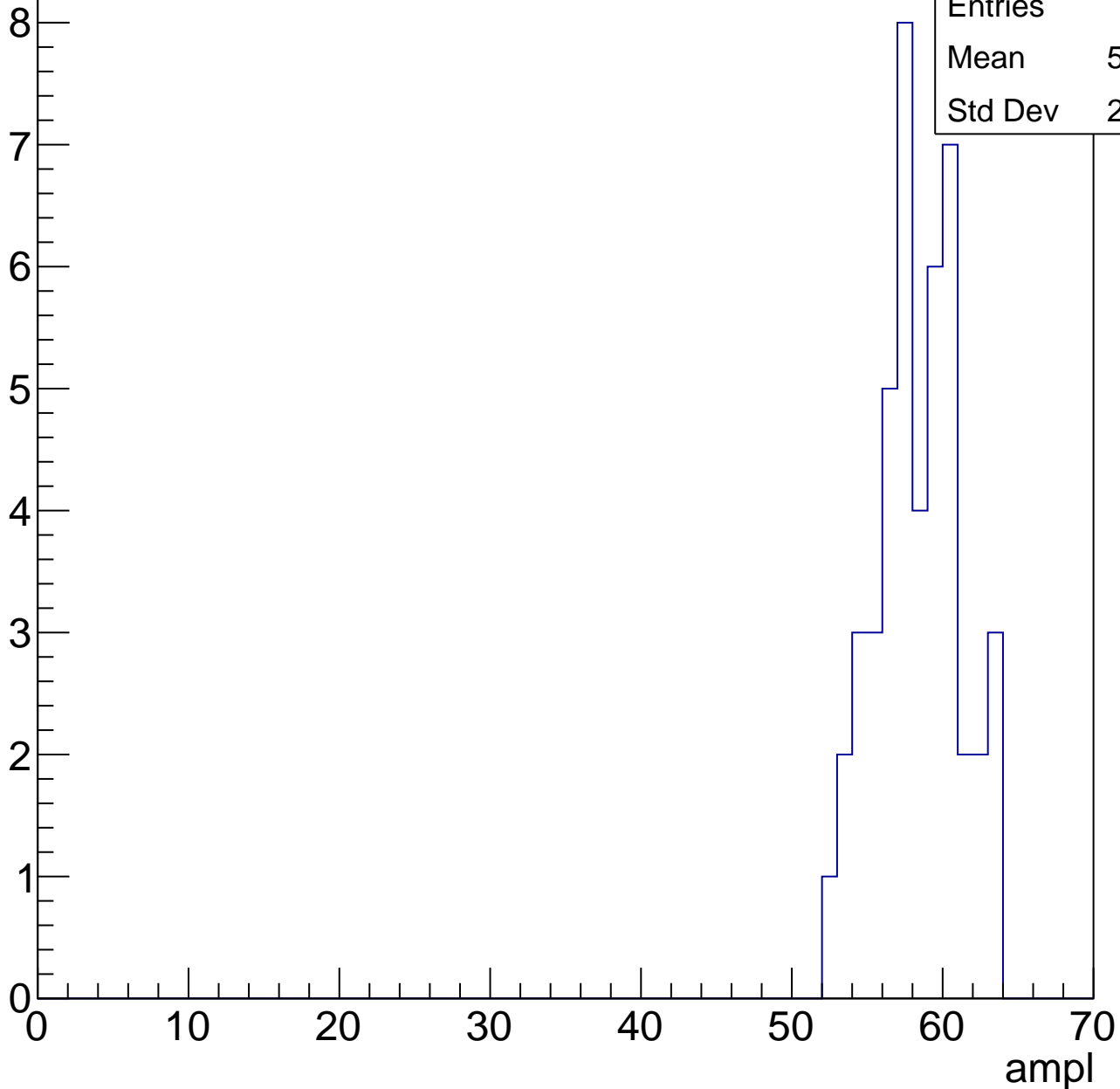
Entries	67
Mean	52.3
Std Dev	3.364



B1L100S, U1-ch119, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

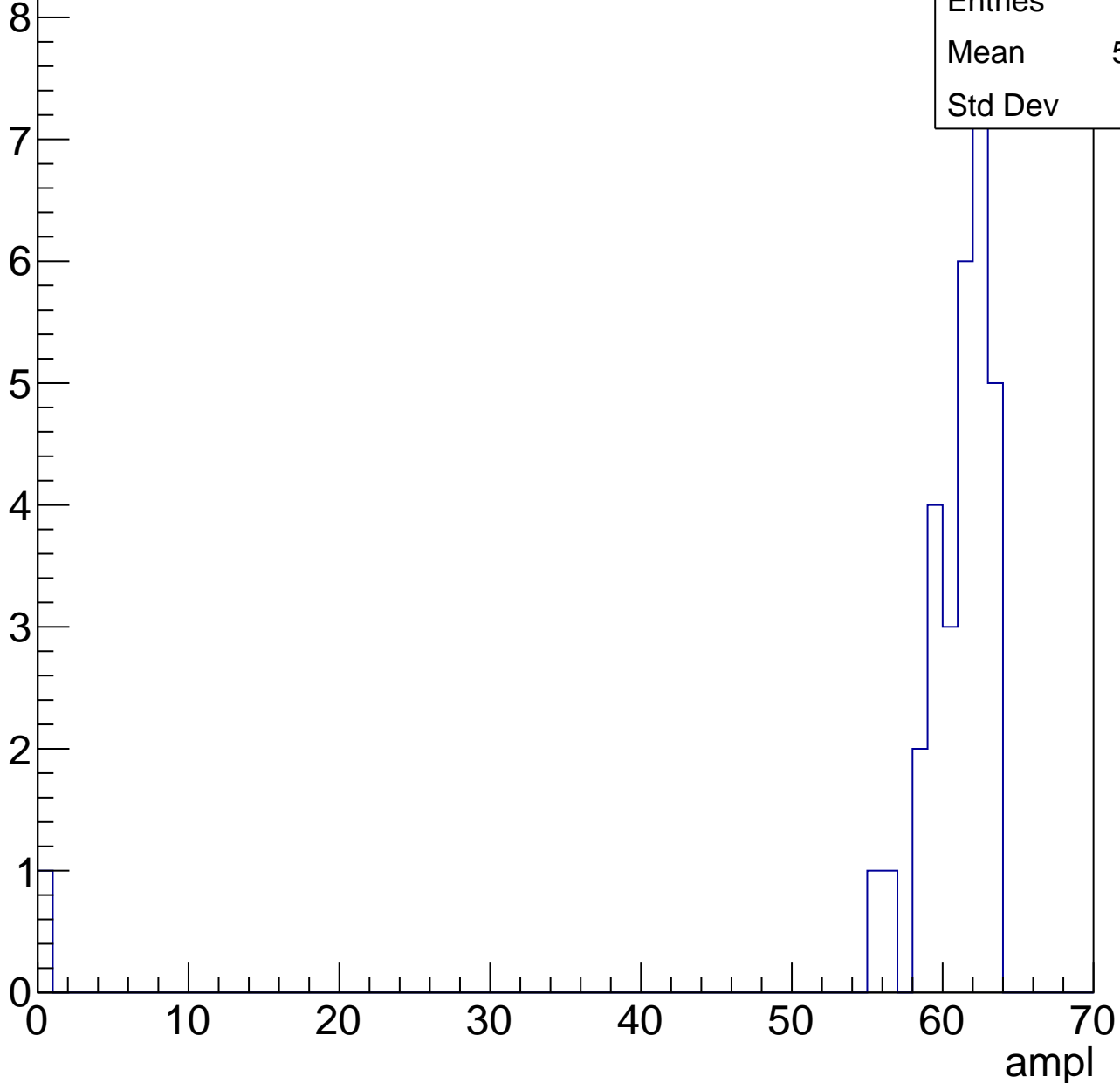


B1L100S, U1-ch119, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	31
Mean	58.71
Std Dev	10.9



B1L100S, U1-ch119, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0

Entries	3
Mean	62.33
Std Dev	0.9428

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch119, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch120, adc0

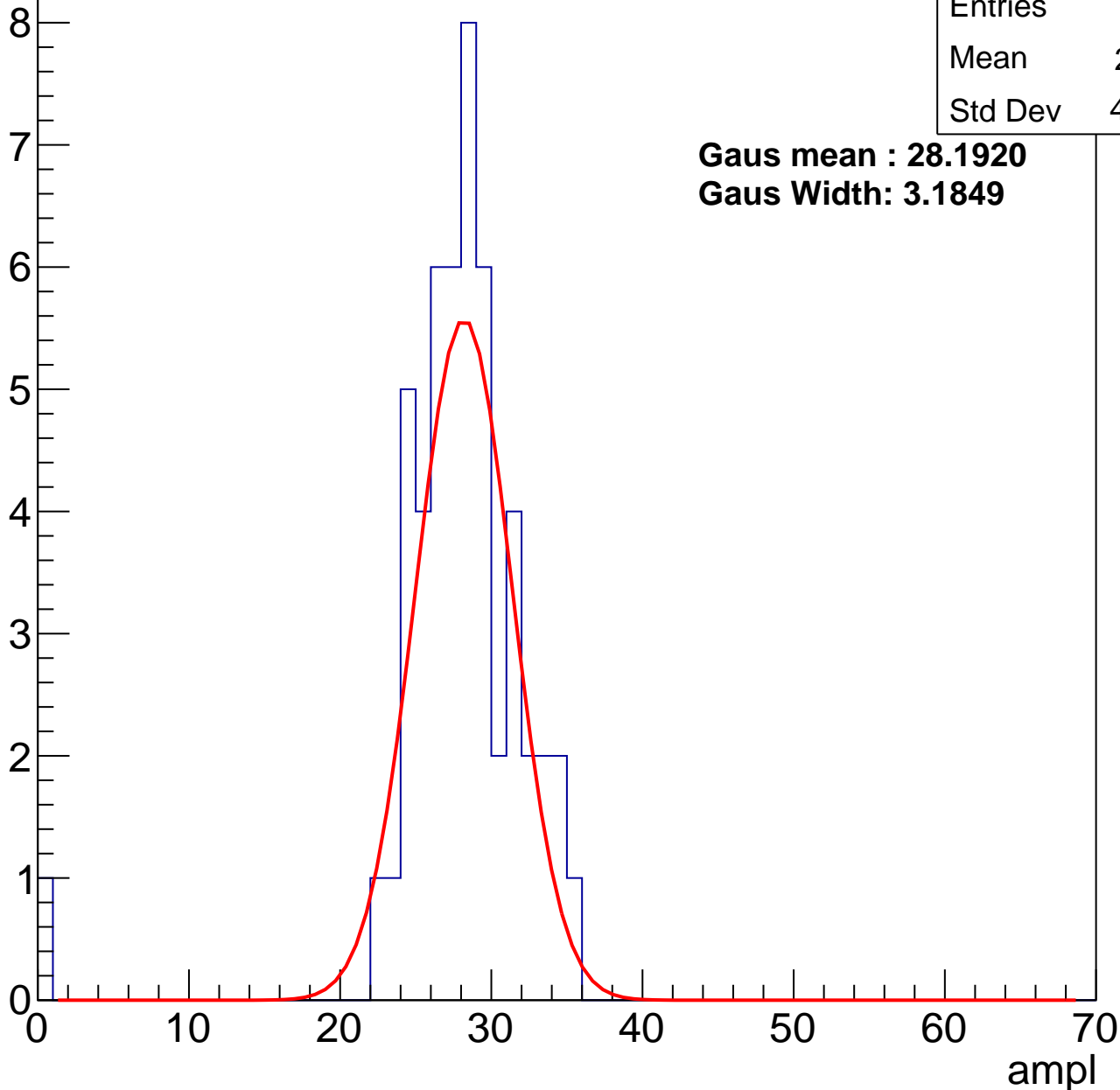
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	27.41
Std Dev	4.904

Gaus mean : 28.1920

Gaus Width: 3.1849



B1L100S, U1-ch120, adc1

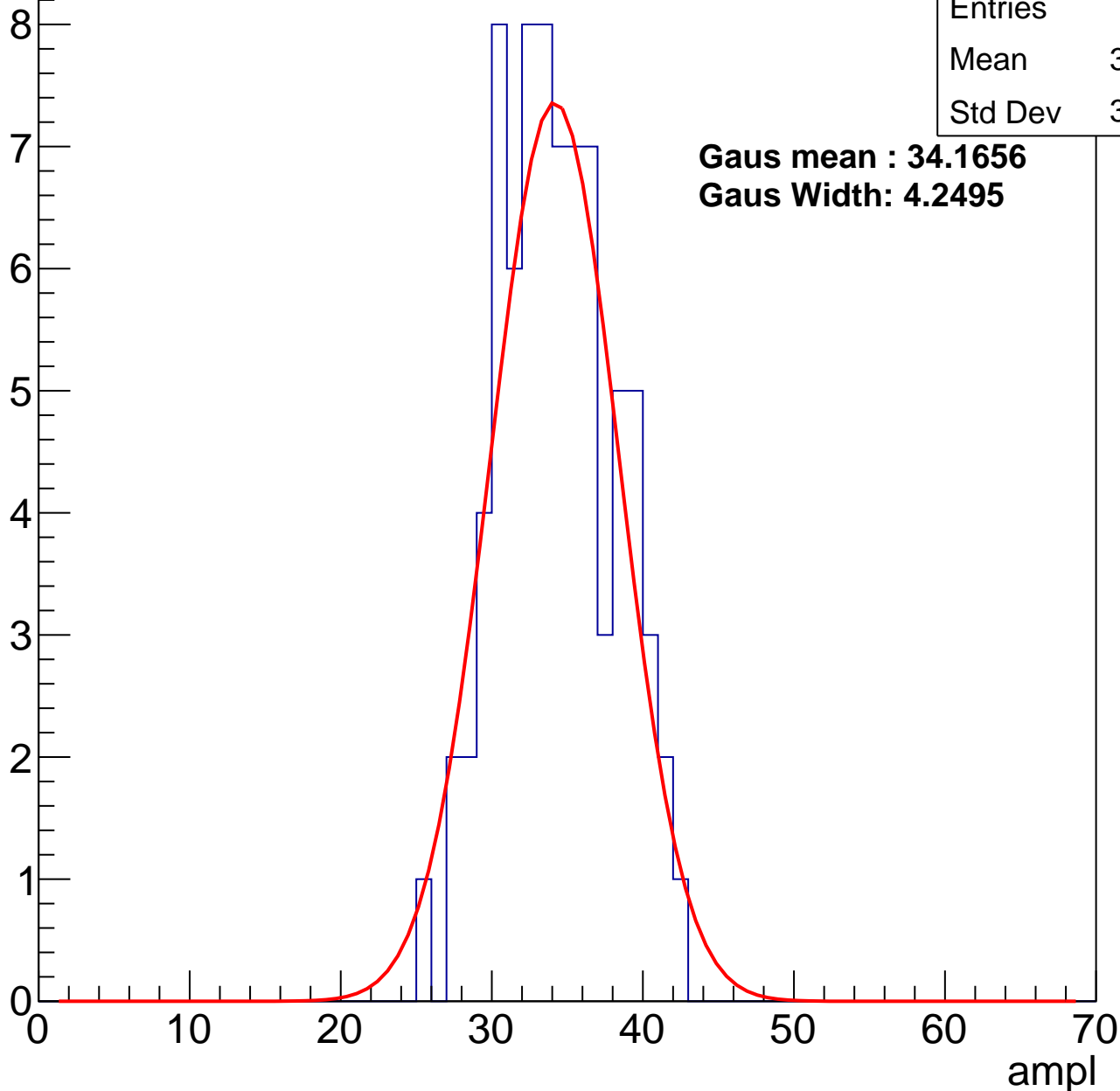
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	33.82
Std Dev	3.744

Gaus mean : 34.1656

Gaus Width: 4.2495



B1L100S, U1-ch120, adc2

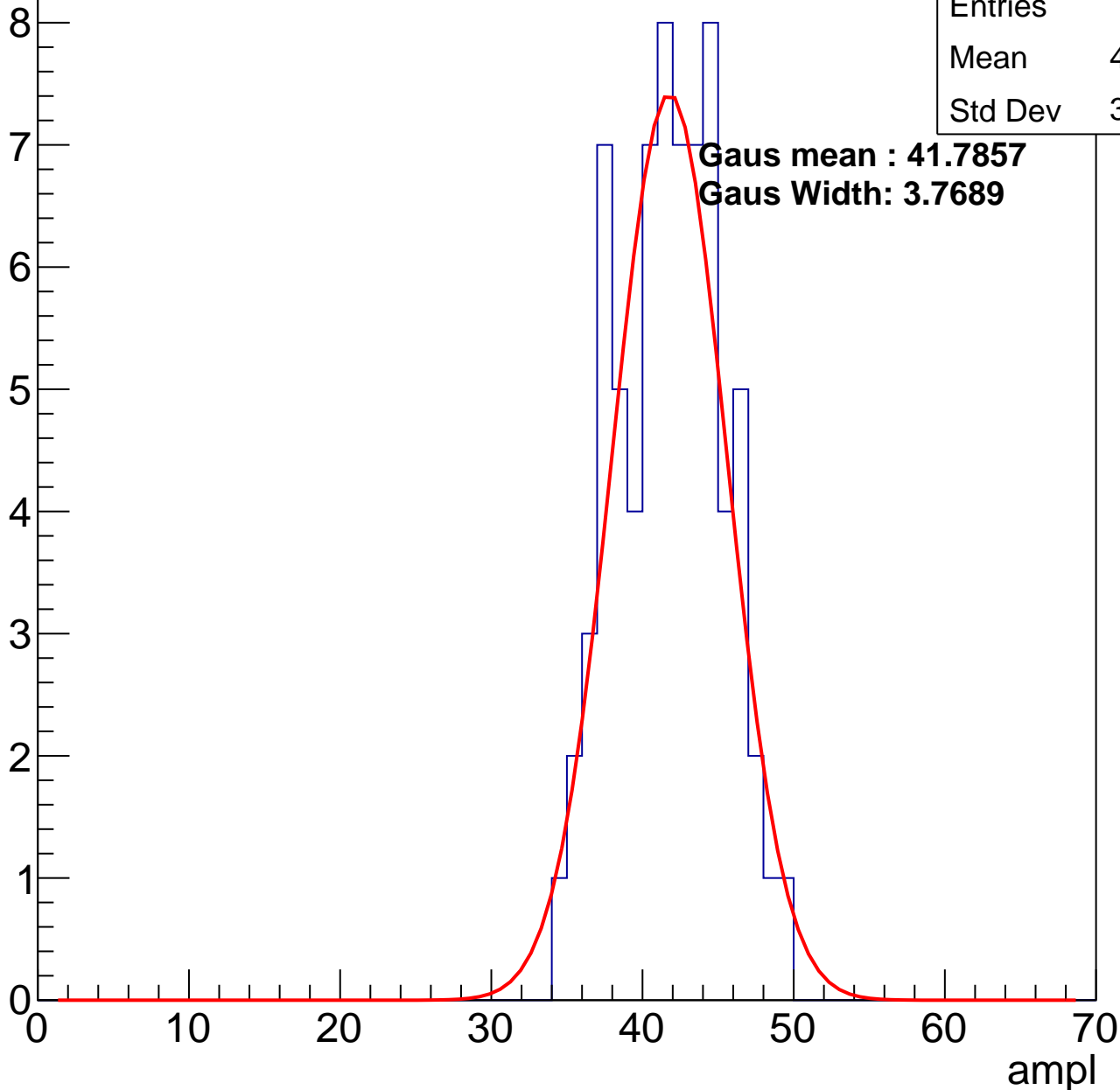
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	41.29
Std Dev	3.438

Gaus mean : 41.7857

Gaus Width: 3.7689

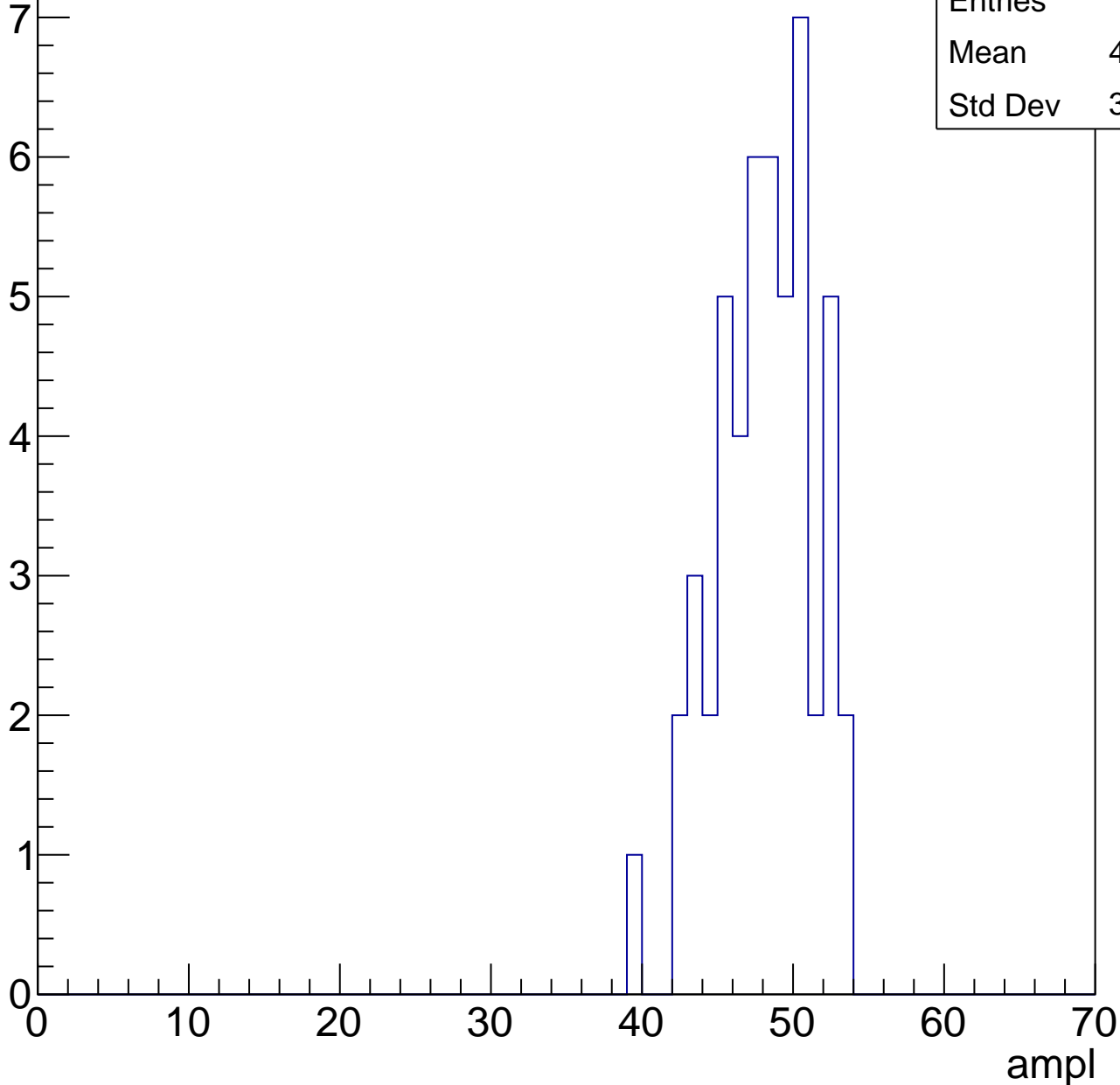


B1L100S, U1-ch120, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	50
Mean	47.64
Std Dev	3.167

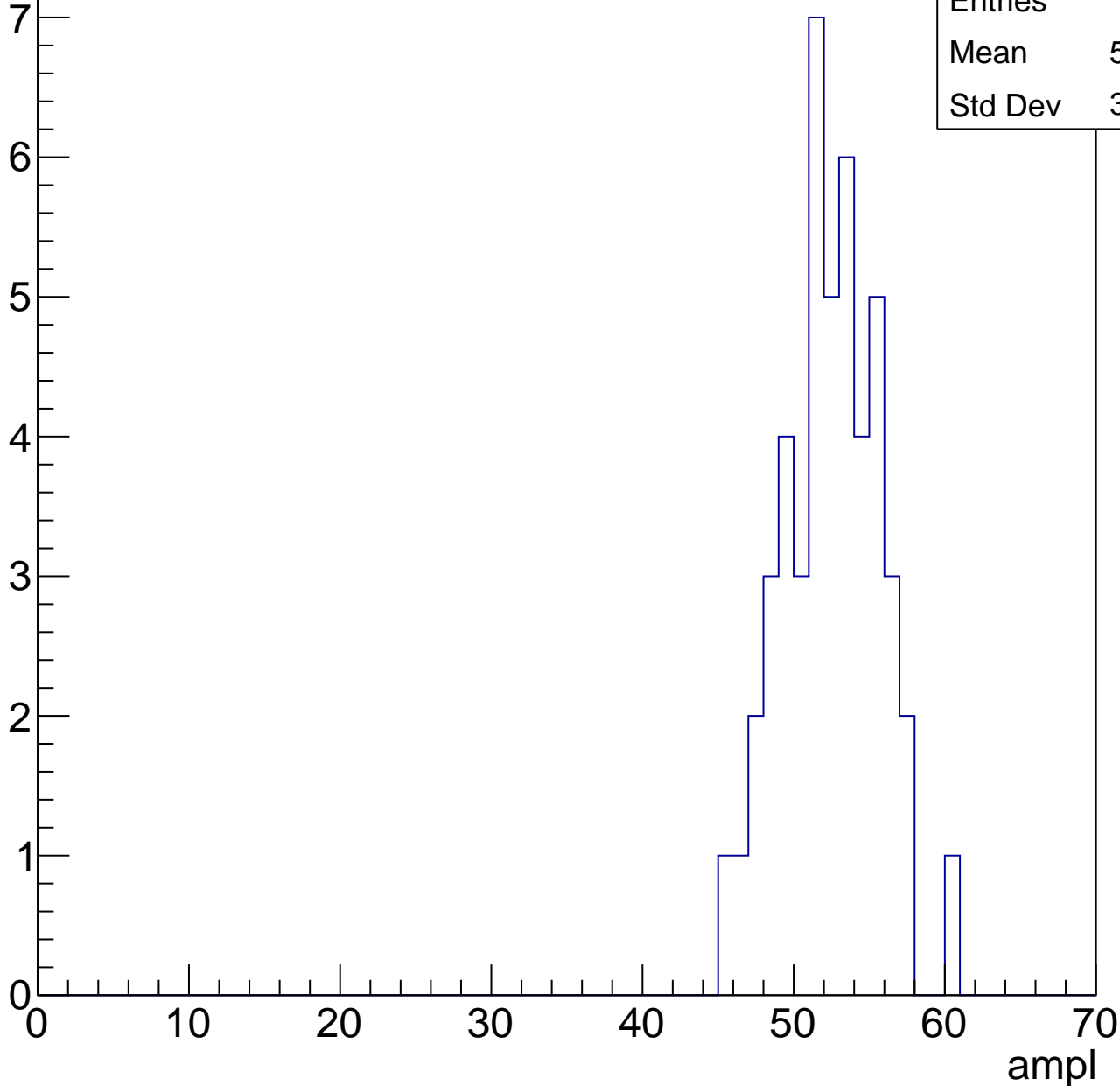


B1L100S, U1-ch120, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

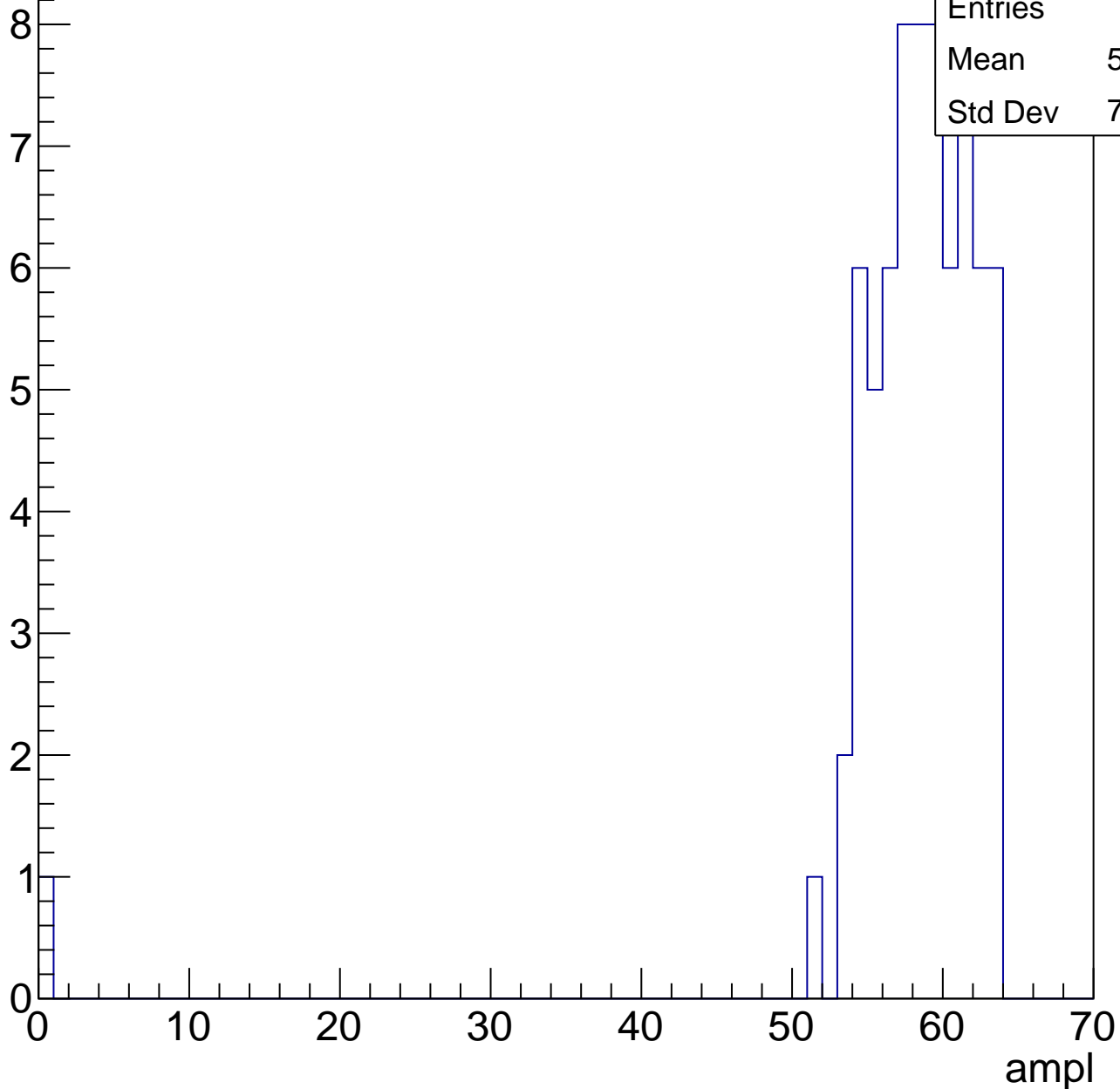
Entries	47
Mean	51.98
Std Dev	3.152



B1L100S, U1-ch120, adc5

calib_packv5_042523_0143.root, FC#4, port A2

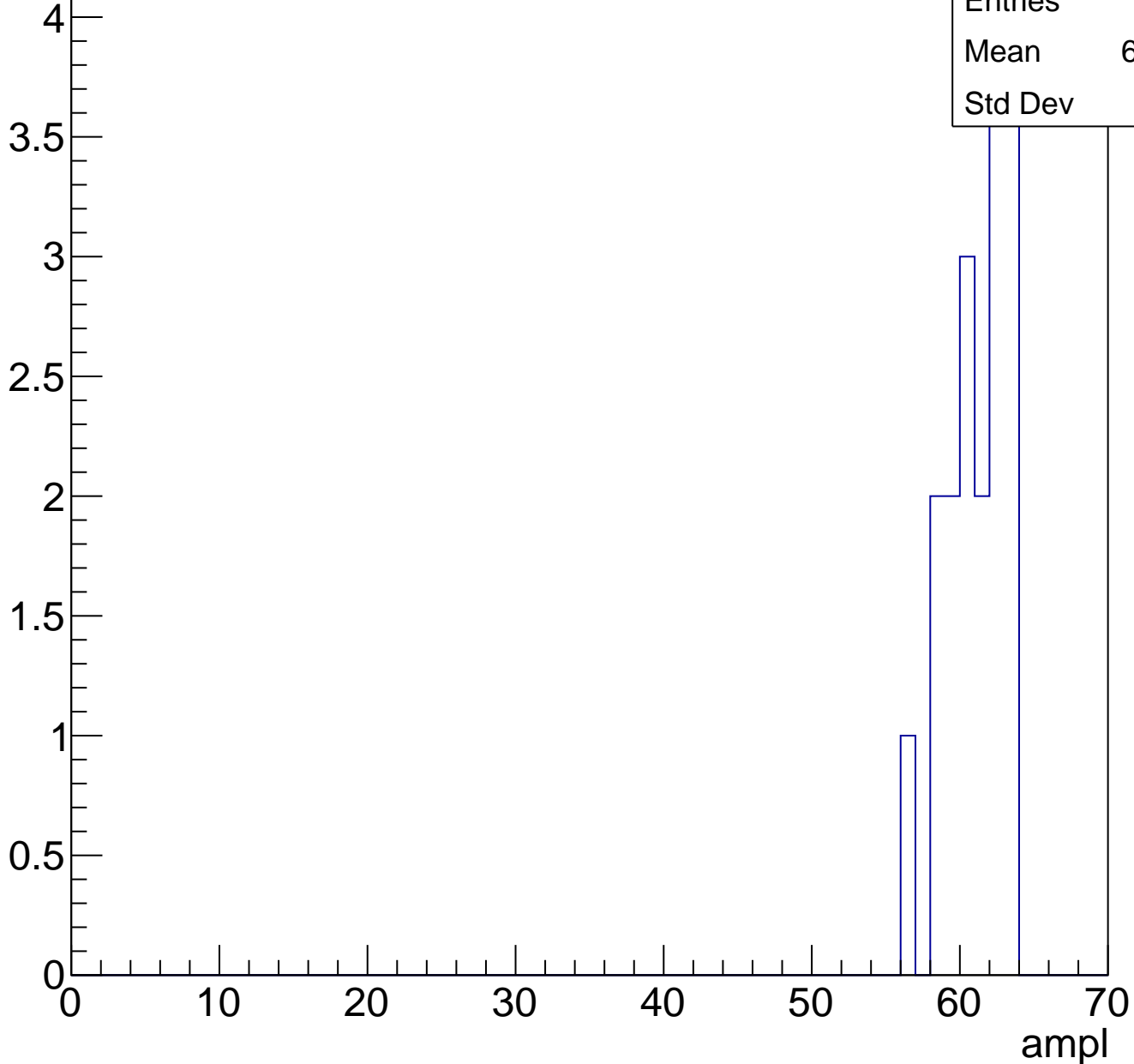
Entry



B1L100S, U1-ch120, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch120, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch121, adc0

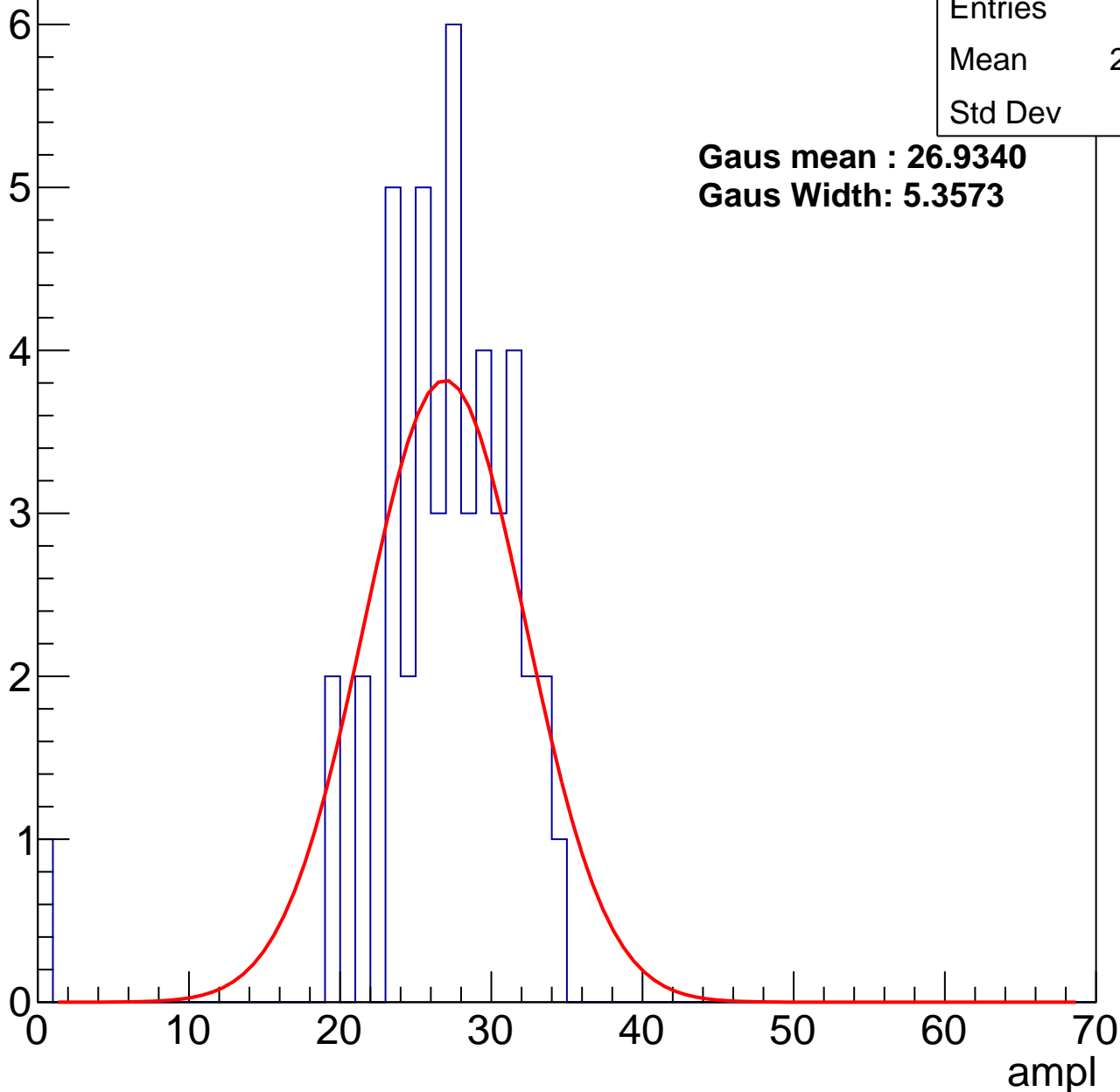
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	45
Mean	26.36
Std Dev	5.4

Gaus mean : 26.9340

Gaus Width: 5.3573



B1L100S, U1-ch121, adc1

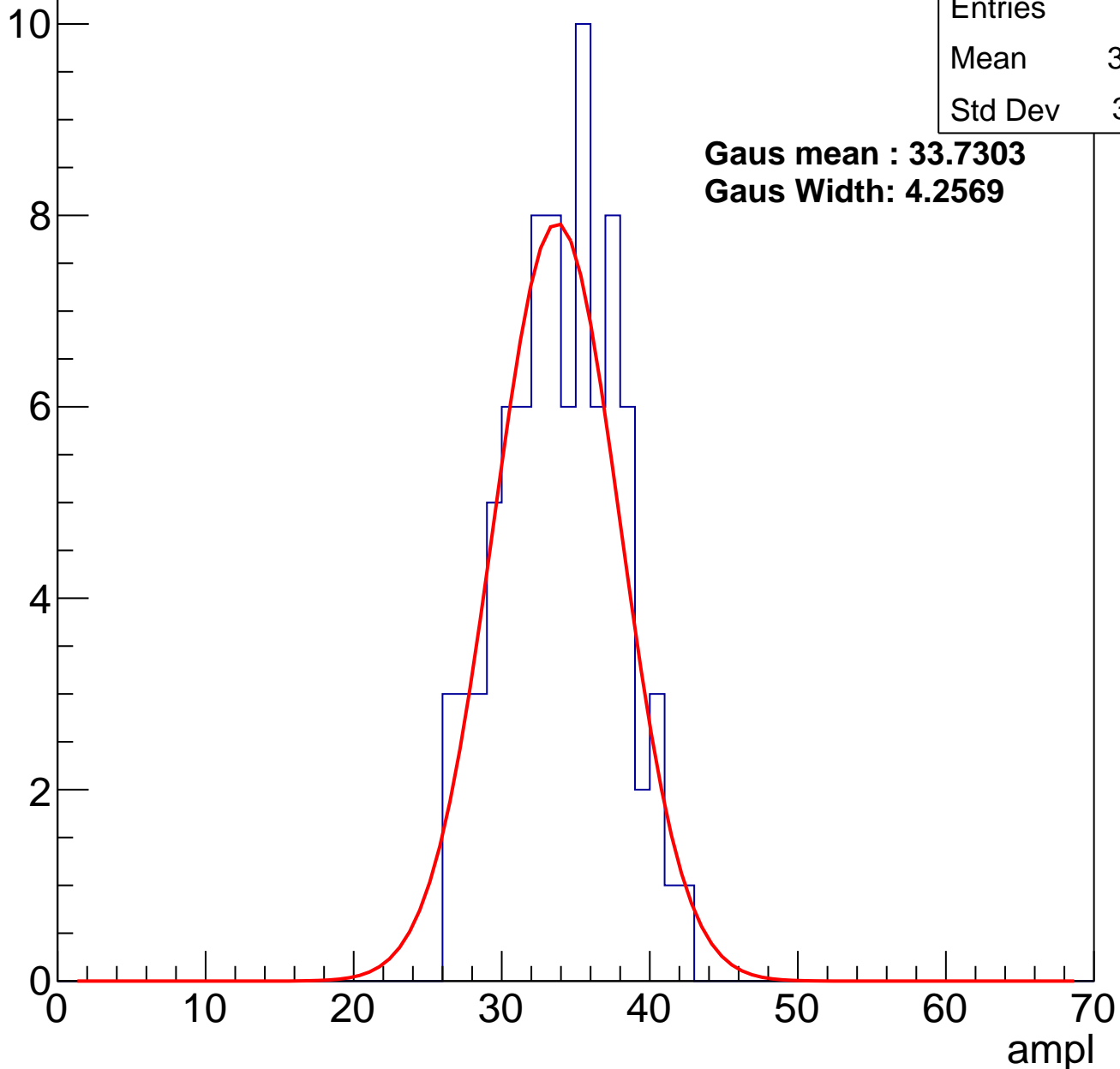
calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	33.52
Std Dev	3.781

Gaus mean : 33.7303

Gaus Width: 4.2569

Entry



B1L100S, U1-ch121, adc2

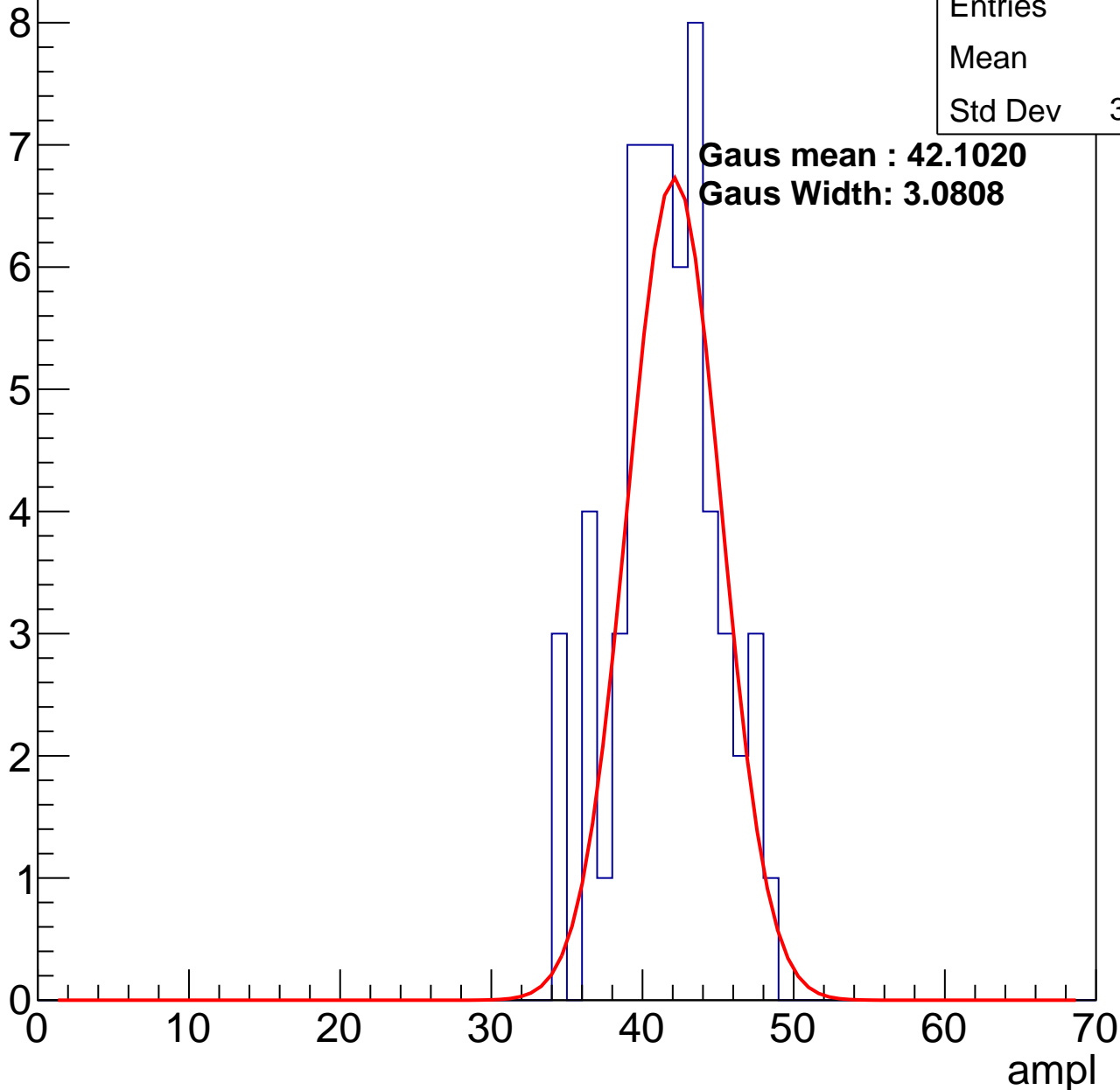
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	59
Mean	41.1
Std Dev	3.328

Gaus mean : 42.1020

Gaus Width: 3.0808

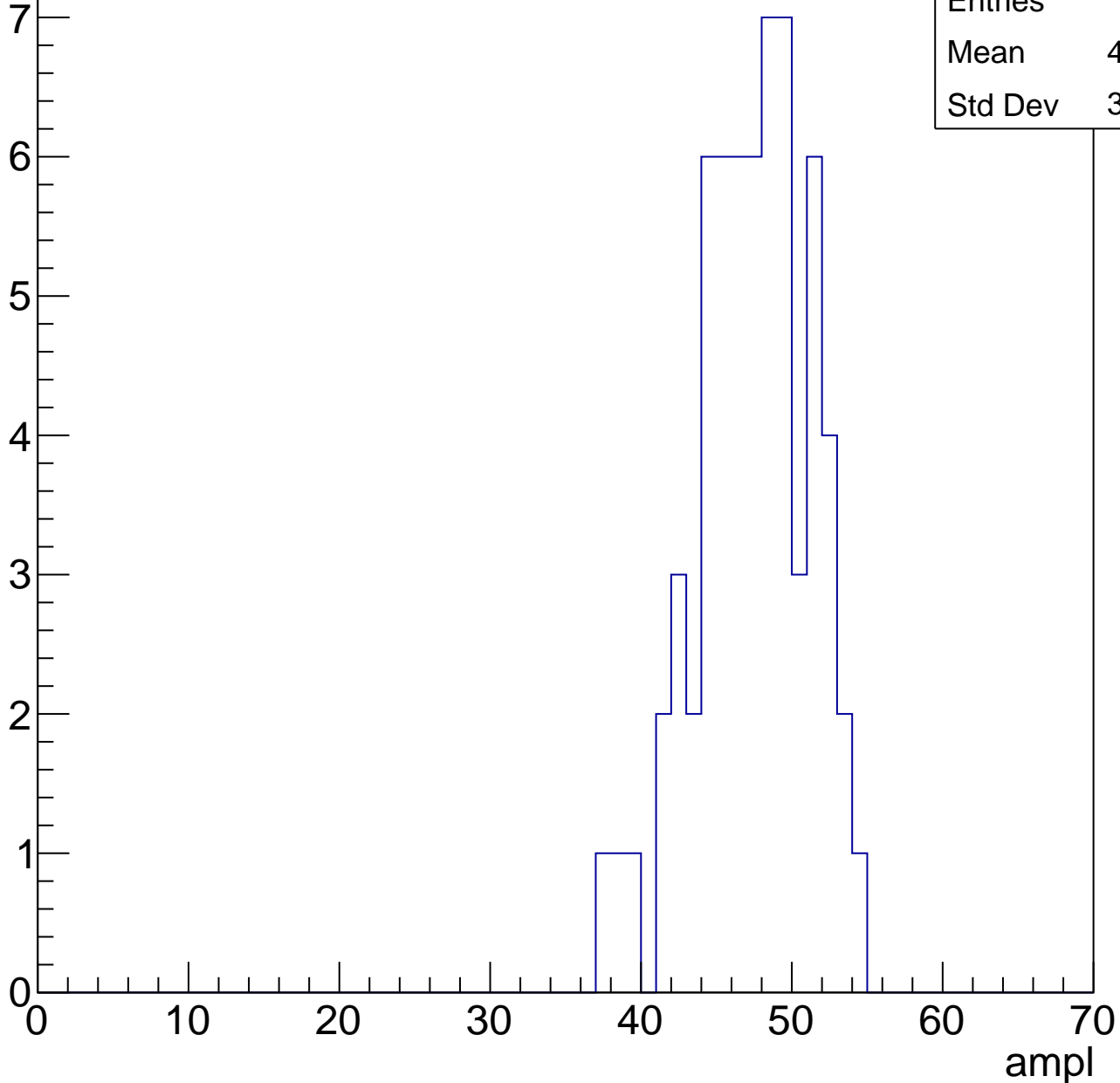


B1L100S, U1-ch121, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

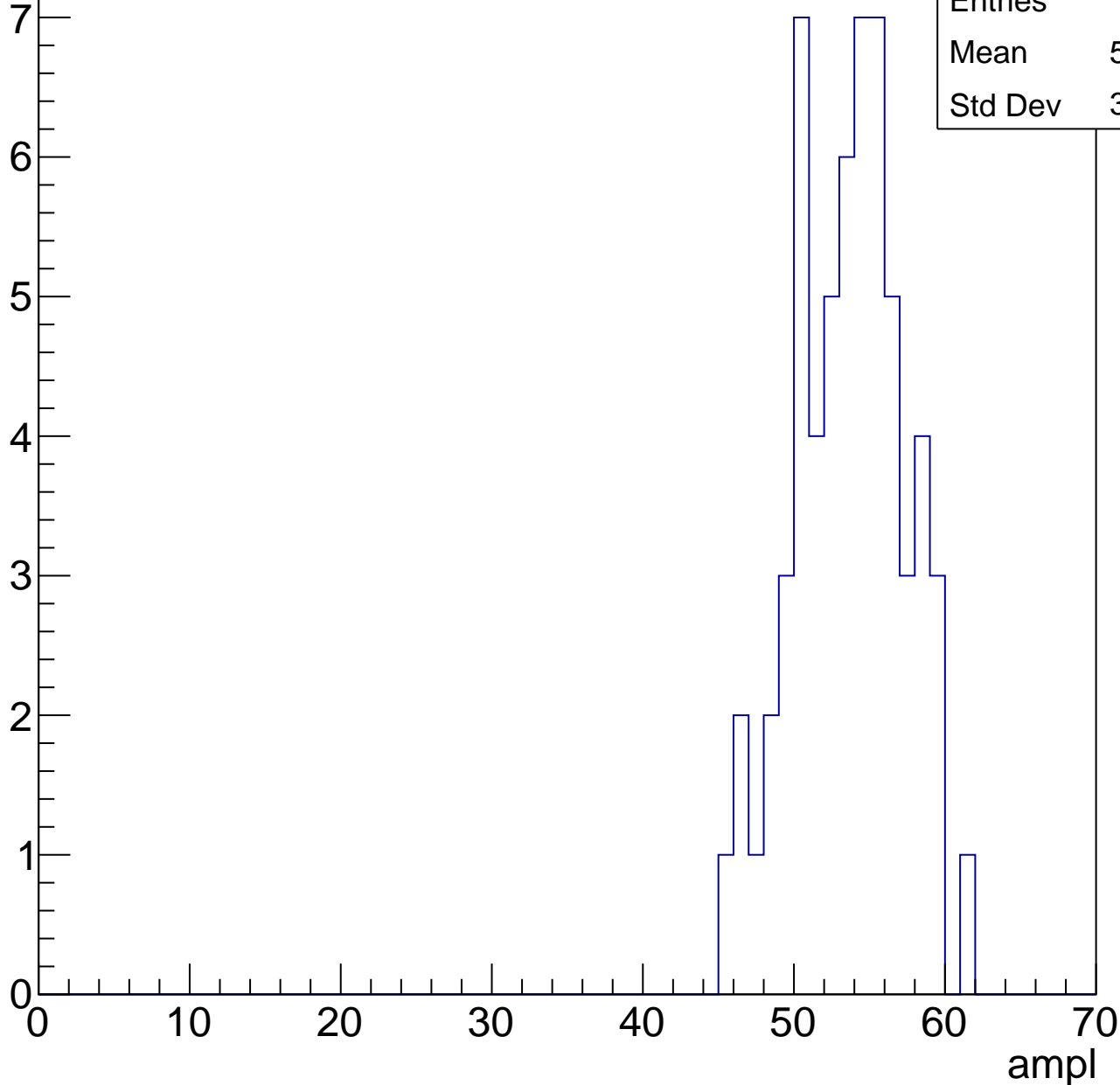
Entries	64
Mean	46.92
Std Dev	3.722



B1L100S, U1-ch121, adc4

calib_packv5_042523_0143.root, FC#4, port A2

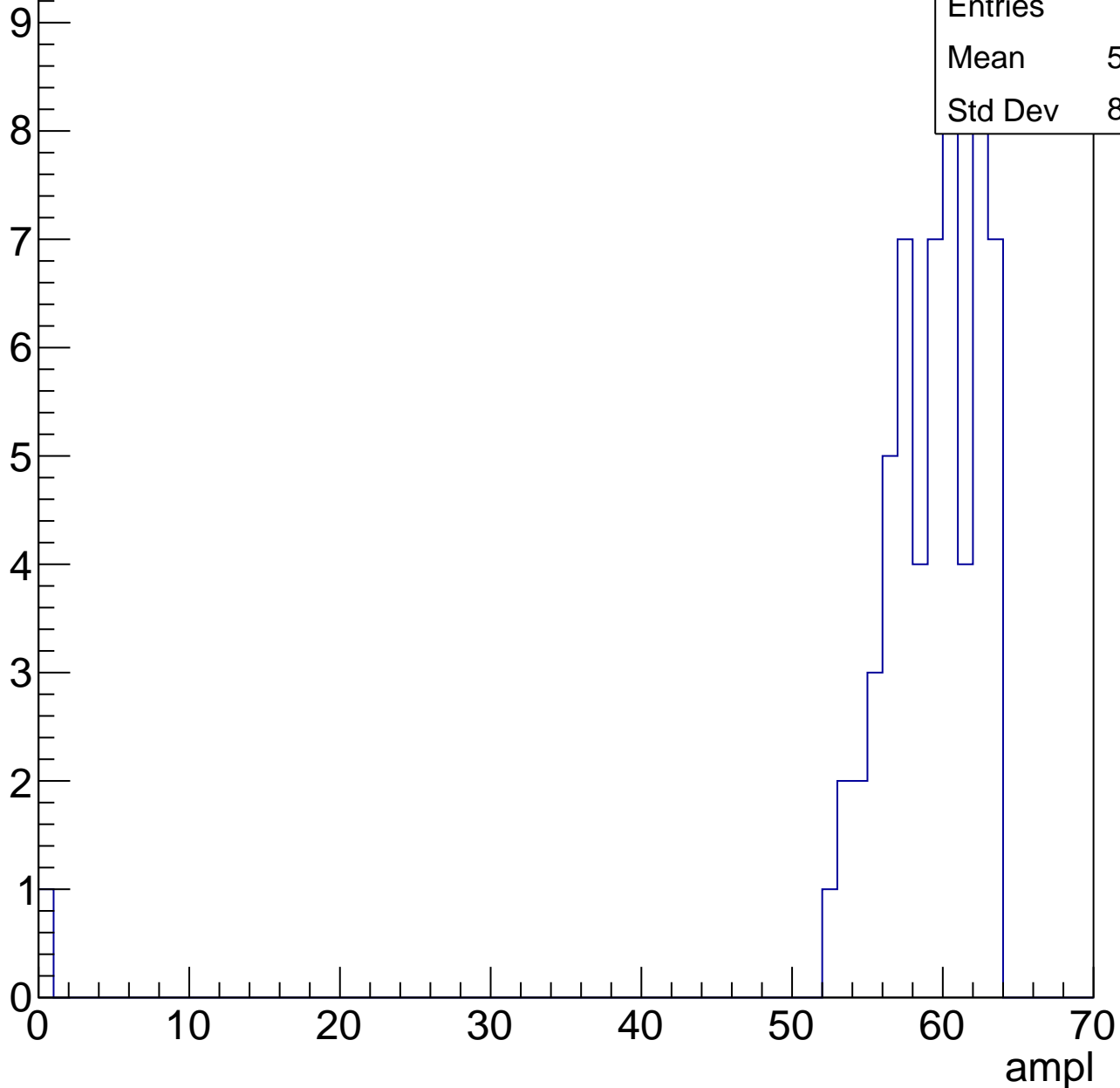
Entry



B1L100S, U1-ch121, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



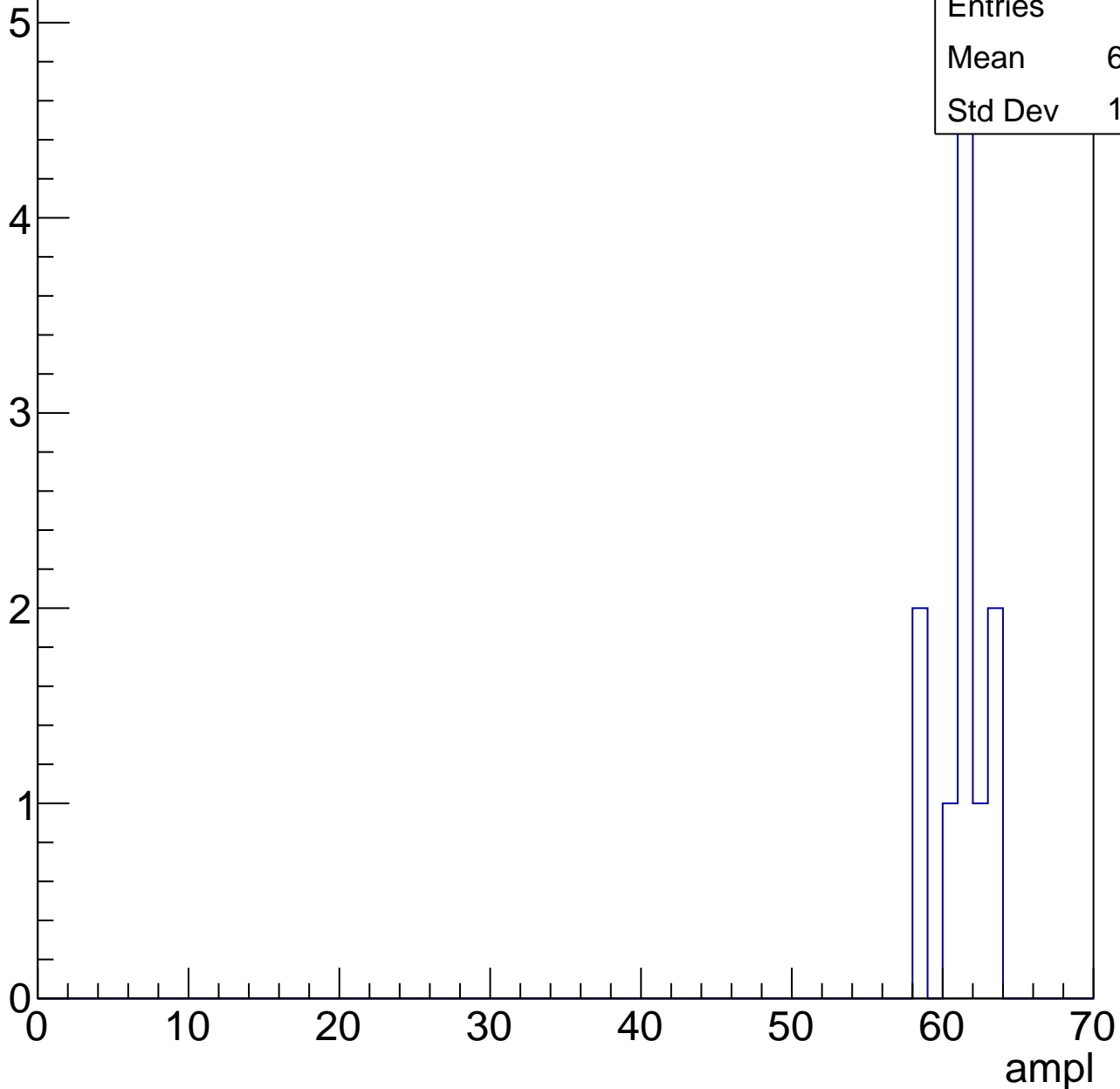
Entries	60
Mean	57.97
Std Dev	8.093

B1L100S, U1-ch121, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

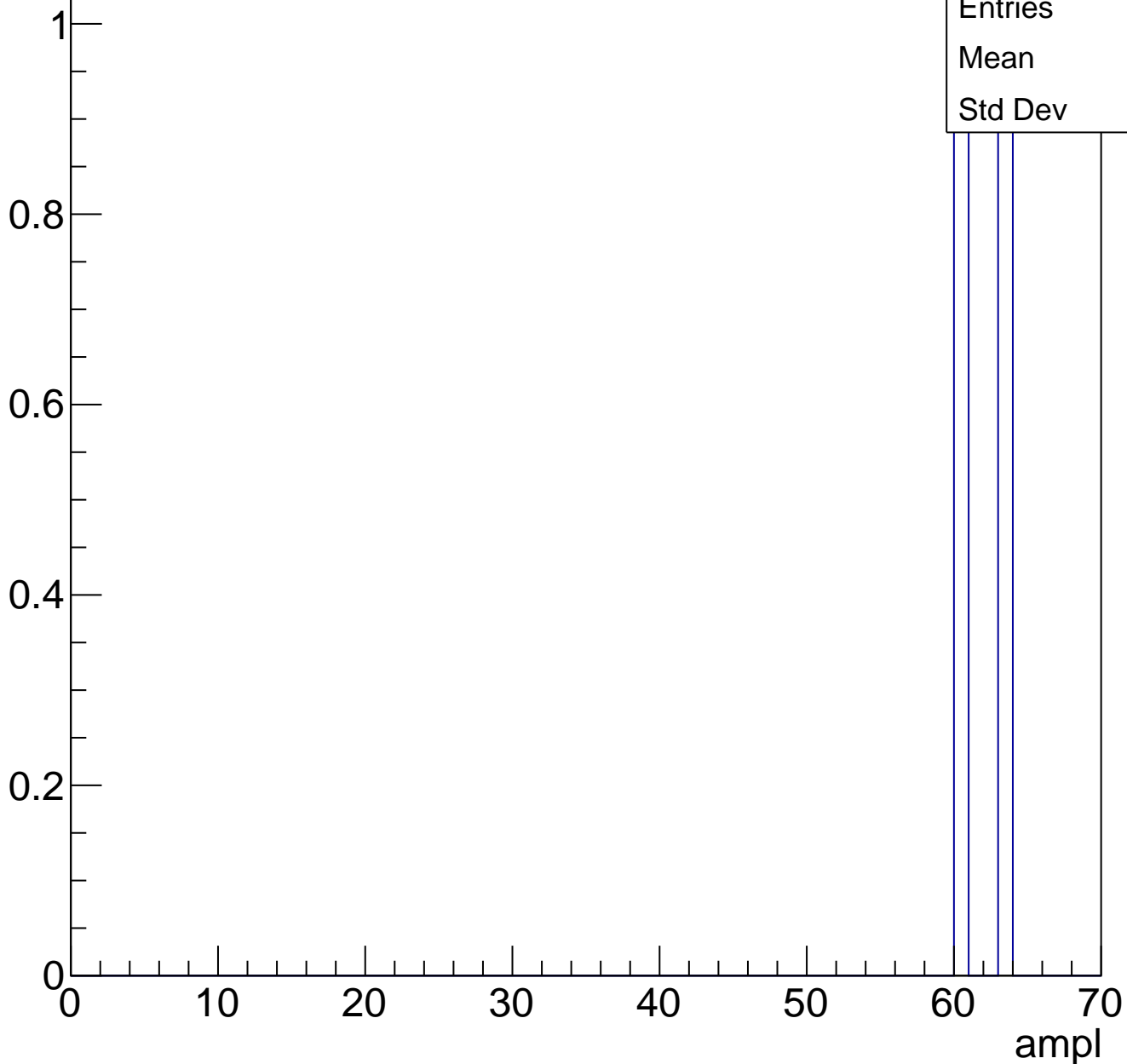
Entries	11
Mean	60.82
Std Dev	1.585



B1L100S, U1-ch121, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch122, adc0

calib_packv5_042523_0143.root, FC#4, port A2

Entries	81
Mean	26.57
Std Dev	7.567

Gaus mean : 28.7104

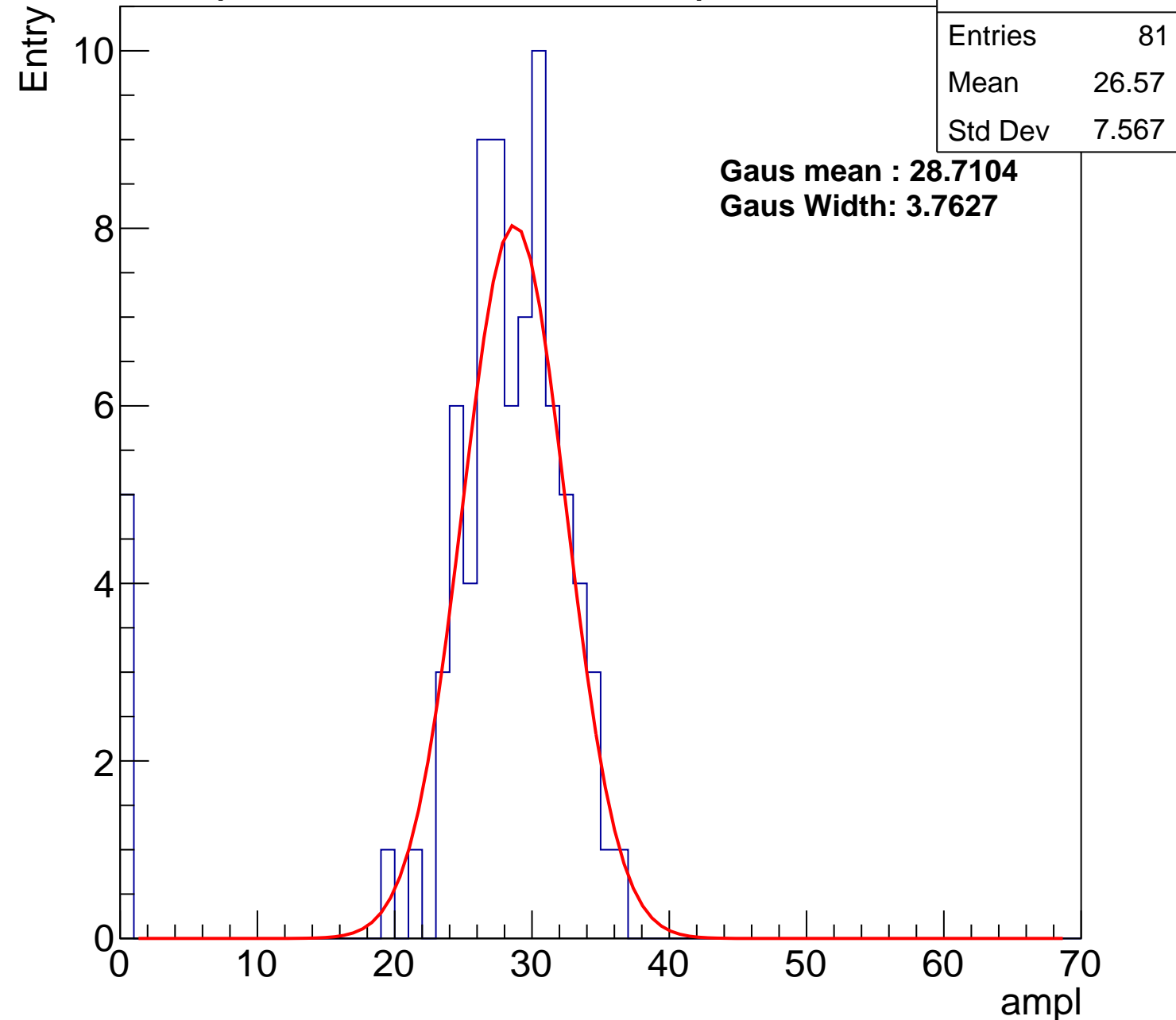
Gaus Width: 3.7627

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch122, adc1

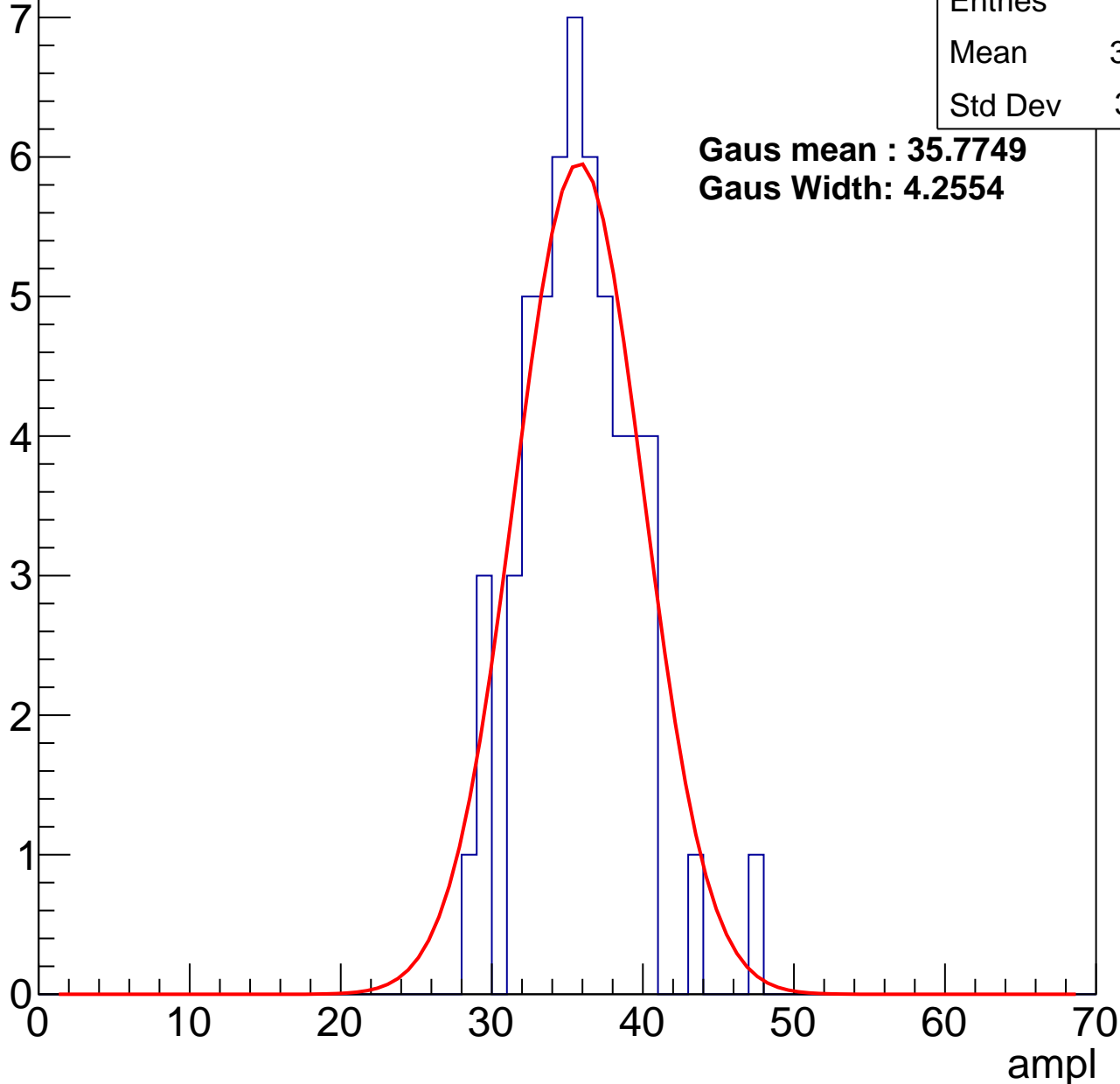
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	35.29
Std Dev	3.581

Gaus mean : 35.7749

Gaus Width: 4.2554



B1L100S, U1-ch122, adc2

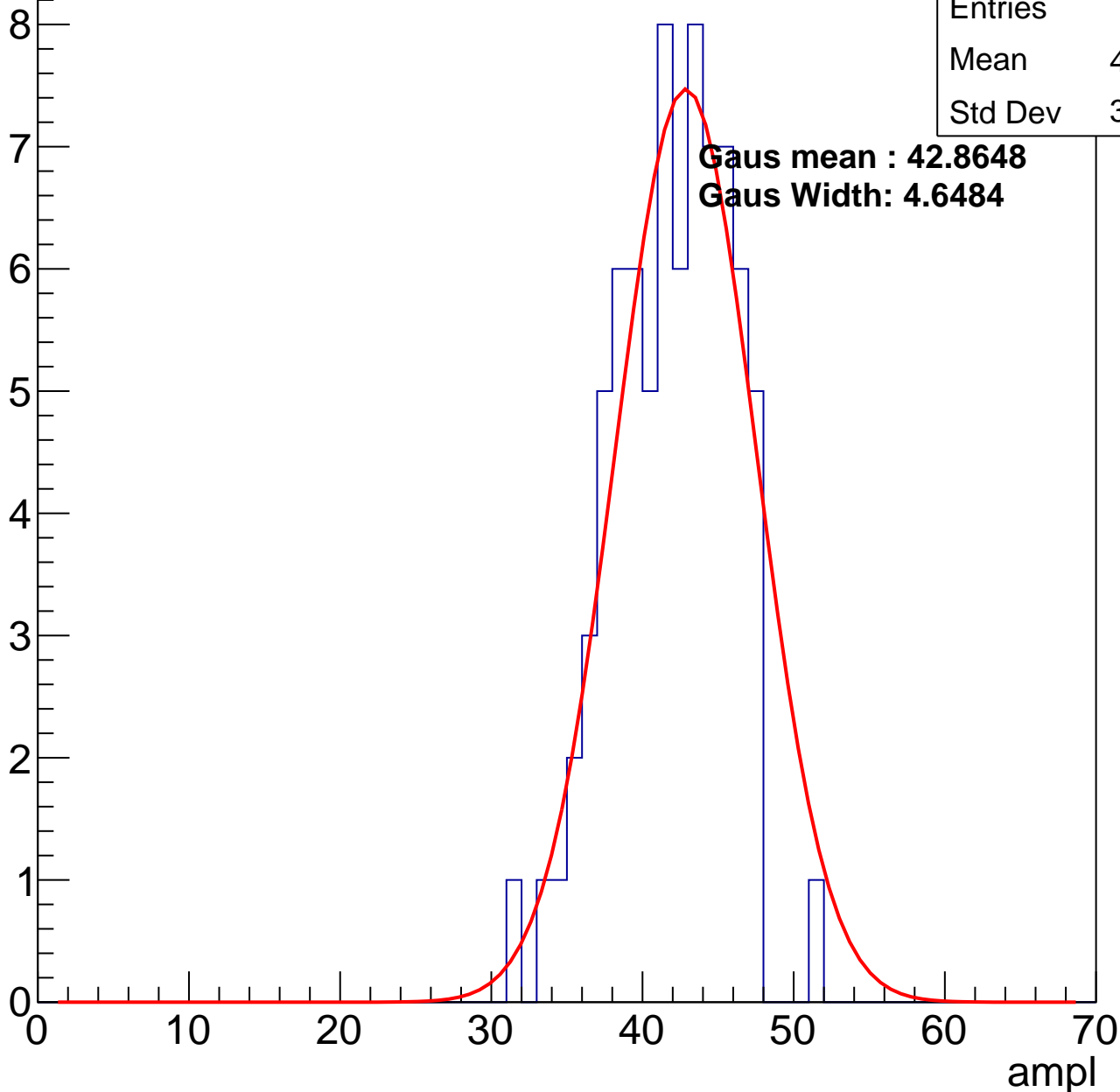
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	78
Mean	41.44
Std Dev	3.842

Gaus mean : 42.8648

Gaus Width: 4.6484



B1L100S, U1-ch122, adc3

calib_packv5_042523_0143.root, FC#4, port A2

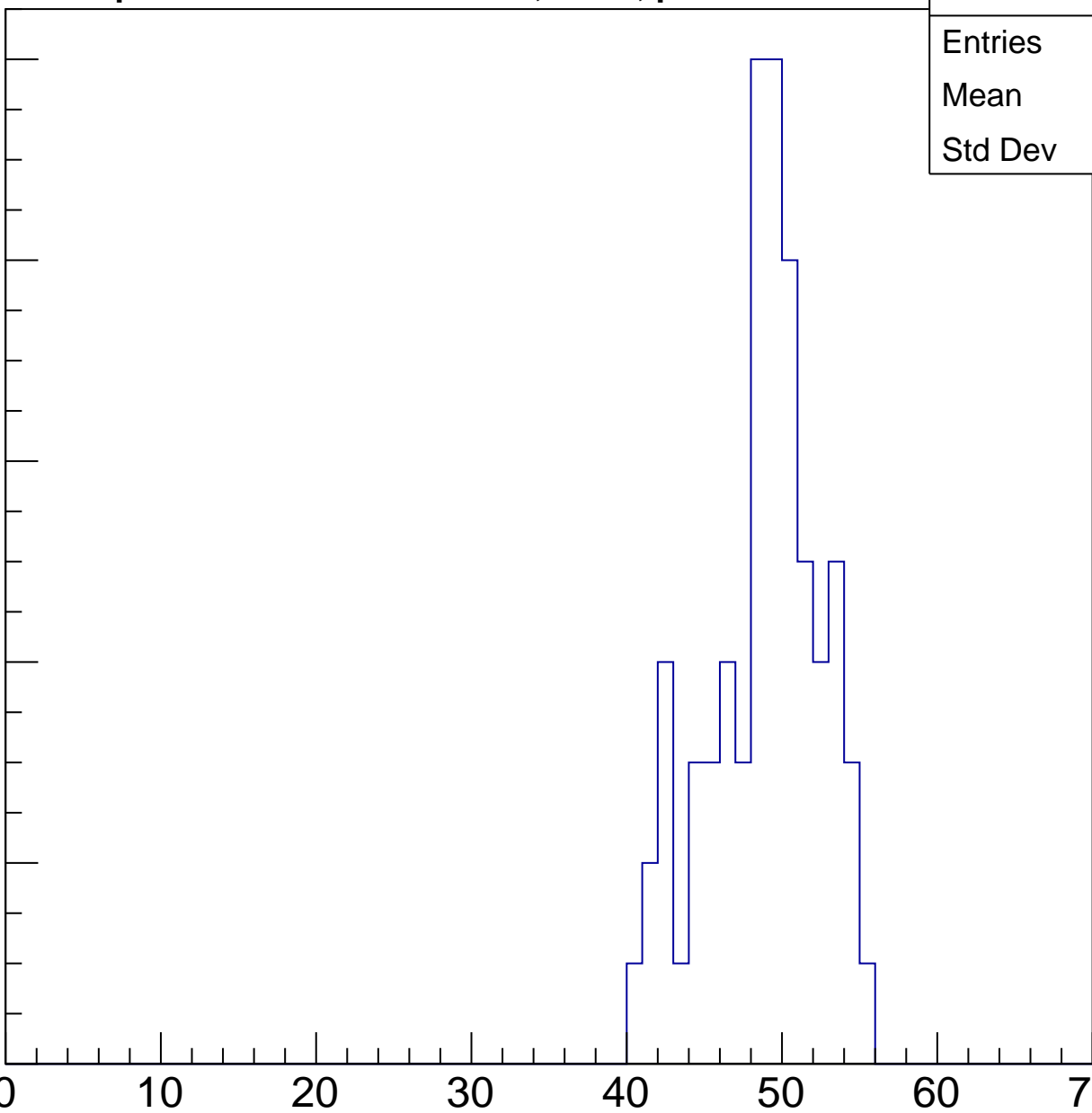
Entries	67
Mean	48.36
Std Dev	3.561

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

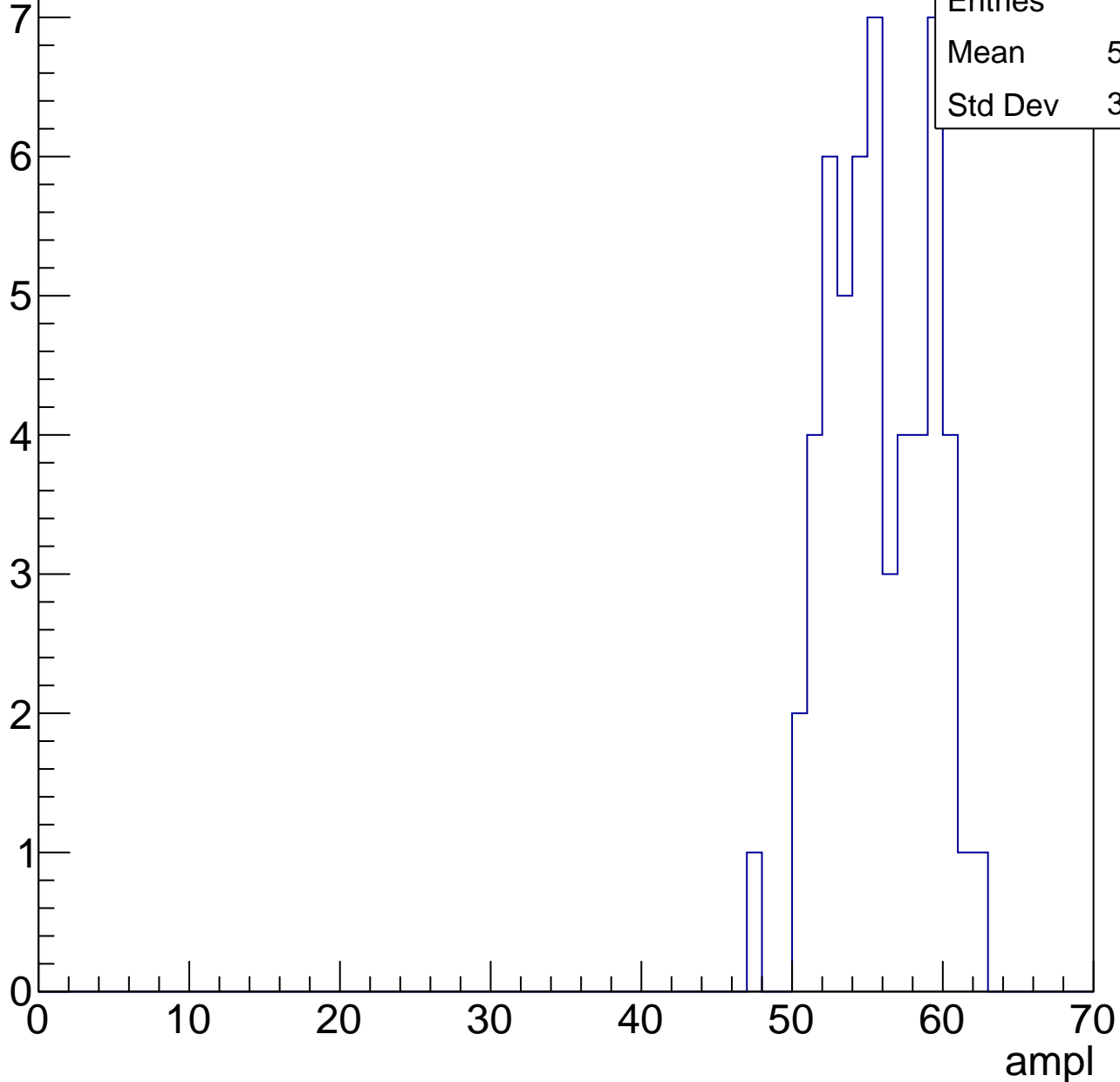
ampl



B1L100S, U1-ch122, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry



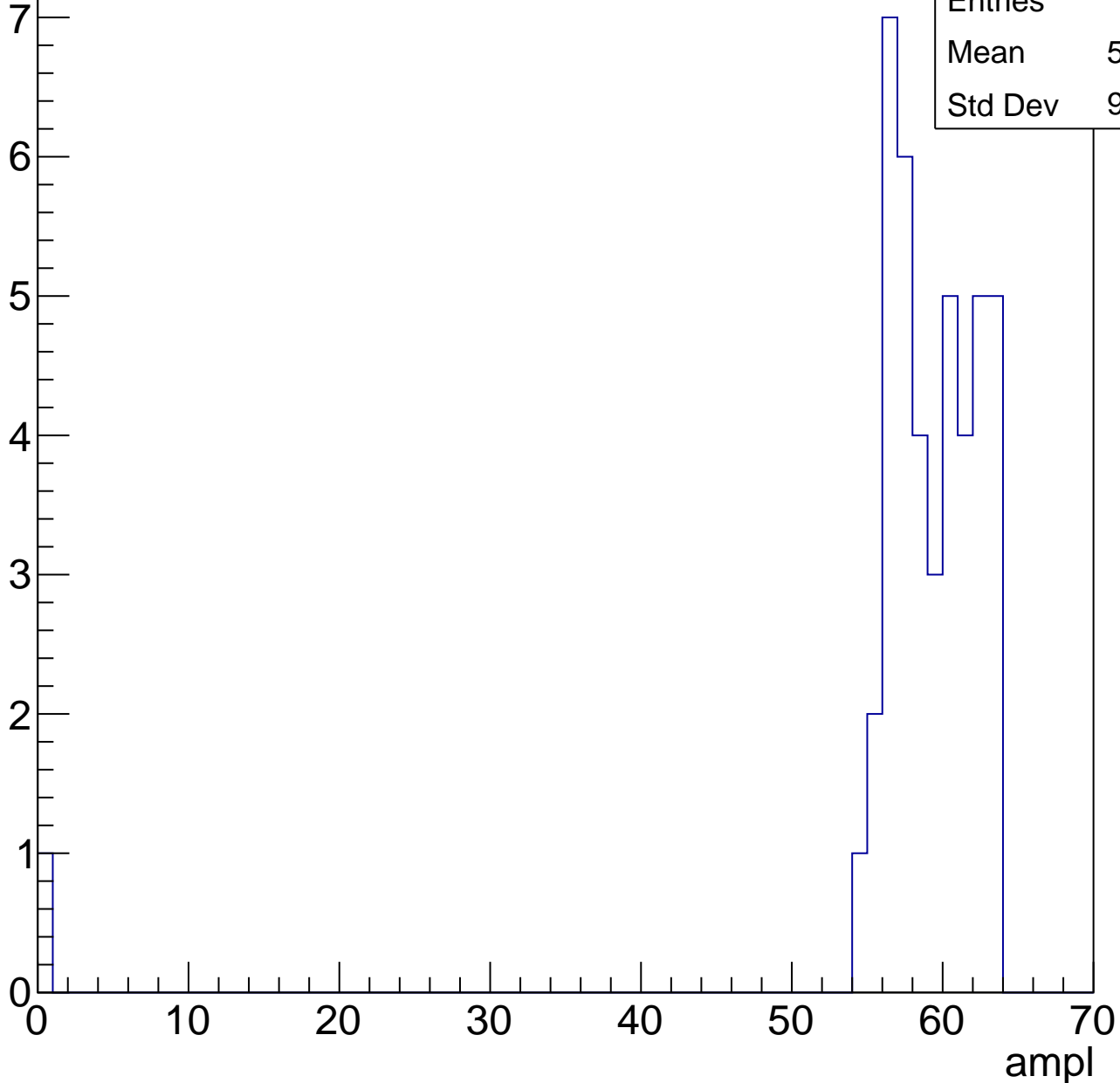
Entries	55
Mean	55.29
Std Dev	3.318

B1L100S, U1-ch122, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	43
Mean	57.58
Std Dev	9.262

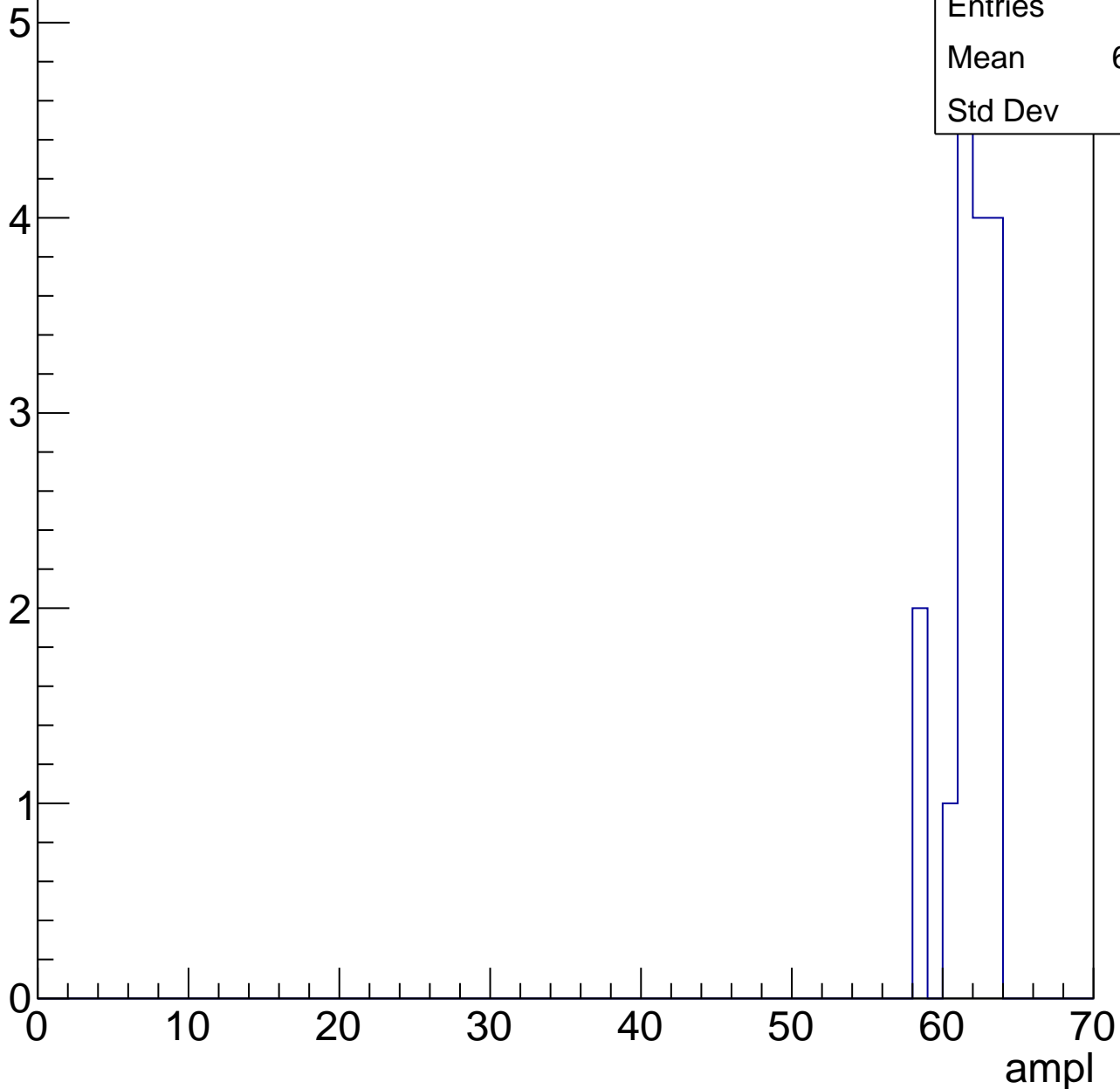


B1L100S, U1-ch122, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	16
Mean	61.31
Std Dev	1.53



B1L100S, U1-ch122, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch123, adc0

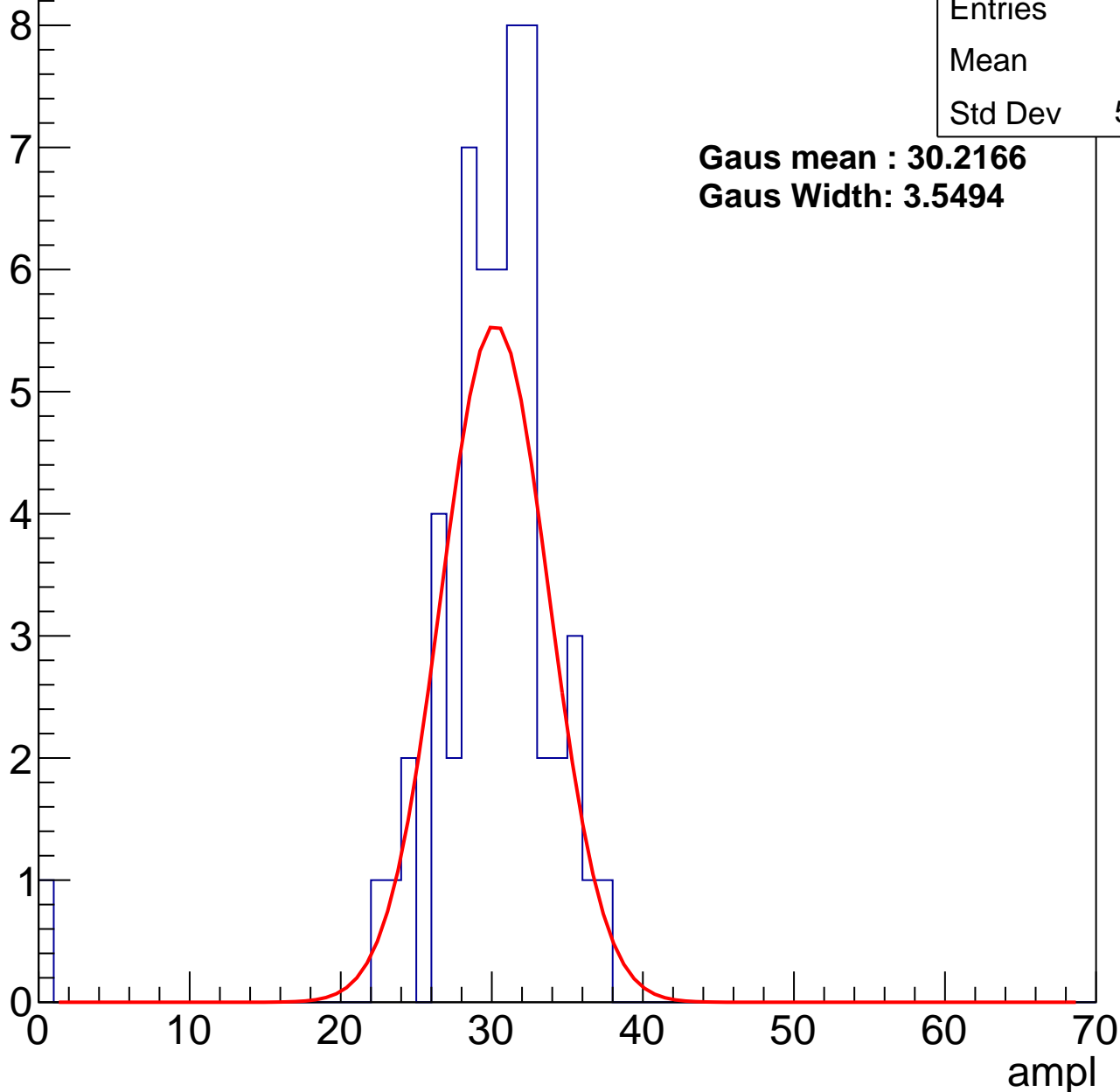
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	55
Mean	29.4
Std Dev	5.101

Gaus mean : 30.2166

Gaus Width: 3.5494



B1L100S, U1-ch123, adc1

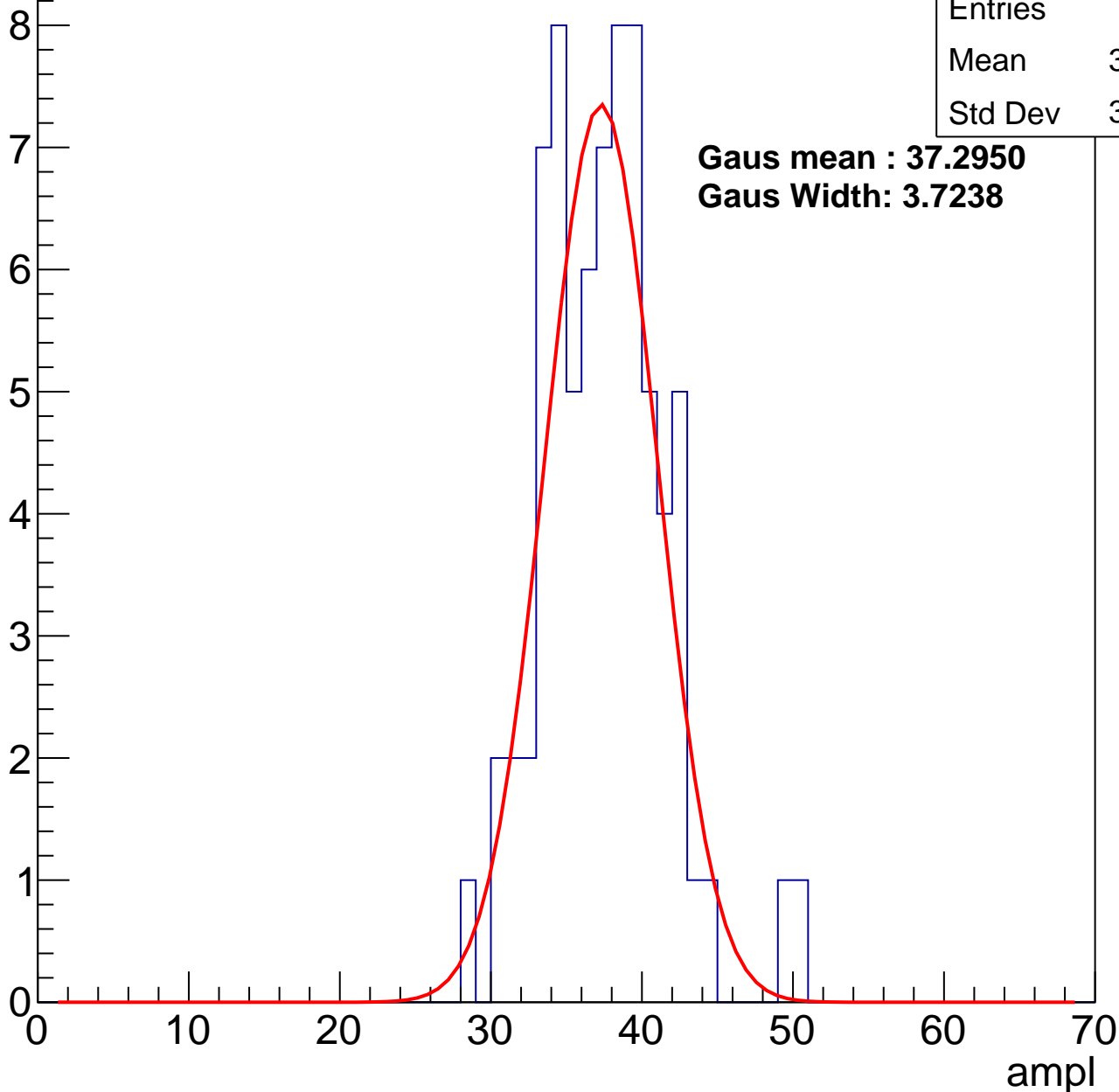
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	74
Mean	37.07
Std Dev	3.998

Gaus mean : 37.2950

Gaus Width: 3.7238



B1L100S, U1-ch123, adc2

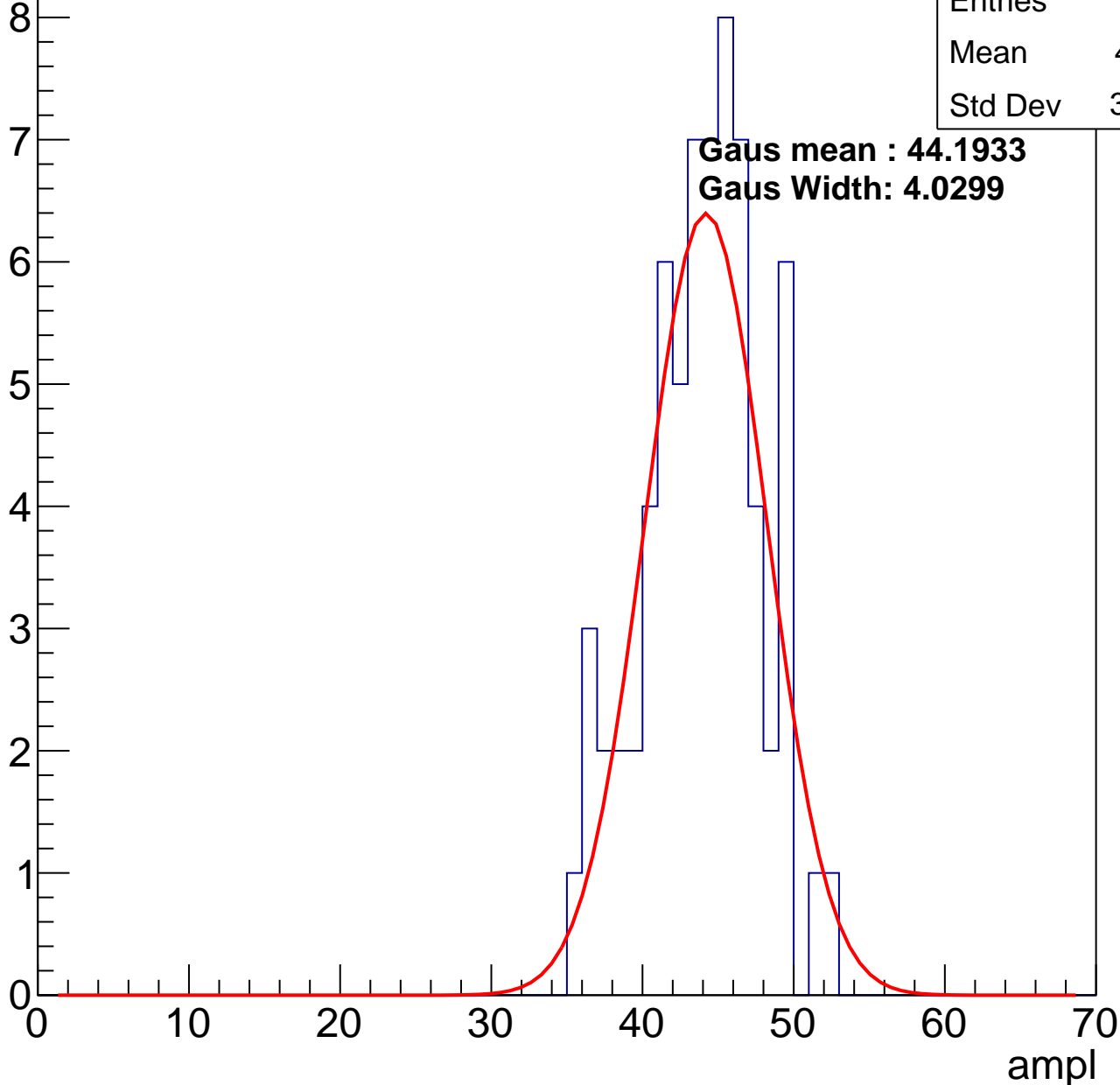
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	68
Mean	43.51
Std Dev	3.814

Gaus mean : 44.1933

Gaus Width: 4.0299

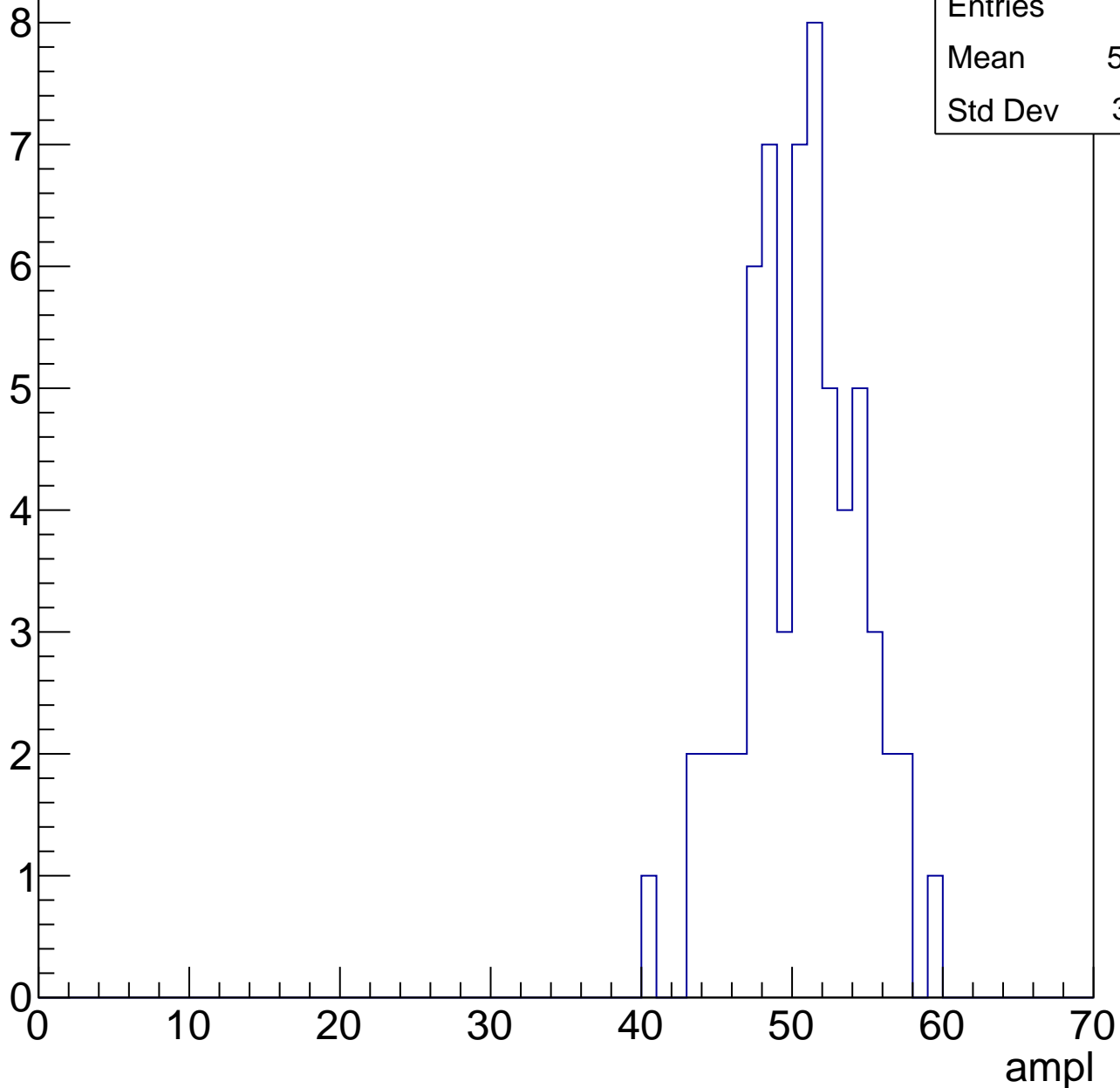


B1L100S, U1-ch123, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	50.18
Std Dev	3.821

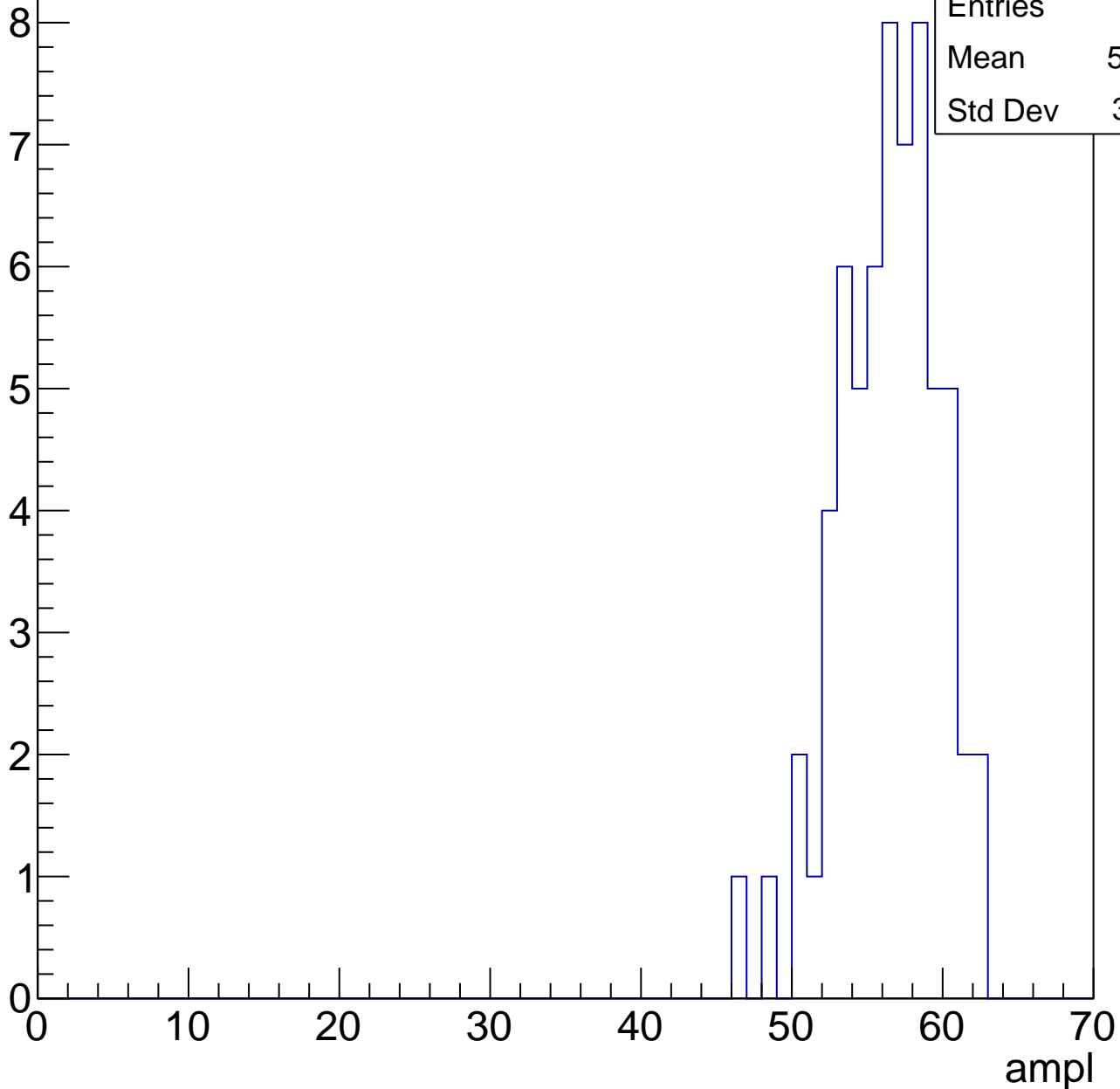


B1L100S, U1-ch123, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	55.92
Std Dev	3.311

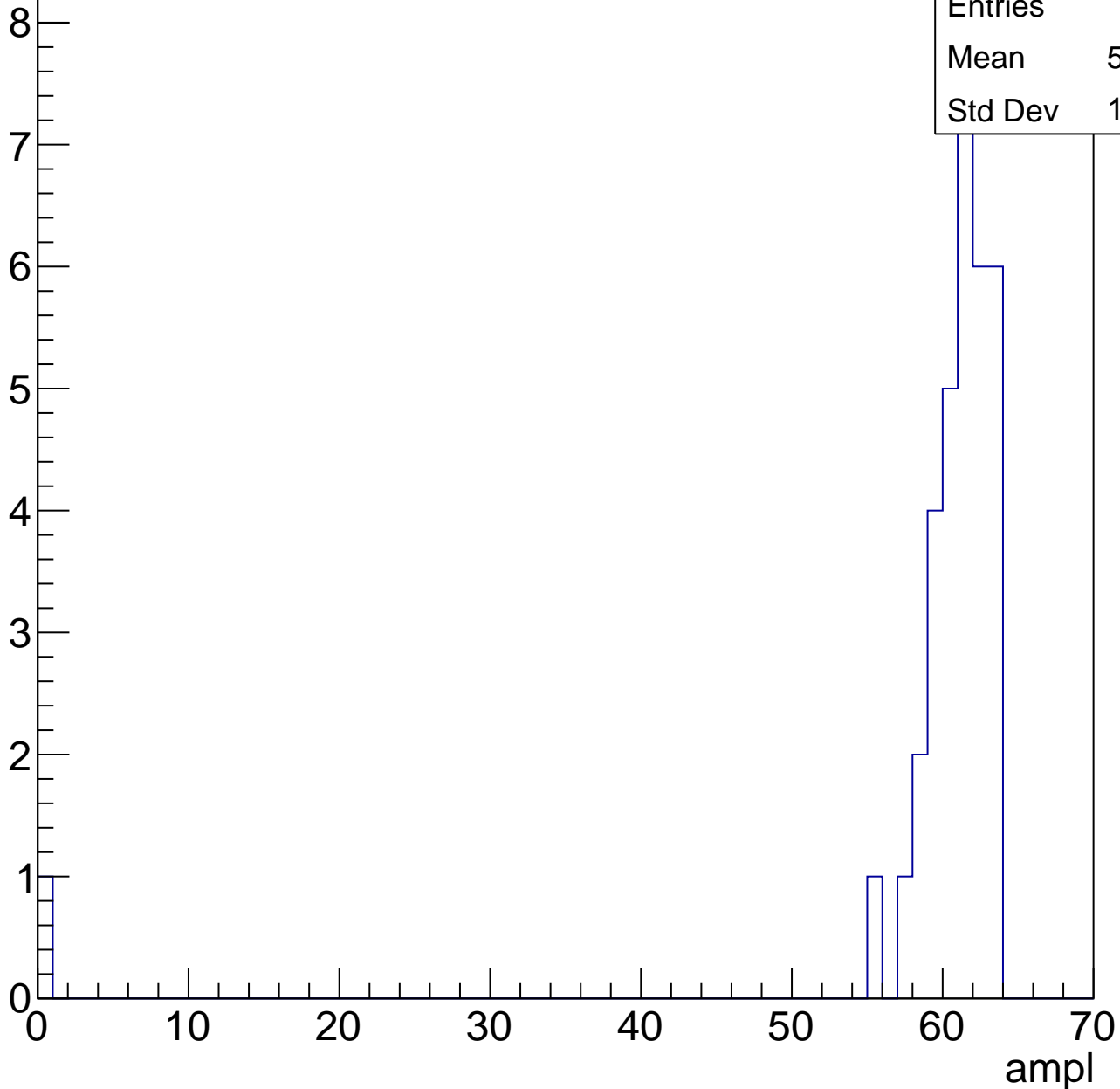


B1L100S, U1-ch123, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

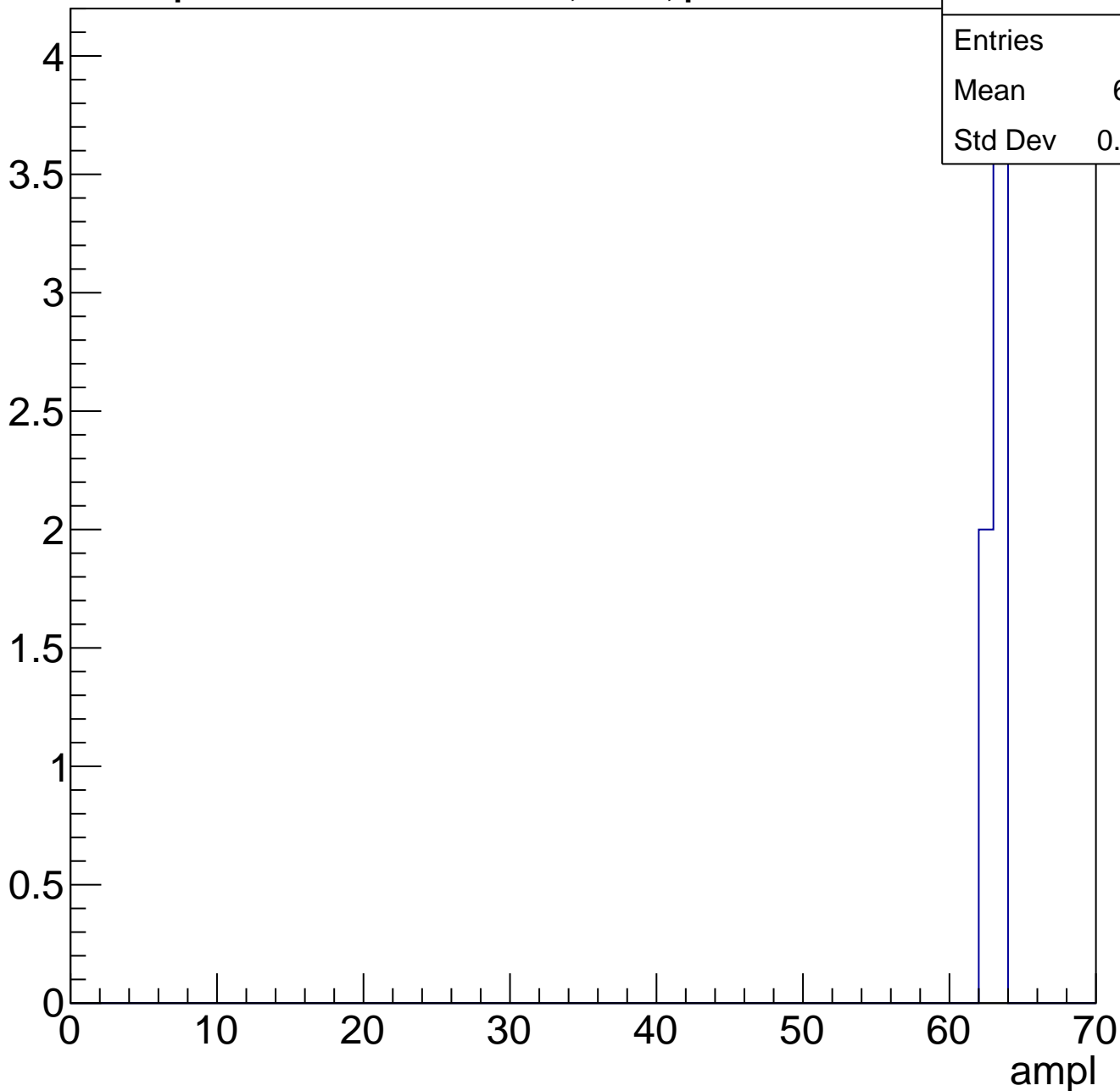
Entries	34
Mean	58.88
Std Dev	10.42



B1L100S, U1-ch123, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch123, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch124, adc0

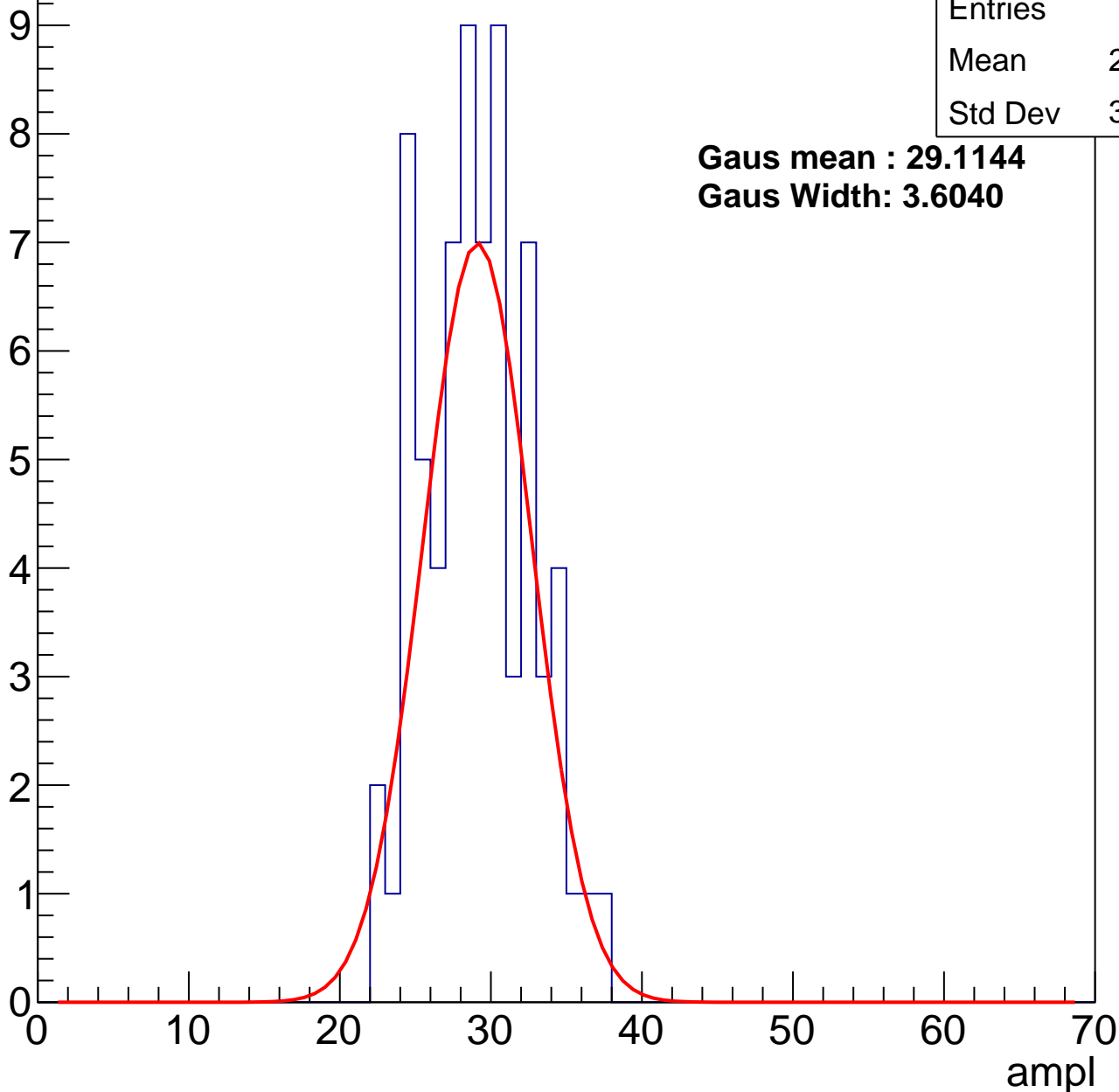
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	28.64
Std Dev	3.453

Gaus mean : 29.1144

Gaus Width: 3.6040



B1L100S, U1-ch124, adc1

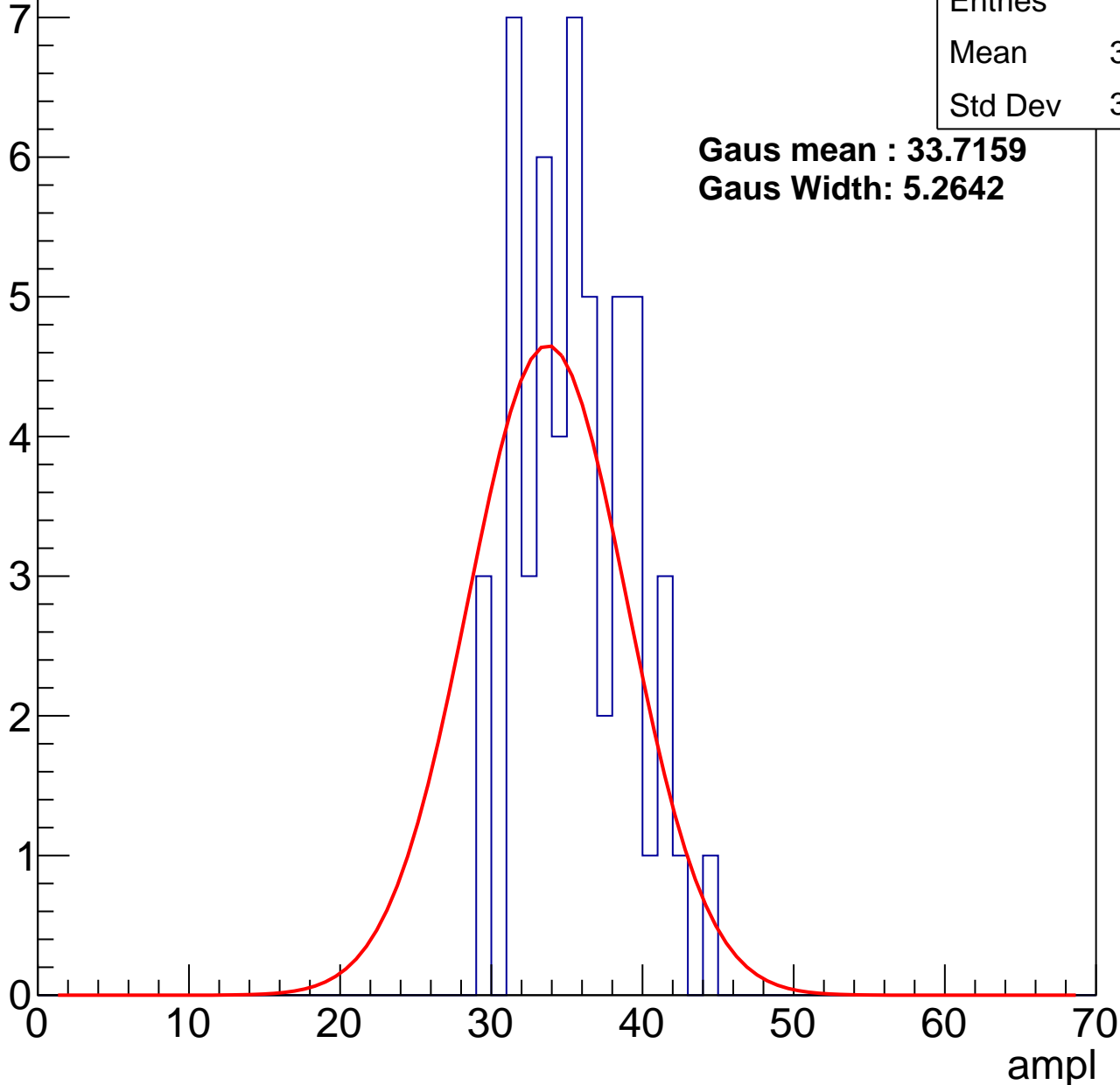
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	35.23
Std Dev	3.564

Gaus mean : 33.7159

Gaus Width: 5.2642



B1L100S, U1-ch124, adc2

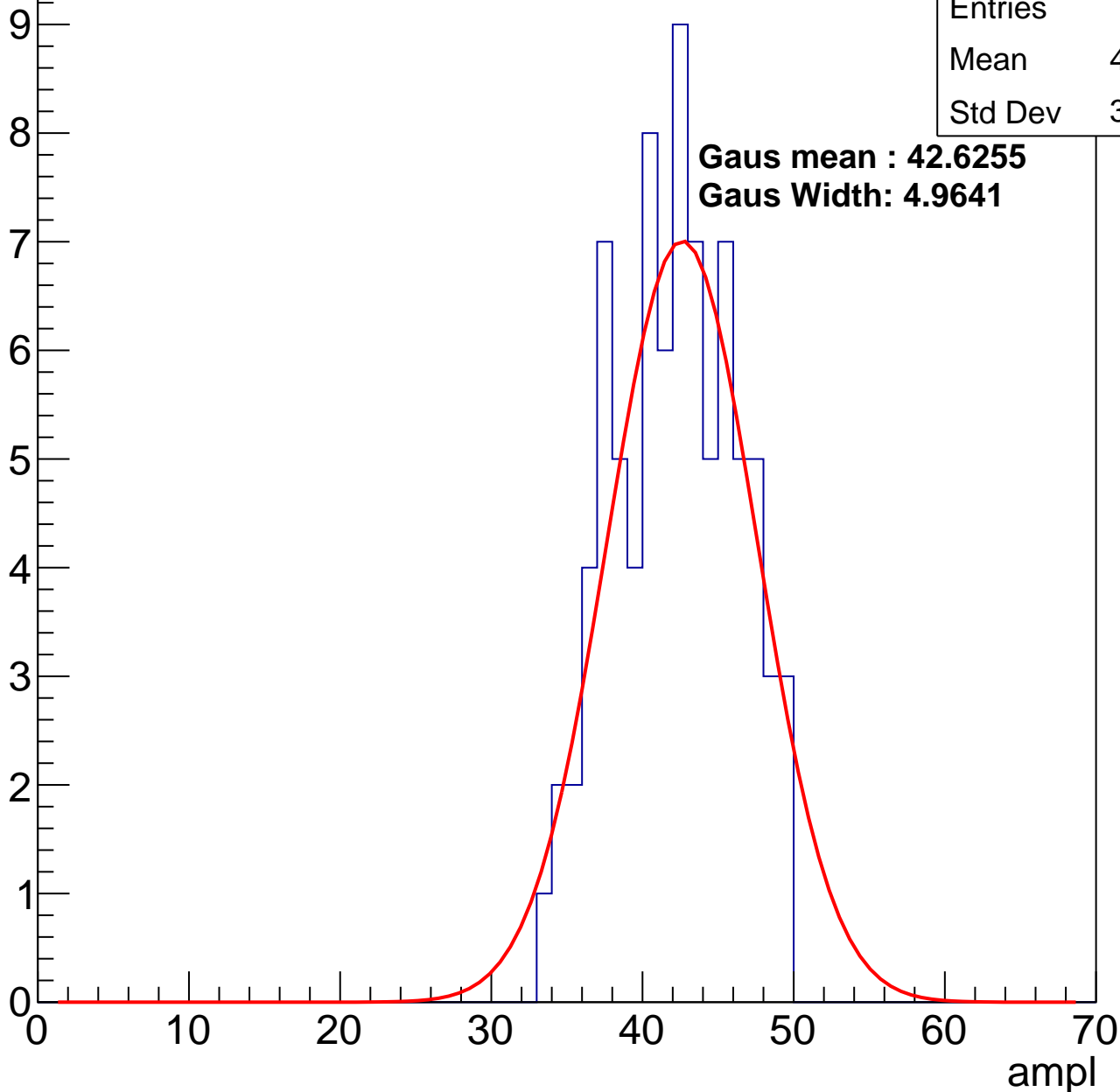
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	83
Mean	41.64
Std Dev	3.987

Gaus mean : 42.6255

Gaus Width: 4.9641

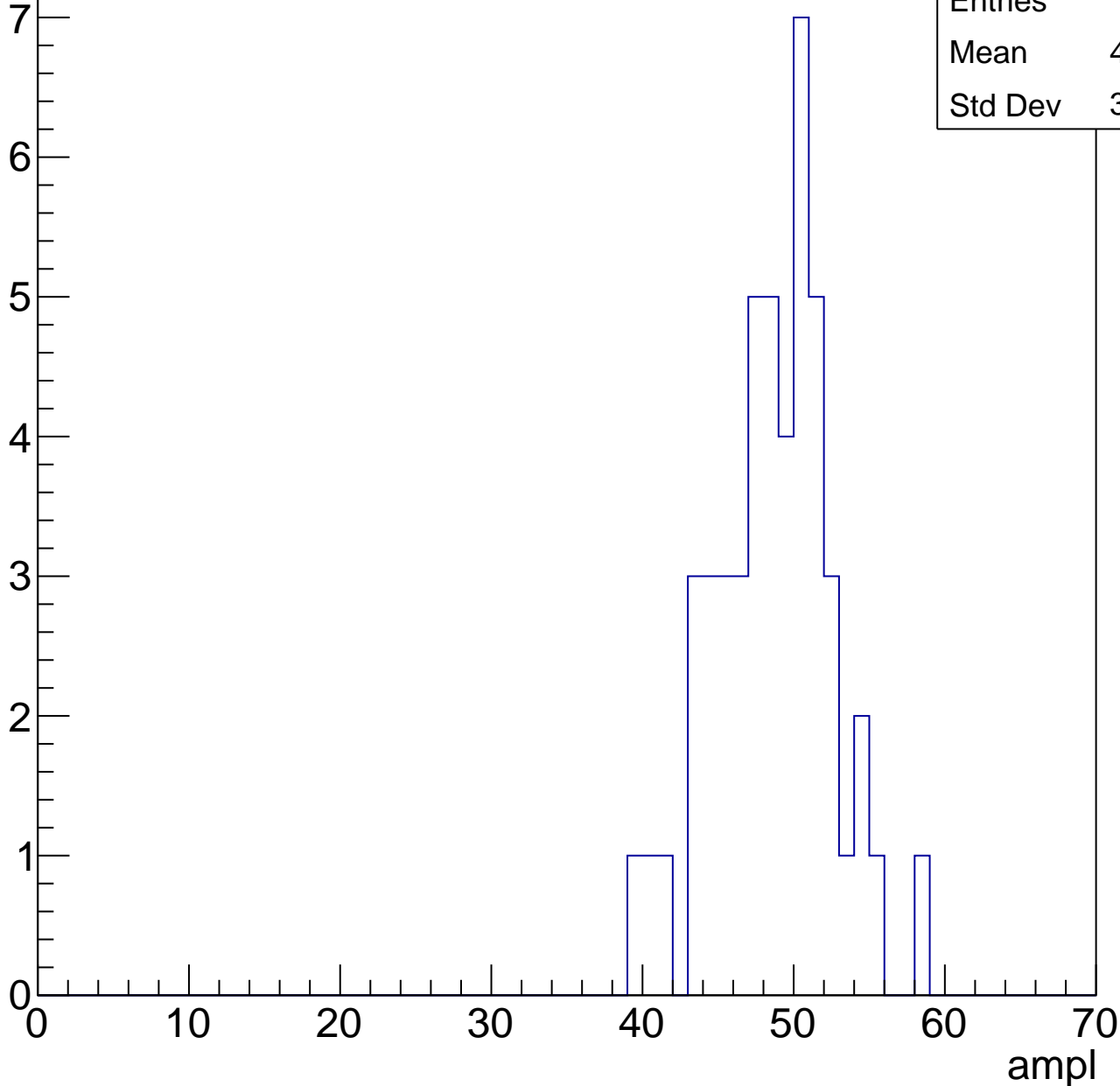


B1L100S, U1-ch124, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	49
Mean	48.16
Std Dev	3.862

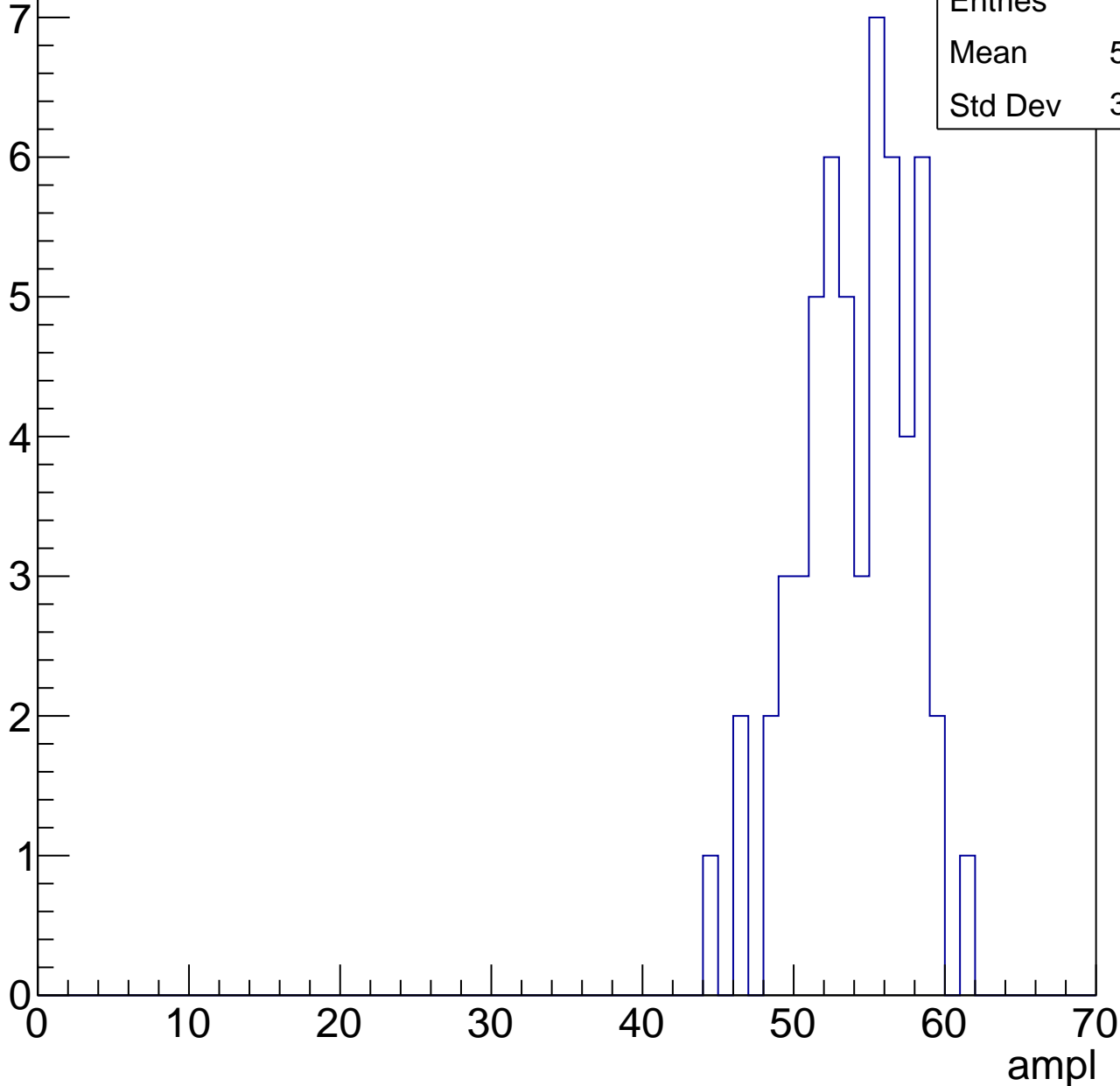


B1L100S, U1-ch124, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

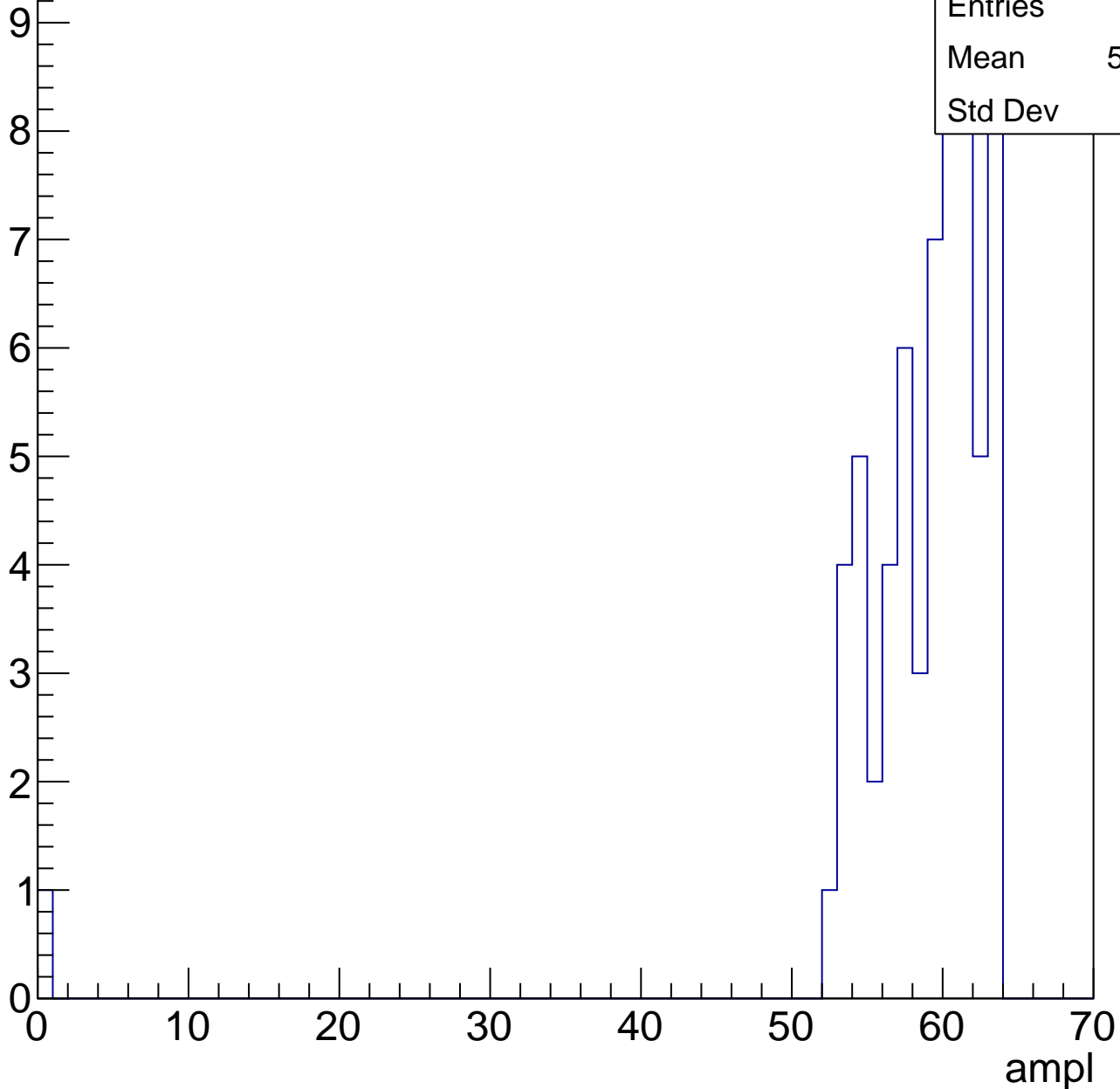
Entries	56
Mean	53.55
Std Dev	3.654



B1L100S, U1-ch124, adc5

calib_packv5_042523_0143.root, FC#4, port A2

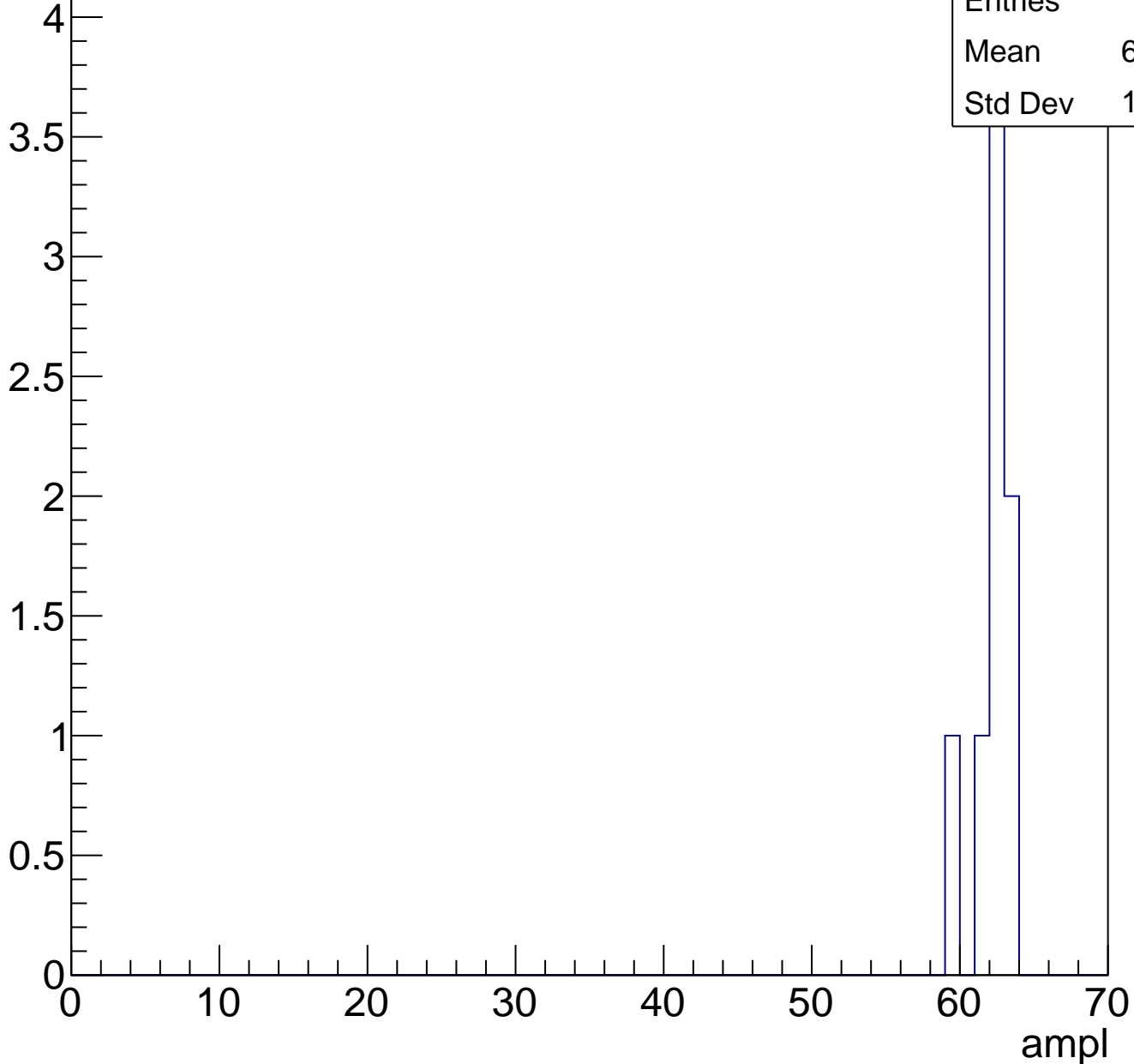
Entry



B1L100S, U1-ch124, adc6

calib_packv5_042523_0143.root, FC#4, port A2

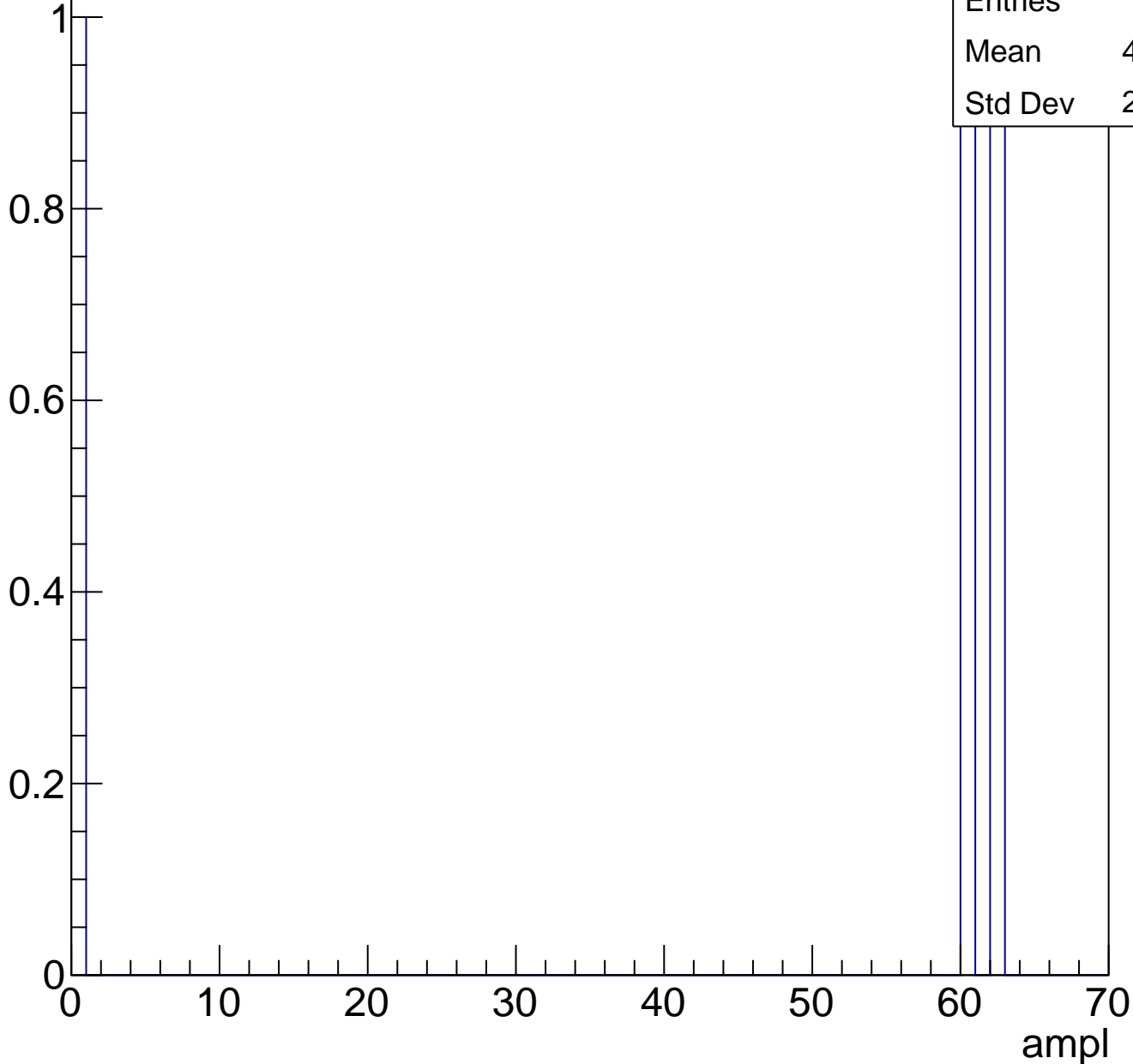
Entry



B1L100S, U1-ch124, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch125, adc0

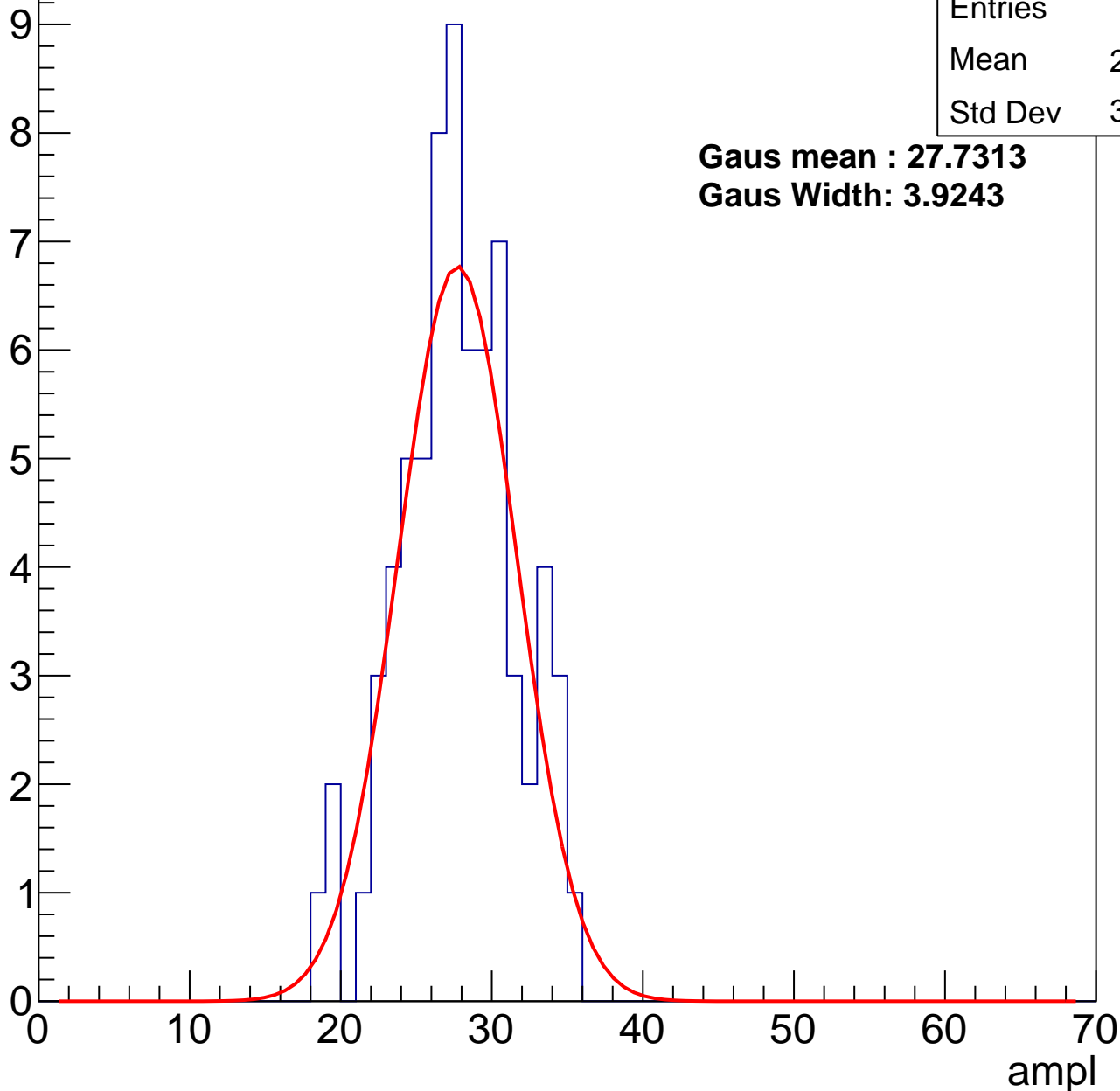
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	27.27
Std Dev	3.776

Gaus mean : 27.7313

Gaus Width: 3.9243



B1L100S, U1-ch125, adc1

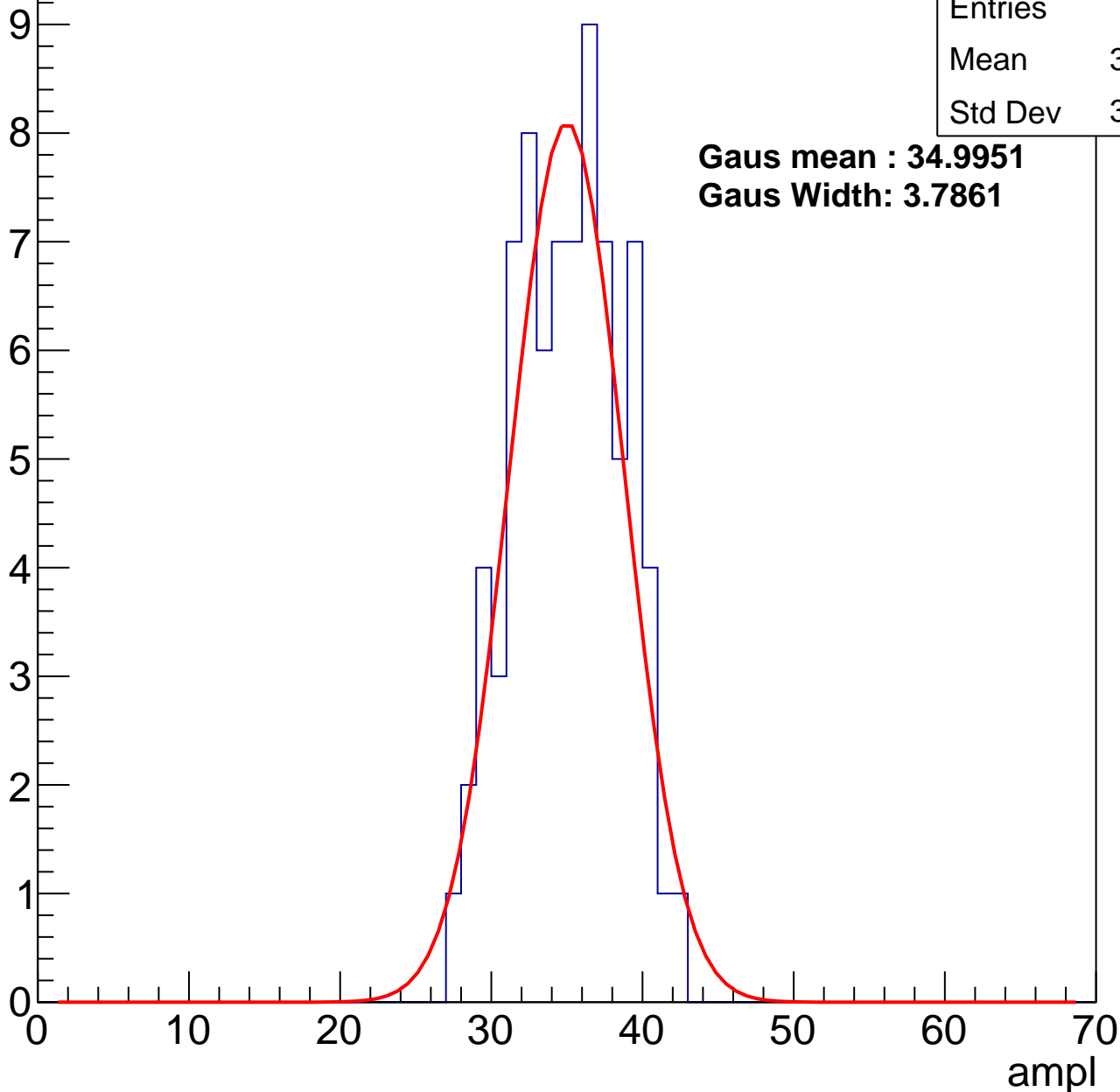
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	79
Mean	34.58
Std Dev	3.488

Gaus mean : 34.9951

Gaus Width: 3.7861



B1L100S, U1-ch125, adc2

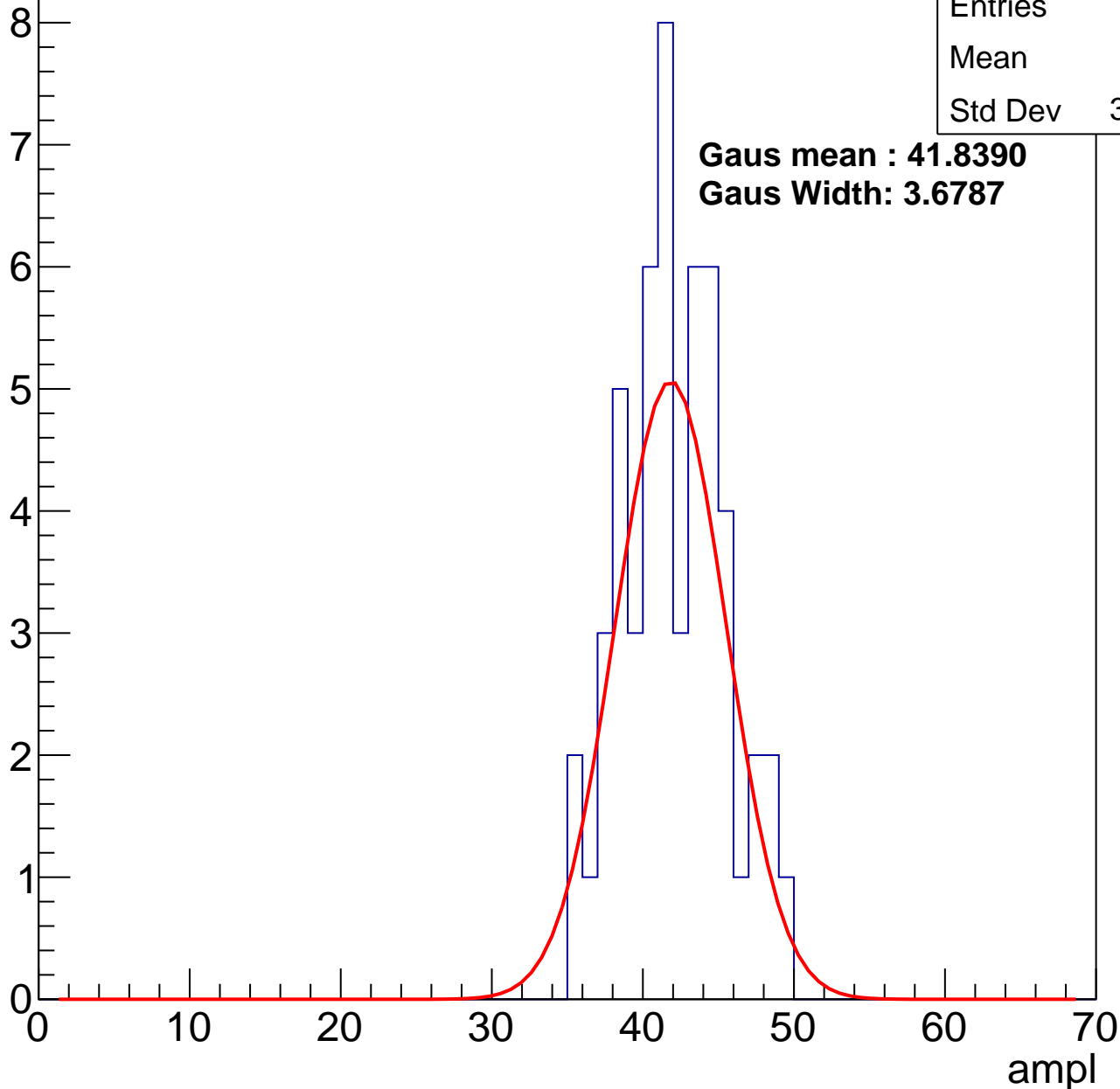
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	53
Mean	41.6
Std Dev	3.344

Gaus mean : 41.8390

Gaus Width: 3.6787

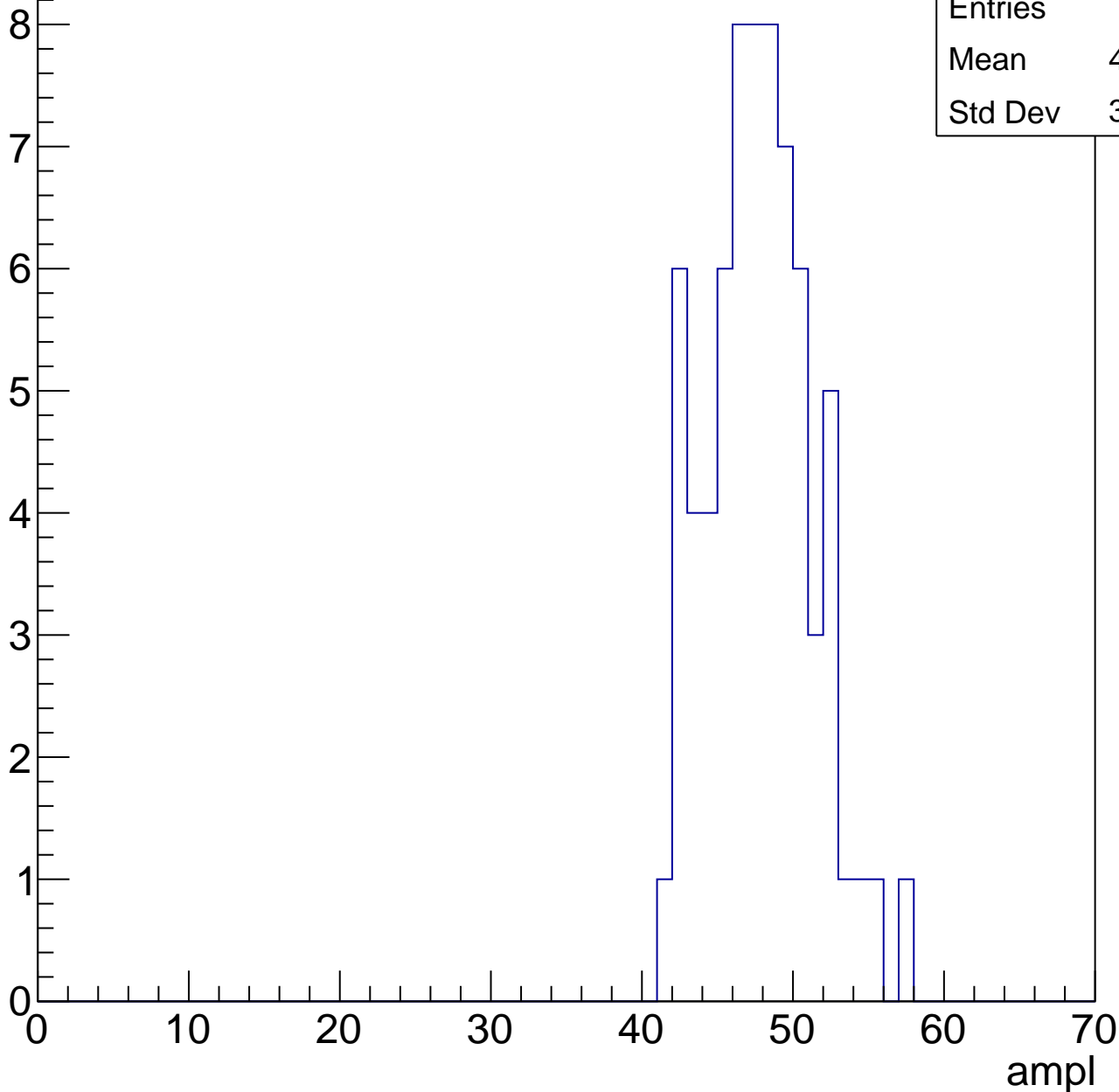


B1L100S, U1-ch125, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	70
Mean	47.34
Std Dev	3.426

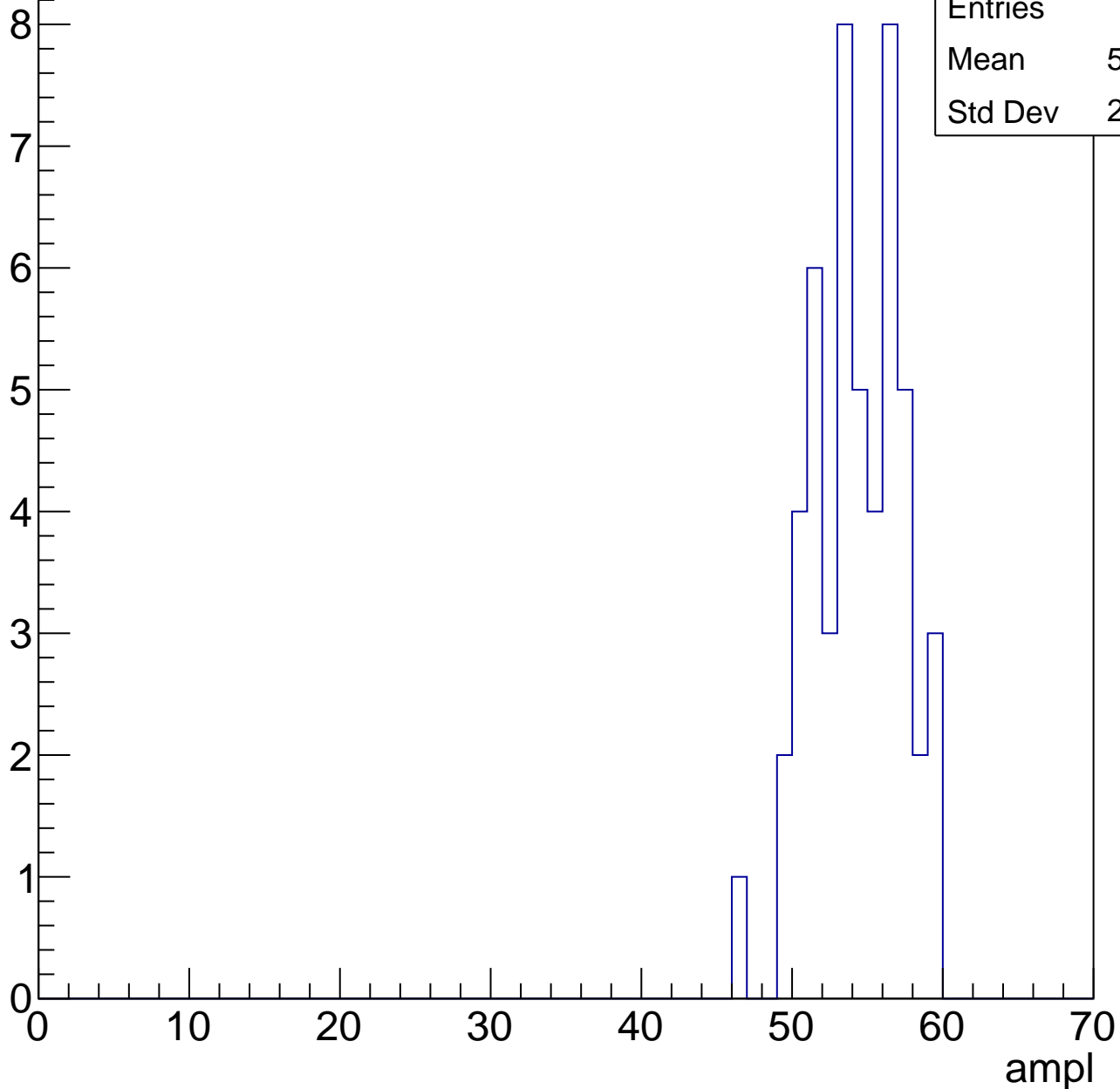


B1L100S, U1-ch125, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	51
Mean	53.84
Std Dev	2.933

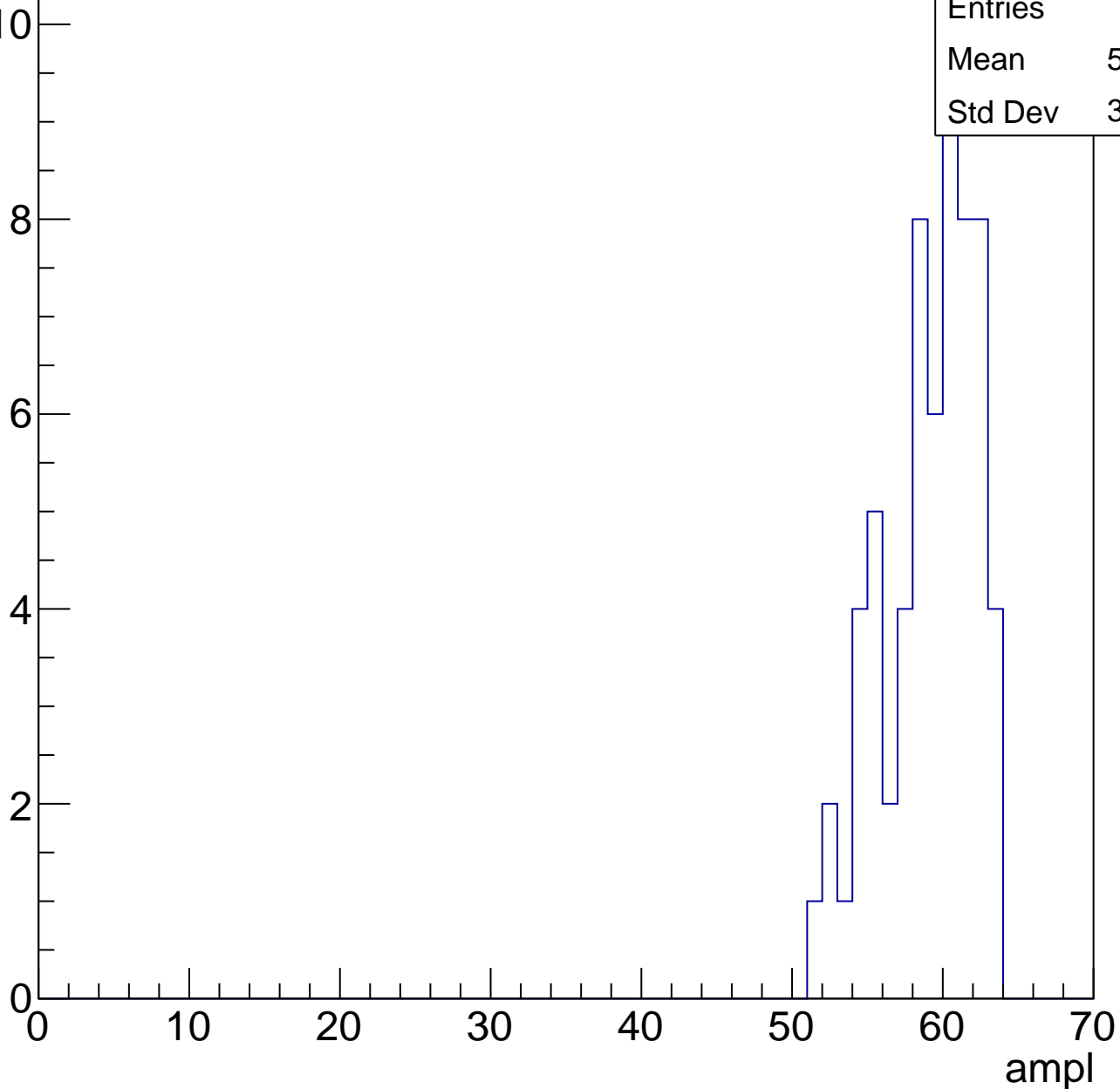


B1L100S, U1-ch125, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	63
Mean	58.62
Std Dev	3.057

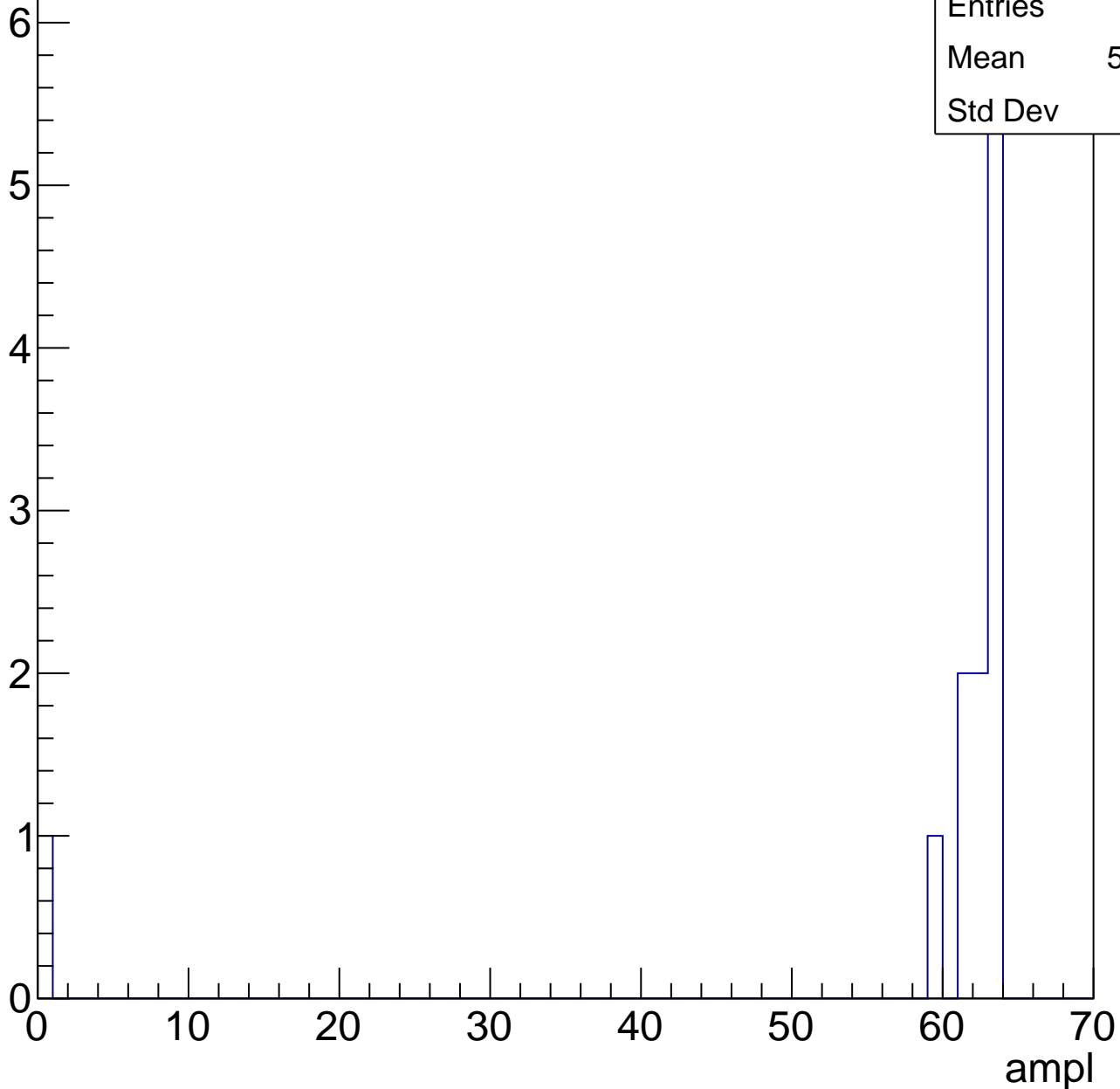


B1L100S, U1-ch125, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	12
Mean	56.92
Std Dev	17.2



B1L100S, U1-ch125, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	1
Mean	0
Std Dev	0

B1L100S, U1-ch126, adc0

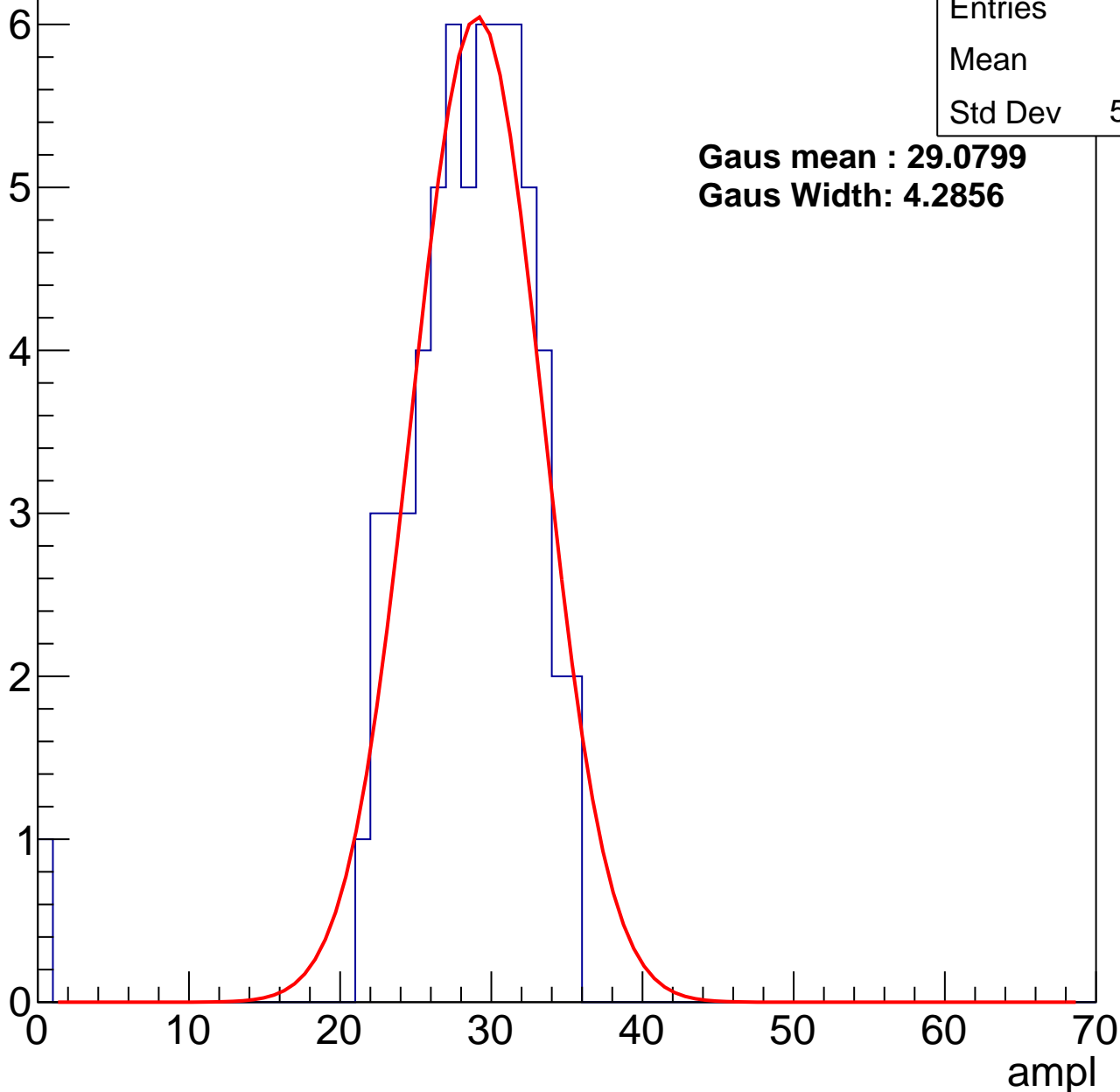
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	62
Mean	27.9
Std Dev	5.015

Gaus mean : 29.0799

Gaus Width: 4.2856



B1L100S, U1-ch126, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	85
Mean	35.92
Std Dev	3.926

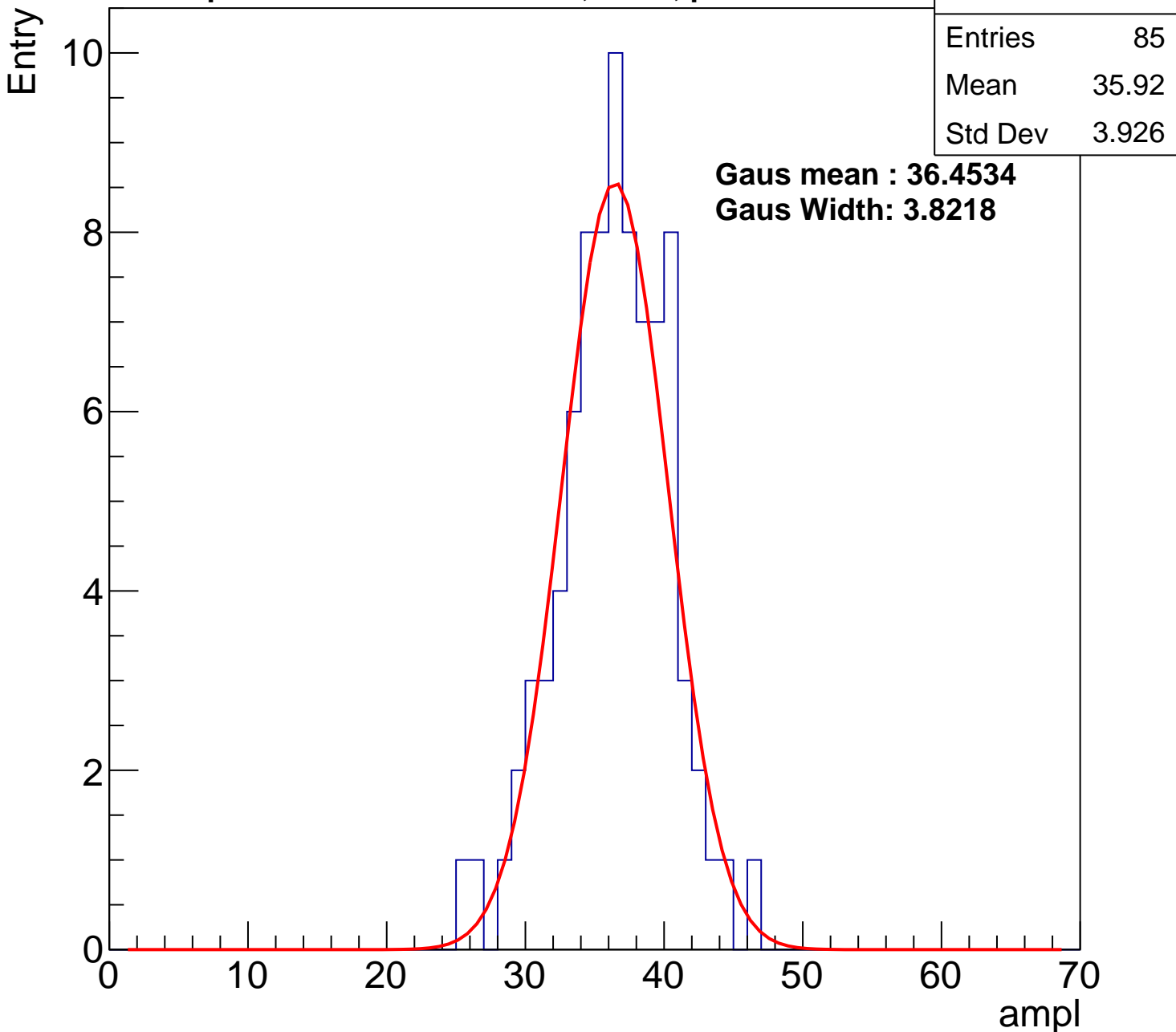
Gaus mean : 36.4534
Gaus Width: 3.8218

Entry

10
8
6
4
2
0

0 10 20 30 40 50 60 70

ampl



B1L100S, U1-ch126, adc2

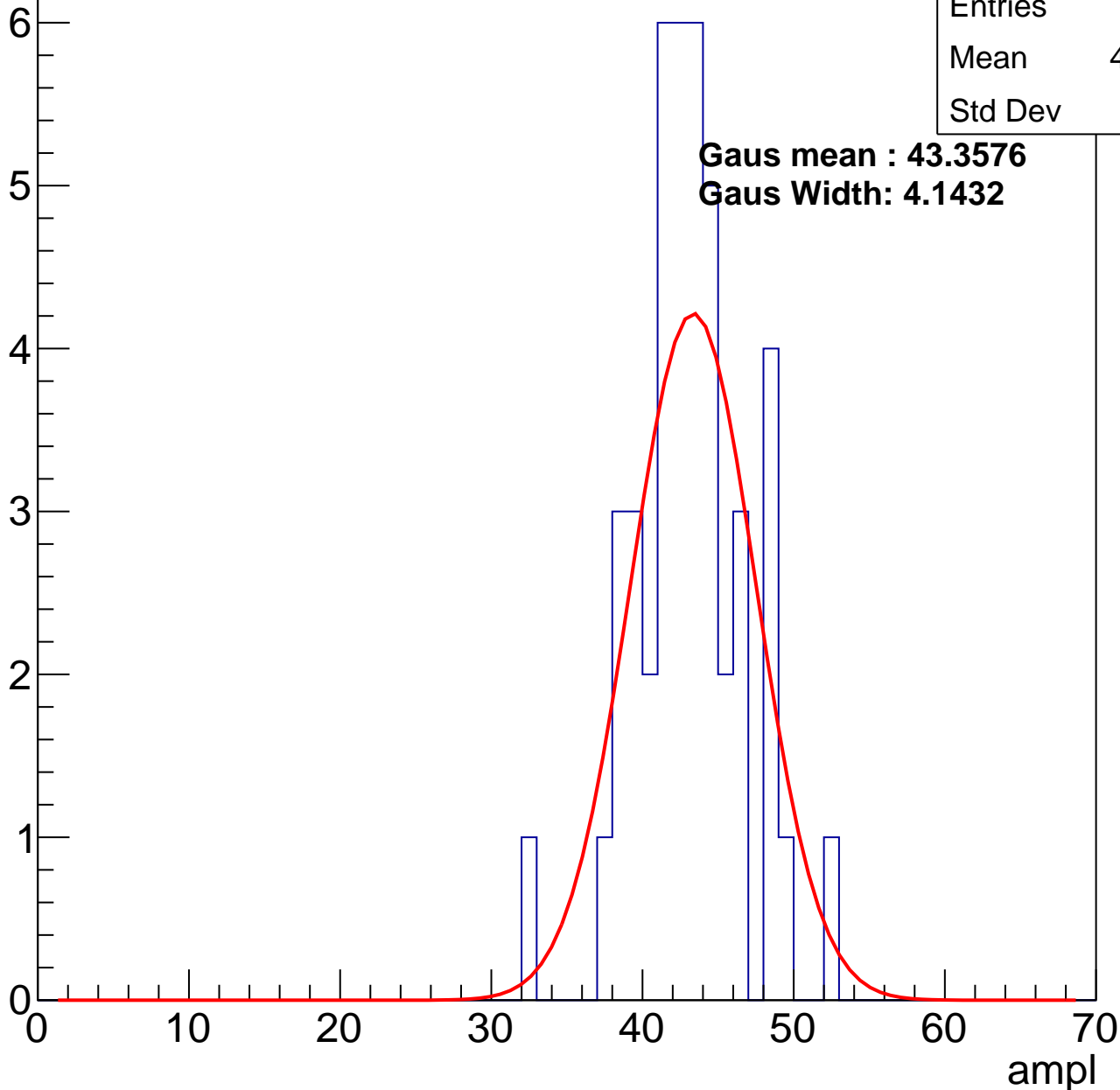
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	44
Mean	42.66
Std Dev	3.63

Gaus mean : 43.3576

Gaus Width: 4.1432

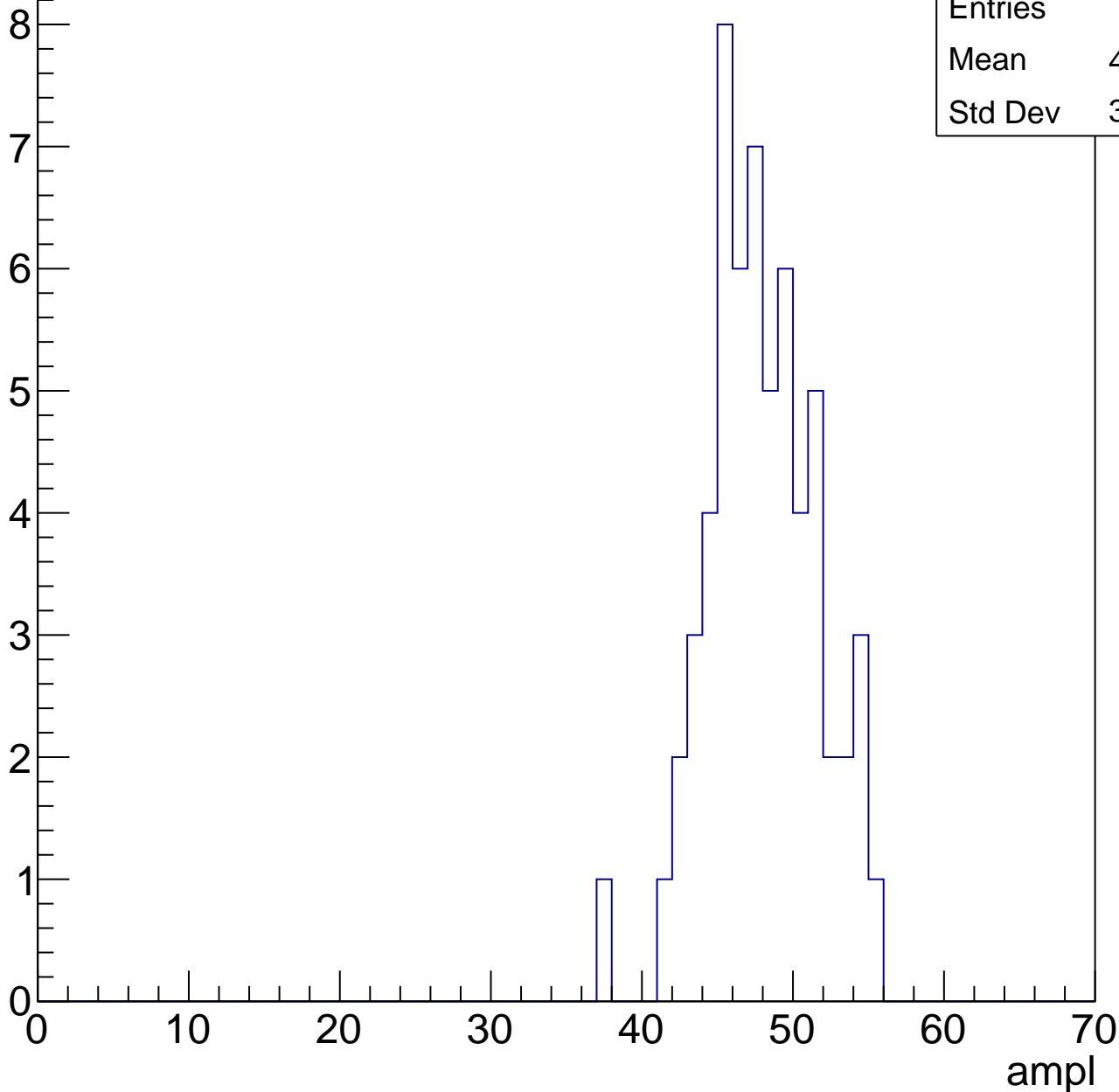


B1L100S, U1-ch126, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

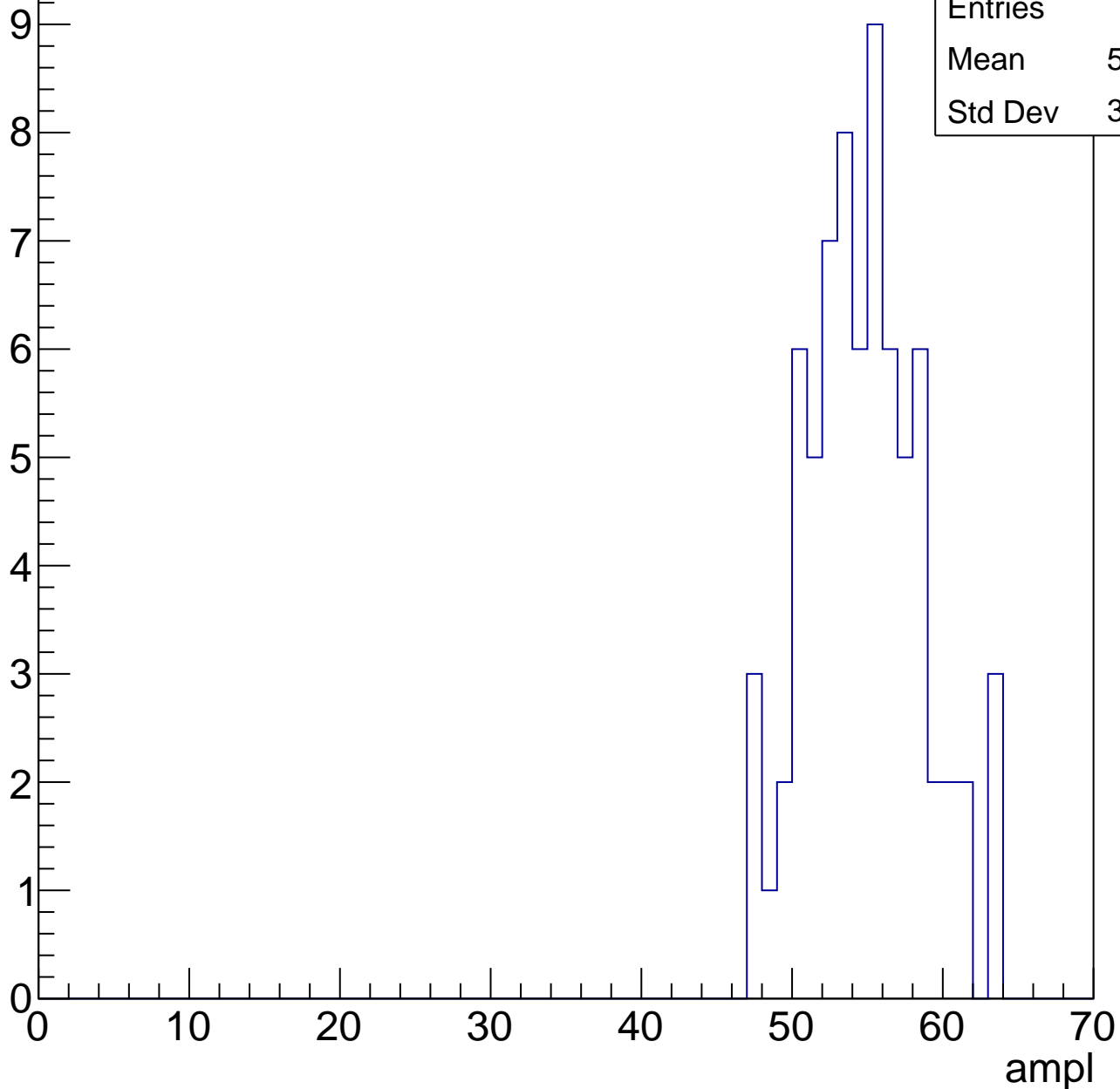
Entries	60
Mean	47.47
Std Dev	3.589



B1L100S, U1-ch126, adc4

calib_packv5_042523_0143.root, FC#4, port A2

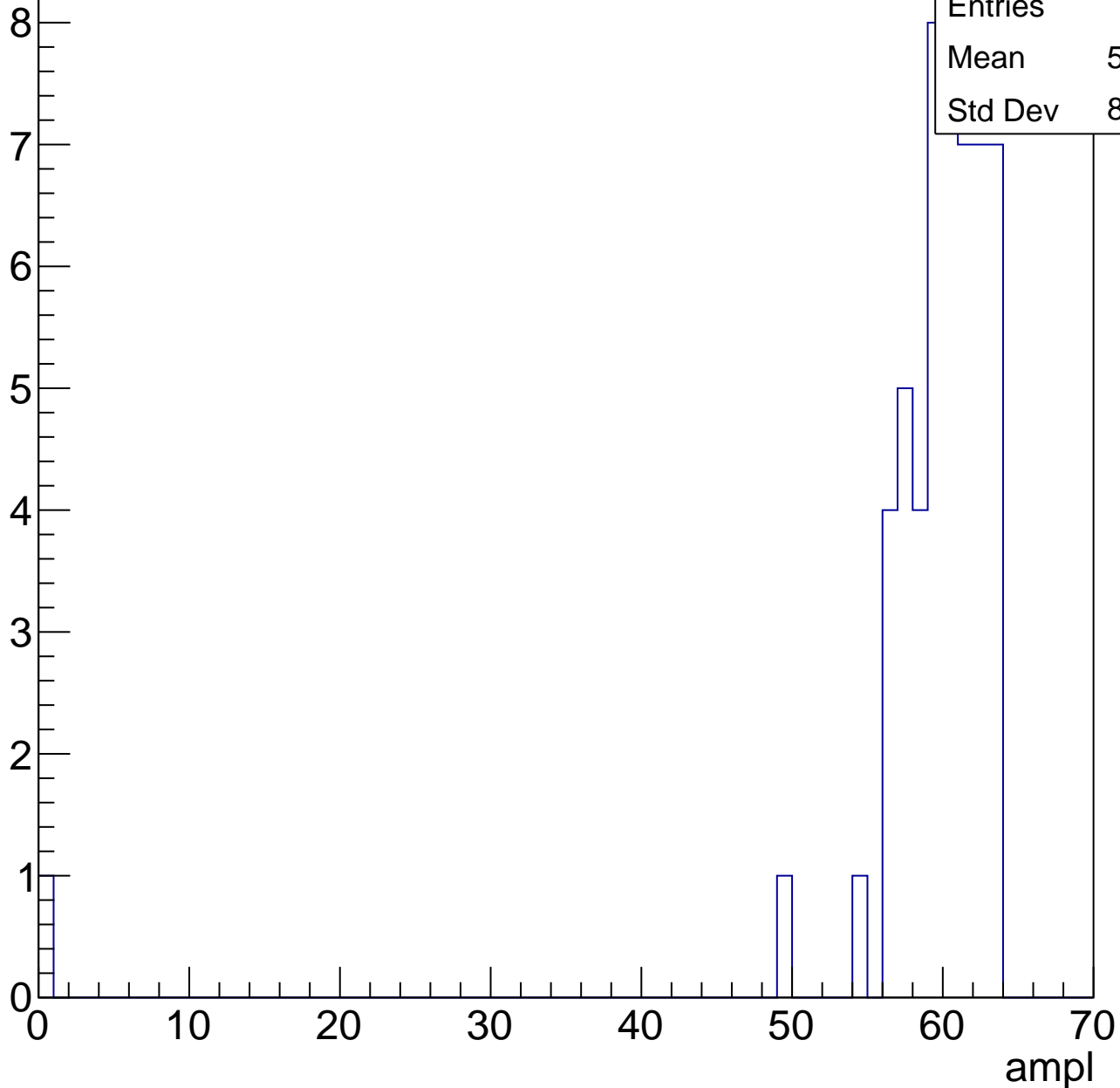
Entry



B1L100S, U1-ch126, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch126, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch126, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	2
Mean	0
Std Dev	0

B1L100S, U1-ch127, adc0

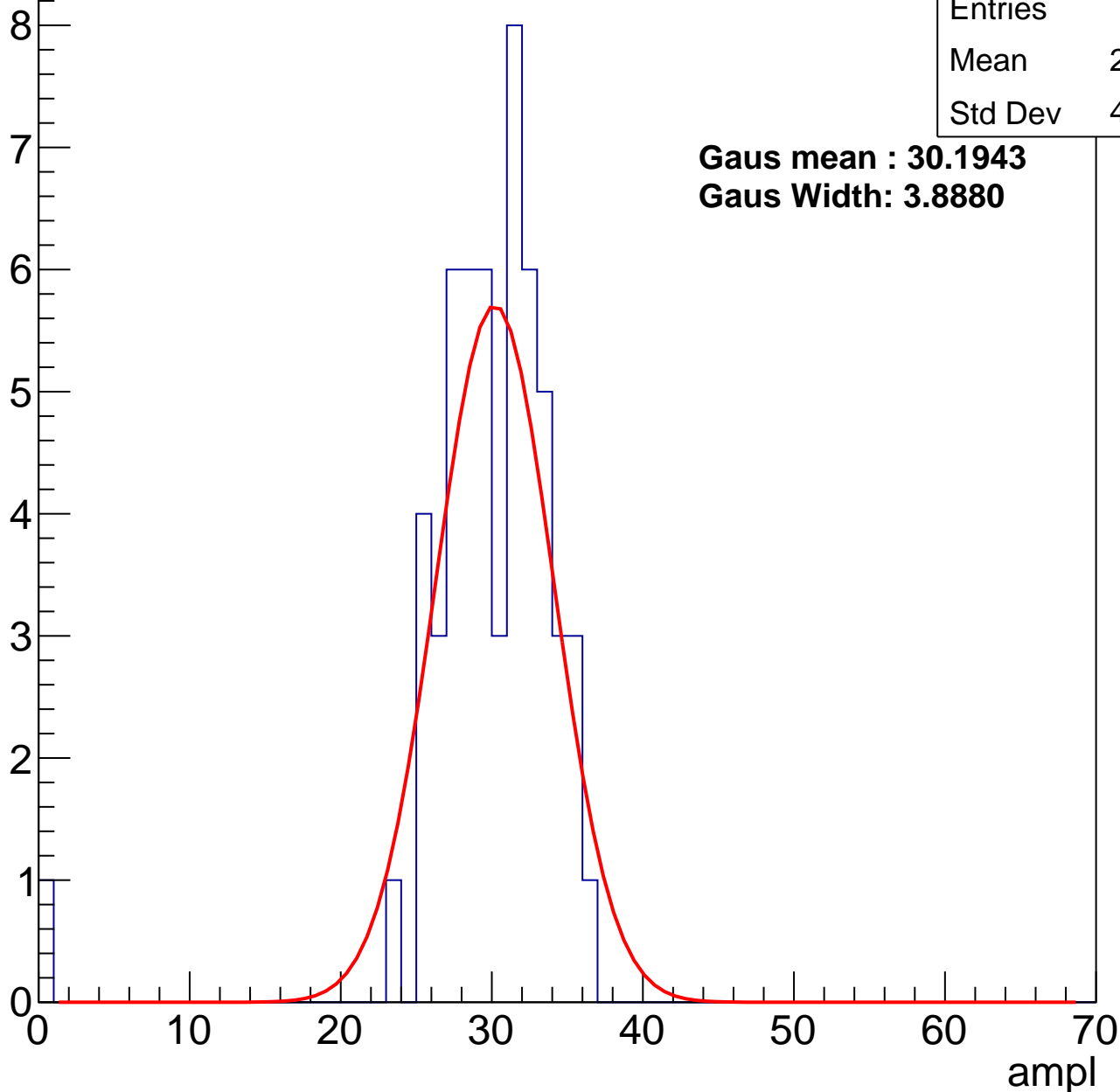
calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	56
Mean	29.34
Std Dev	4.987

Gaus mean : 30.1943

Gaus Width: 3.8880



B1L100S, U1-ch127, adc1

calib_packv5_042523_0143.root, FC#4, port A2

Entries	79
Mean	36.04
Std Dev	3.736

Gaus mean : 37.1338

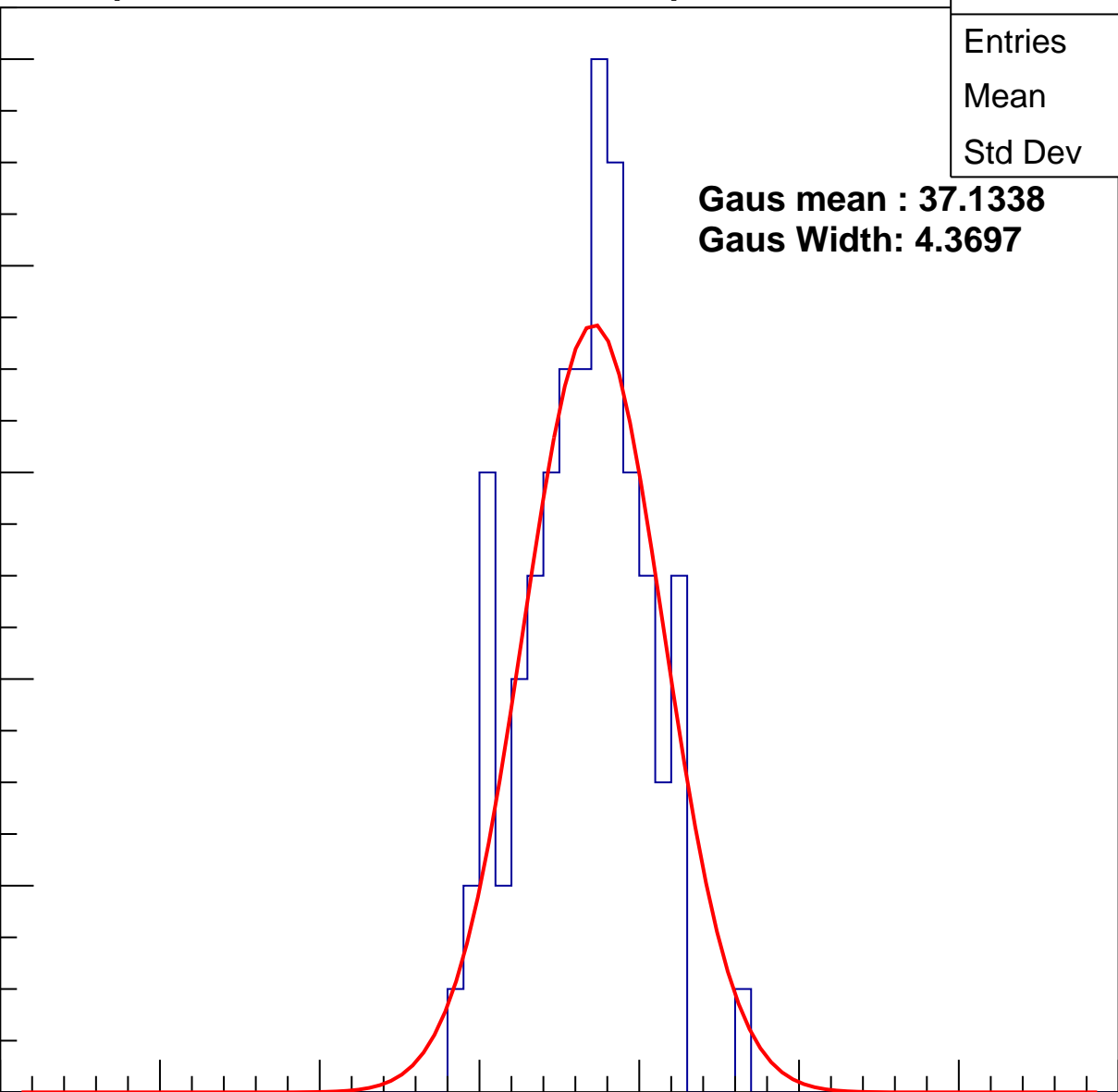
Gaus Width: 4.3697

Entry

10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L100S, U1-ch127, adc2

calib_packv5_042523_0143.root, FC#4, port A2

Entry

Entries	72
Mean	43.69
Std Dev	3.585

Gaus mean : 44.1000

Gaus Width: 3.1795

10

8

6

4

2

0

ampl

0

10

20

30

40

50

60

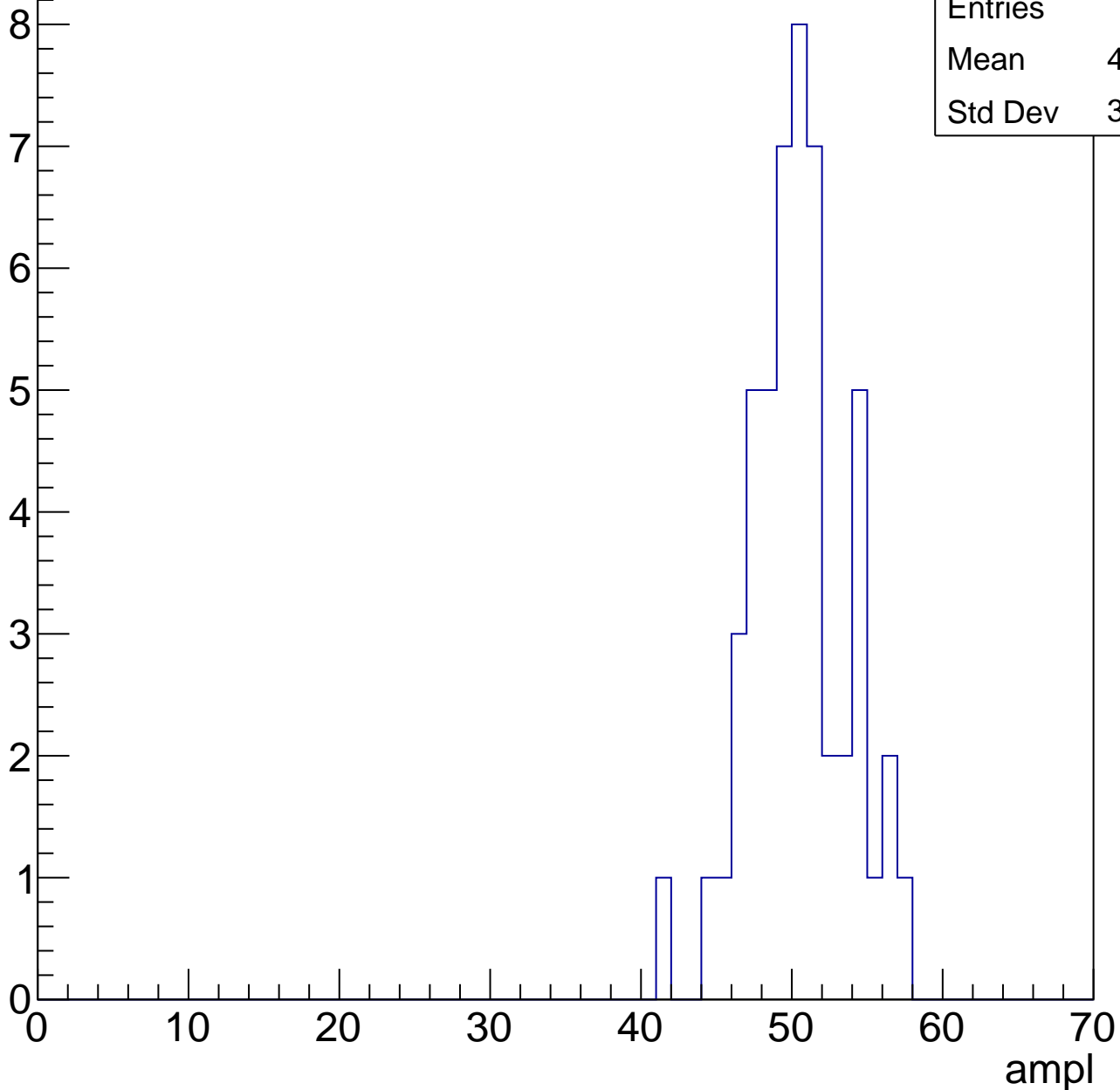
70

B1L100S, U1-ch127, adc3

calib_packv5_042523_0143.root, FC#4, port A2

Entry

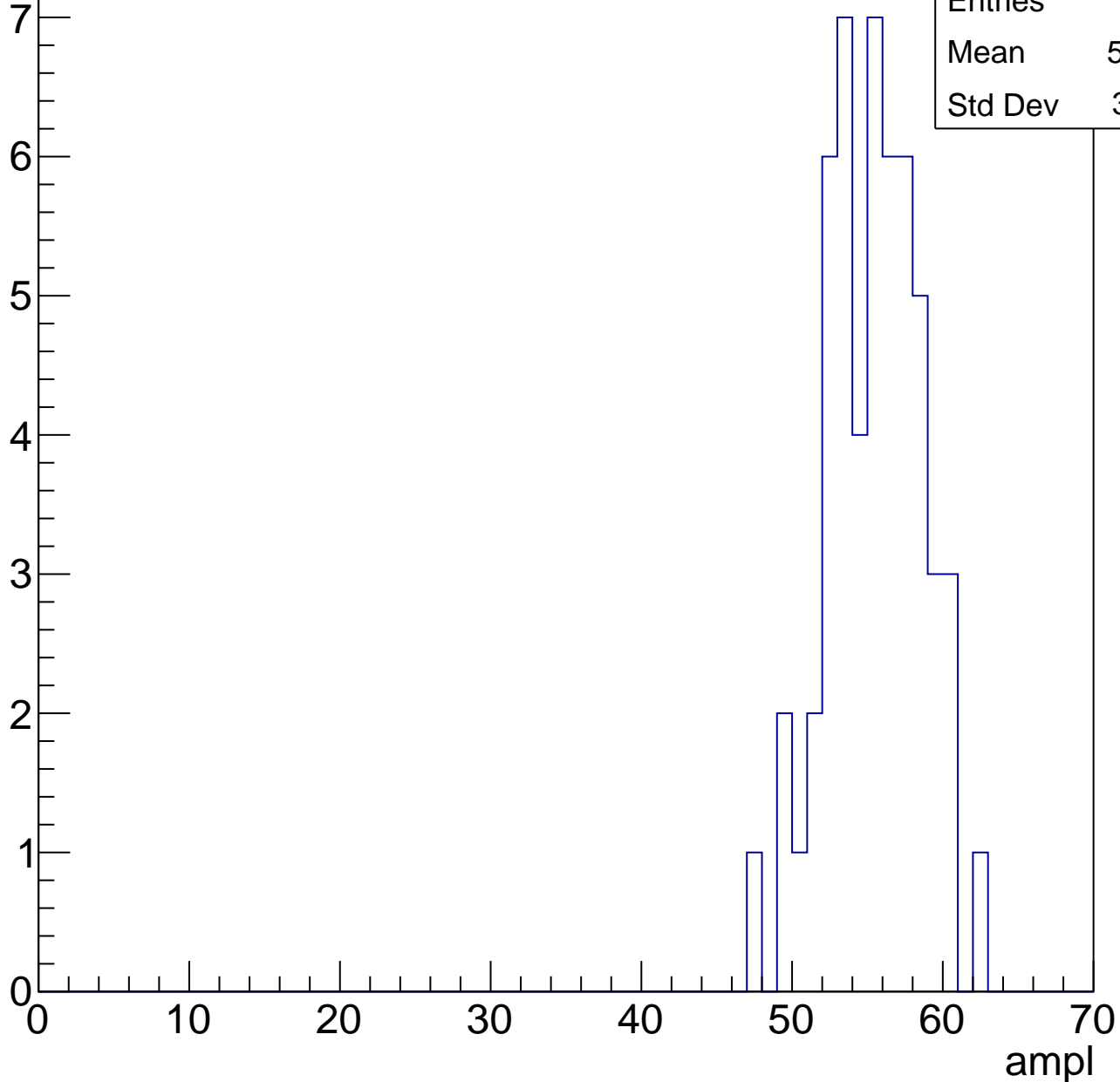
Entries	51
Mean	49.94
Std Dev	3.196



B1L100S, U1-ch127, adc4

calib_packv5_042523_0143.root, FC#4, port A2

Entry

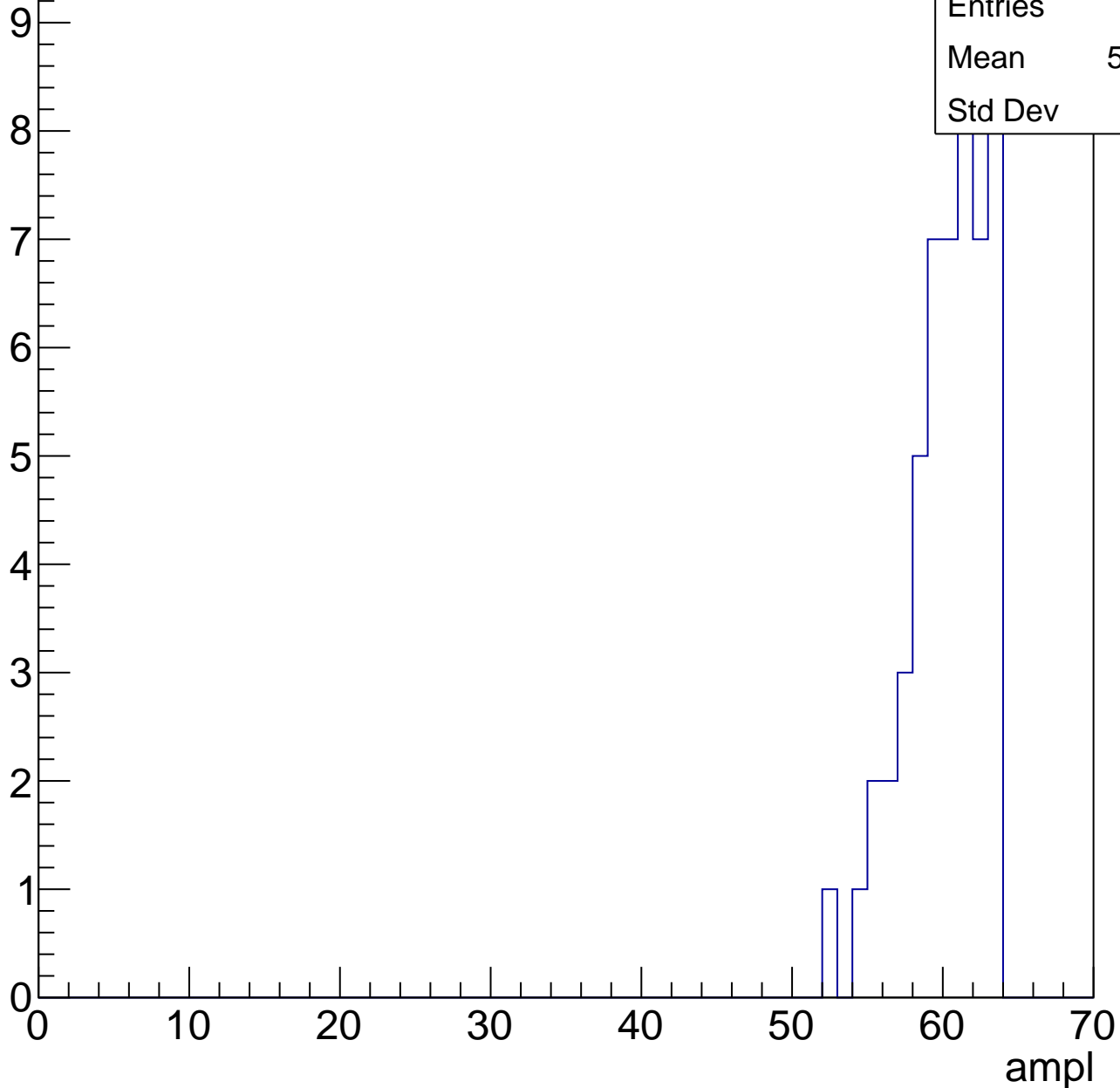


Entries	54
Mean	54.96
Std Dev	3.121

B1L100S, U1-ch127, adc5

calib_packv5_042523_0143.root, FC#4, port A2

Entry

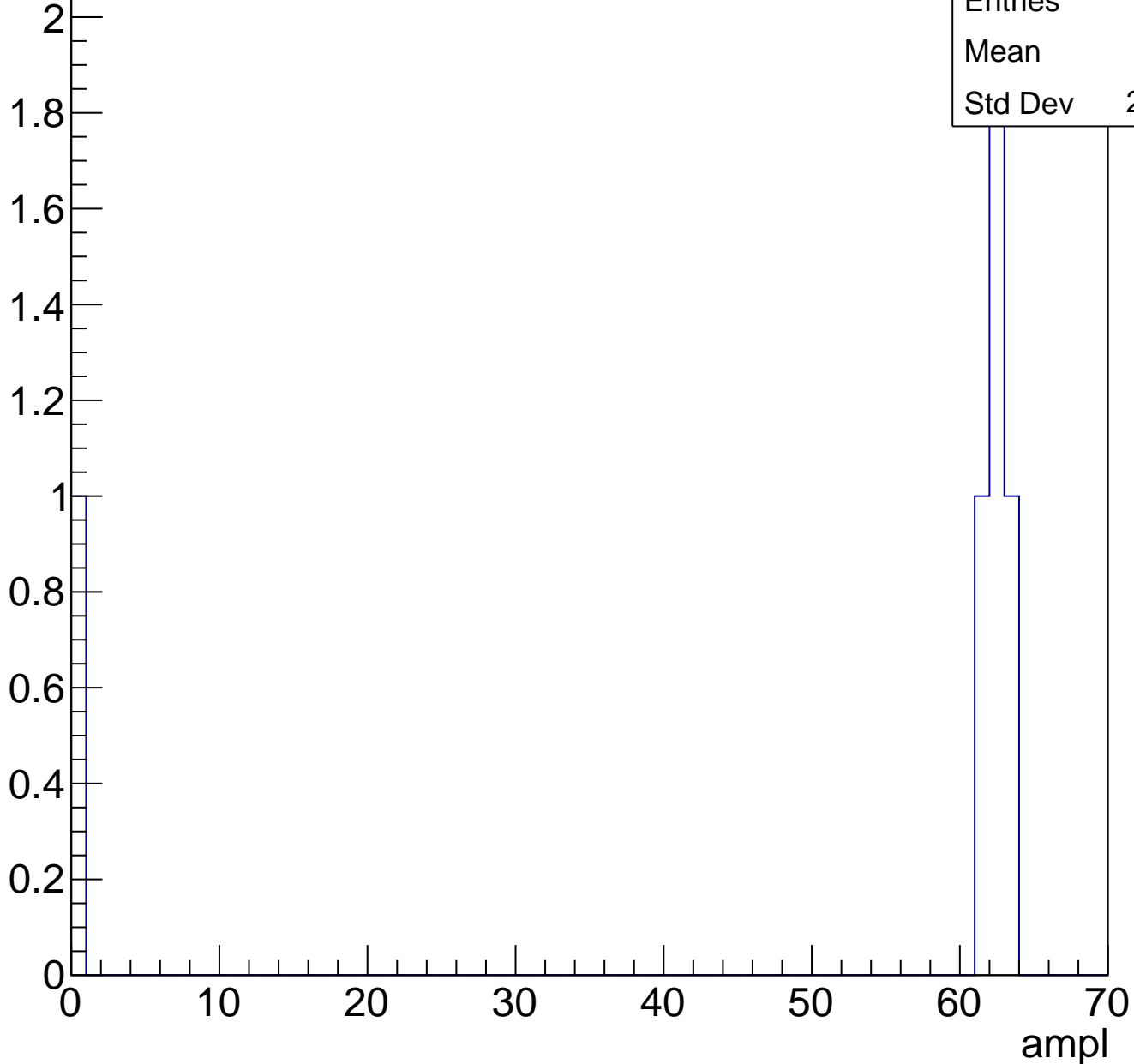


Entries	53
Mean	59.85
Std Dev	2.58

B1L100S, U1-ch127, adc6

calib_packv5_042523_0143.root, FC#4, port A2

Entry



B1L100S, U1-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0

B1L100S, U1-ch127, adc7

calib_packv5_042523_0143.root, FC#4, port A2

Entry



Entries	0
Mean	0
Std Dev	0