

B1L002S, U18-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.89
Std Dev	11.81

Turn on : 25.3466

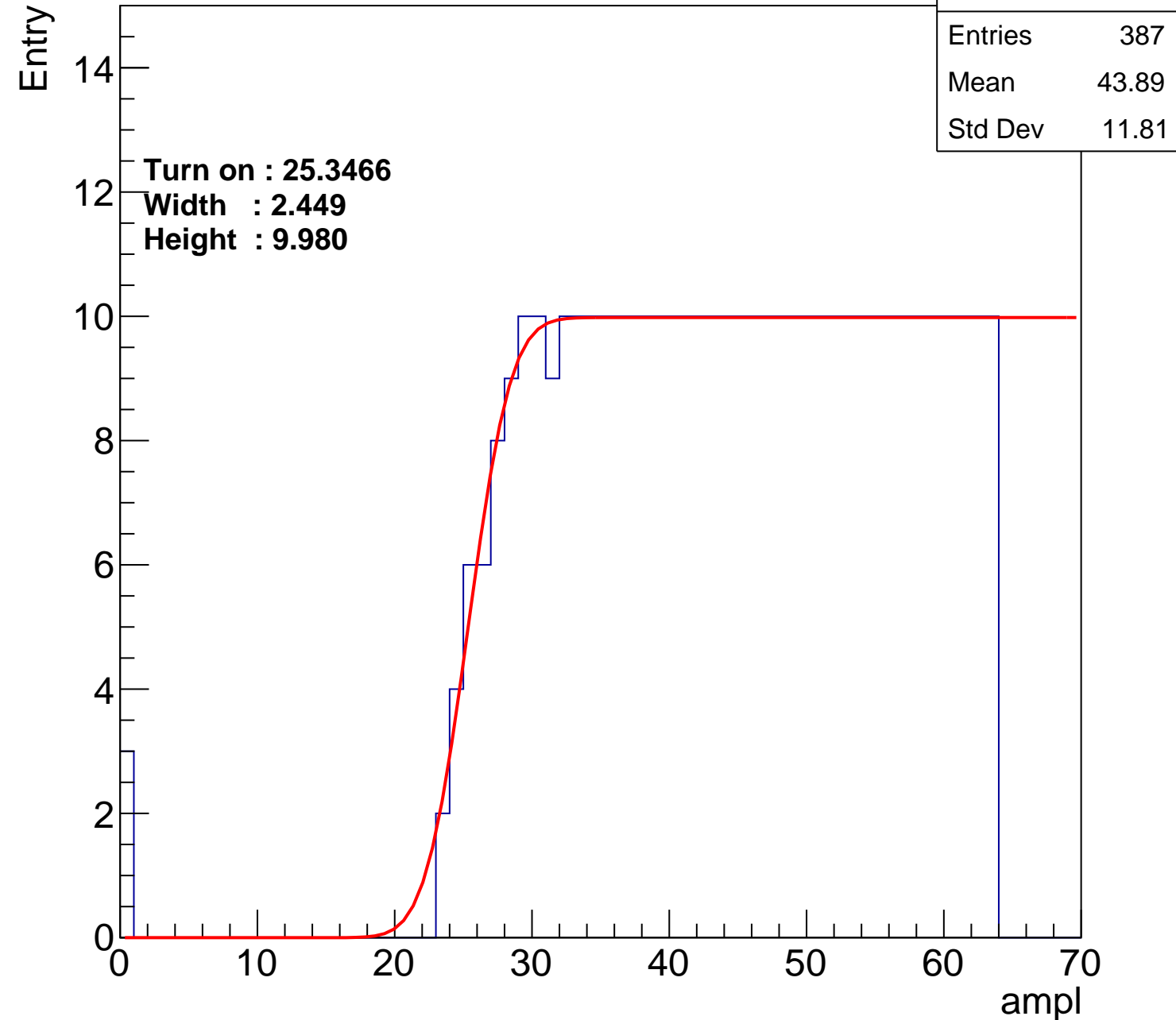
Width : 2.449

Height : 9.980

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.48
Std Dev	11.37

Turn on : 26.7888

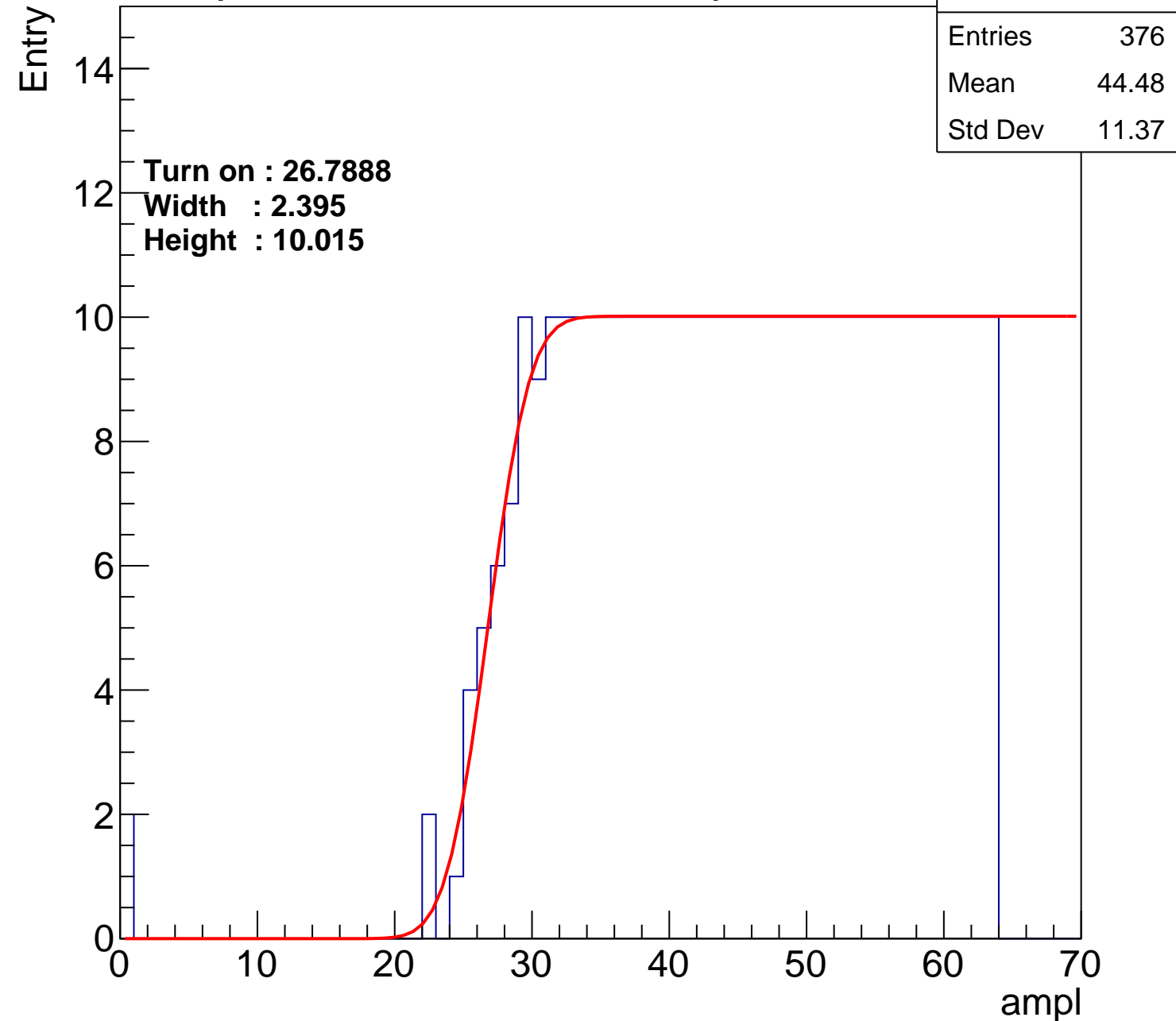
Width : 2.395

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.61
Std Dev	11.99

Turn on : 25.6934

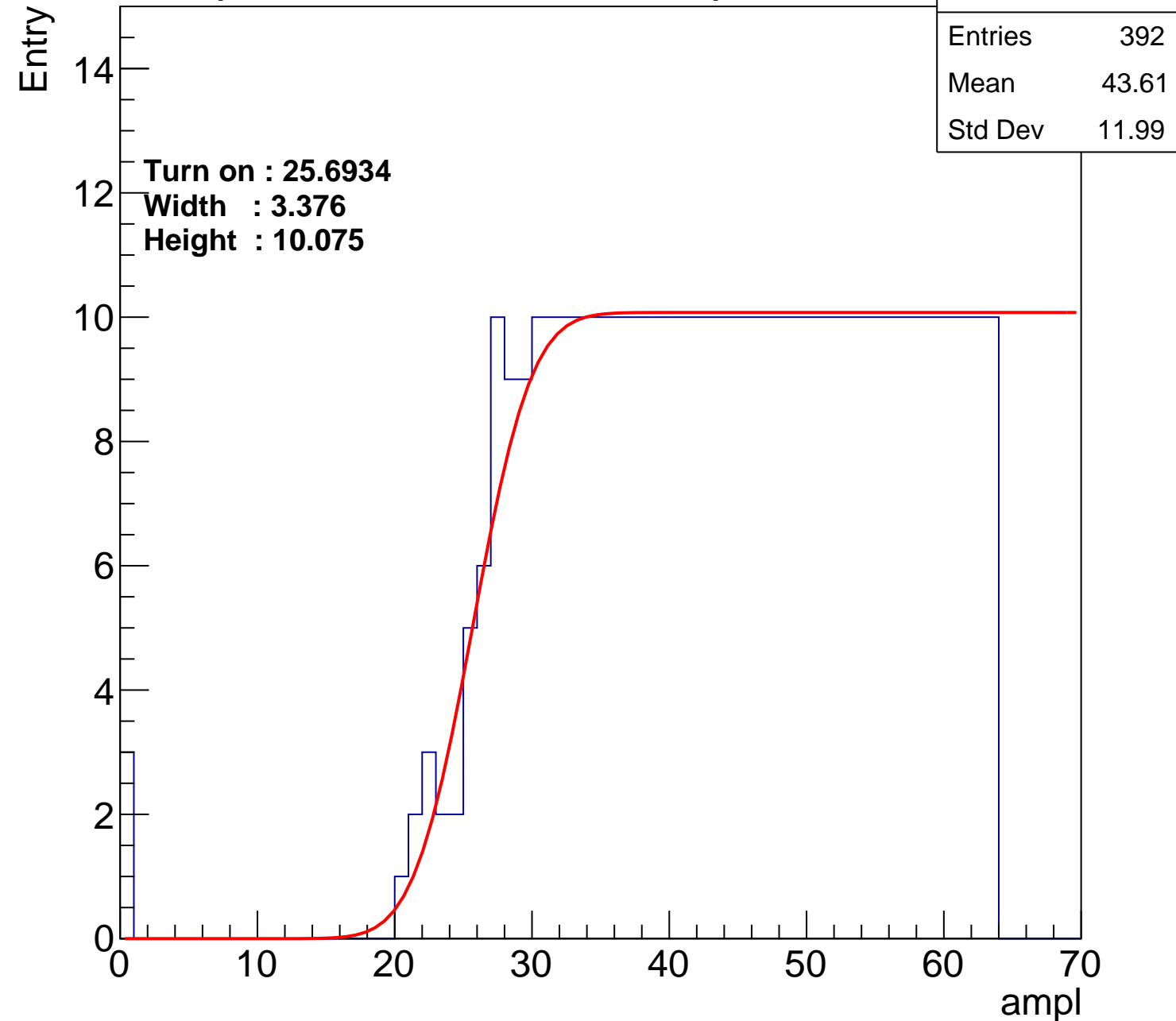
Width : 3.376

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.92
Std Dev	11.1

Turn on : 27.4521

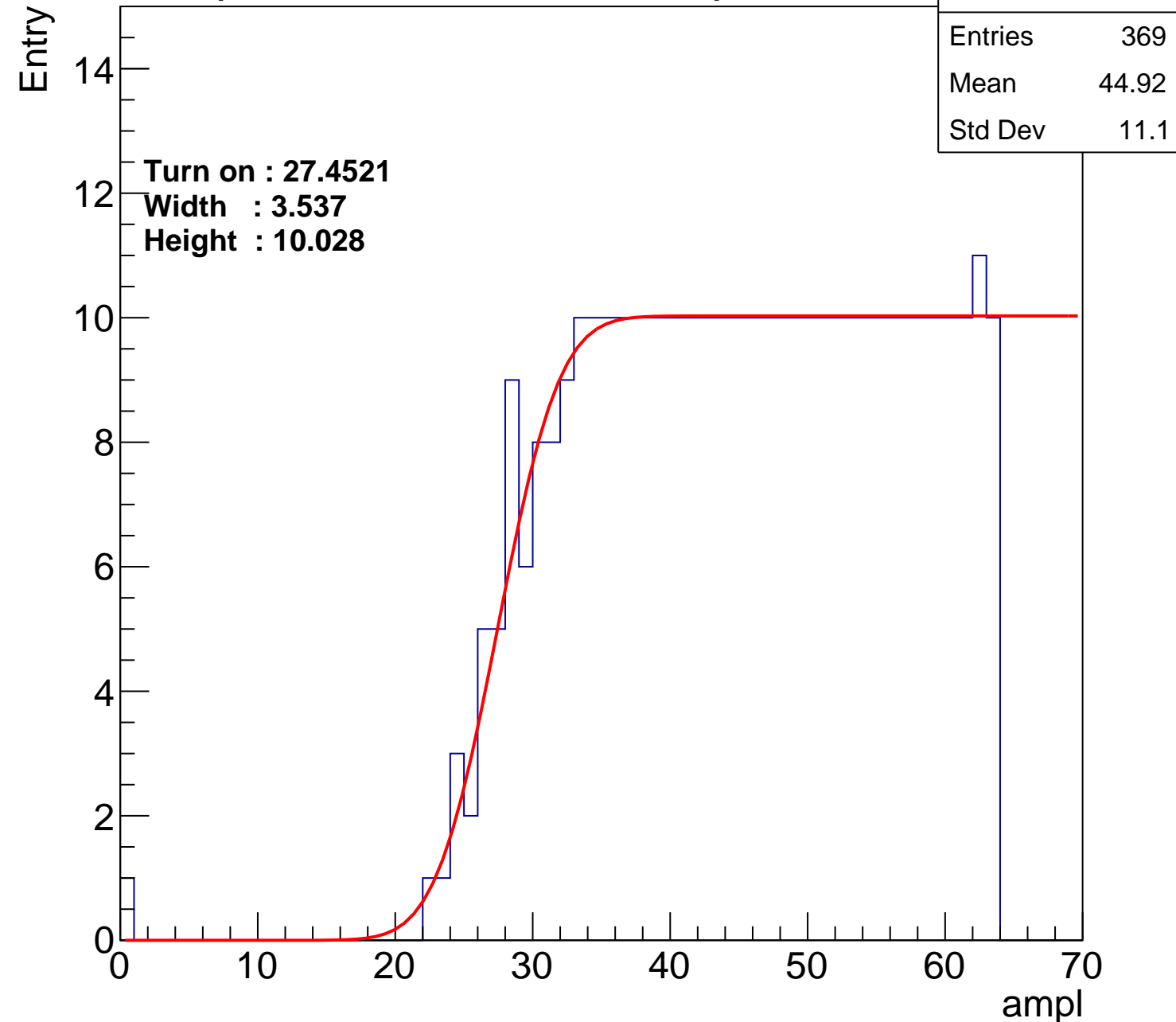
Width : 3.537

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch4

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 25.7373

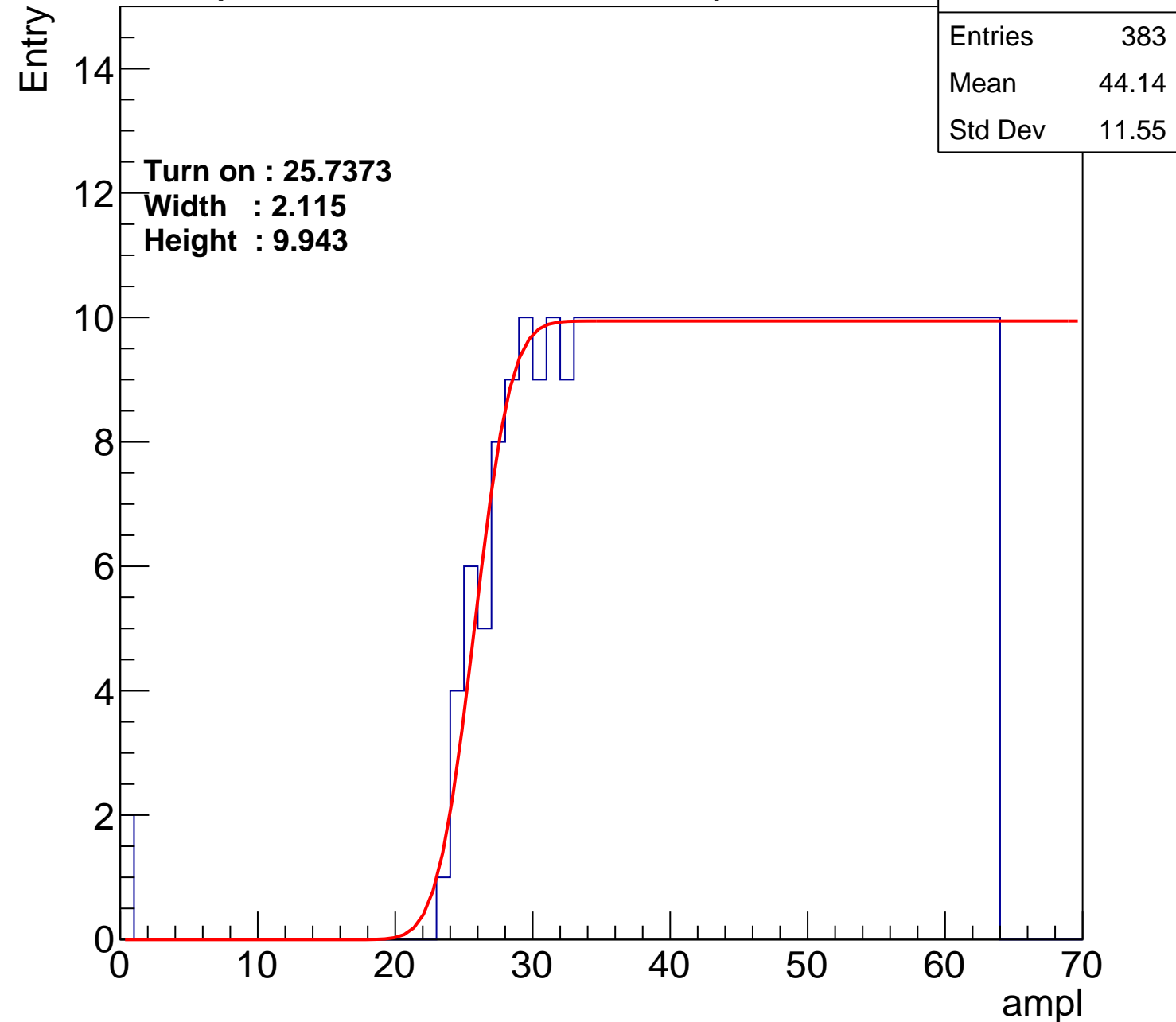
Width : 2.115

Height : 9.943

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.55
Std Dev	11.51

Turn on : 27.2965

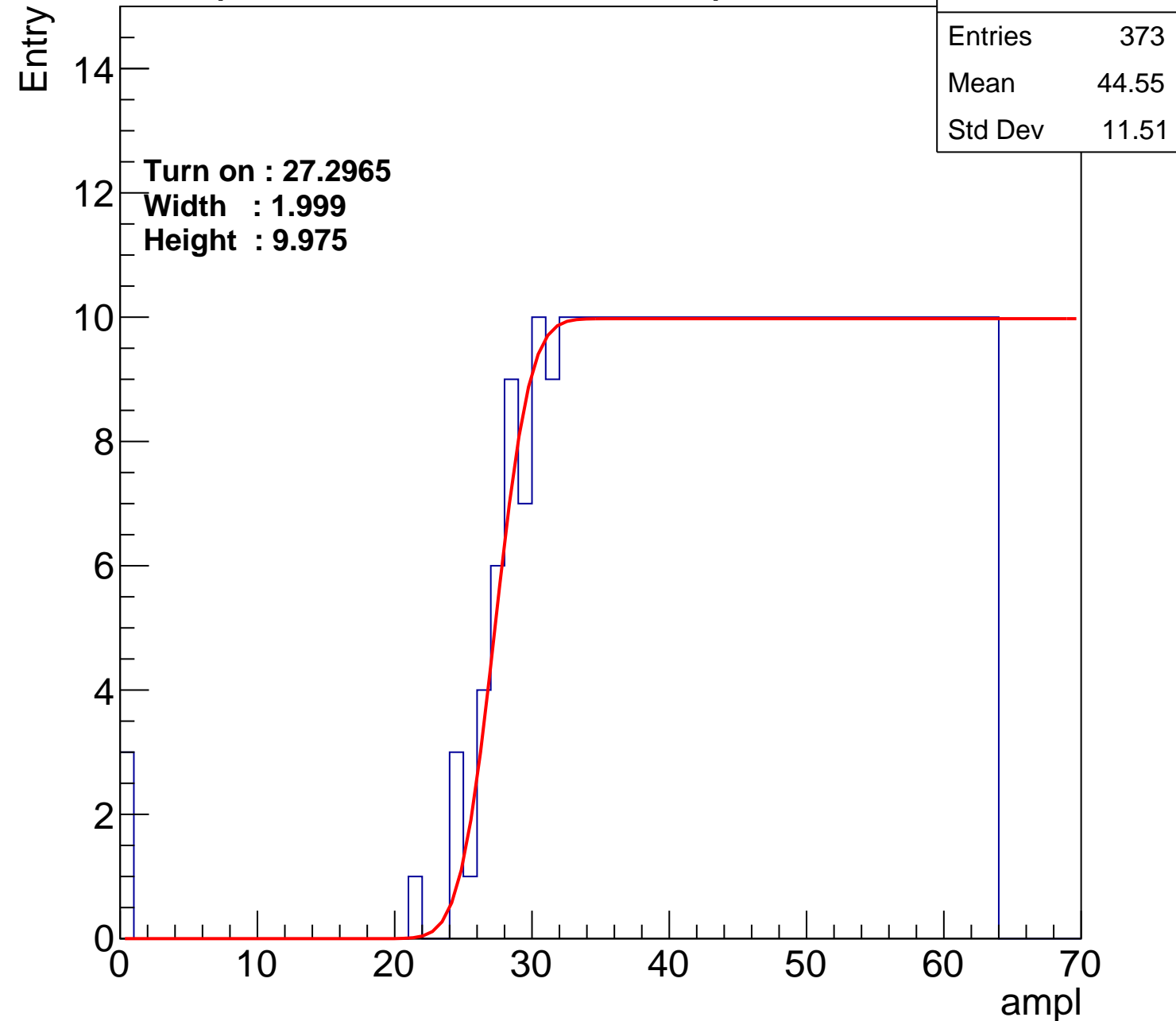
Width : 1.999

Height : 9.975

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch6

calib_packv5_042523_0143.root, FC#10, port B3

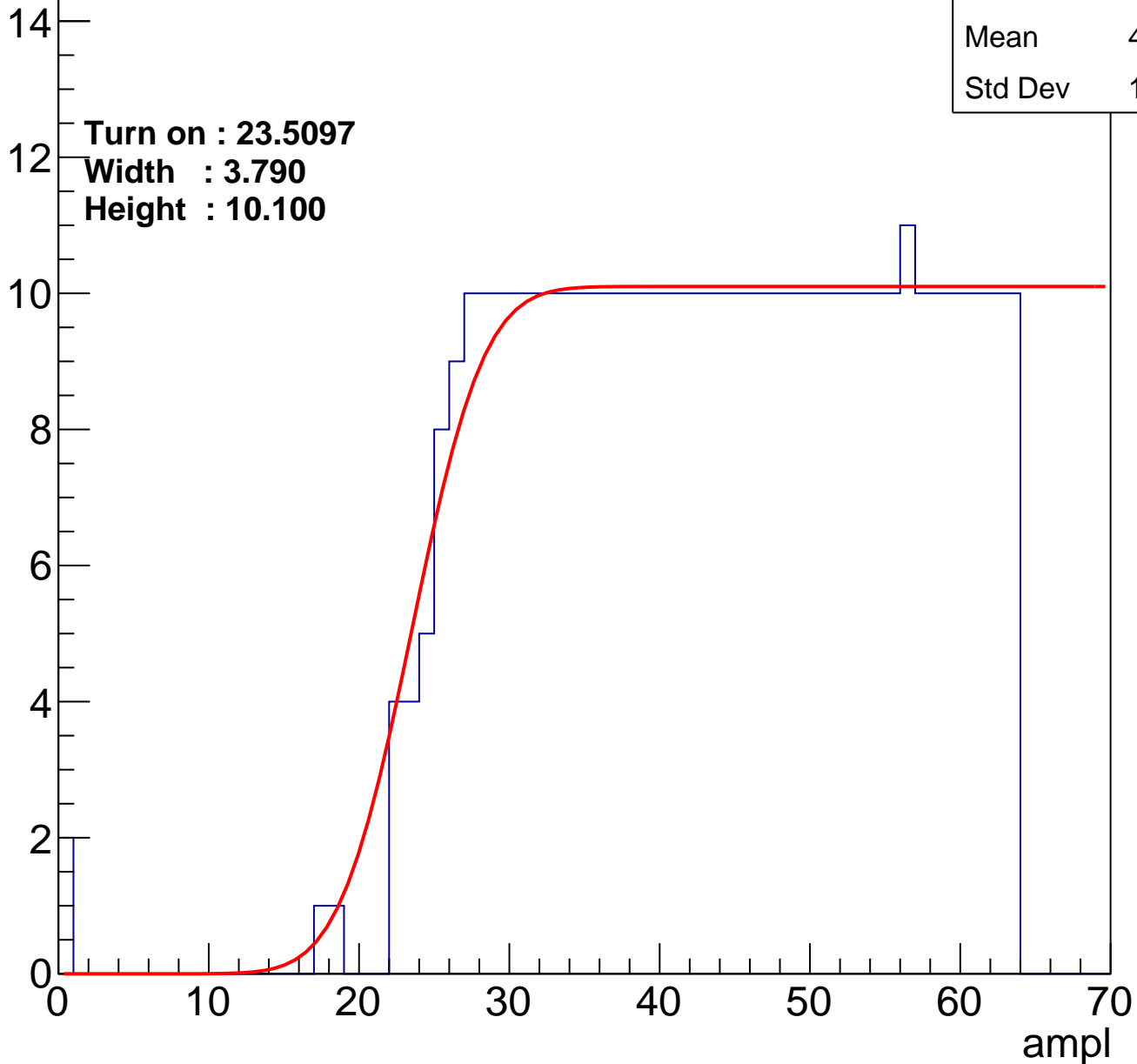
Entries	405
Mean	43.15
Std Dev	12.09

Turn on : 23.5097

Width : 3.790

Height : 10.100

Entry



B1L002S, U18-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.33
Std Dev	11.41

Turn on : 26.3432

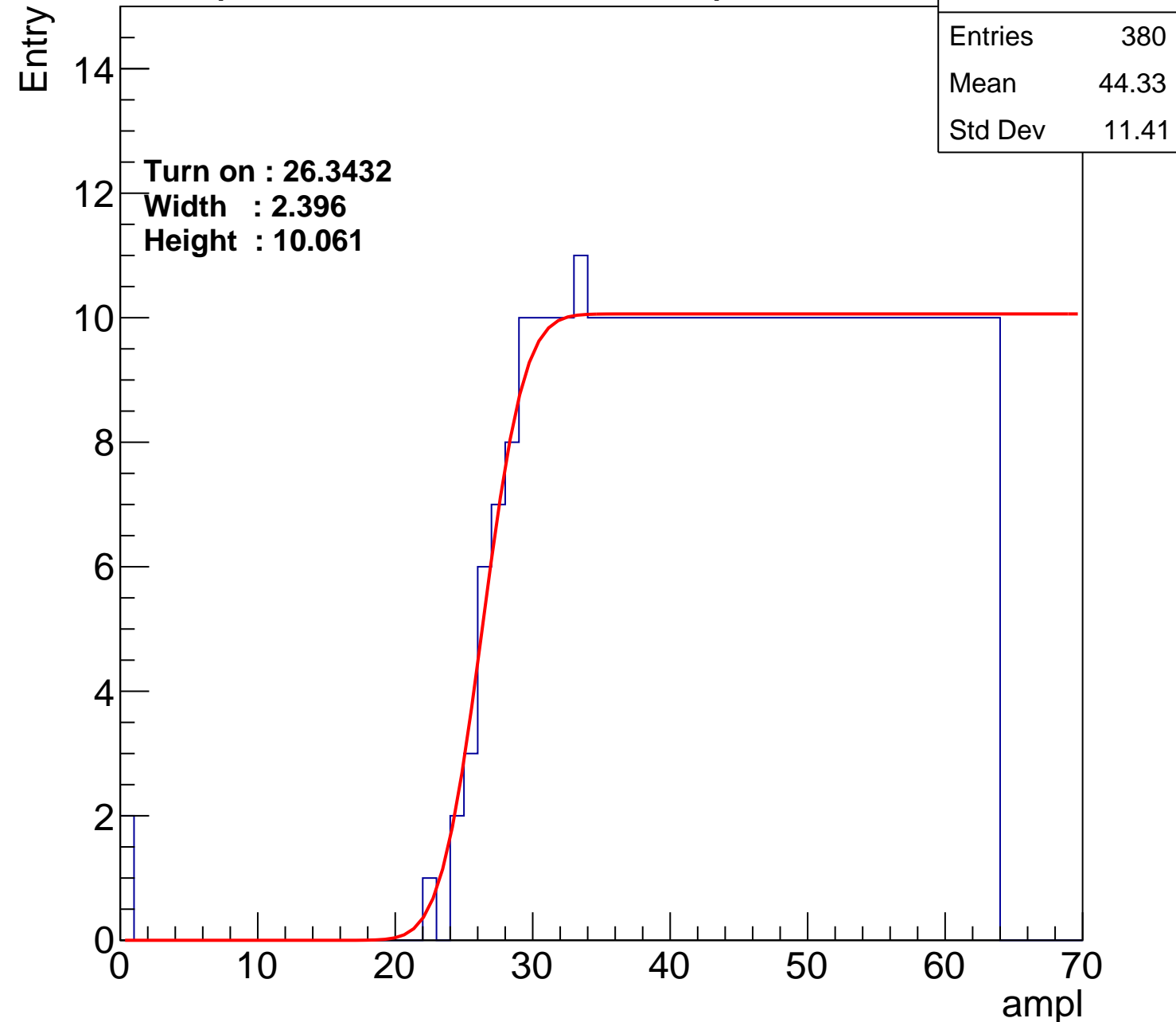
Width : 2.396

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch8

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.71
Std Dev	11.94

Turn on : 25.3596

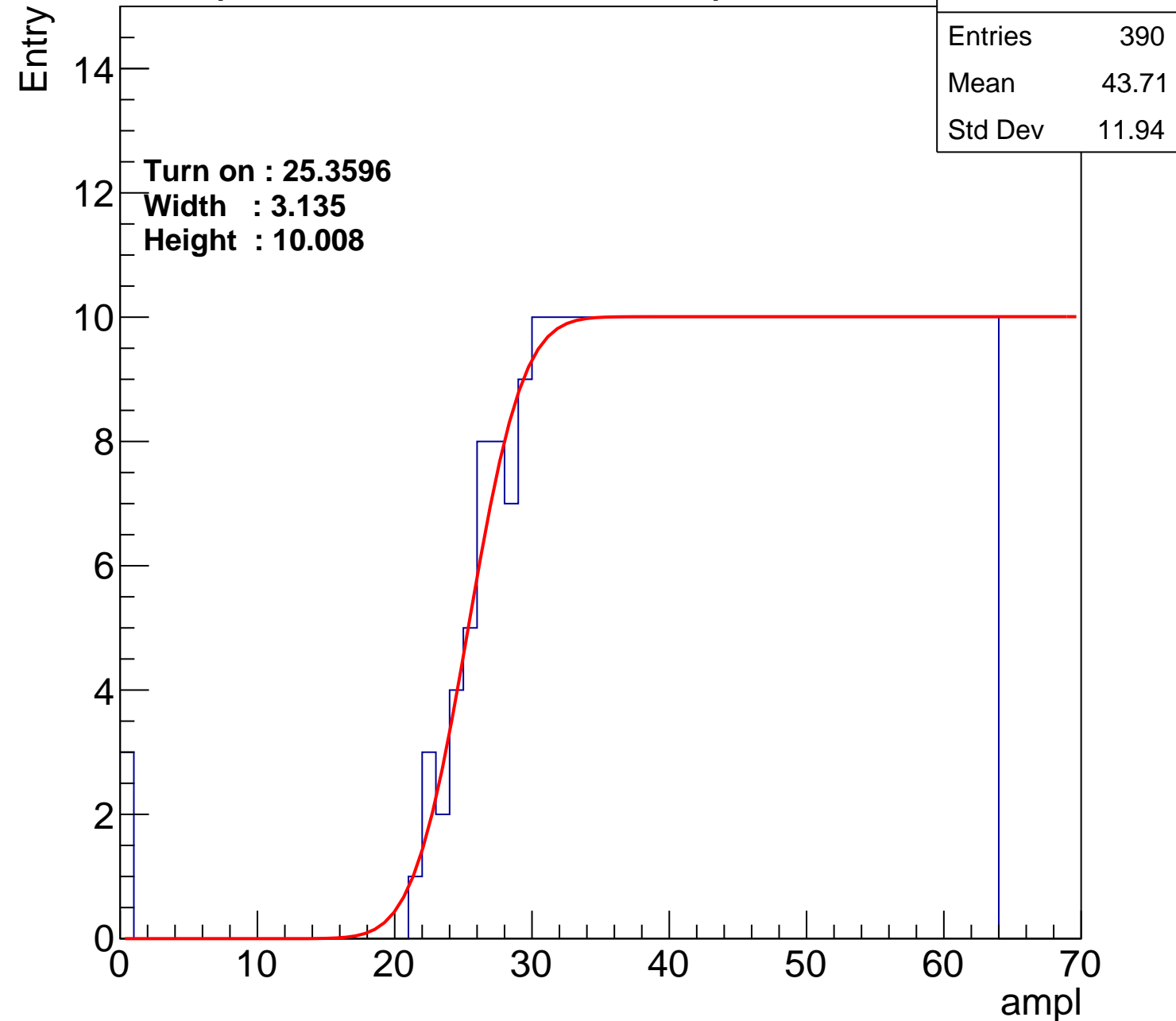
Width : 3.135

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.97
Std Dev	11.78

Turn on : 25.7568

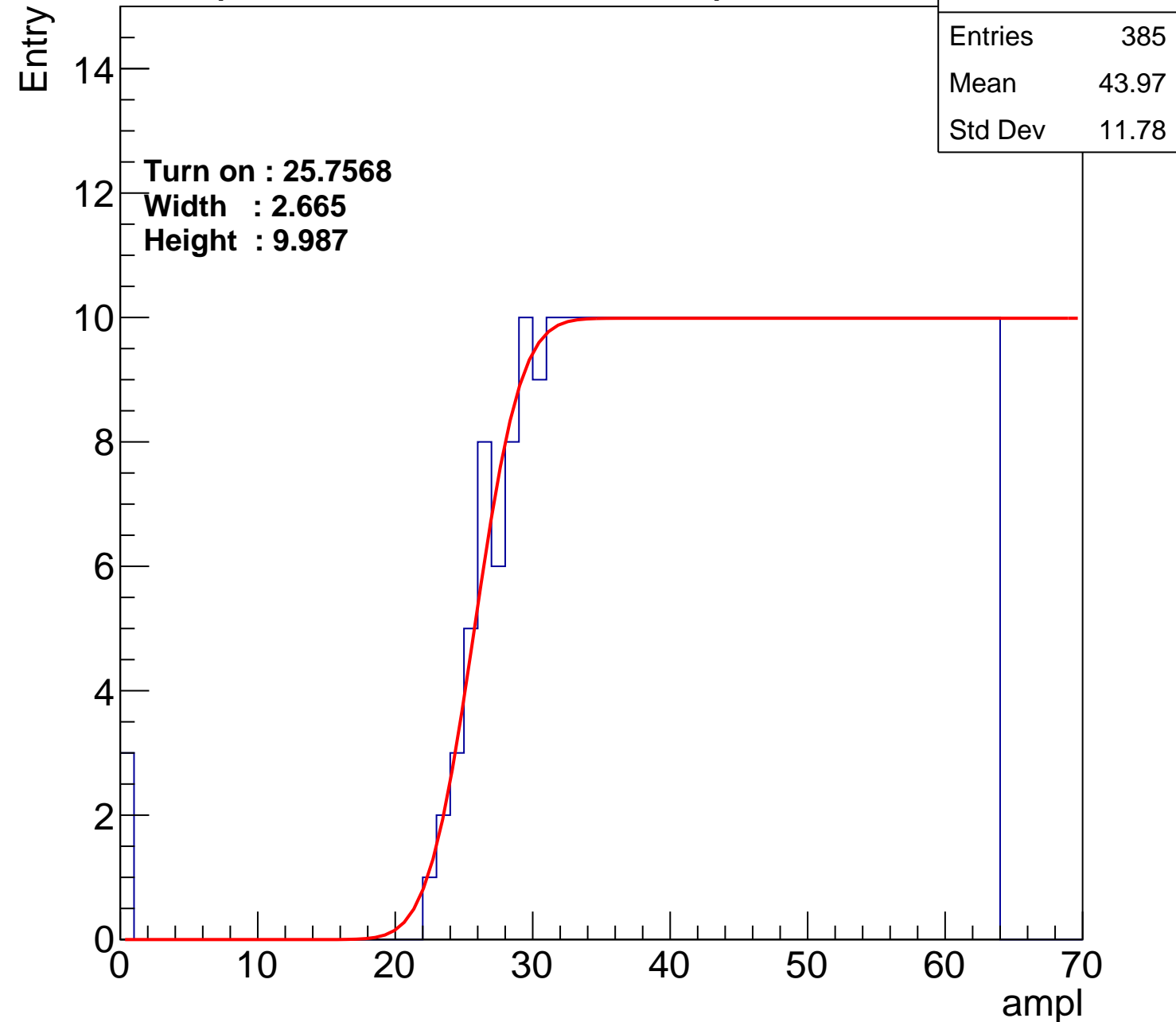
Width : 2.665

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.55
Std Dev	11.5

Turn on : 27.0679

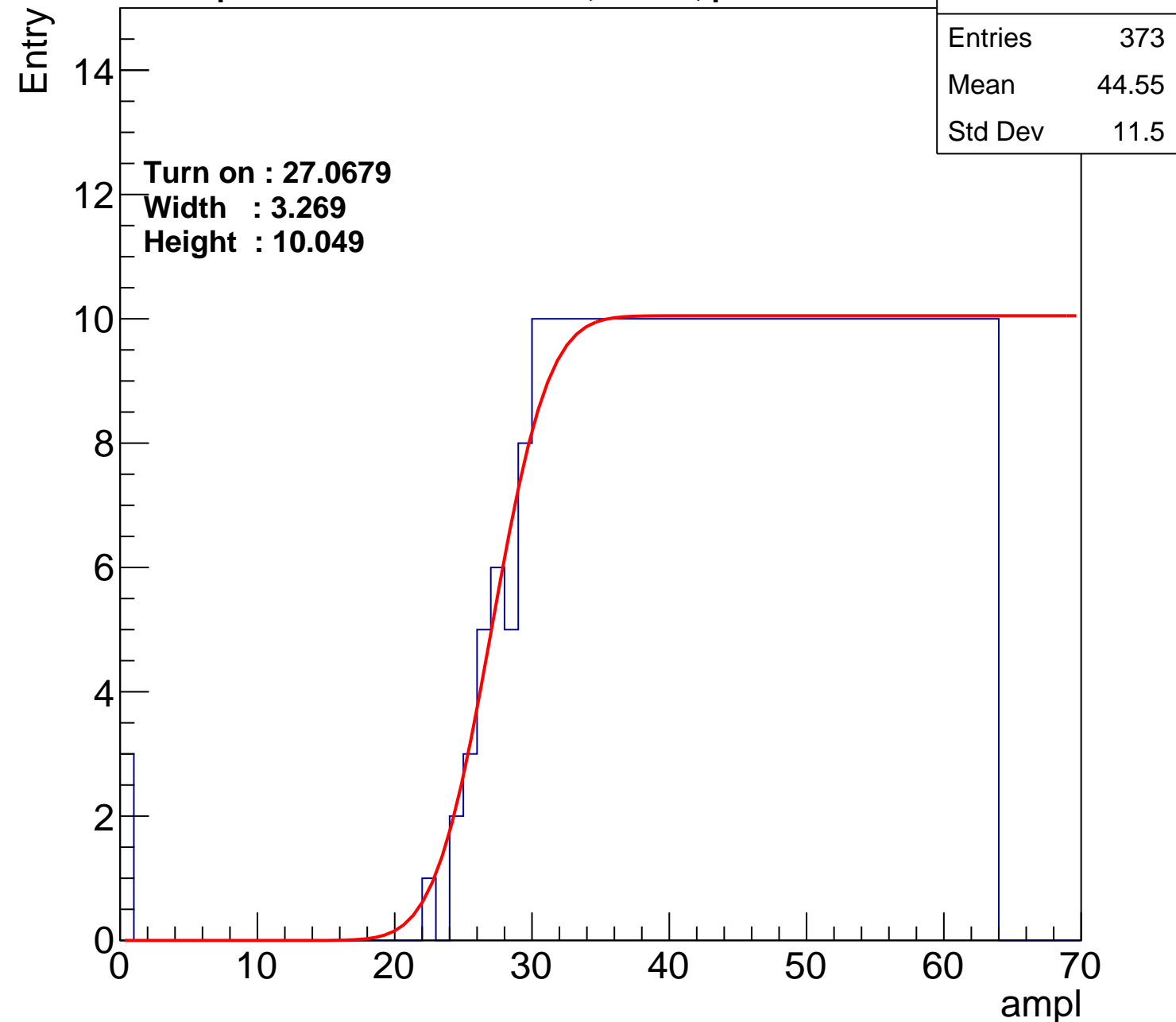
Width : 3.269

Height : 10.049

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch11

calib_packv5_042523_0143.root, FC#10, port B3

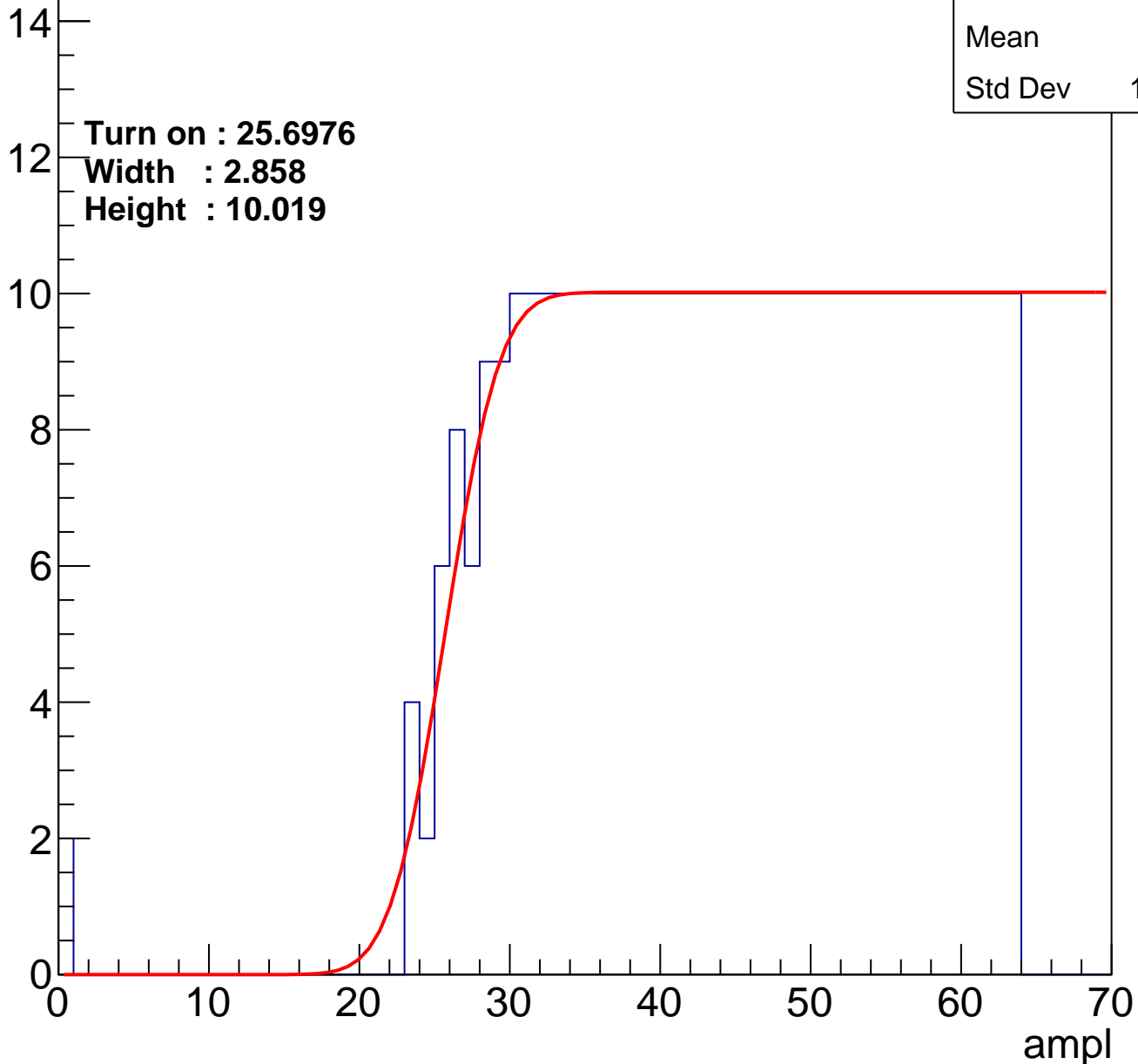
Entries	386
Mean	44
Std Dev	11.62

Turn on : 25.6976

Width : 2.858

Height : 10.019

Entry



B1L002S, U18-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.33
Std Dev	12

Turn on : 23.6473

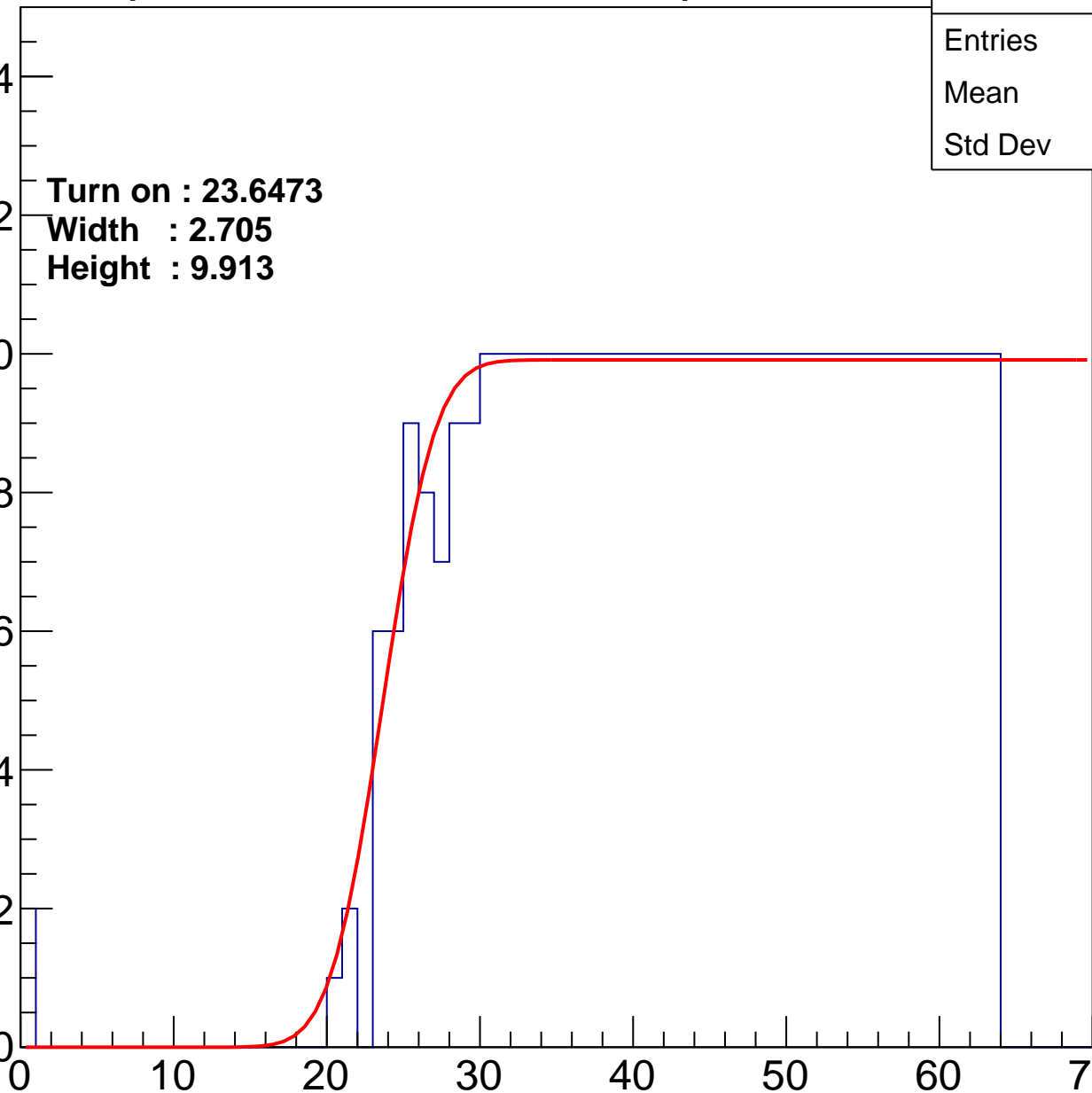
Width : 2.705

Height : 9.913

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch13

calib_packv5_042523_0143.root, FC#10, port B3

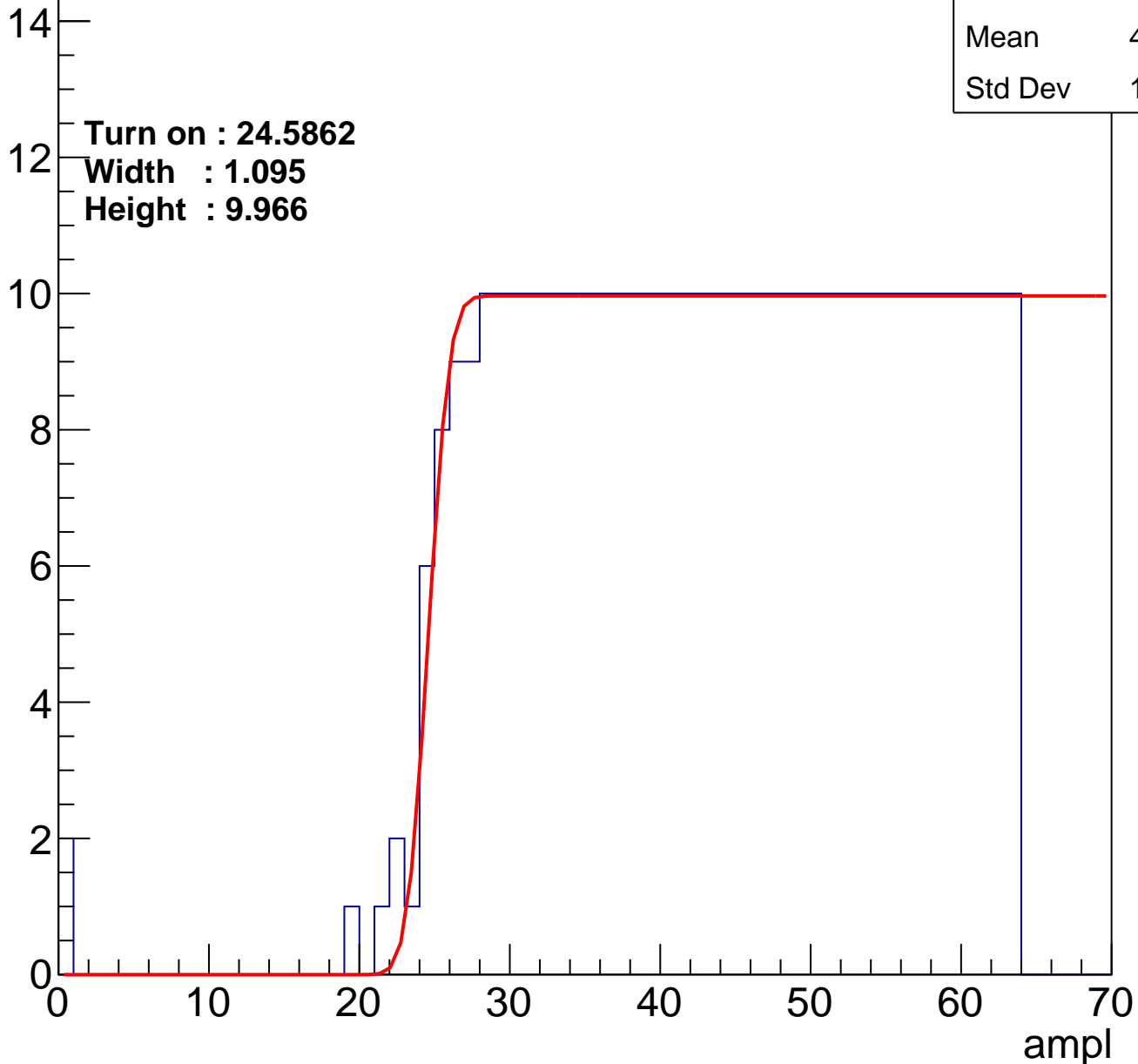
Entries	399
Mean	43.38
Std Dev	11.93

Turn on : 24.5862

Width : 1.095

Height : 9.966

Entry



B1L002S, U18-ch14

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.34
Std Dev	12.19

Turn on : 24.7624

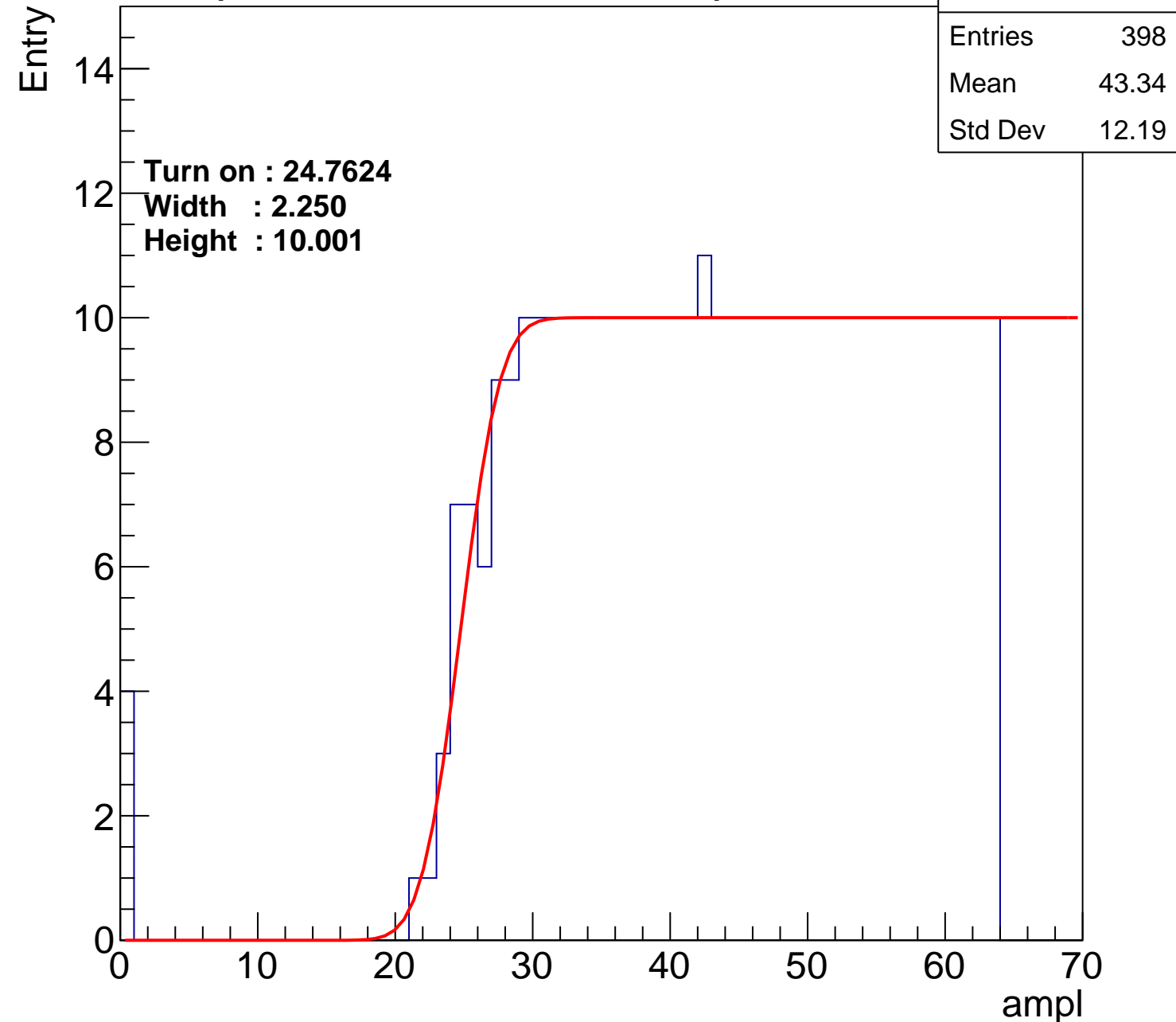
Width : 2.250

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.4
Std Dev	12.06

Turn on : 24.8144

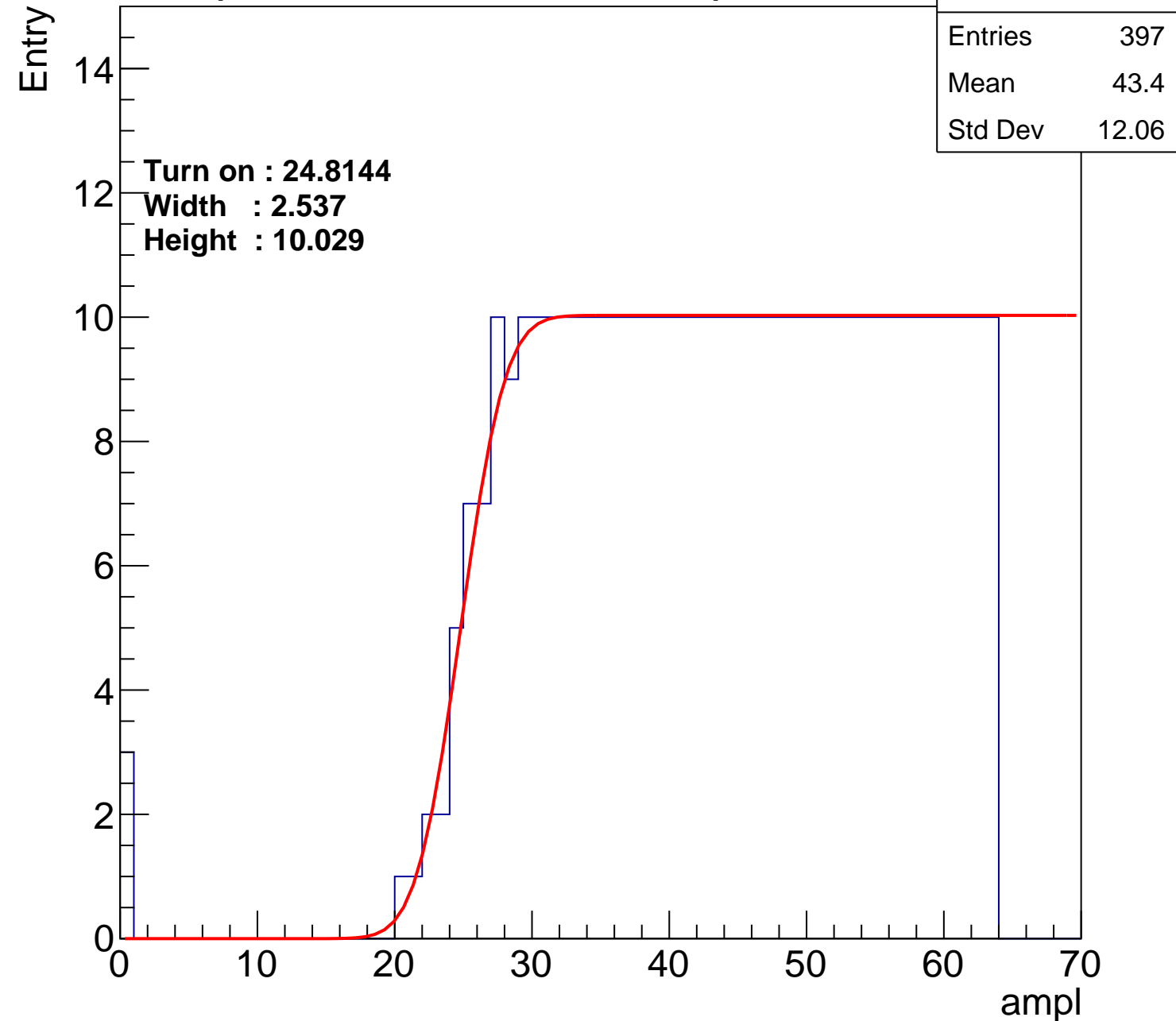
Width : 2.537

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.69
Std Dev	11.92

Turn on : 25.1288

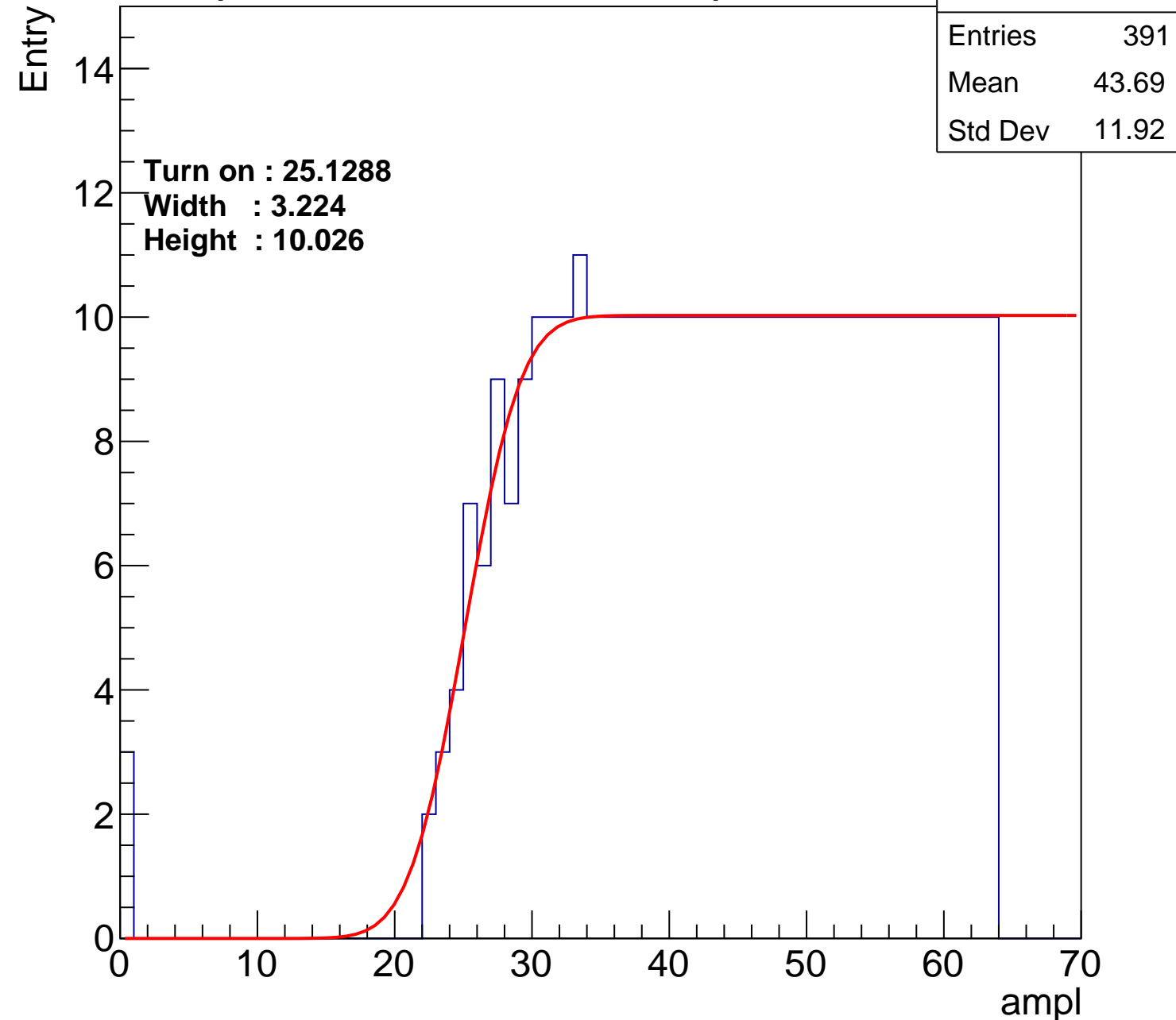
Width : 3.224

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch17

calib_packv5_042523_0143.root, FC#10, port B3

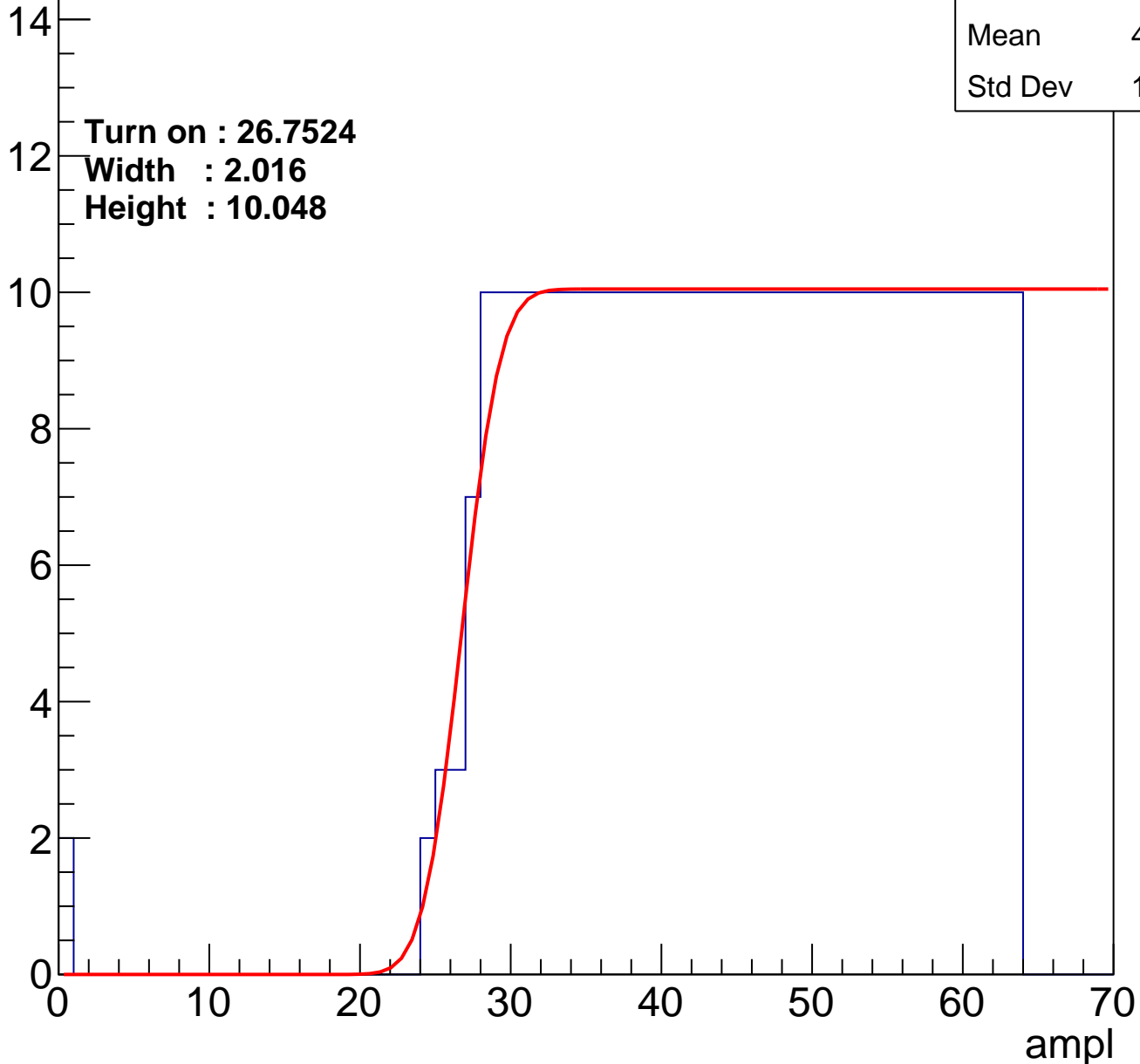
Entries	377
Mean	44.48
Std Dev	11.32

Turn on : 26.7524

Width : 2.016

Height : 10.048

Entry



B1L002S, U18-ch18

calib_packv5_042523_0143.root, FC#10, port B3

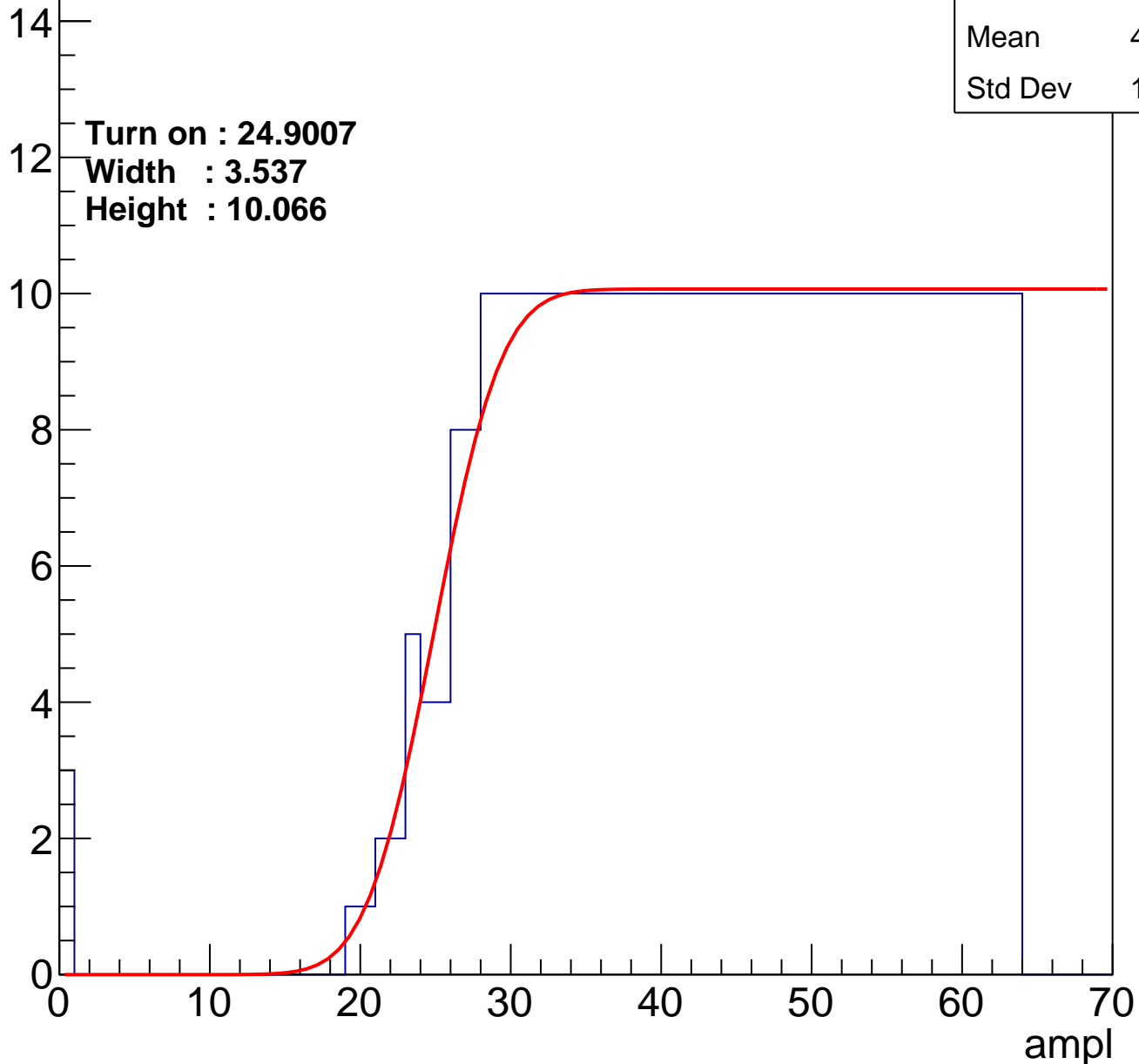
Entries	398
Mean	43.32
Std Dev	12.14

Turn on : 24.9007

Width : 3.537

Height : 10.066

Entry



B1L002S, U18-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.91
Std Dev	11.53

Turn on : 24.5563

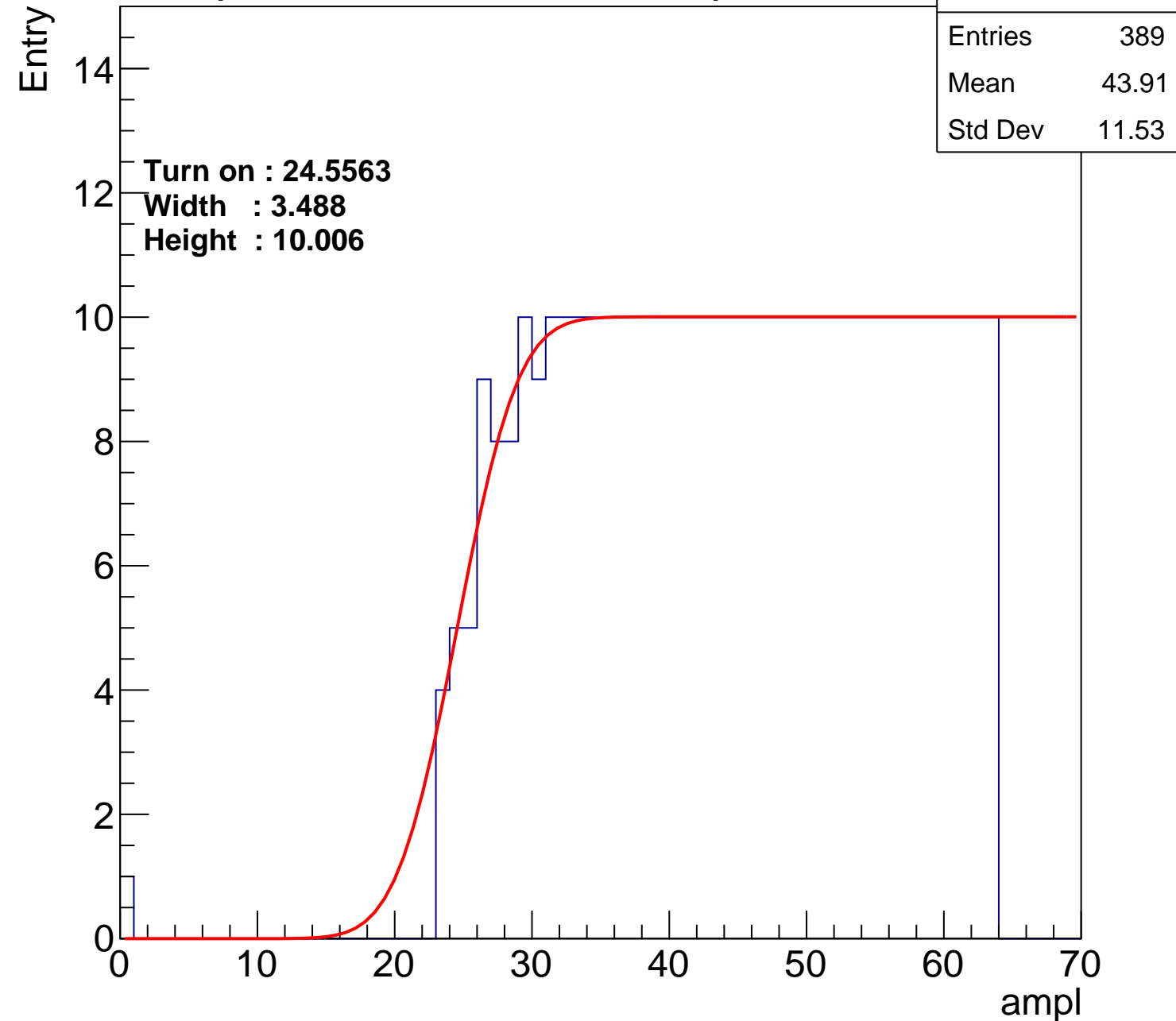
Width : 3.488

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch20

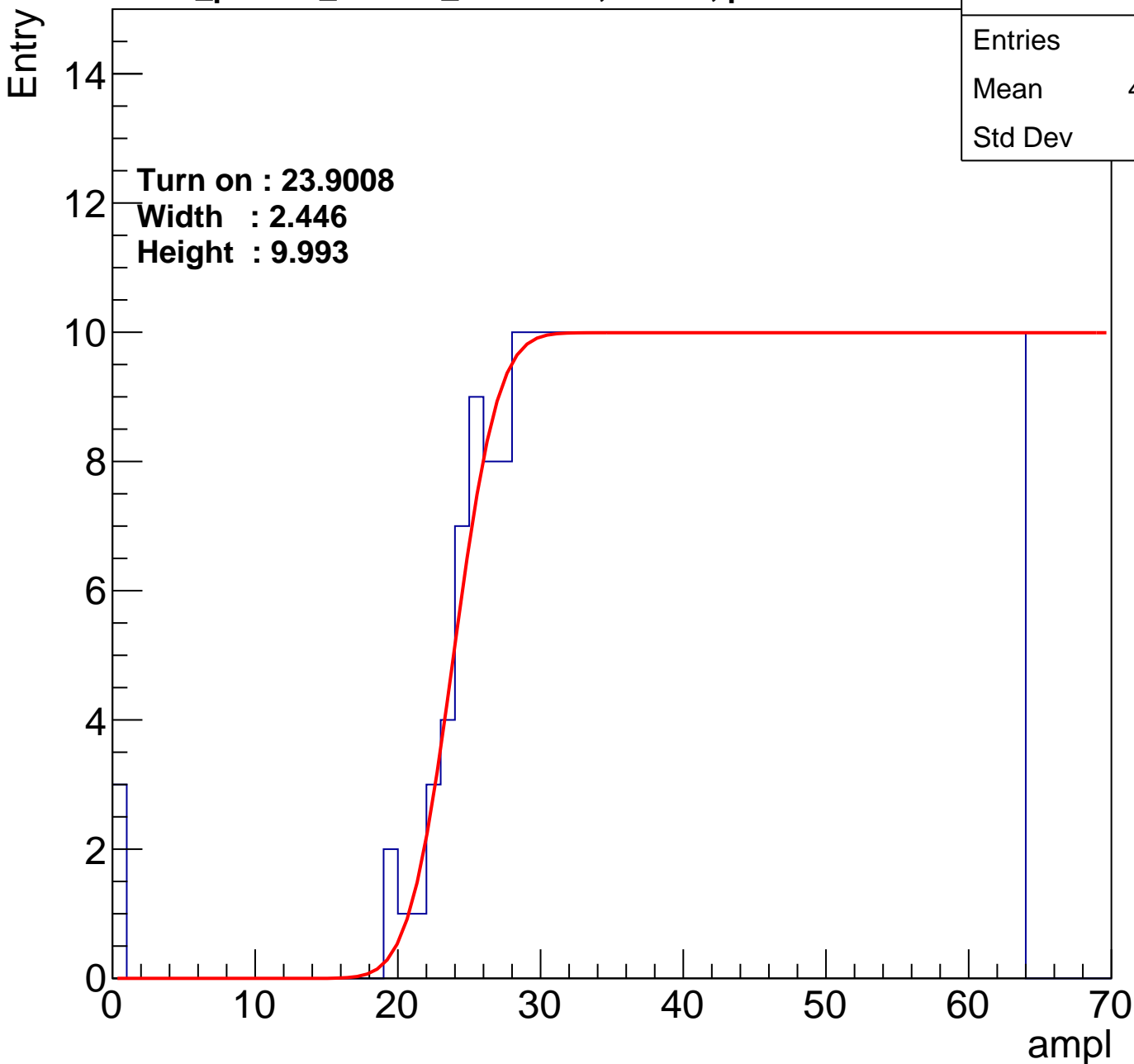
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 23.9008

Width : 2.446

Height : 9.993

Entries	406
Mean	42.94
Std Dev	12.31



B1L002S, U18-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.27
Std Dev	12.12

Turn on : 24.6744

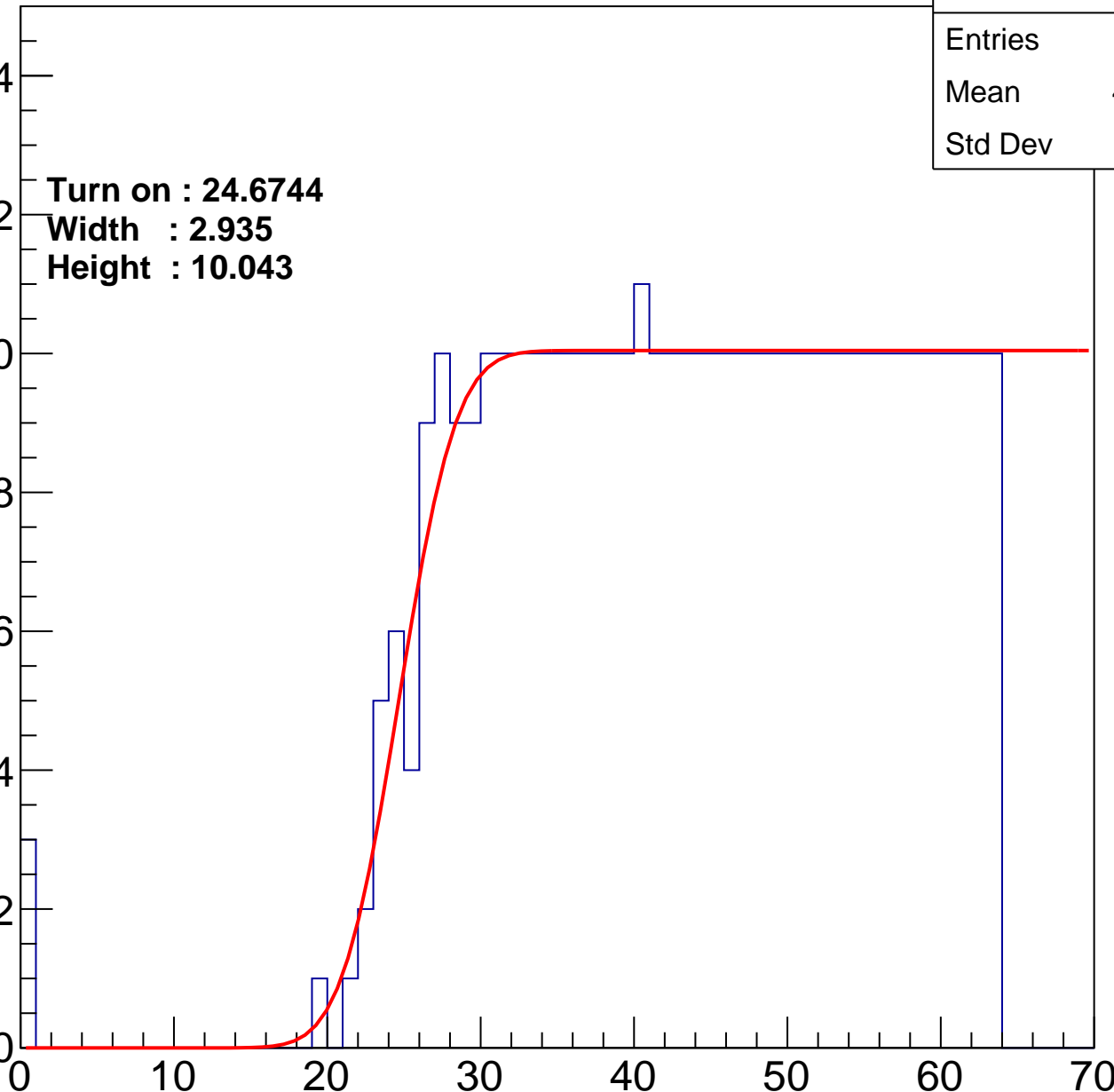
Width : 2.935

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch22

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.55
Std Dev	11.85

Turn on : 24.6337

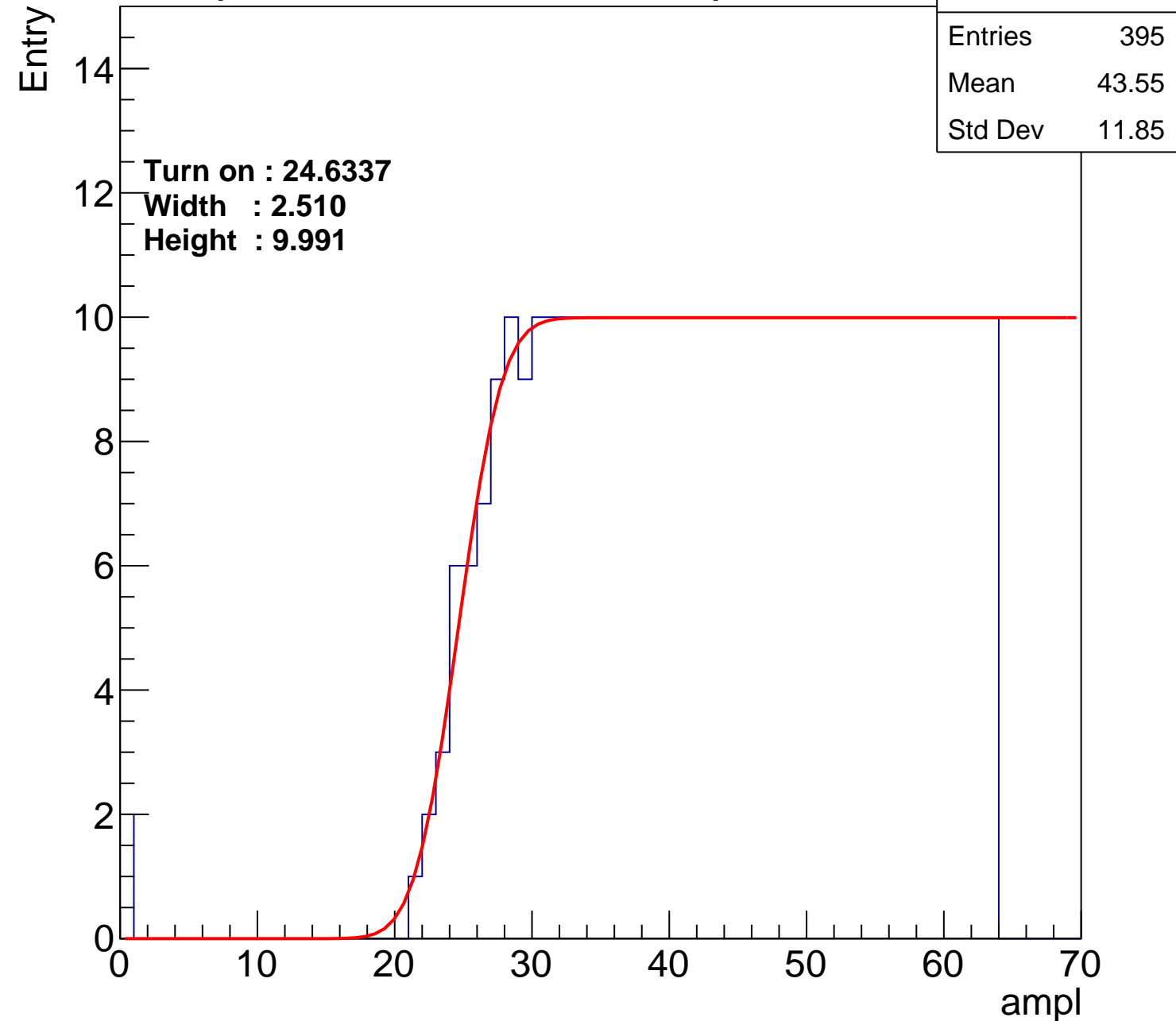
Width : 2.510

Height : 9.991

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.2
Std Dev	11.99

Turn on : 26.9735

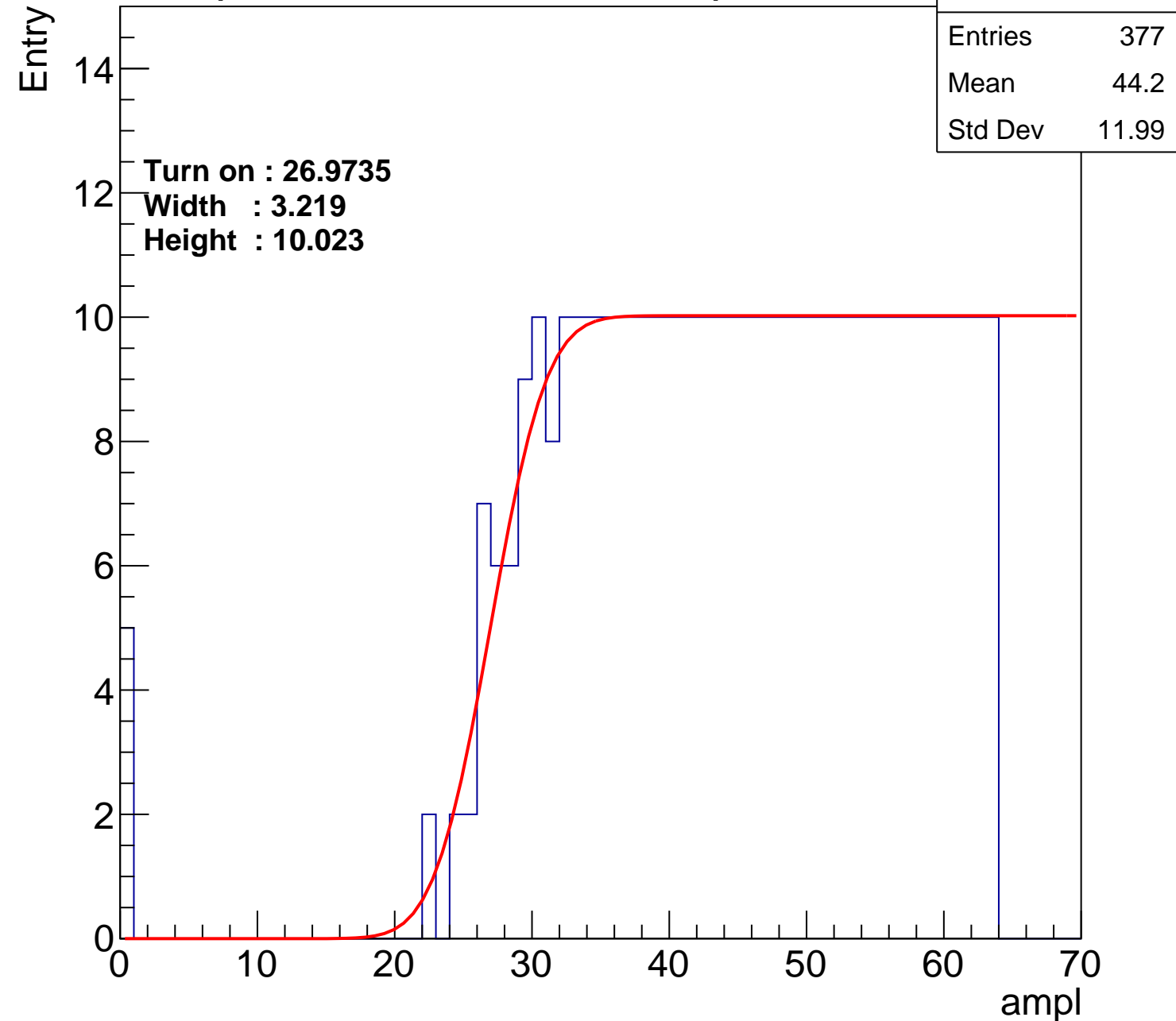
Width : 3.219

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch24

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.65
Std Dev	11.69

Turn on : 24.8779

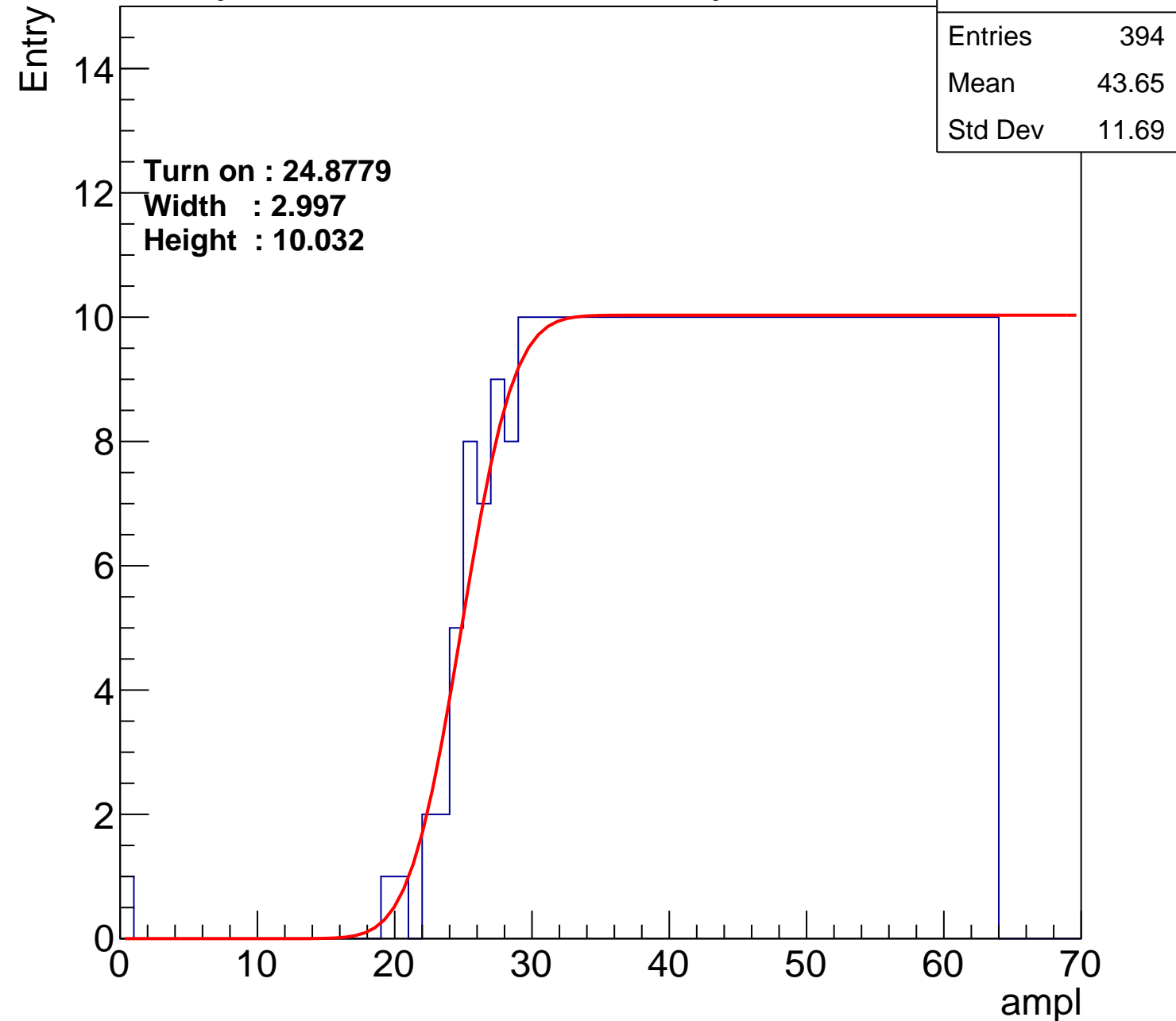
Width : 2.997

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.56
Std Dev	11.2

Turn on : 27.3508

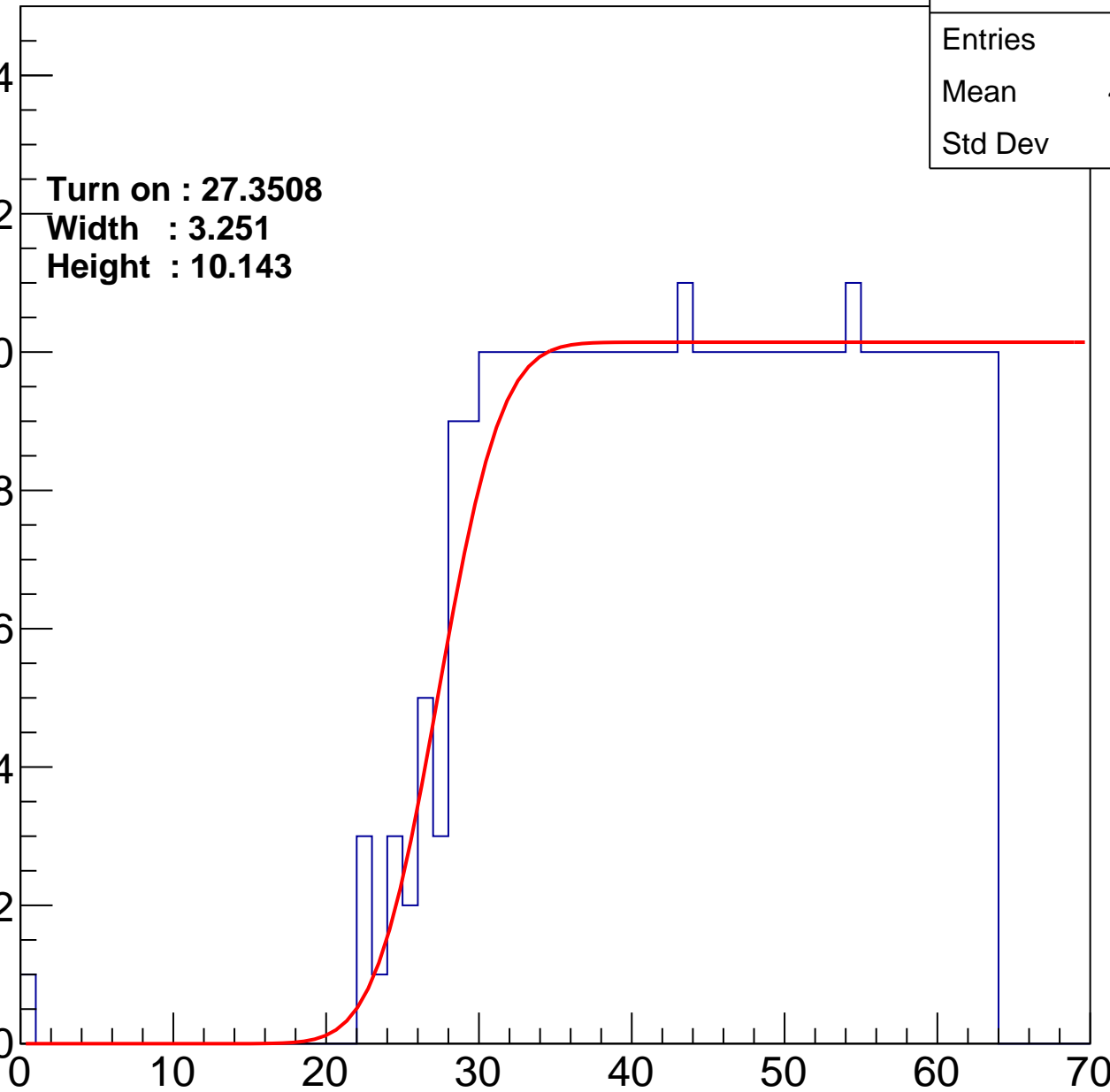
Width : 3.251

Height : 10.143

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.53
Std Dev	11.9

Turn on : 24.8594

Width : 3.327

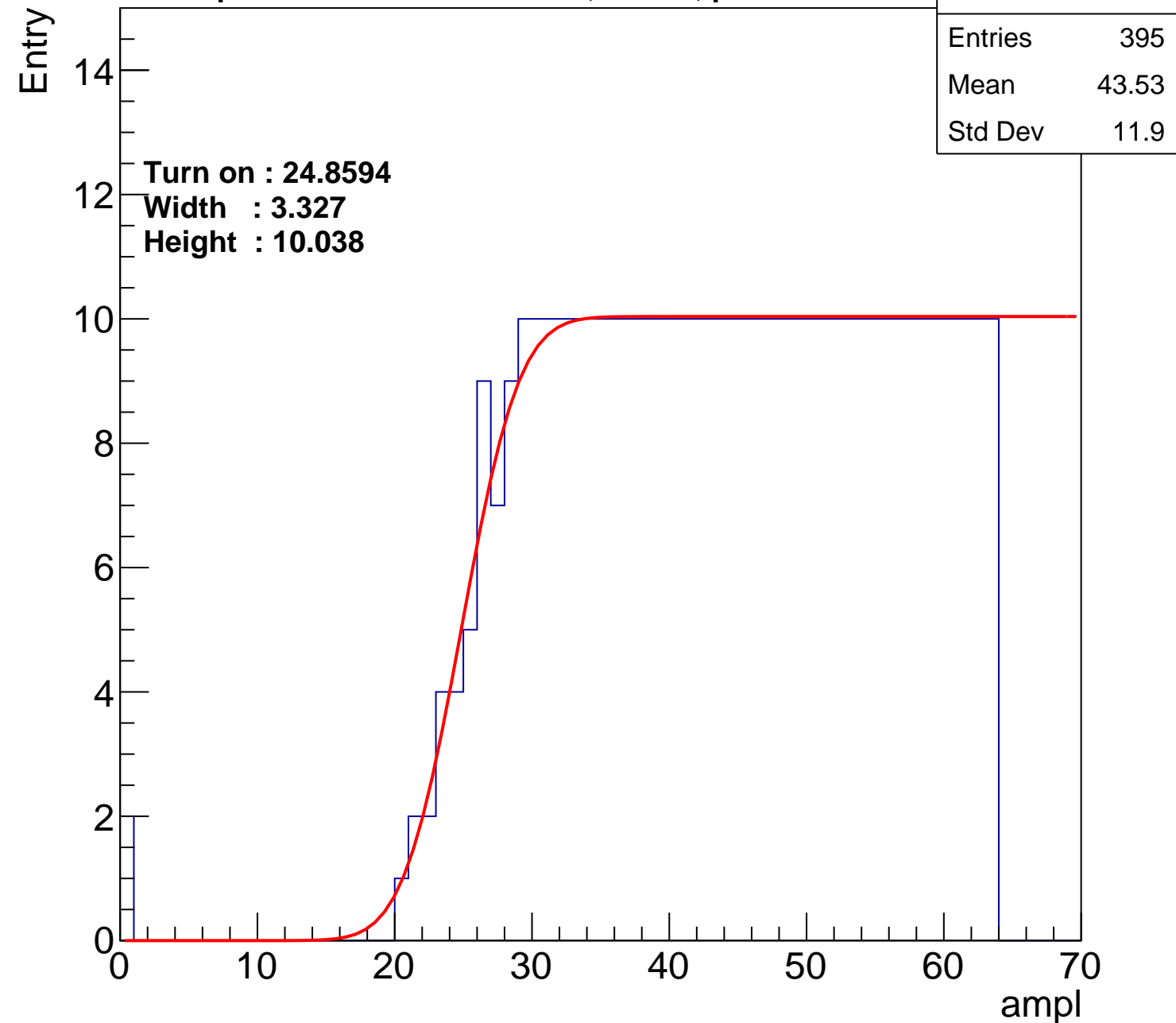
Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U18-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.74
Std Dev	12.27

Turn on : 26.0686

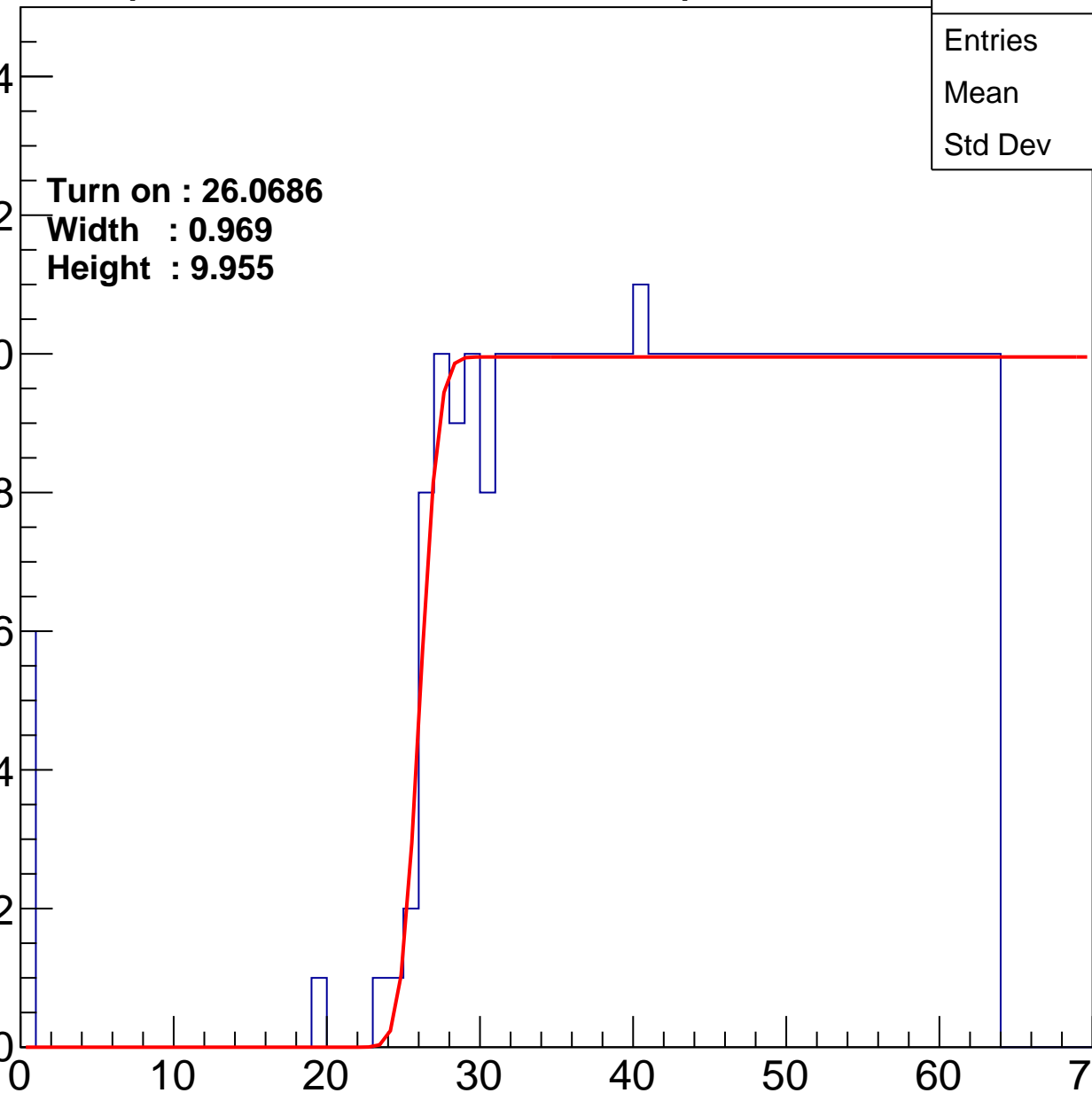
Width : 0.969

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch28

calib_packv5_042523_0143.root, FC#10, port B3

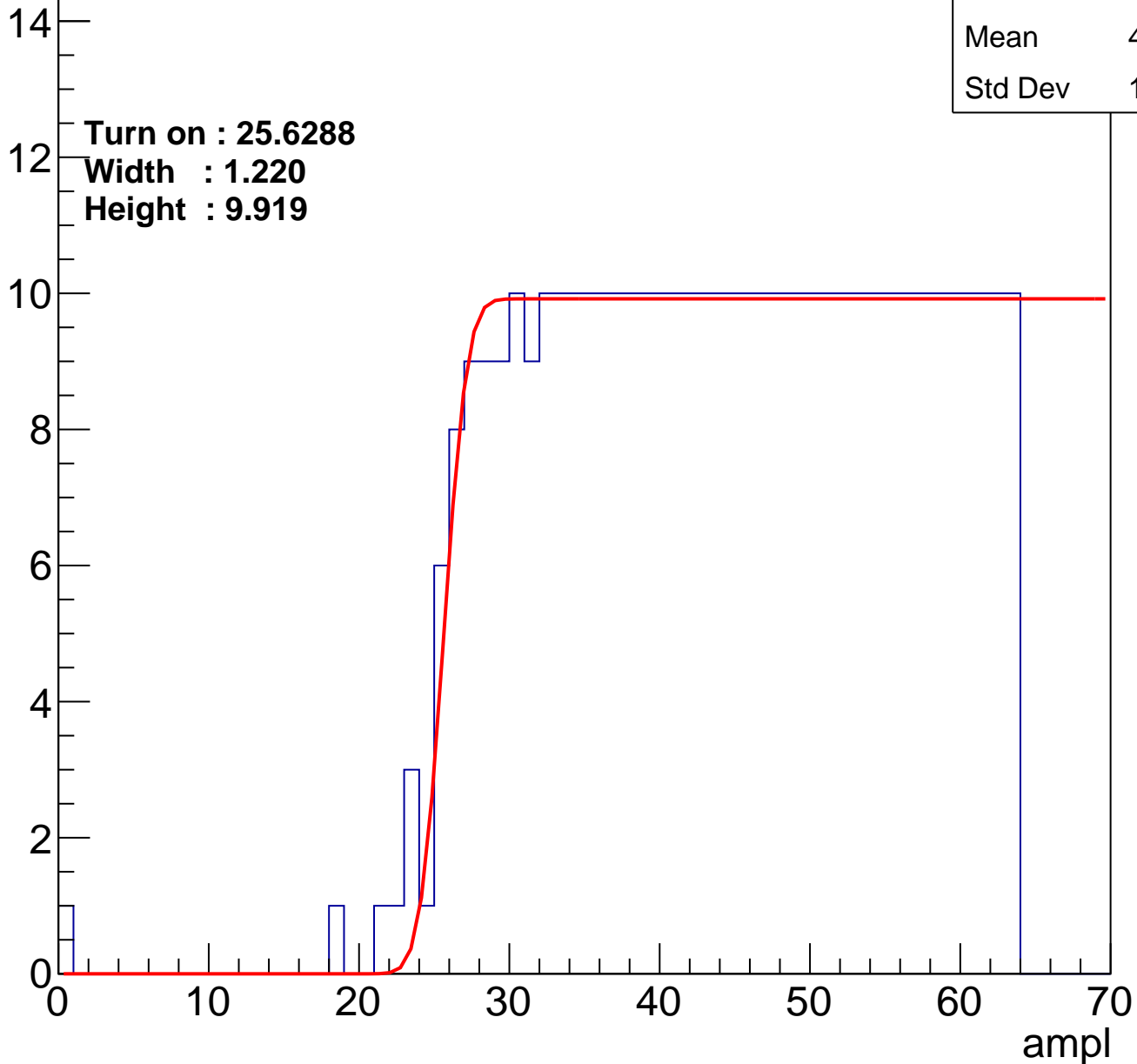
Entries	388
Mean	43.94
Std Dev	11.55

Turn on : 25.6288

Width : 1.220

Height : 9.919

Entry



B1L002S, U18-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.89
Std Dev	12.07

Turn on : 22.9646

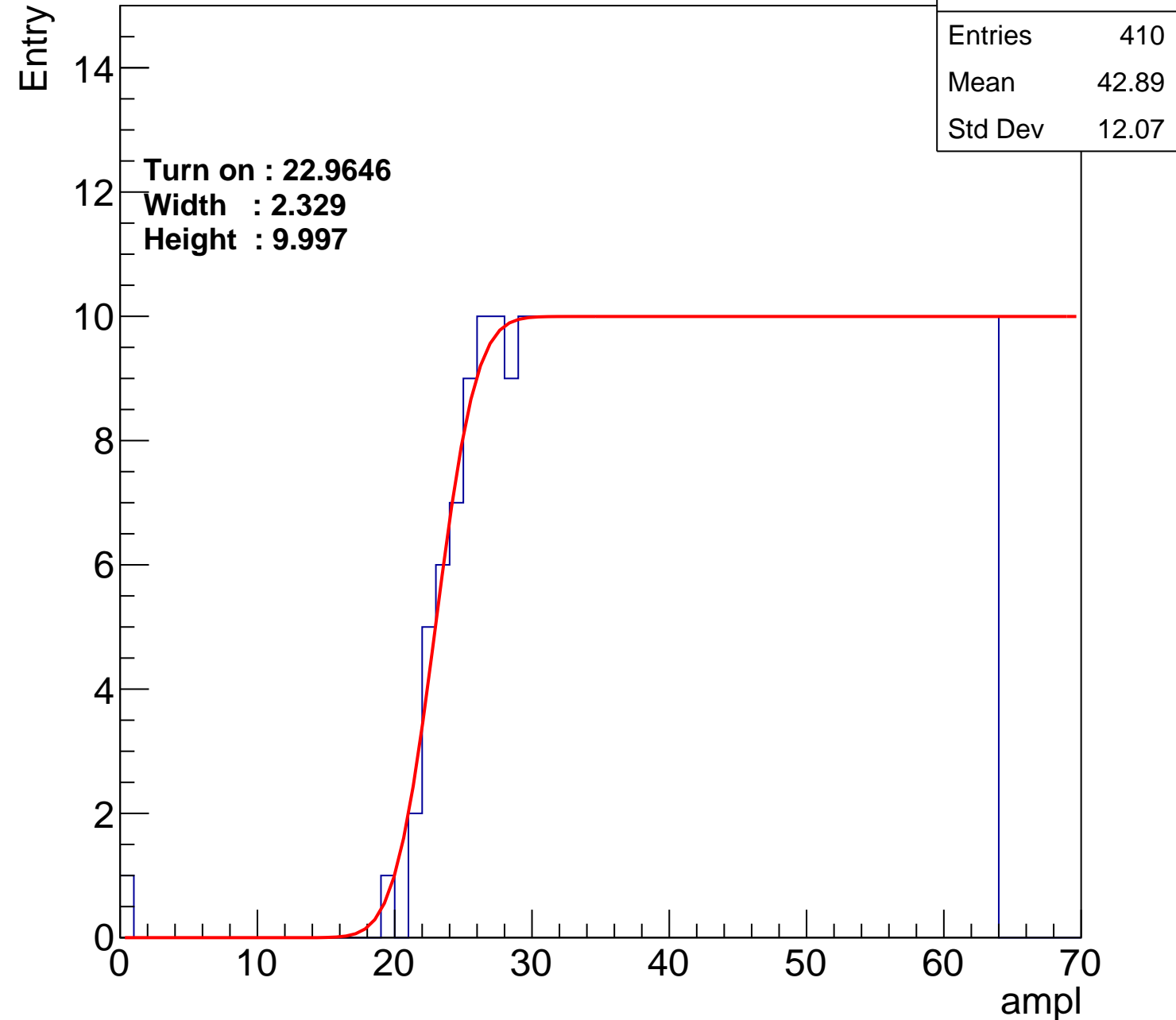
Width : 2.329

Height : 9.997

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch30

calib_packv5_042523_0143.root, FC#10, port B3

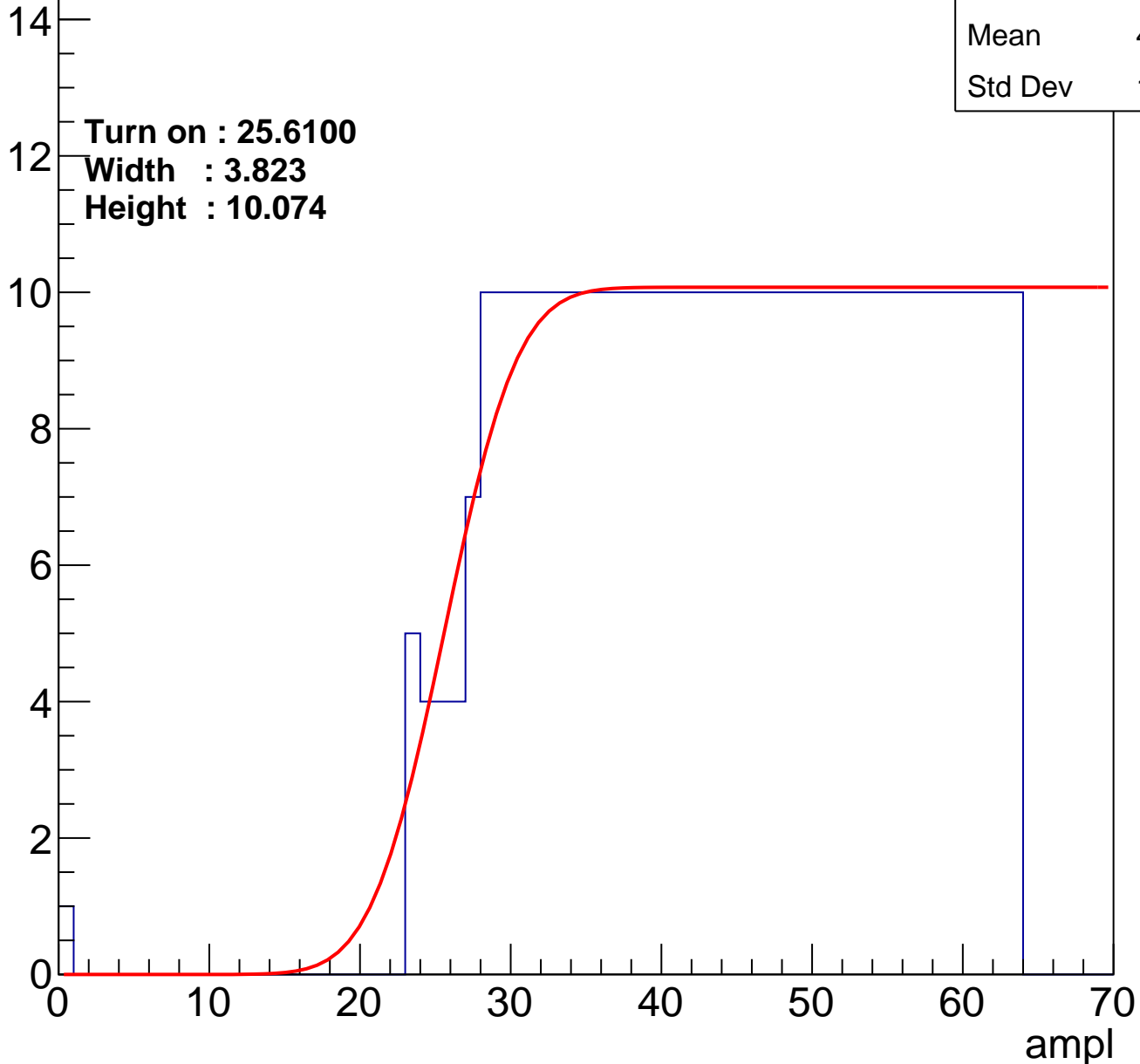
Entries	385
Mean	44.11
Std Dev	11.41

Turn on : 25.6100

Width : 3.823

Height : 10.074

Entry



B1L002S, U18-ch31

calib_packv5_042523_0143.root, FC#10, port B3

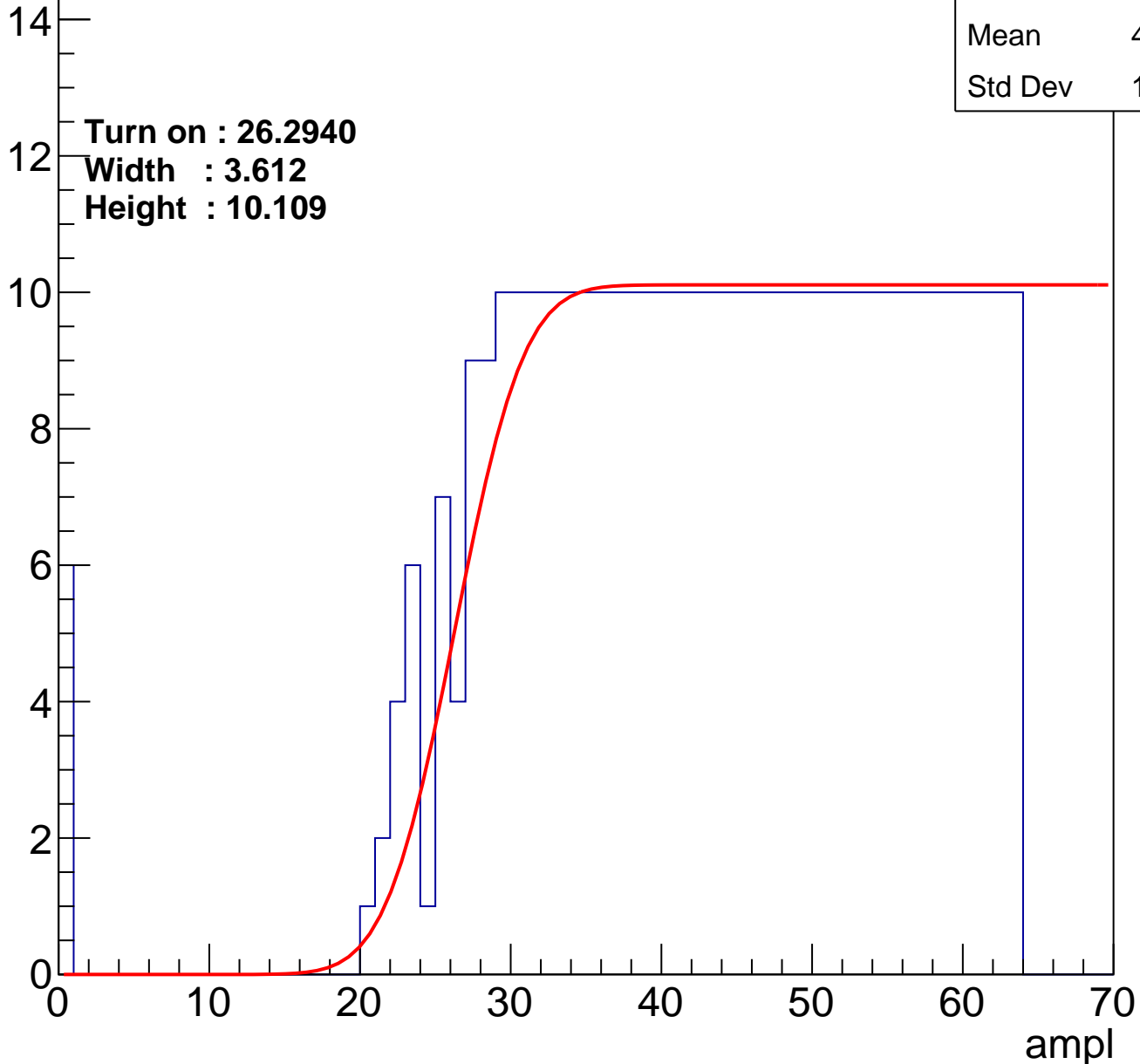
Entries	399
Mean	43.07
Std Dev	12.64

Turn on : 26.2940

Width : 3.612

Height : 10.109

Entry



B1L002S, U18-ch32

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.23
Std Dev	11.51

Turn on : 26.4221

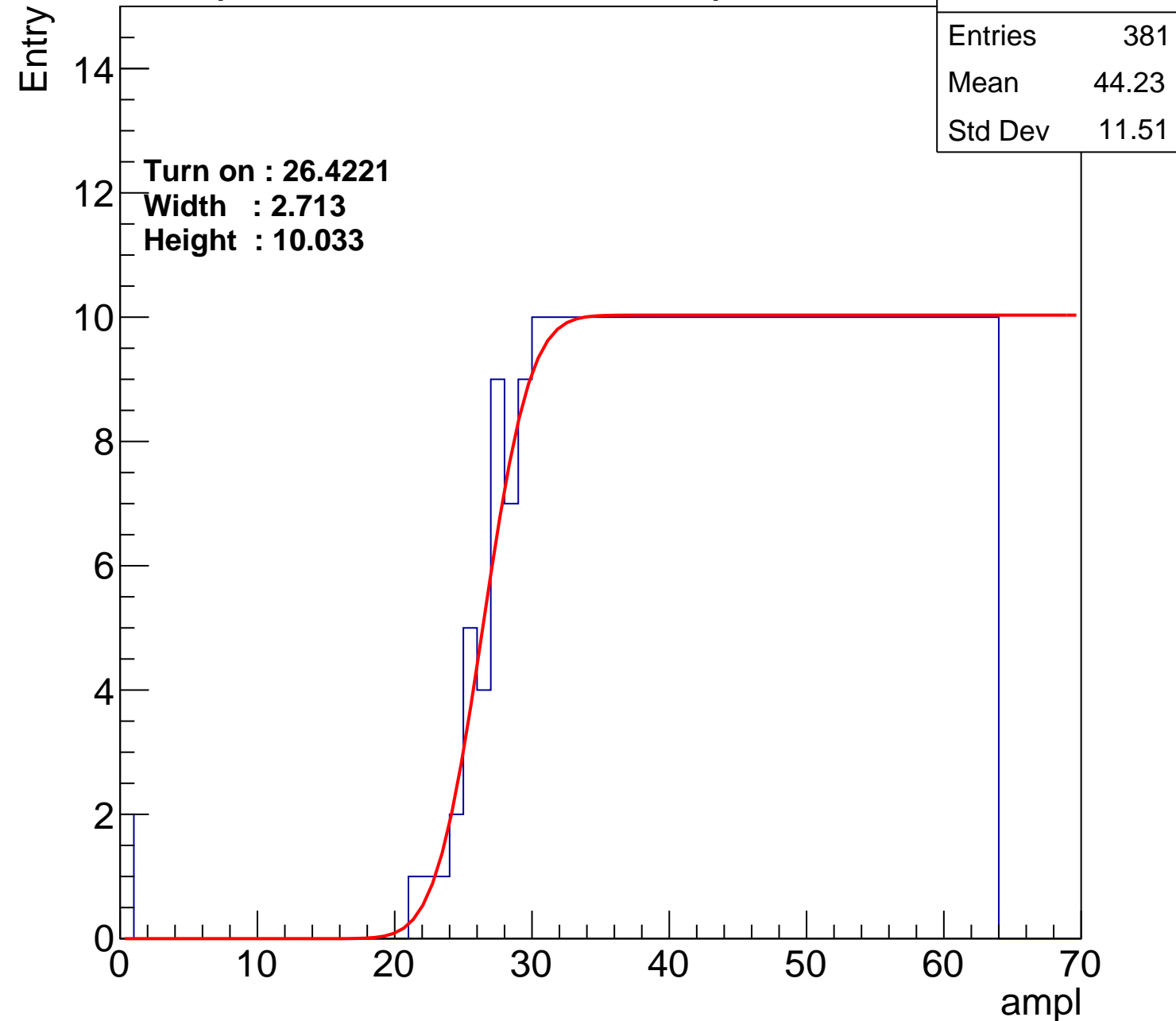
Width : 2.713

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.29
Std Dev	12.13

Turn on : 24.6496

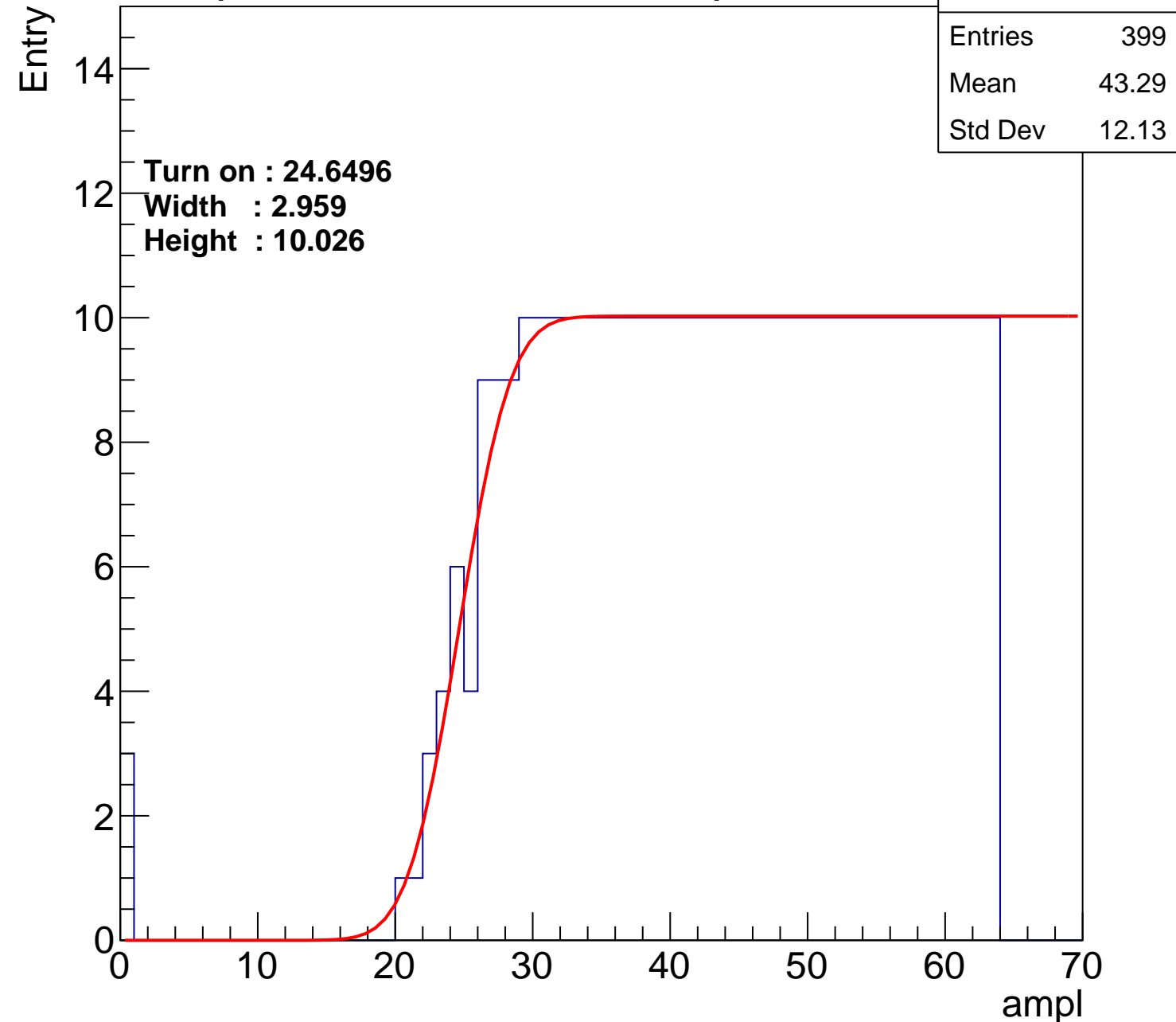
Width : 2.959

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch34

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.17
Std Dev	12.16

Turn on : 24.3336

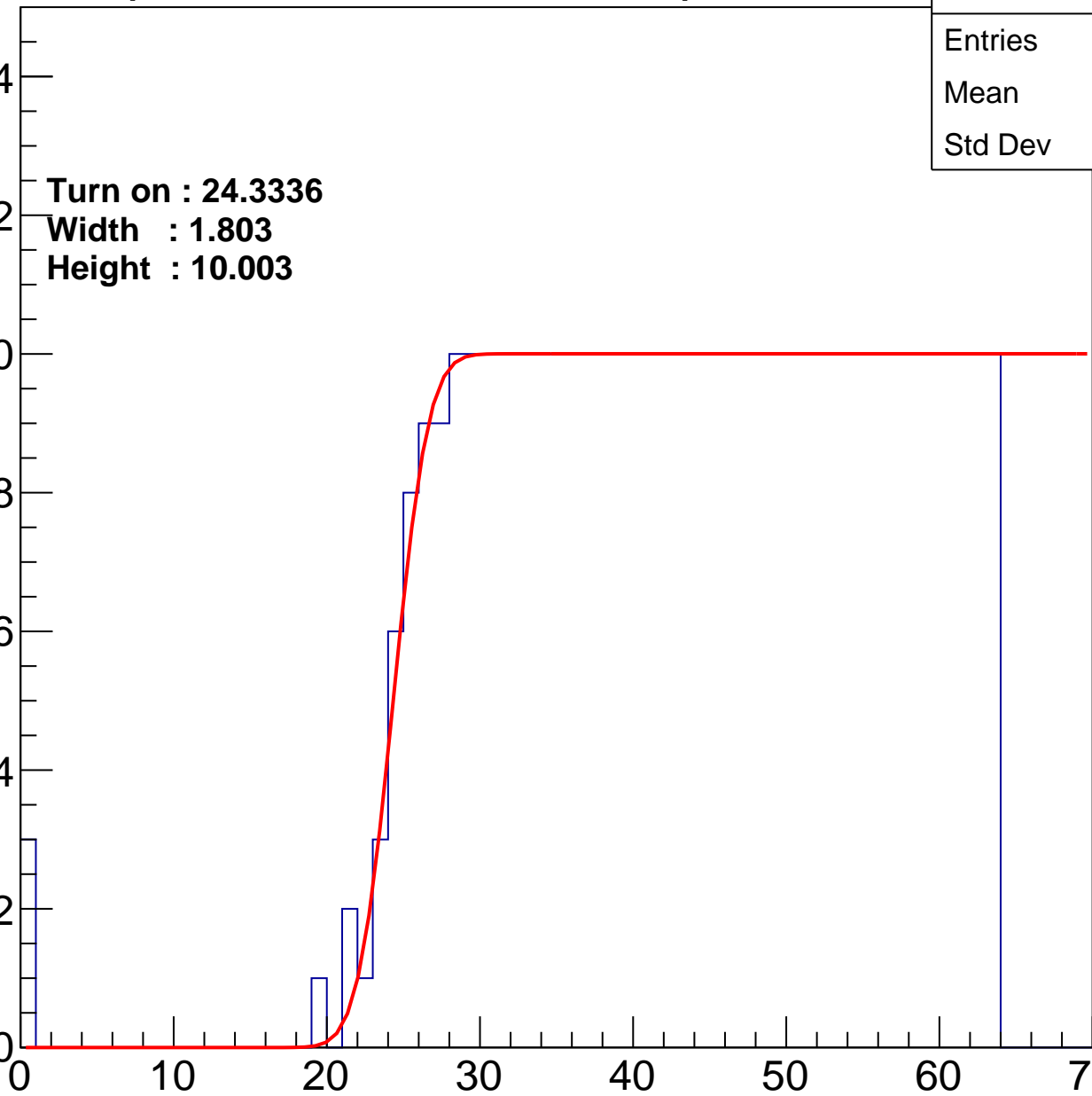
Width : 1.803

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch35

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	44.01
Std Dev	11.48

Turn on : 26.0767

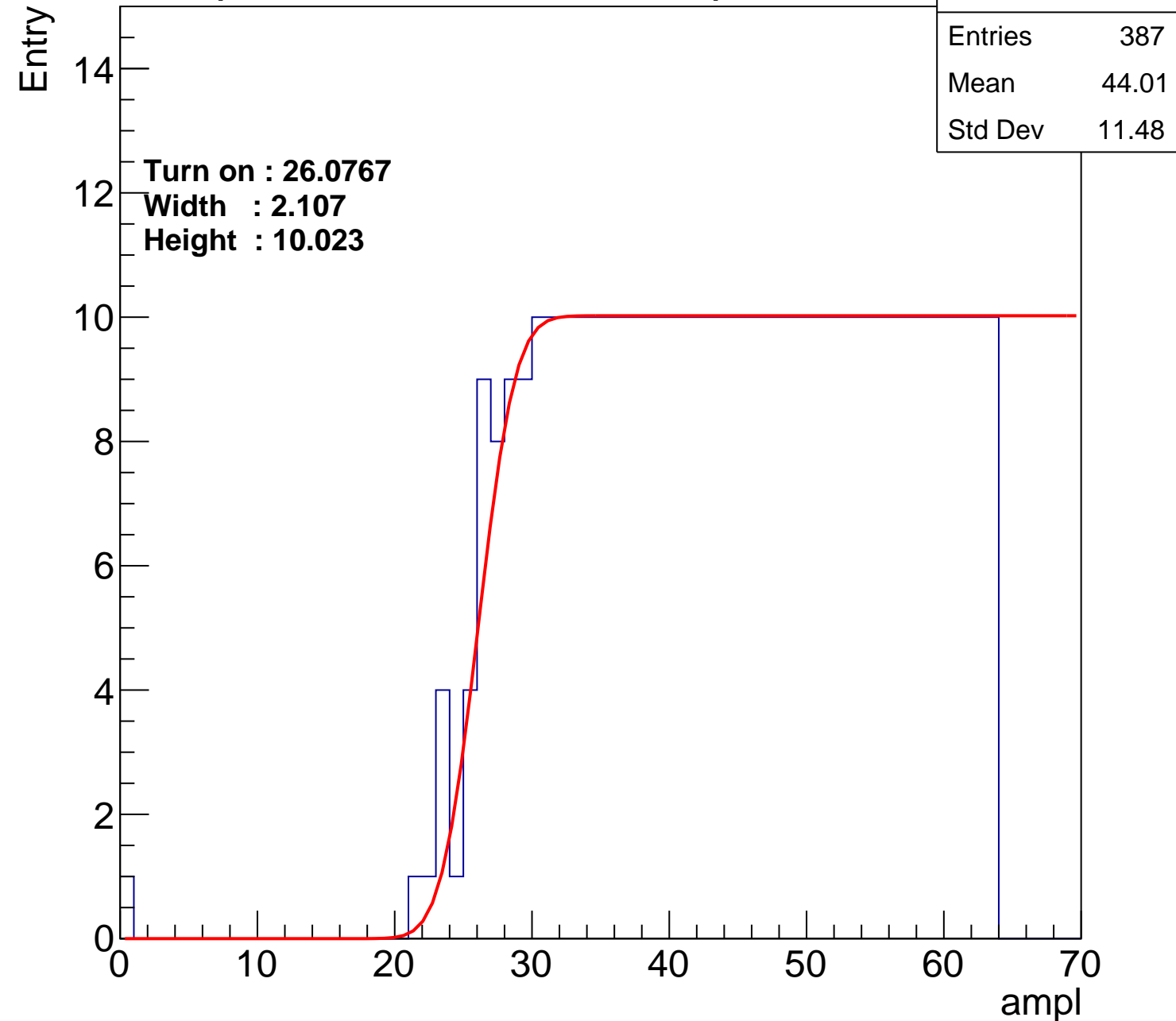
Width : 2.107

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch36

calib_packv5_042523_0143.root, FC#10, port B3

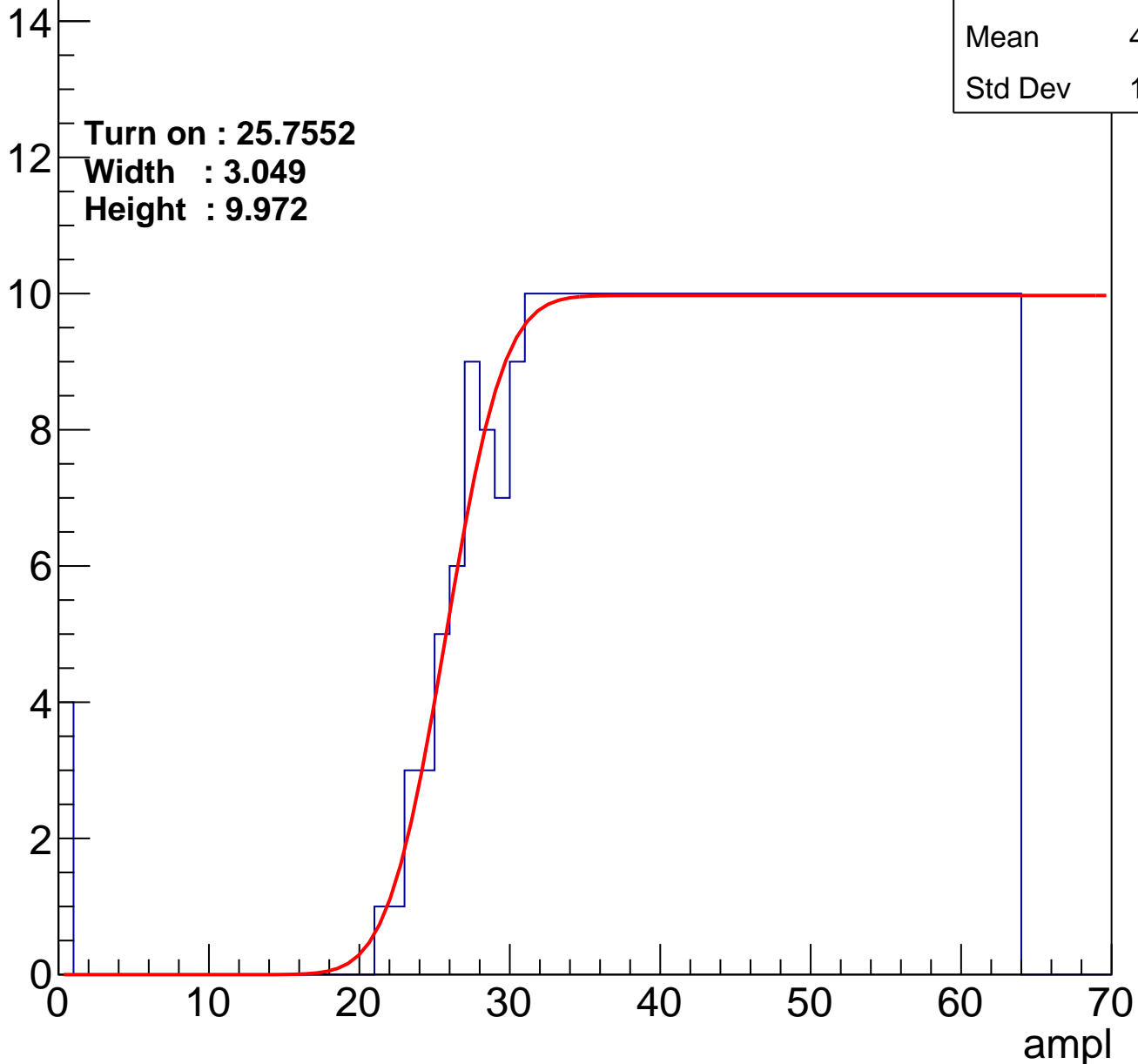
Entries	386
Mean	43.82
Std Dev	12.03

Turn on : 25.7552

Width : 3.049

Height : 9.972

Entry



B1L002S, U18-ch37

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.28
Std Dev	11.92

Turn on : 24.0867

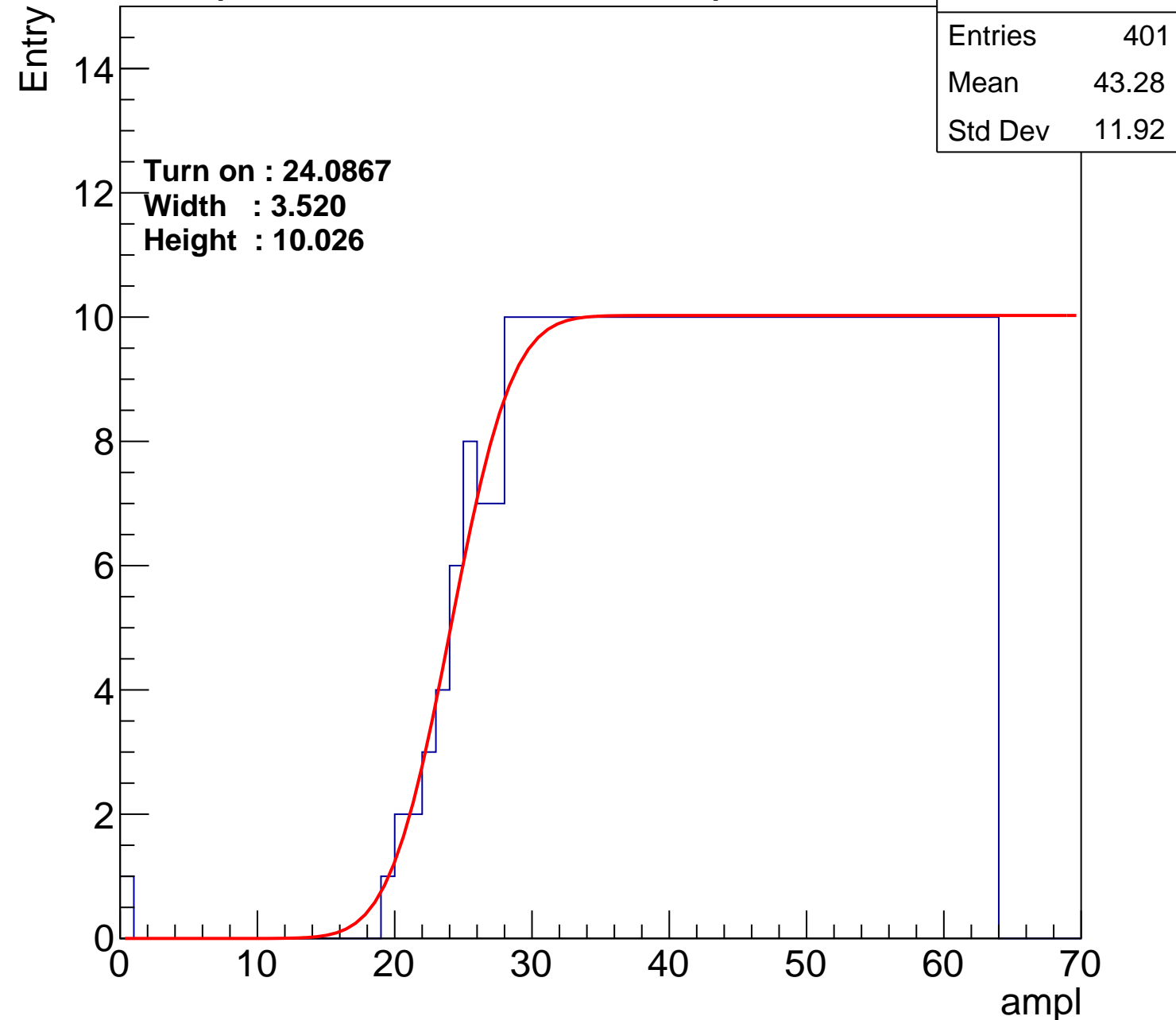
Width : 3.520

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch38

calib_packv5_042523_0143.root, FC#10, port B3

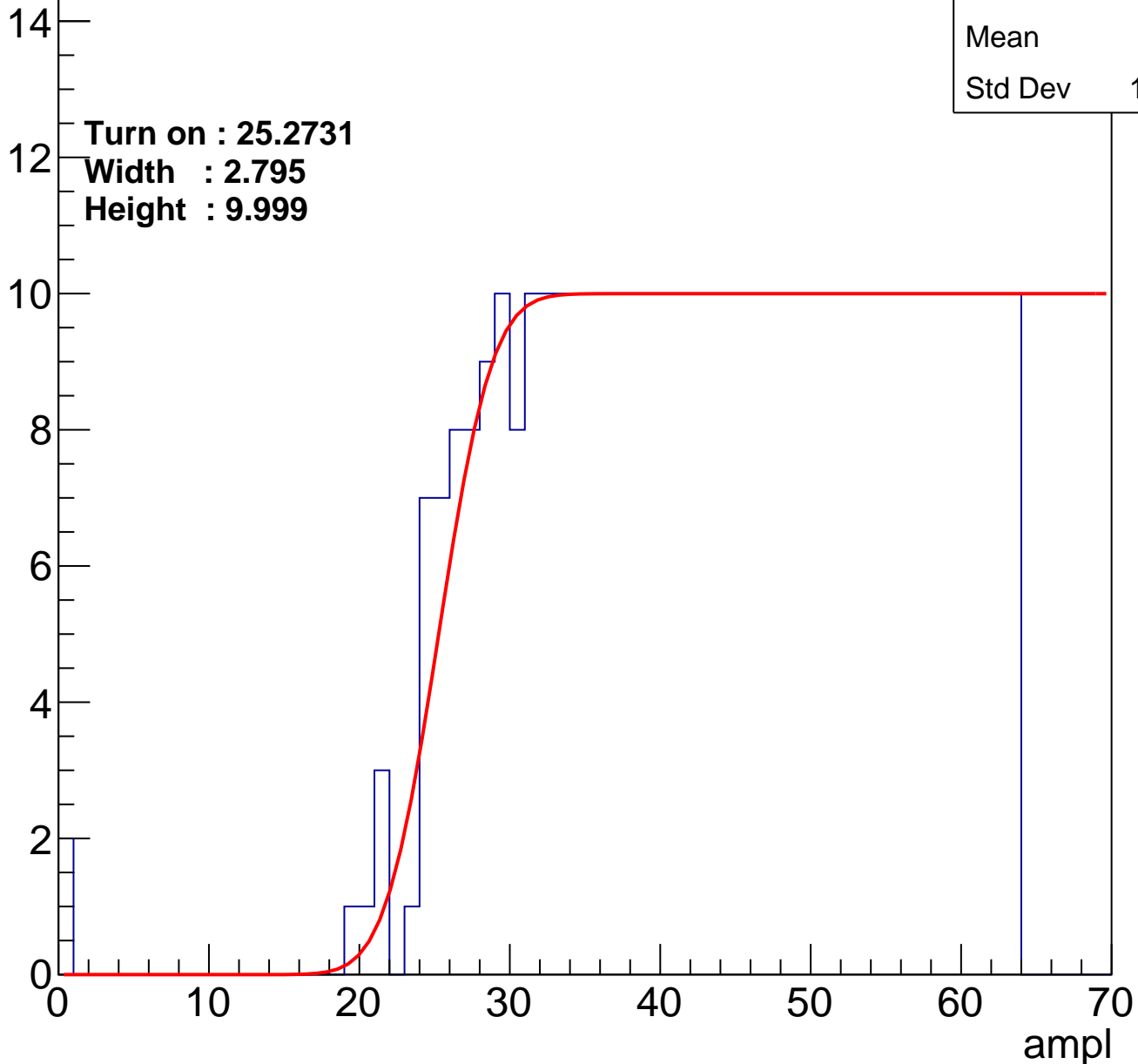
Entries	395
Mean	43.5
Std Dev	11.93

Turn on : 25.2731

Width : 2.795

Height : 9.999

Entry



B1L002S, U18-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.12
Std Dev	11.89

Turn on : 26.4417

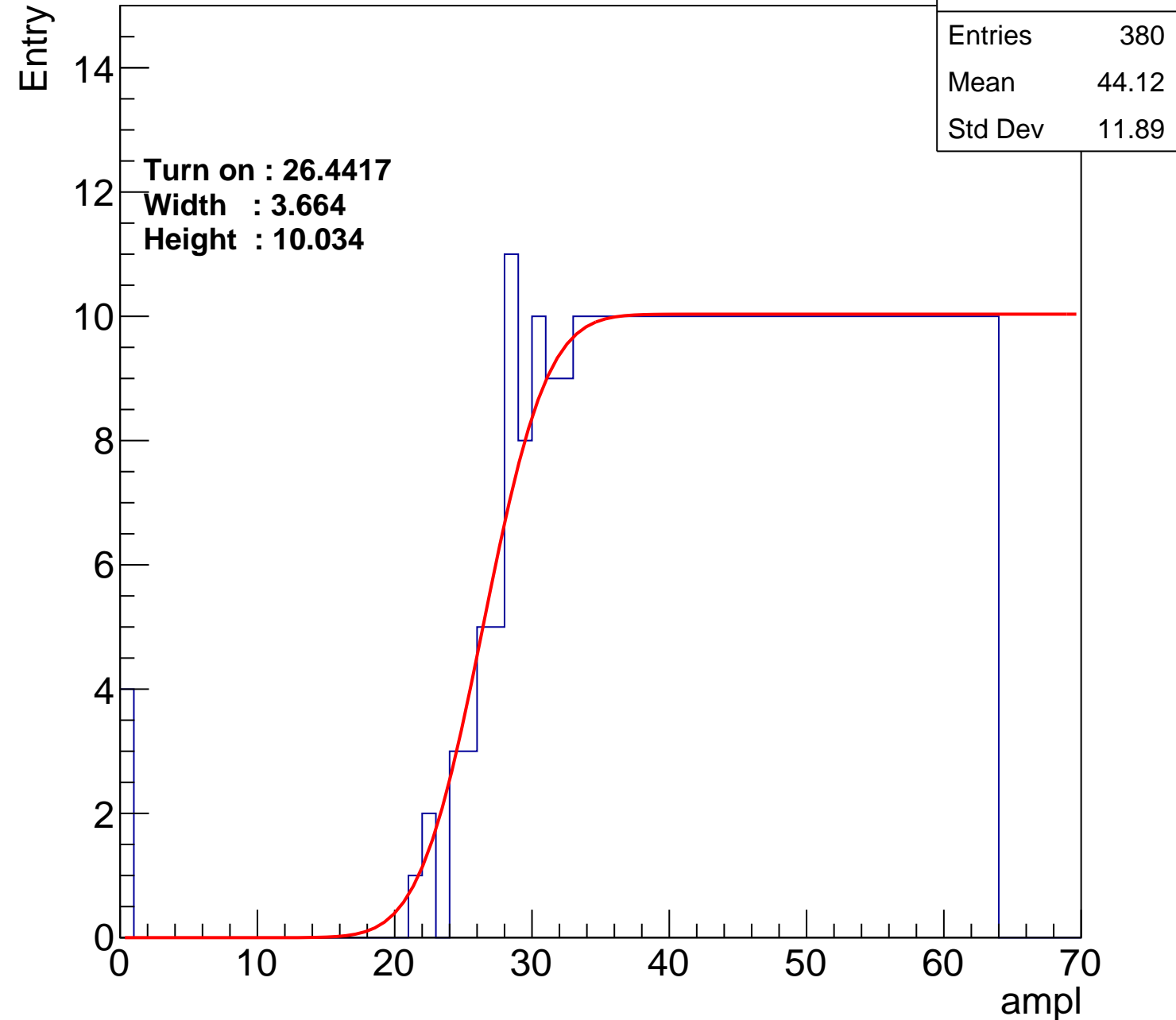
Width : 3.664

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch40

calib_packv5_042523_0143.root, FC#10, port B3

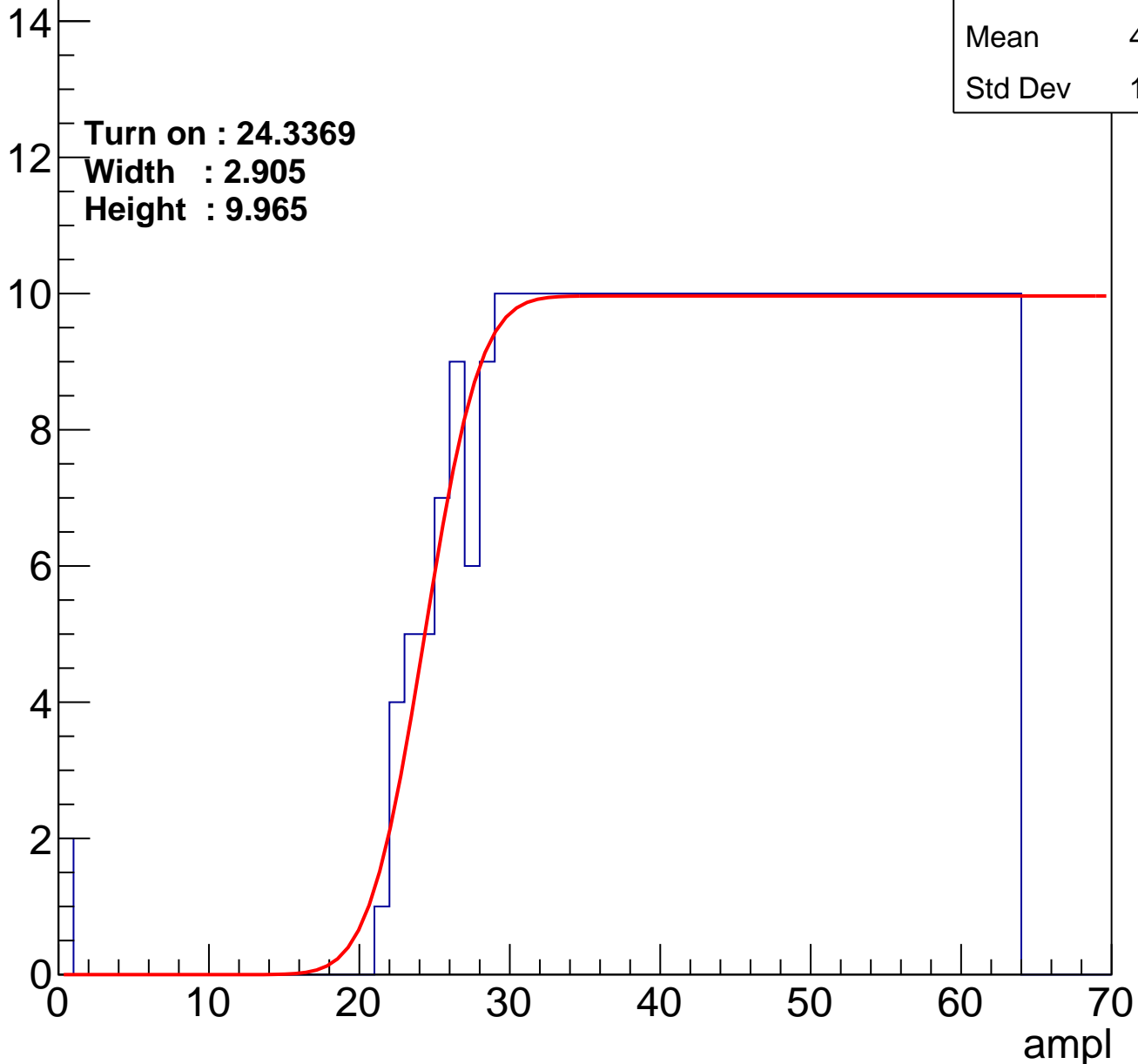
Entries	398
Mean	43.38
Std Dev	11.96

Turn on : 24.3369

Width : 2.905

Height : 9.965

Entry



B1L002S, U18-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.77
Std Dev	11.61

Turn on : 24.9564

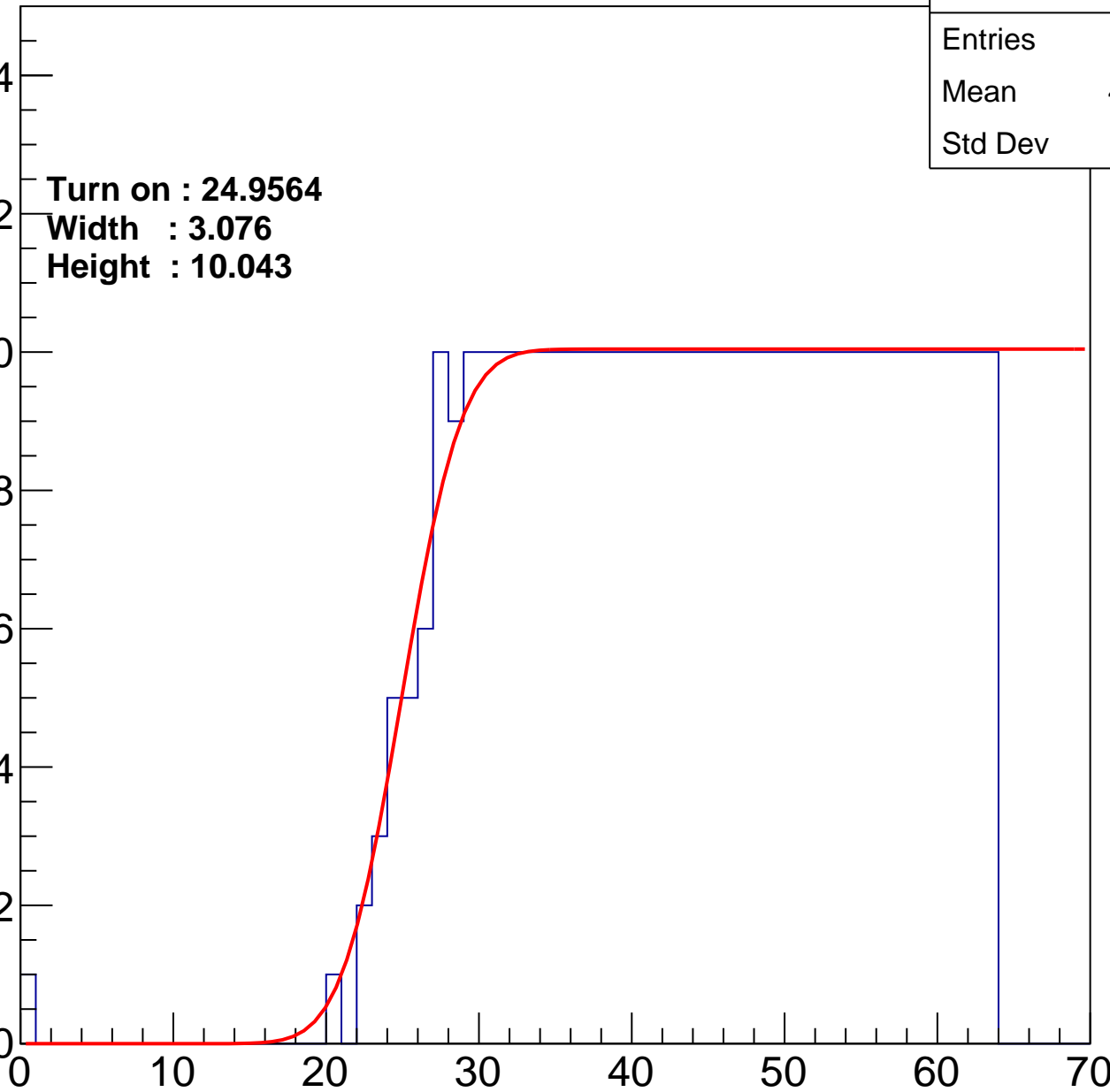
Width : 3.076

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch42

calib_packv5_042523_0143.root, FC#10, port B3

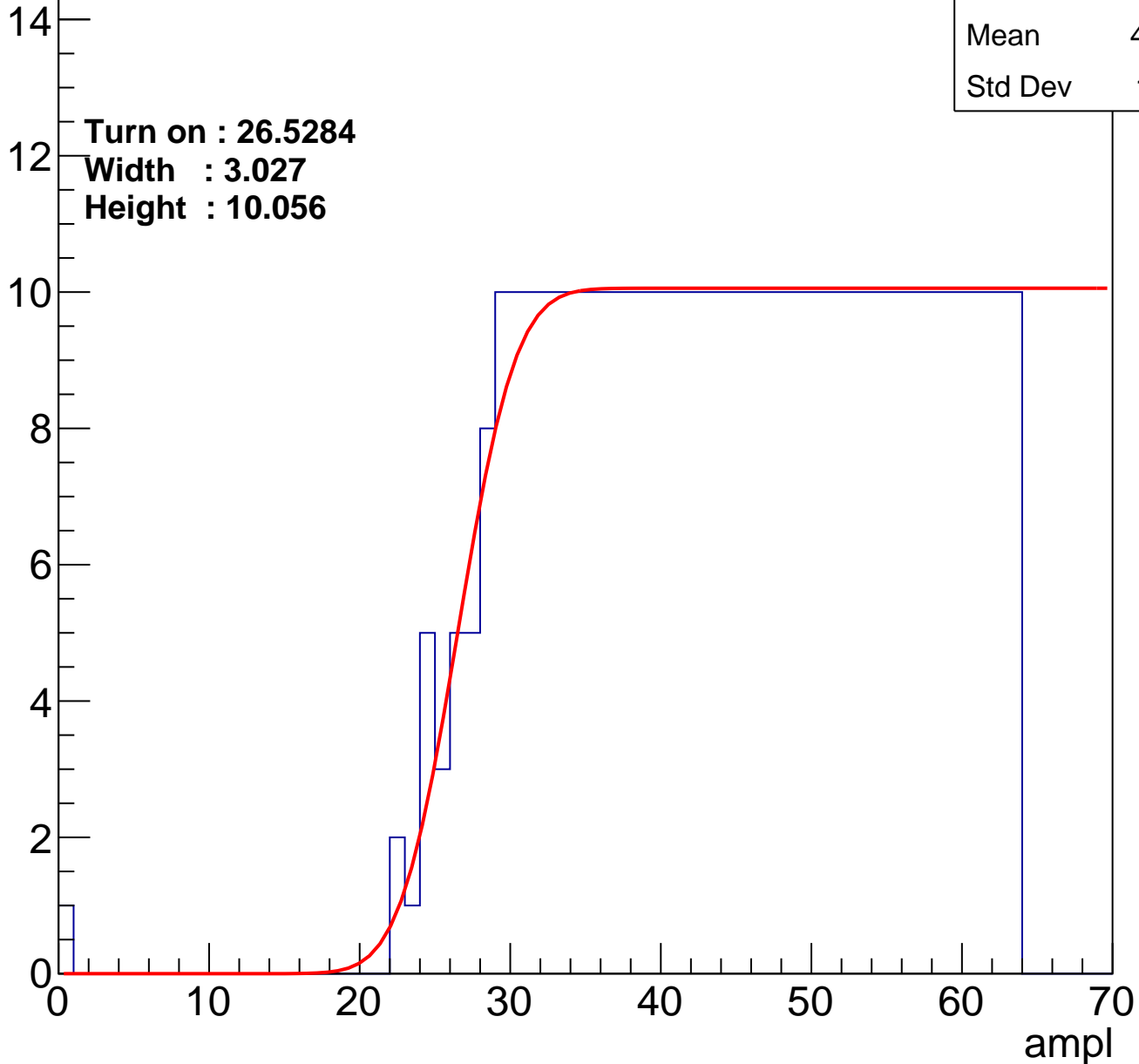
Entries	380
Mean	44.34
Std Dev	11.31

Turn on : 26.5284

Width : 3.027

Height : 10.056

Entry



B1L002S, U18-ch43

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.44
Std Dev	11.38

Turn on : 26.3751

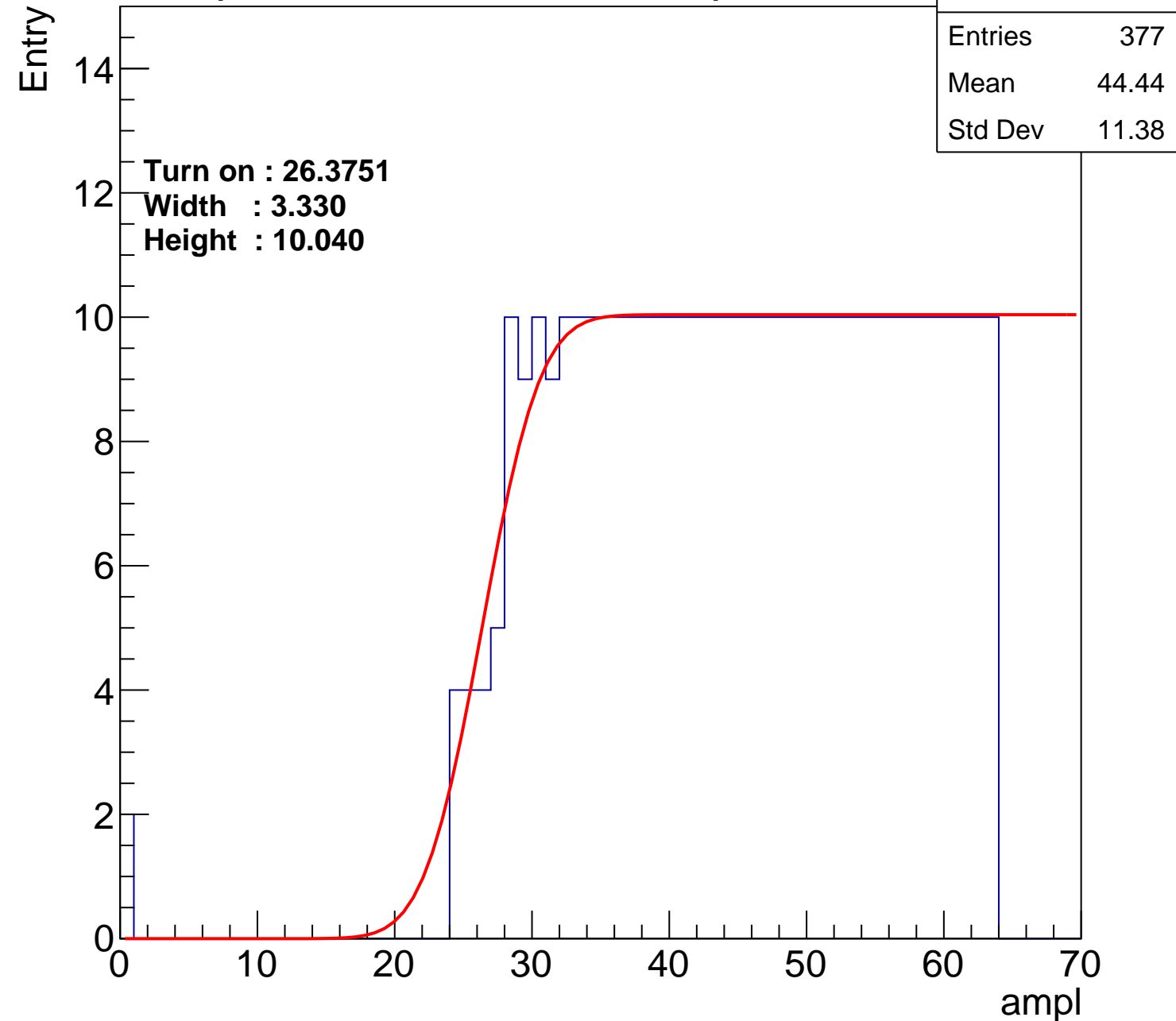
Width : 3.330

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch44

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.47
Std Dev	11.77

Turn on : 24.9014

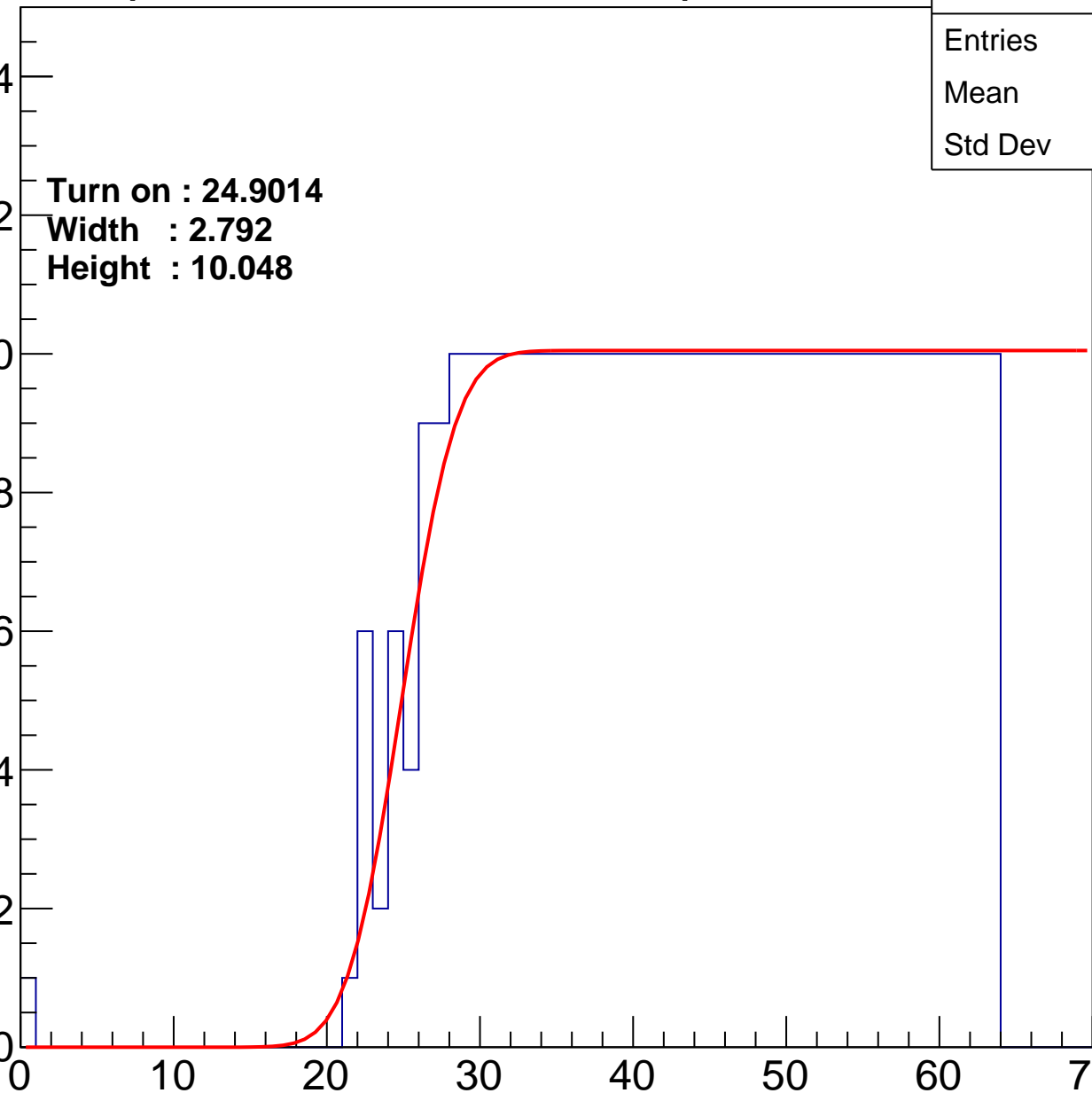
Width : 2.792

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch45

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.52
Std Dev	11.43

Turn on : 27.2770

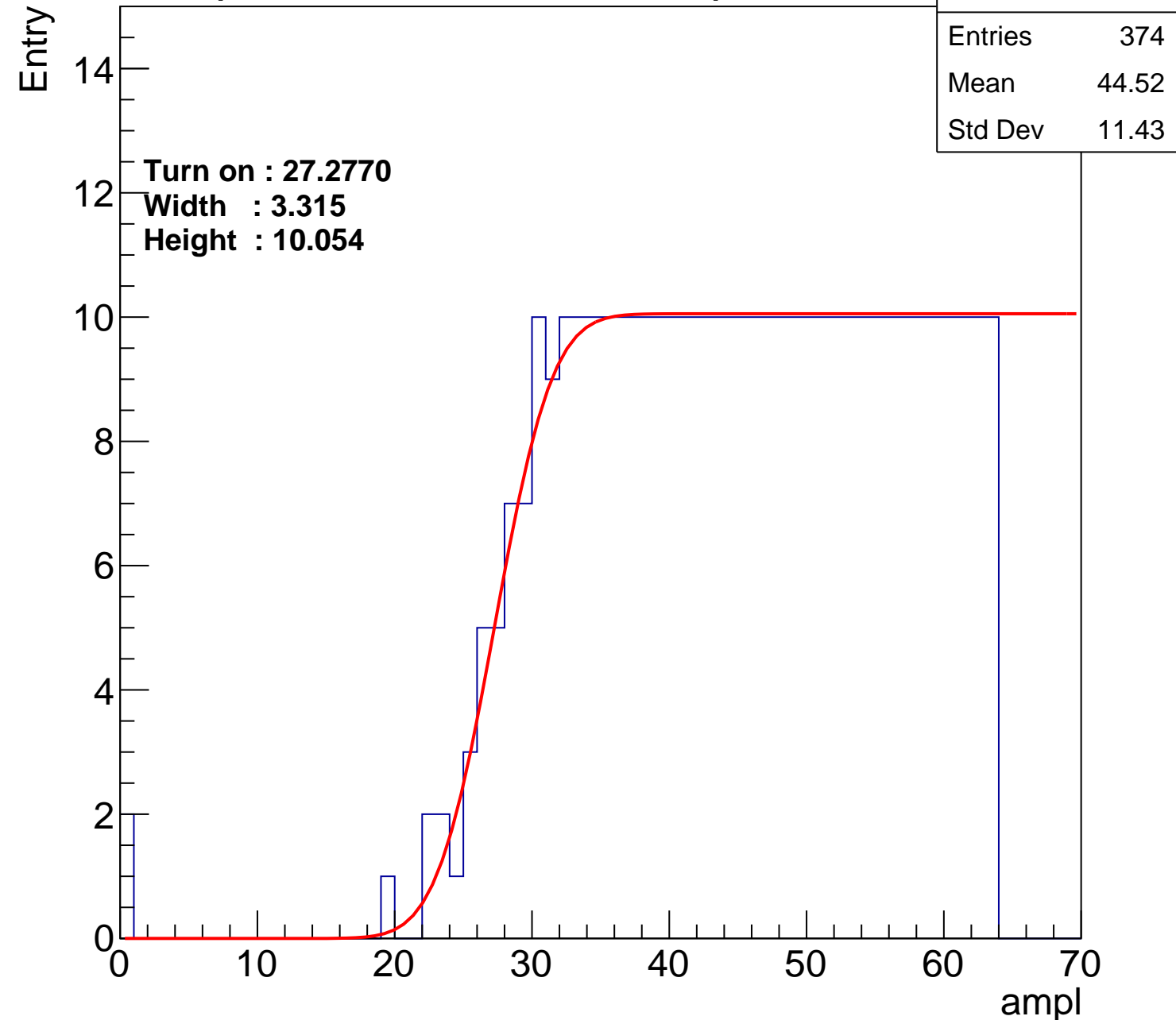
Width : 3.315

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch46

calib_packv5_042523_0143.root, FC#10, port B3

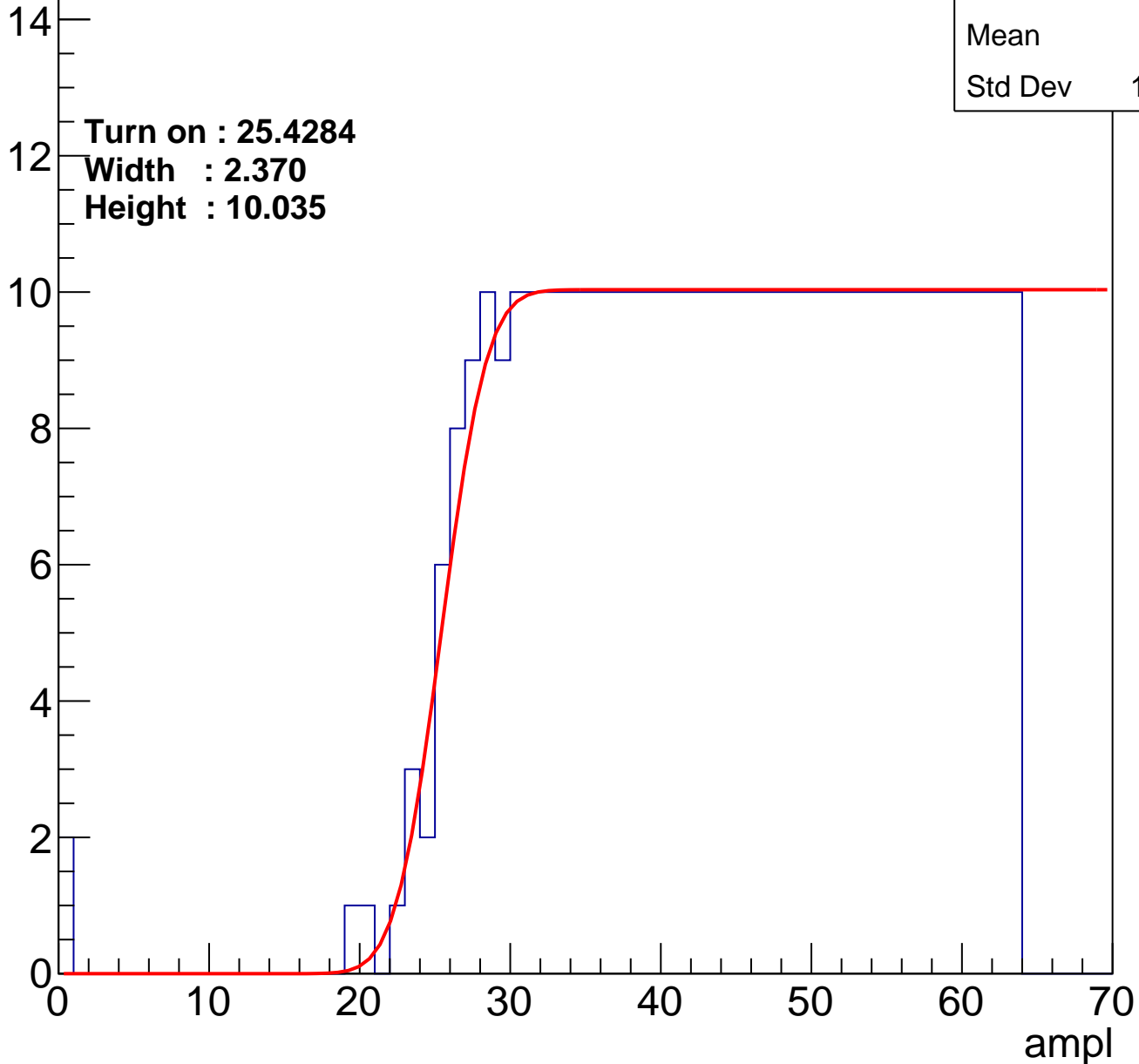
Entries	392
Mean	43.7
Std Dev	11.78

Turn on : 25.4284

Width : 2.370

Height : 10.035

Entry



B1L002S, U18-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.62
Std Dev	11.44

Turn on : 27.3034

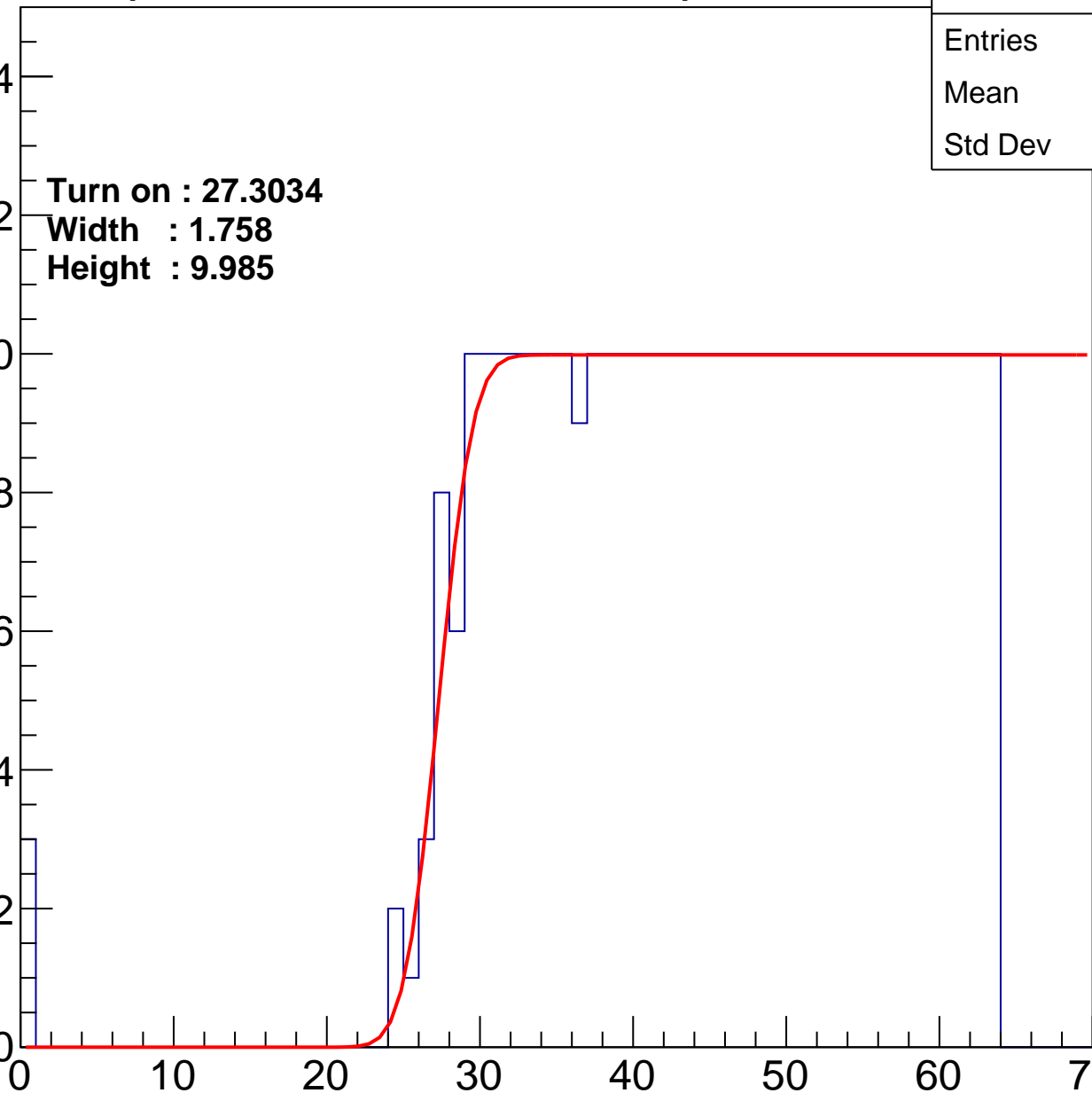
Width : 1.758

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch48

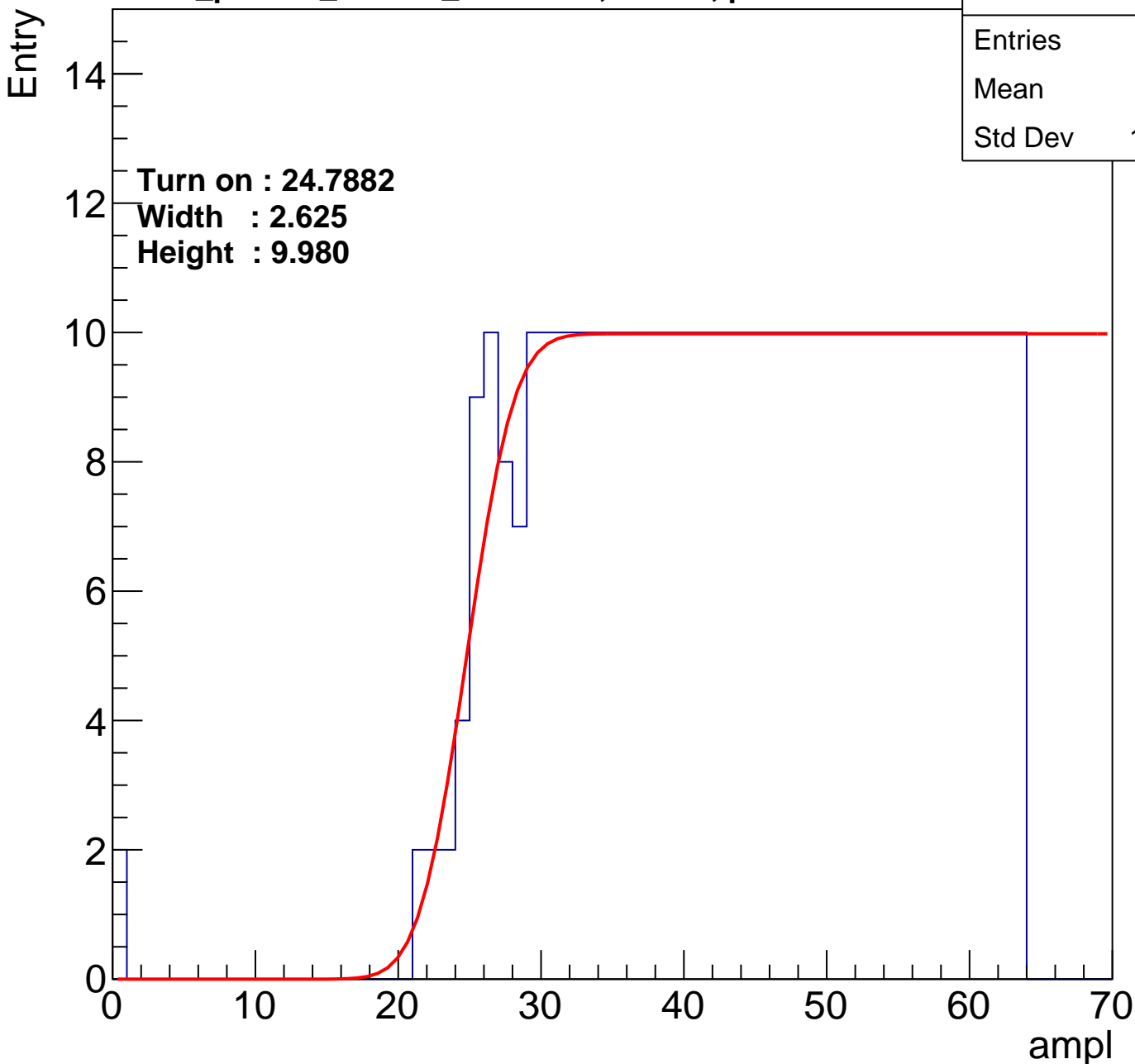
calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.5
Std Dev	11.89

Turn on : 24.7882

Width : 2.625

Height : 9.980



B1L002S, U18-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.42
Std Dev	11.19

Turn on : 26.3596

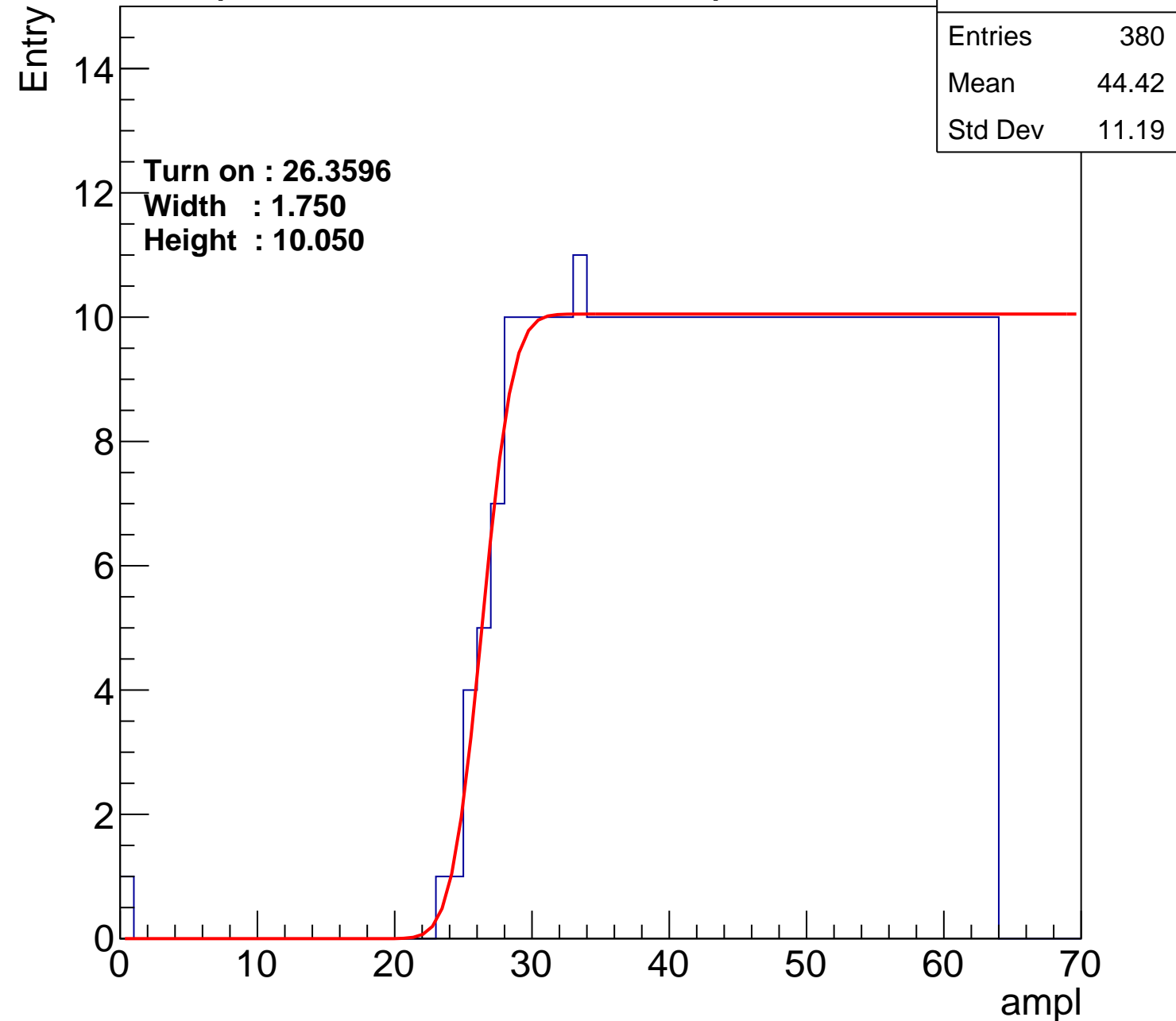
Width : 1.750

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch50

calib_packv5_042523_0143.root, FC#10, port B3

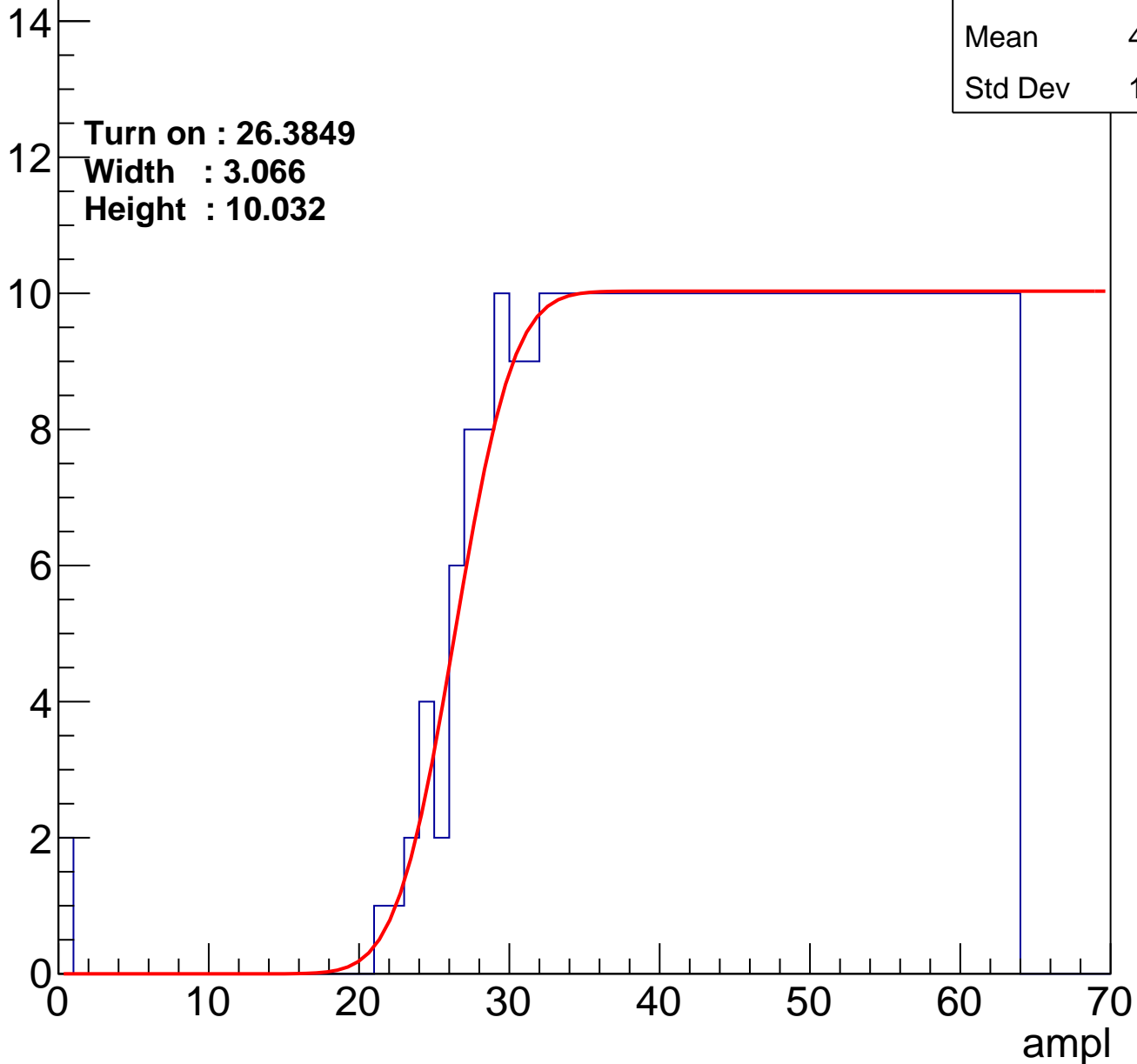
Entries	382
Mean	44.16
Std Dev	11.57

Turn on : 26.3849

Width : 3.066

Height : 10.032

Entry



B1L002S, U18-ch51

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.7
Std Dev	12.06

Turn on : 25.6080

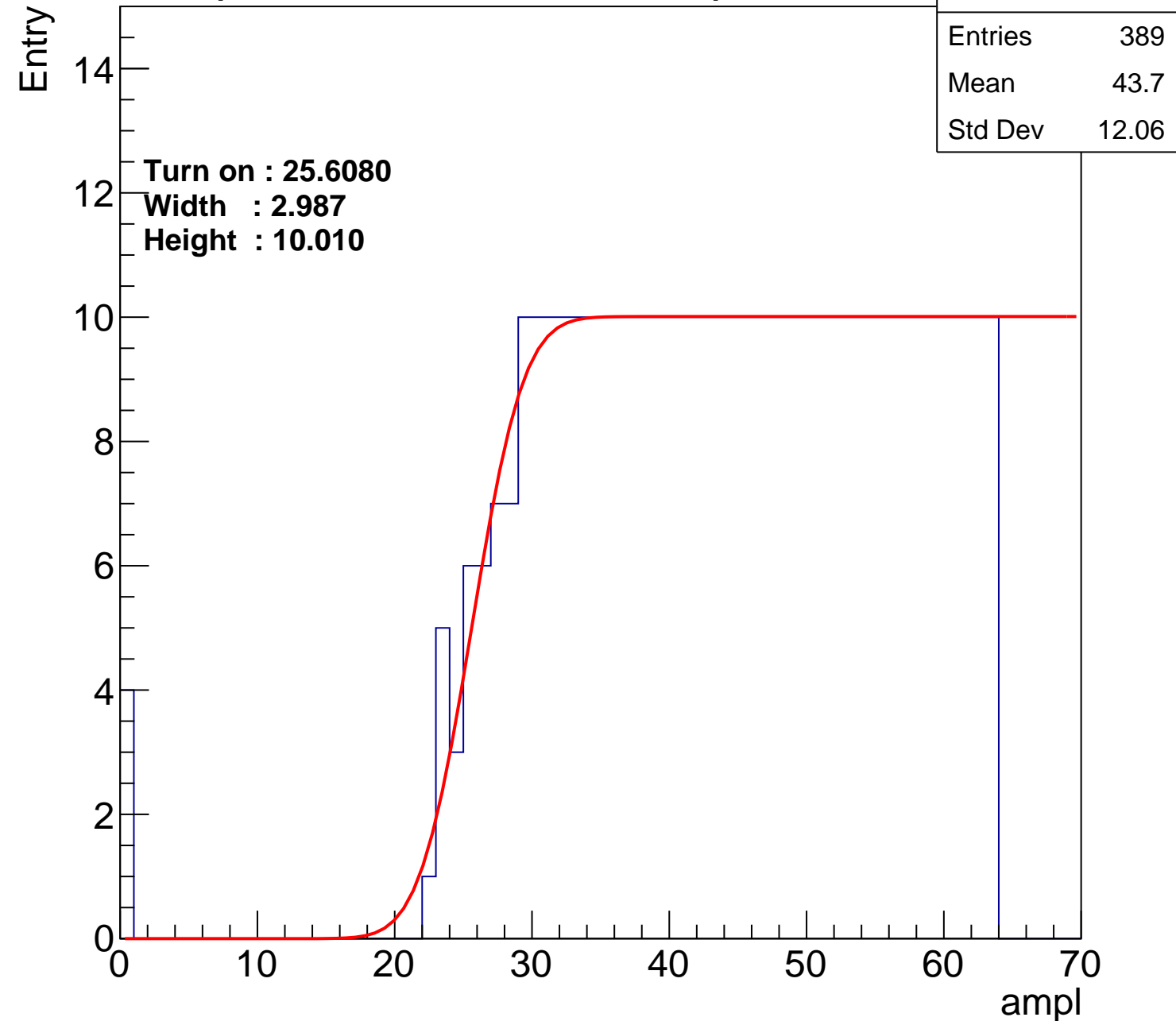
Width : 2.987

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch52

calib_packv5_042523_0143.root, FC#10, port B3

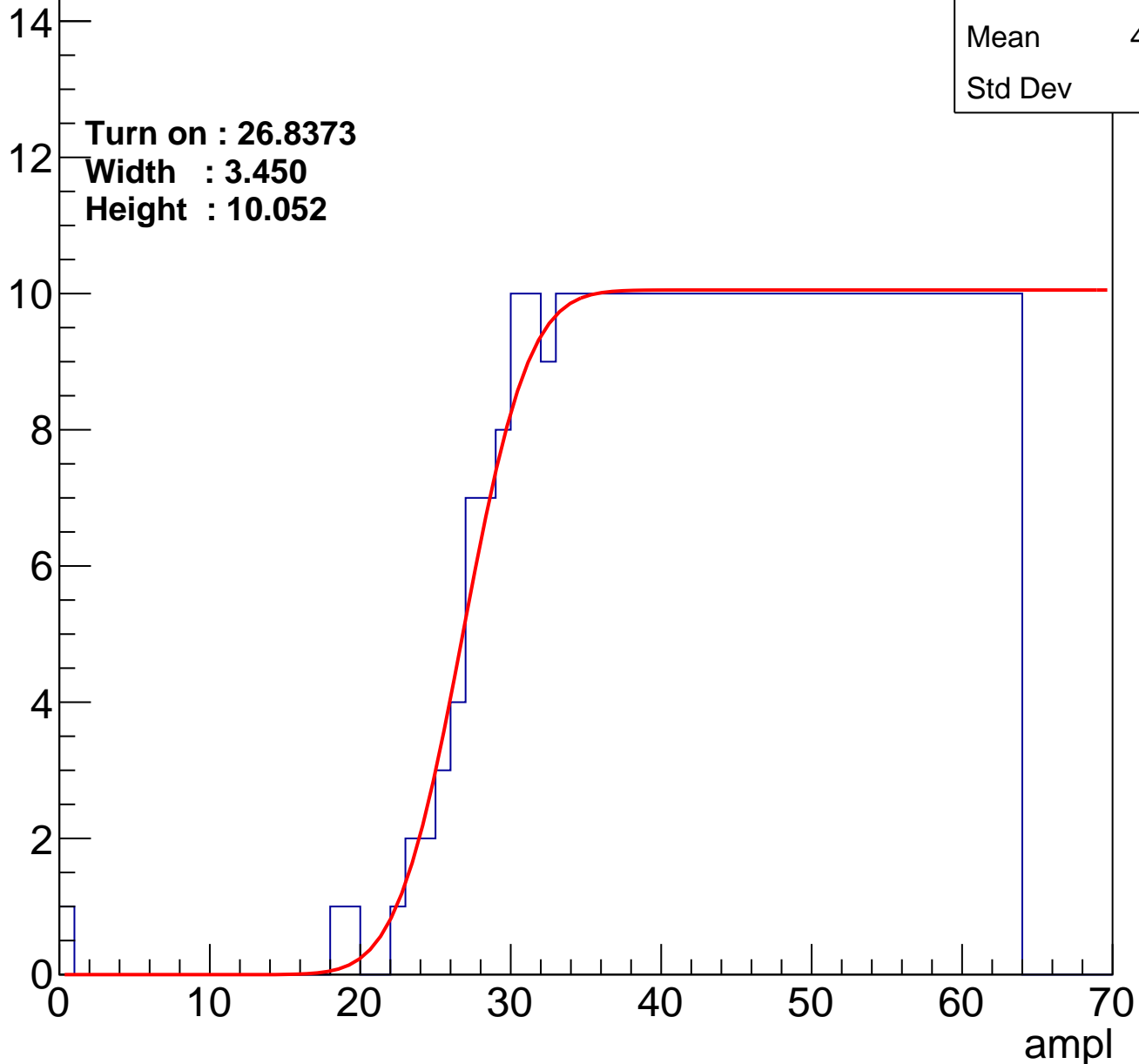
Entries	376
Mean	44.49
Std Dev	11.3

Turn on : 26.8373

Width : 3.450

Height : 10.052

Entry



B1L002S, U18-ch53

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.76
Std Dev	11.73

Turn on : 25.3354

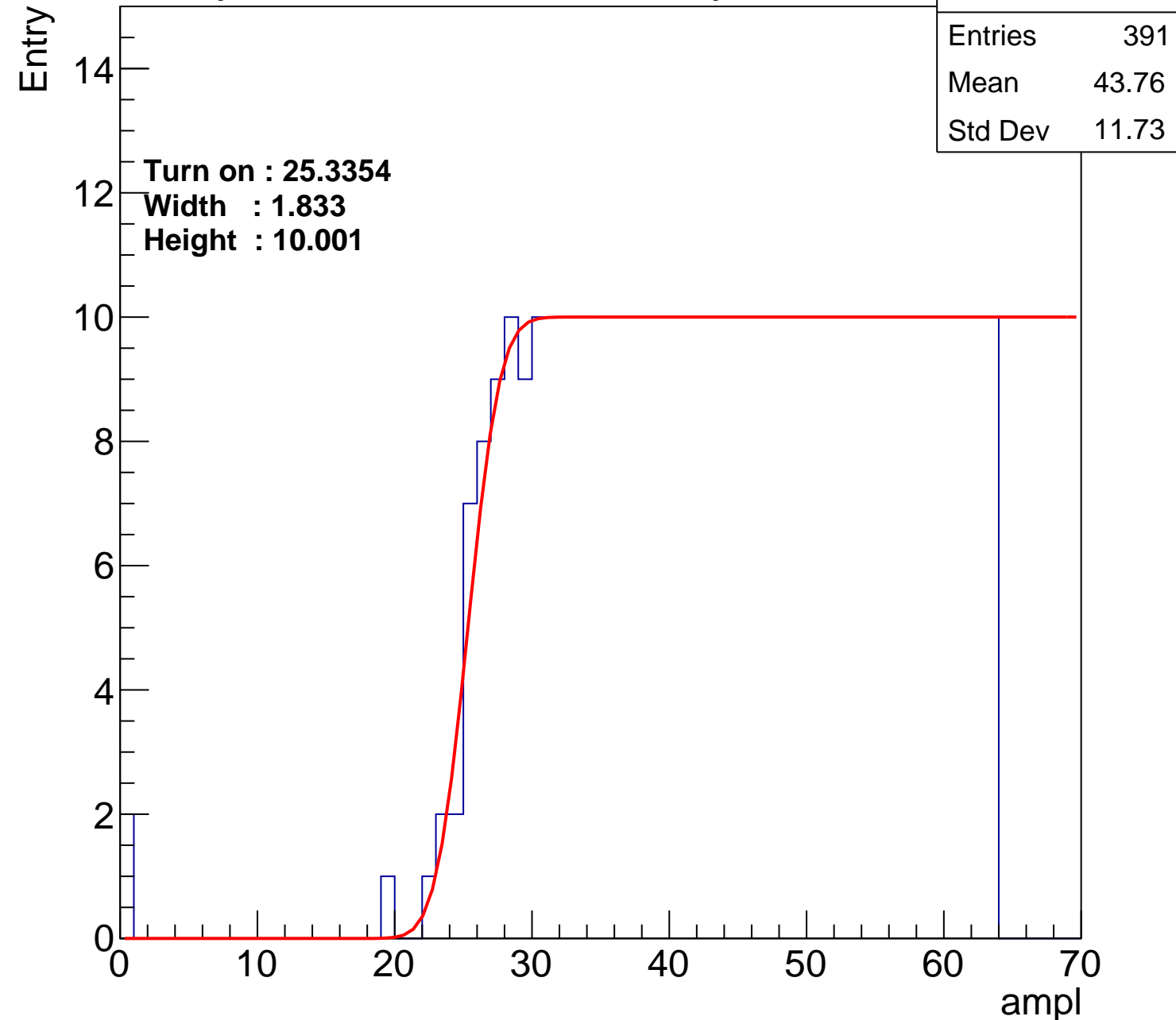
Width : 1.833

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch54

calib_packv5_042523_0143.root, FC#10, port B3

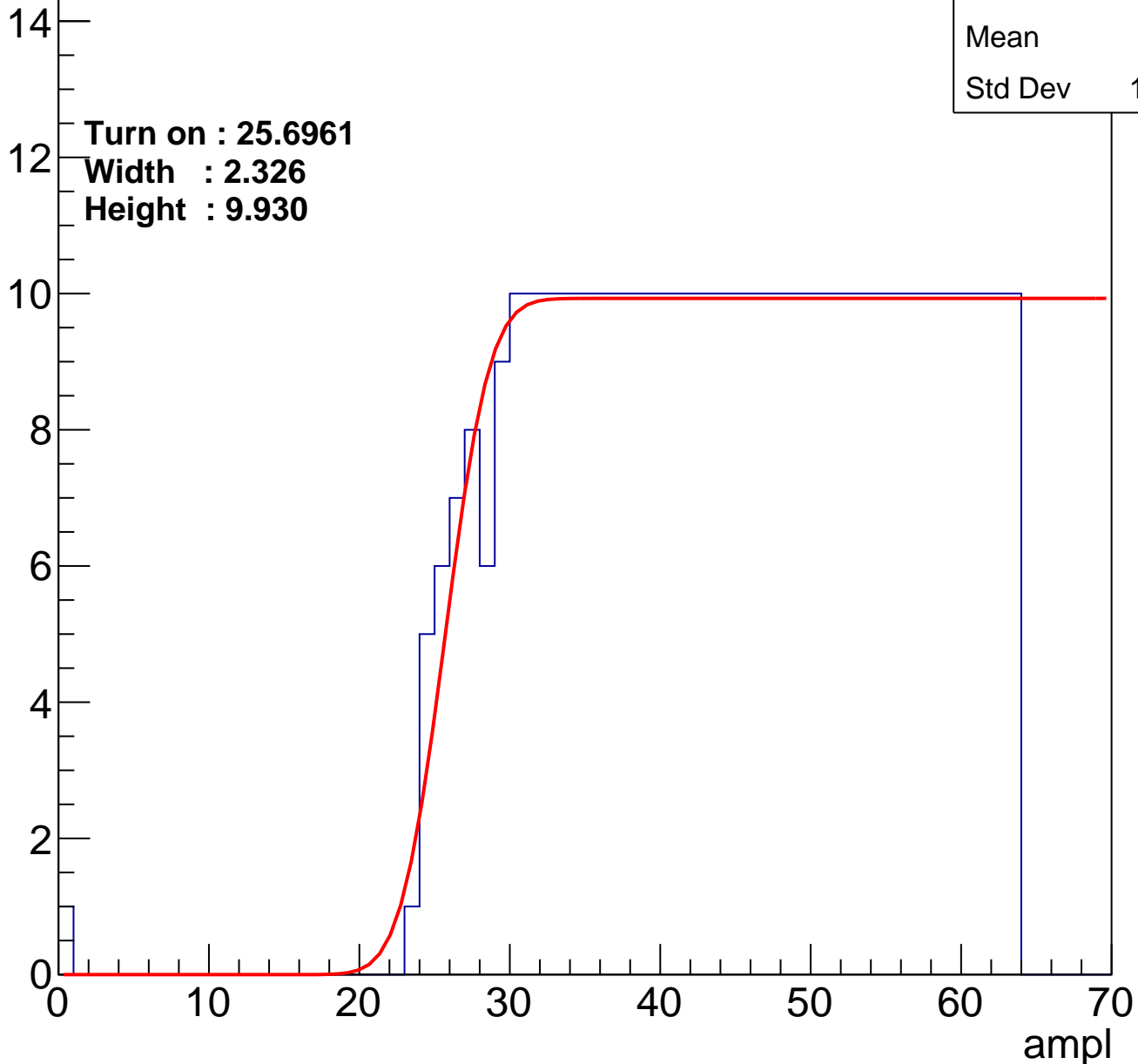
Entries	383
Mean	44.2
Std Dev	11.37

Turn on : 25.6961

Width : 2.326

Height : 9.930

Entry



B1L002S, U18-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.54
Std Dev	11.88

Turn on : 24.7631

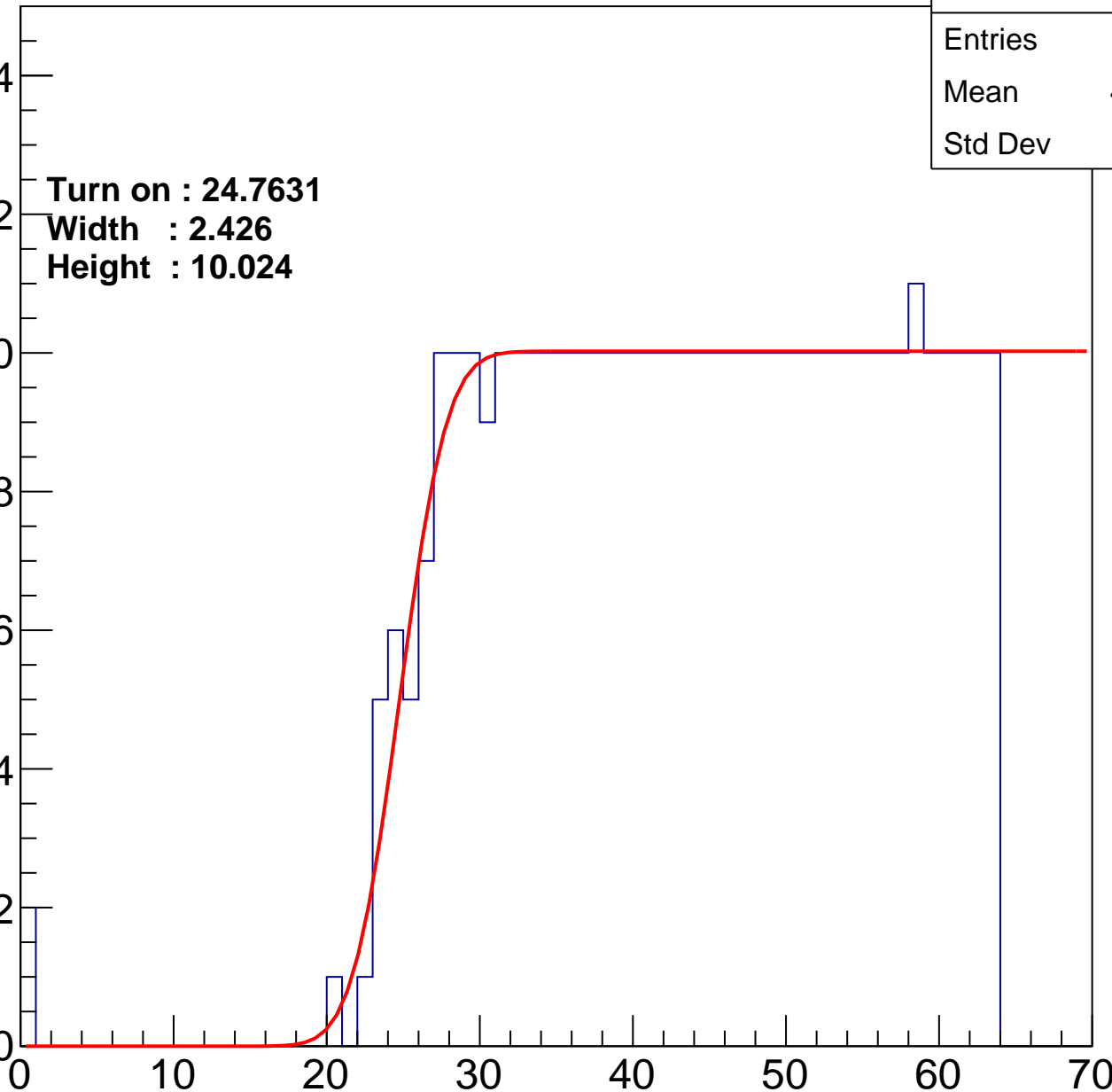
Width : 2.426

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch56

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.73
Std Dev	11.9

Turn on : 25.3952

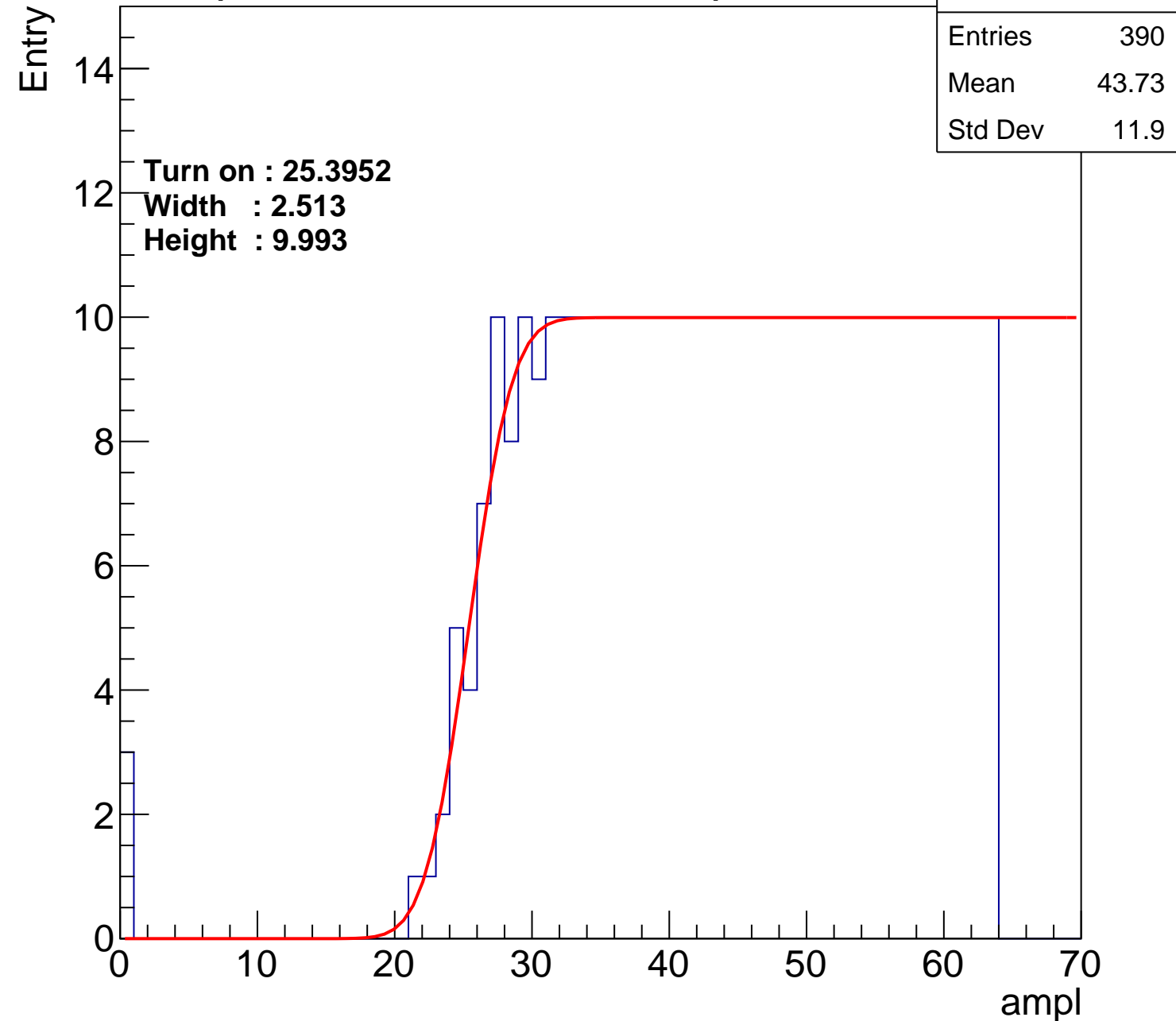
Width : 2.513

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.9
Std Dev	11.68

Turn on : 25.9212

Width : 2.564

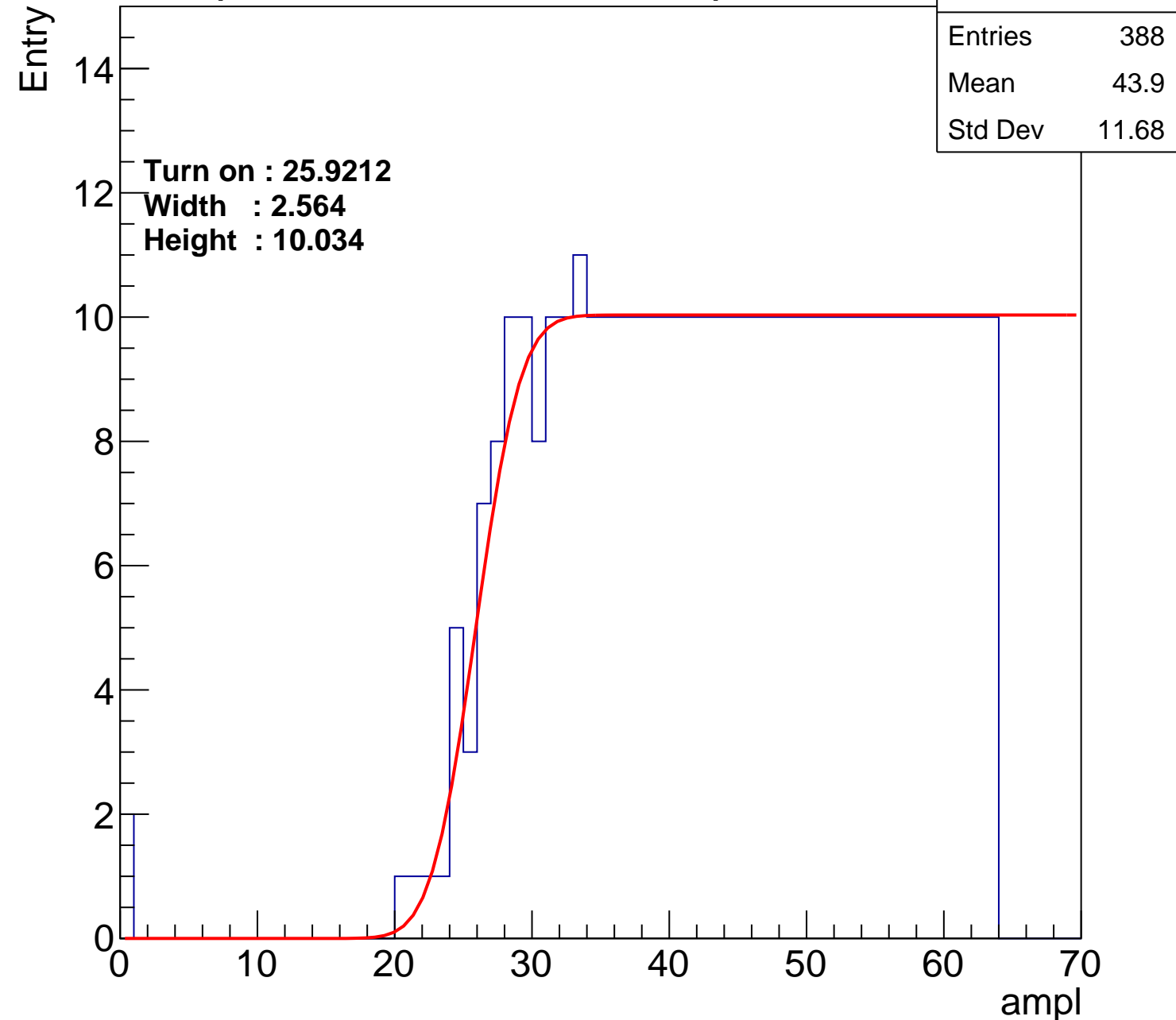
Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U18-ch58

calib_packv5_042523_0143.root, FC#10, port B3

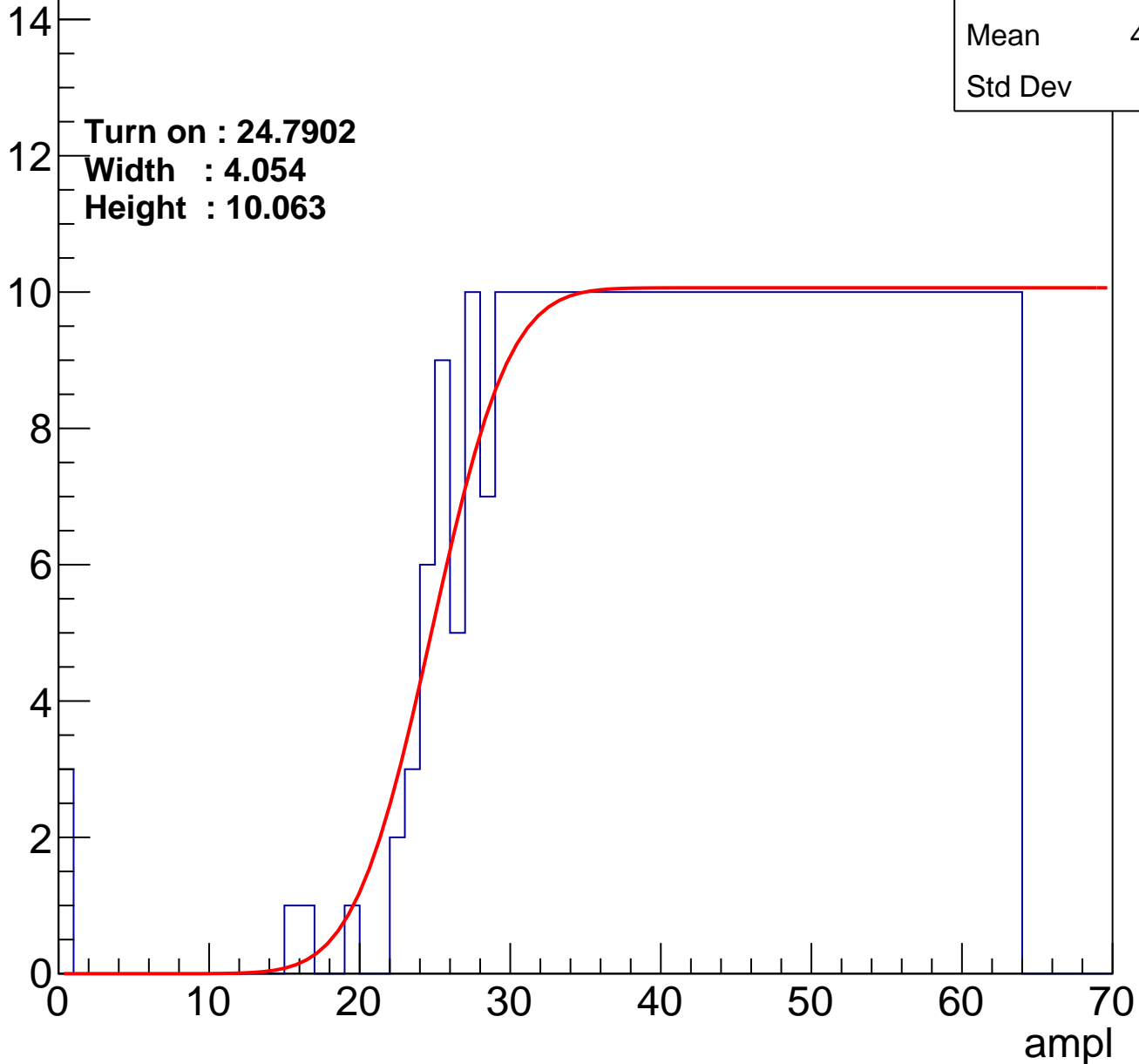
Entries	398
Mean	43.29
Std Dev	12.2

Turn on : 24.7902

Width : 4.054

Height : 10.063

Entry



B1L002S, U18-ch59

calib_packv5_042523_0143.root, FC#10, port B3

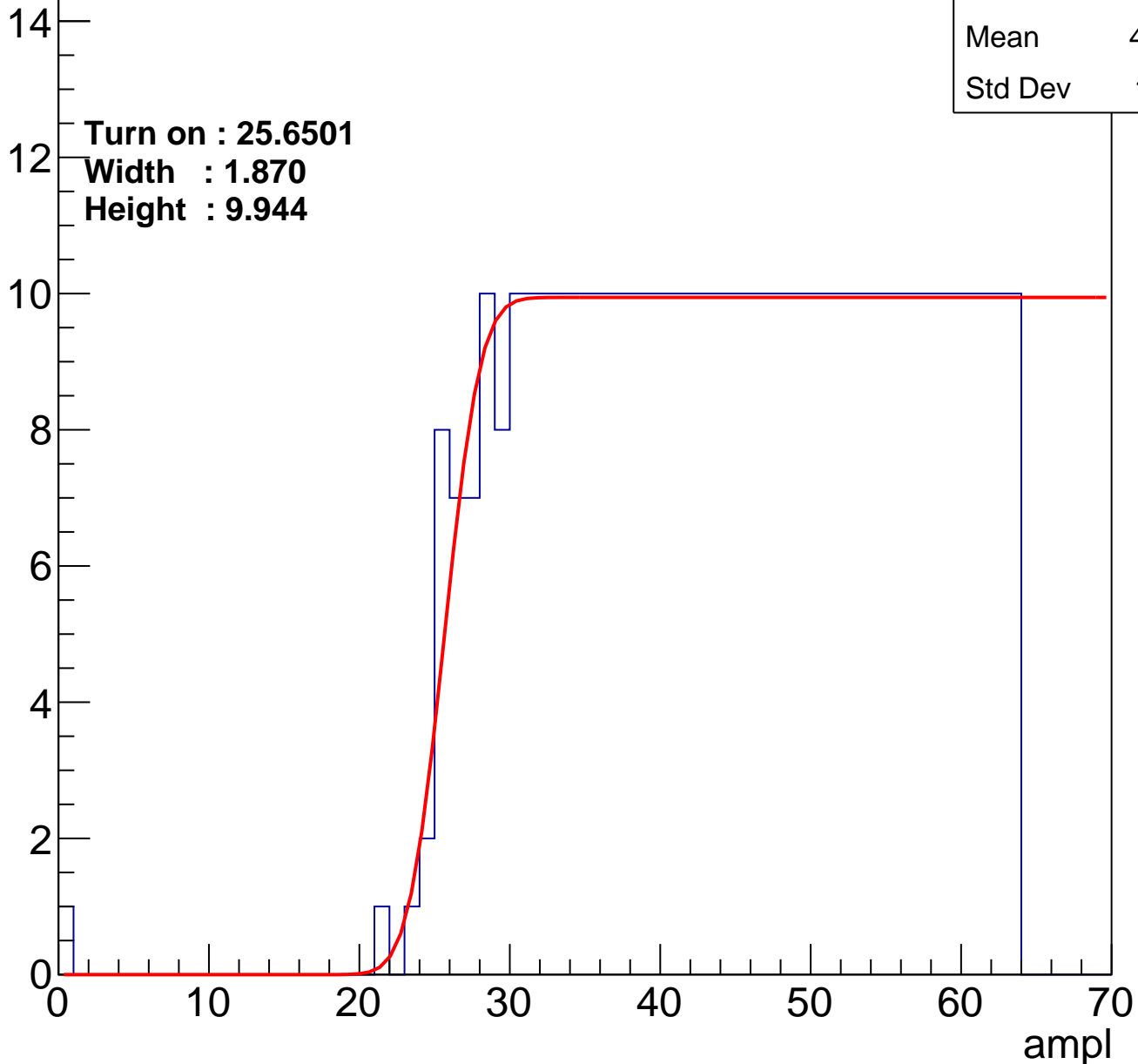
Entries	385
Mean	44.12
Std Dev	11.41

Turn on : 25.6501

Width : 1.870

Height : 9.944

Entry



B1L002S, U18-ch60

calib_packv5_042523_0143.root, FC#10, port B3

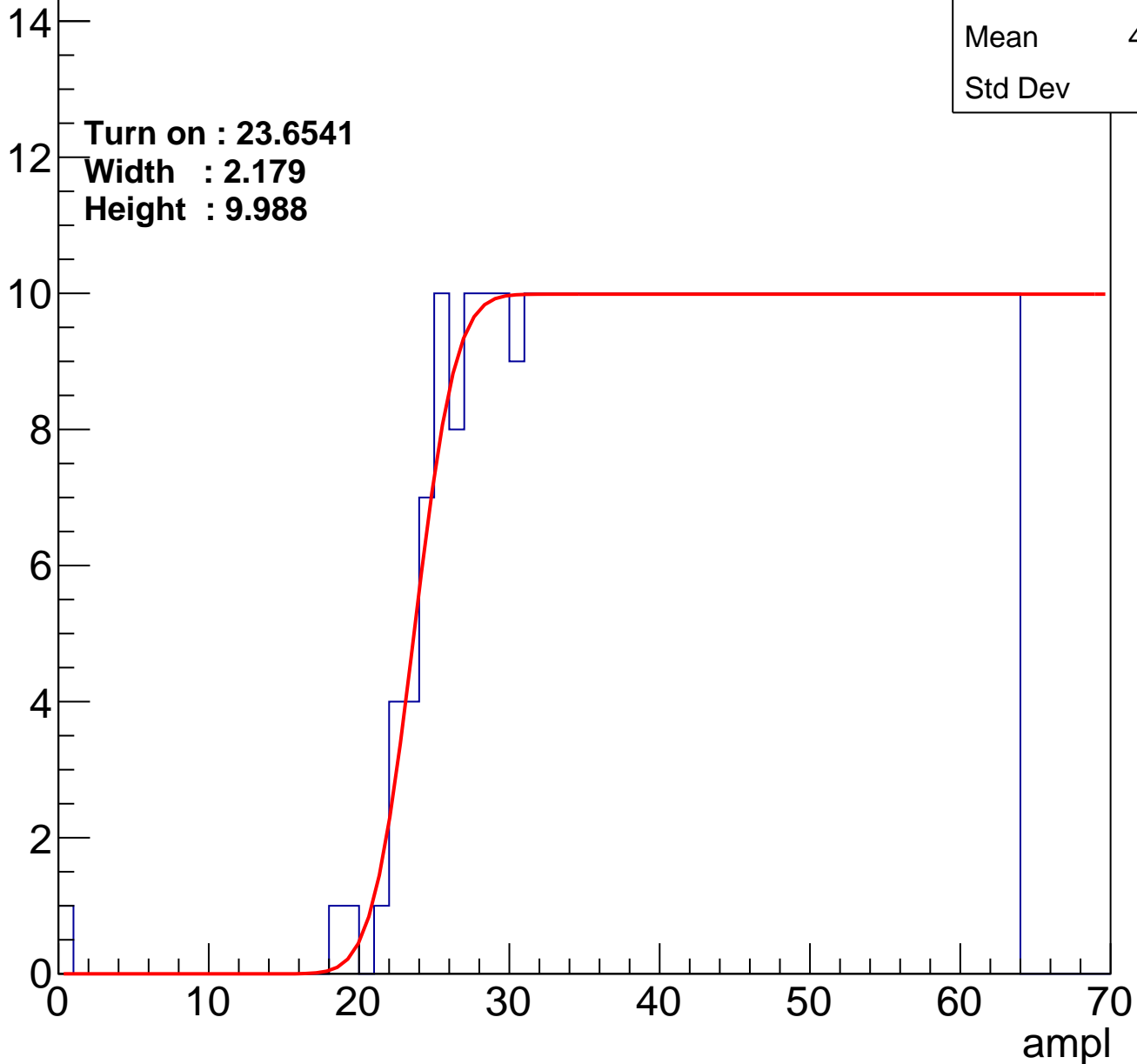
Entries	406
Mean	43.06
Std Dev	12

Turn on : 23.6541

Width : 2.179

Height : 9.988

Entry



B1L002S, U18-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.52
Std Dev	11.74

Turn on : 24.4392

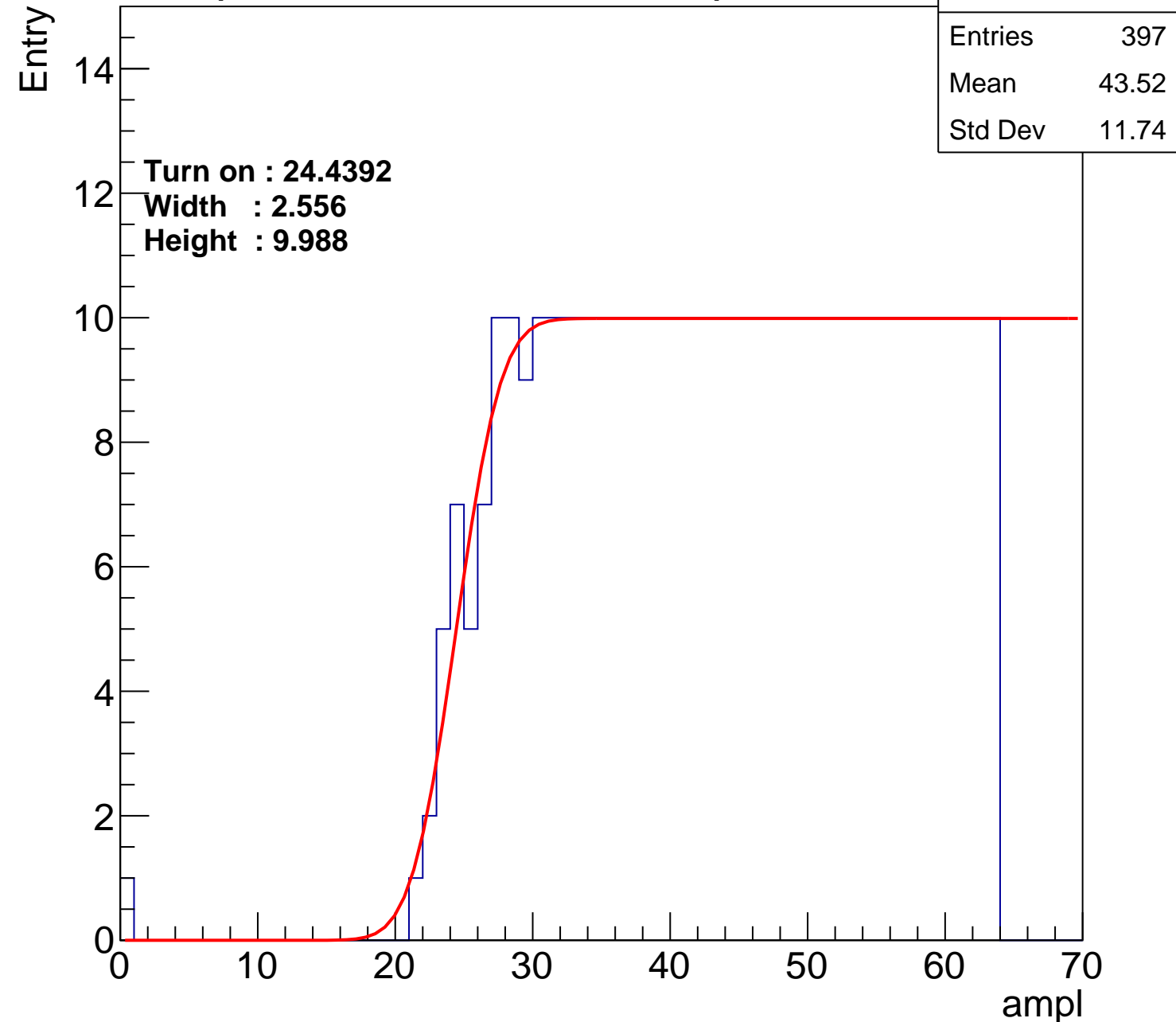
Width : 2.556

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.49
Std Dev	11.9

Turn on : 24.5882

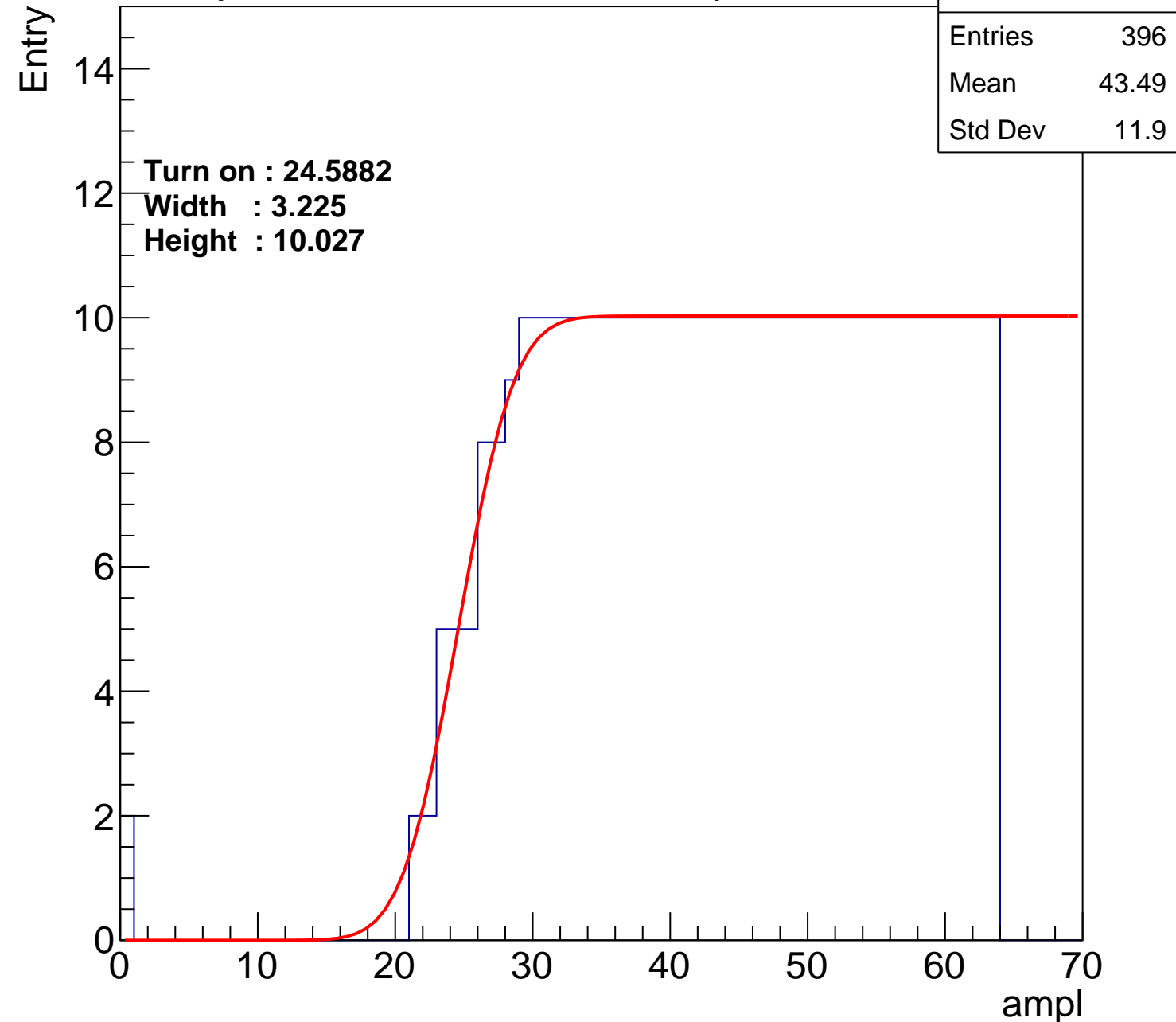
Width : 3.225

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch63

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.67
Std Dev	11.77

Turn on : 24.9845

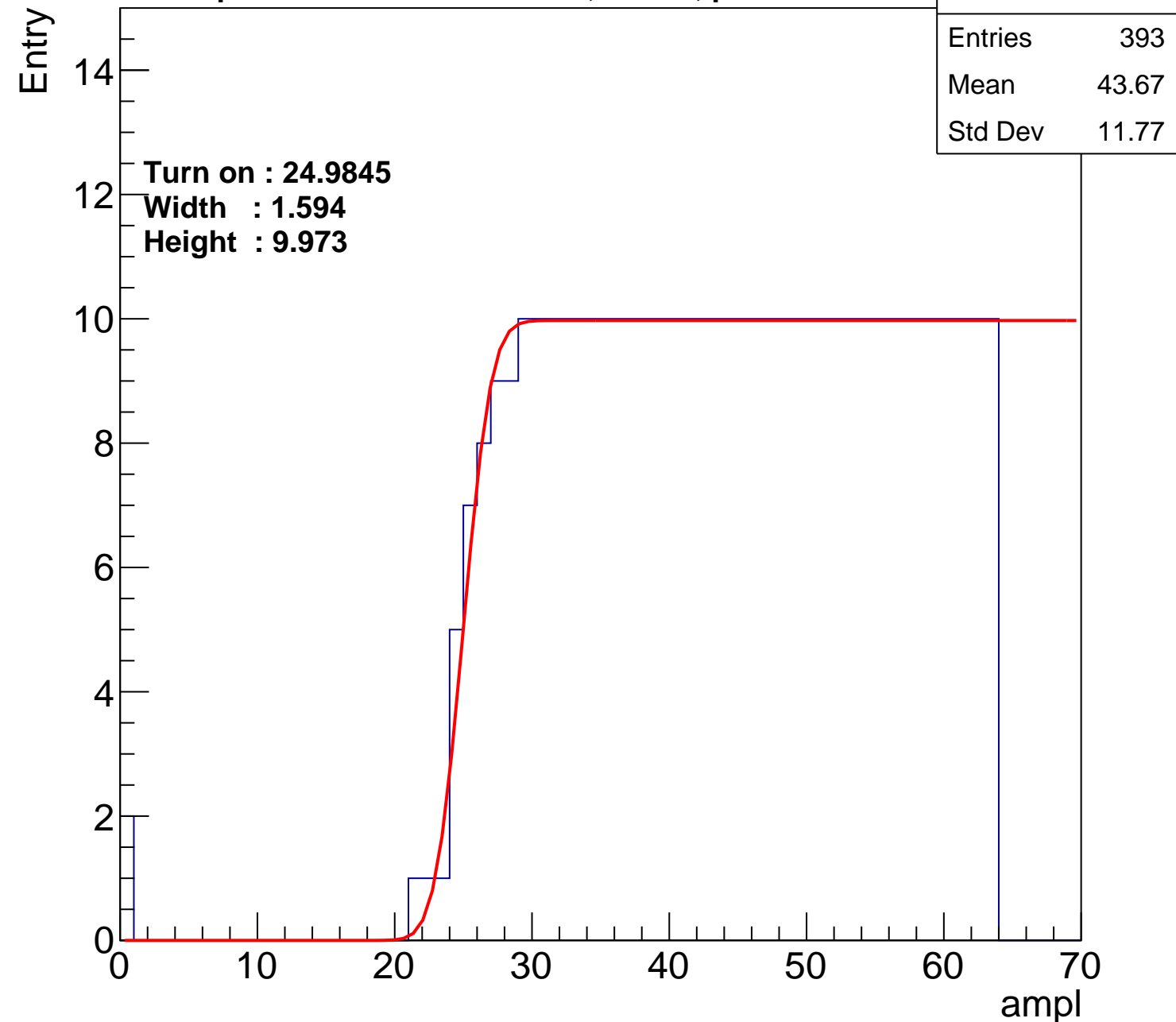
Width : 1.594

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	44.05
Std Dev	11.51

Turn on : 25.7661

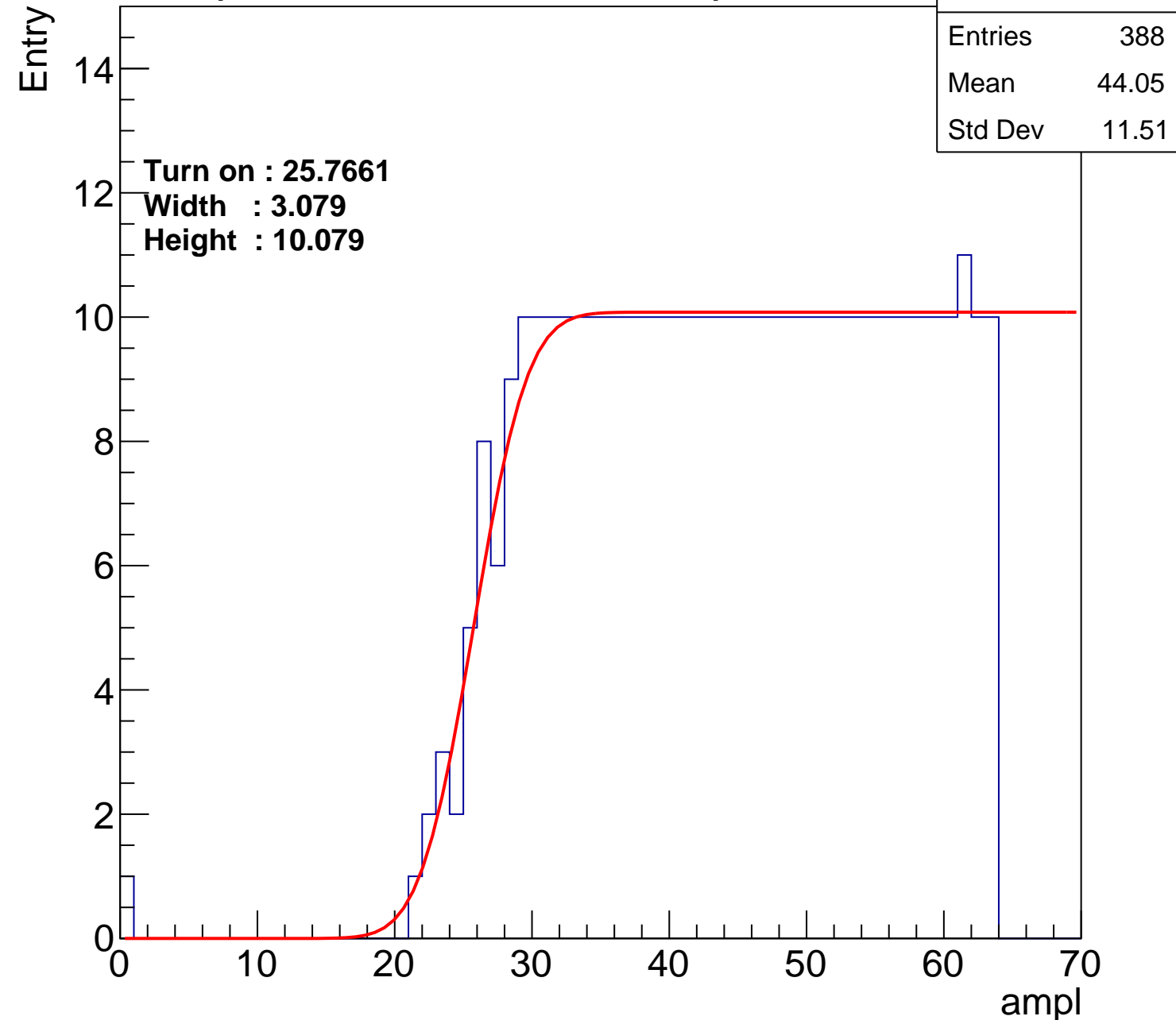
Width : 3.079

Height : 10.079

Entry

14
12
10
8
6
4
2
0

ampl



calib_packv5_042523_0143.root, FC#10, port B3

calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.4160
Width : 2.486
Height : 9.974



B1L002S, U18-ch66

calib_packv5_042523_0143.root, FC#10, port B3

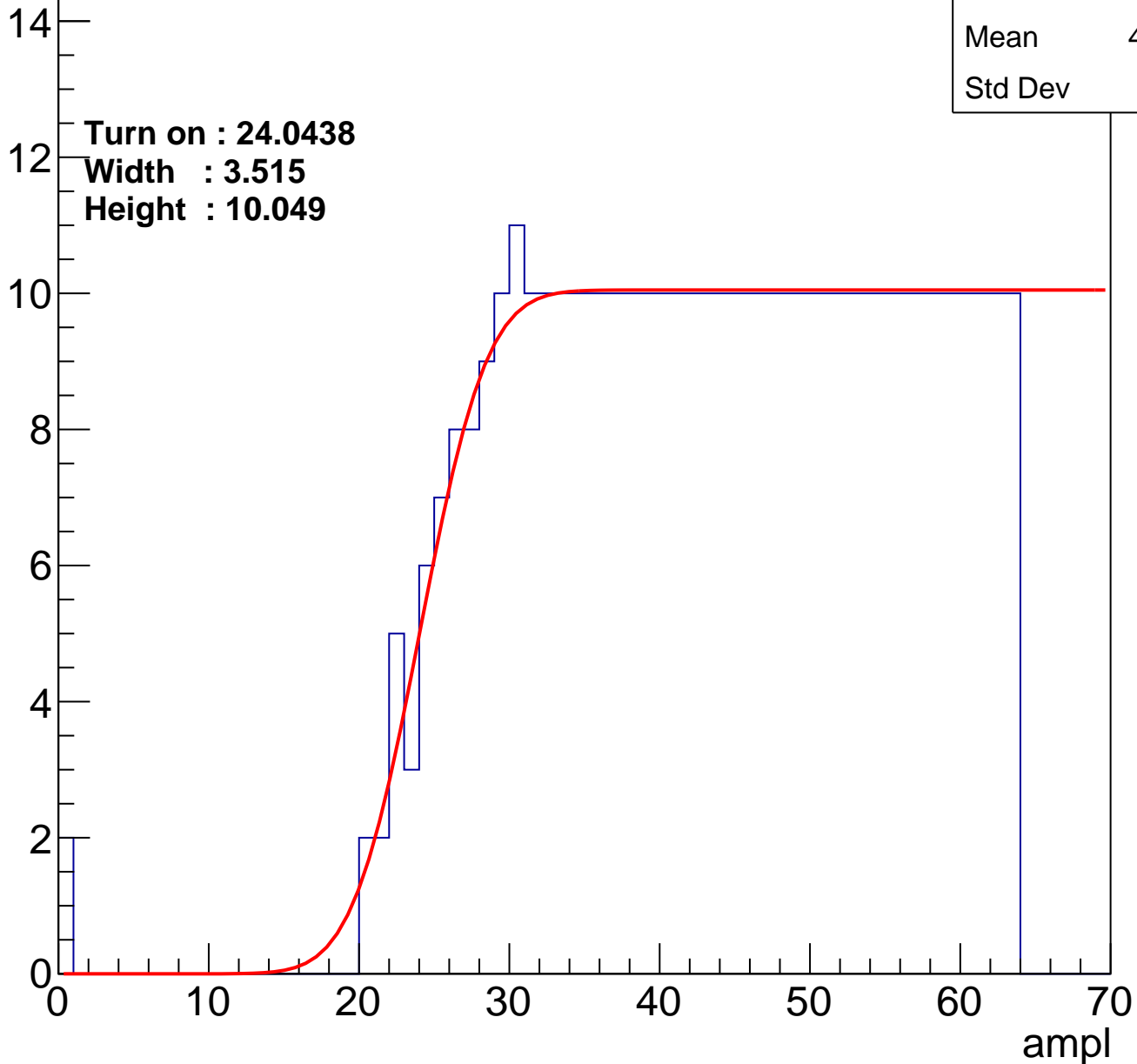
Entries	403
Mean	43.14
Std Dev	12.1

Turn on : 24.0438

Width : 3.515

Height : 10.049

Entry



B1L002S, U18-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.6
Std Dev	12.78

Turn on : 23.8207

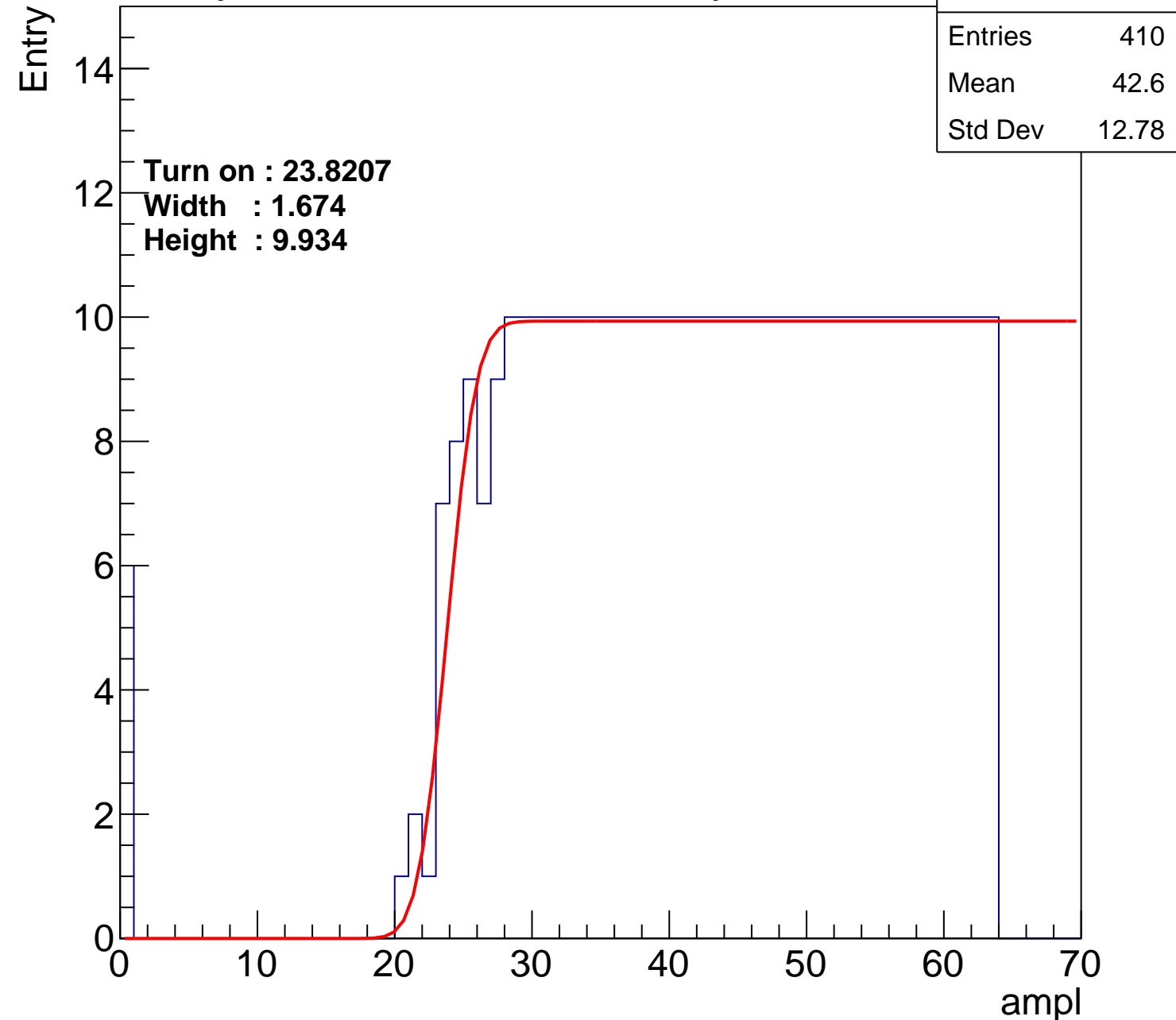
Width : 1.674

Height : 9.934

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch68

calib_packv5_042523_0143.root, FC#10, port B3

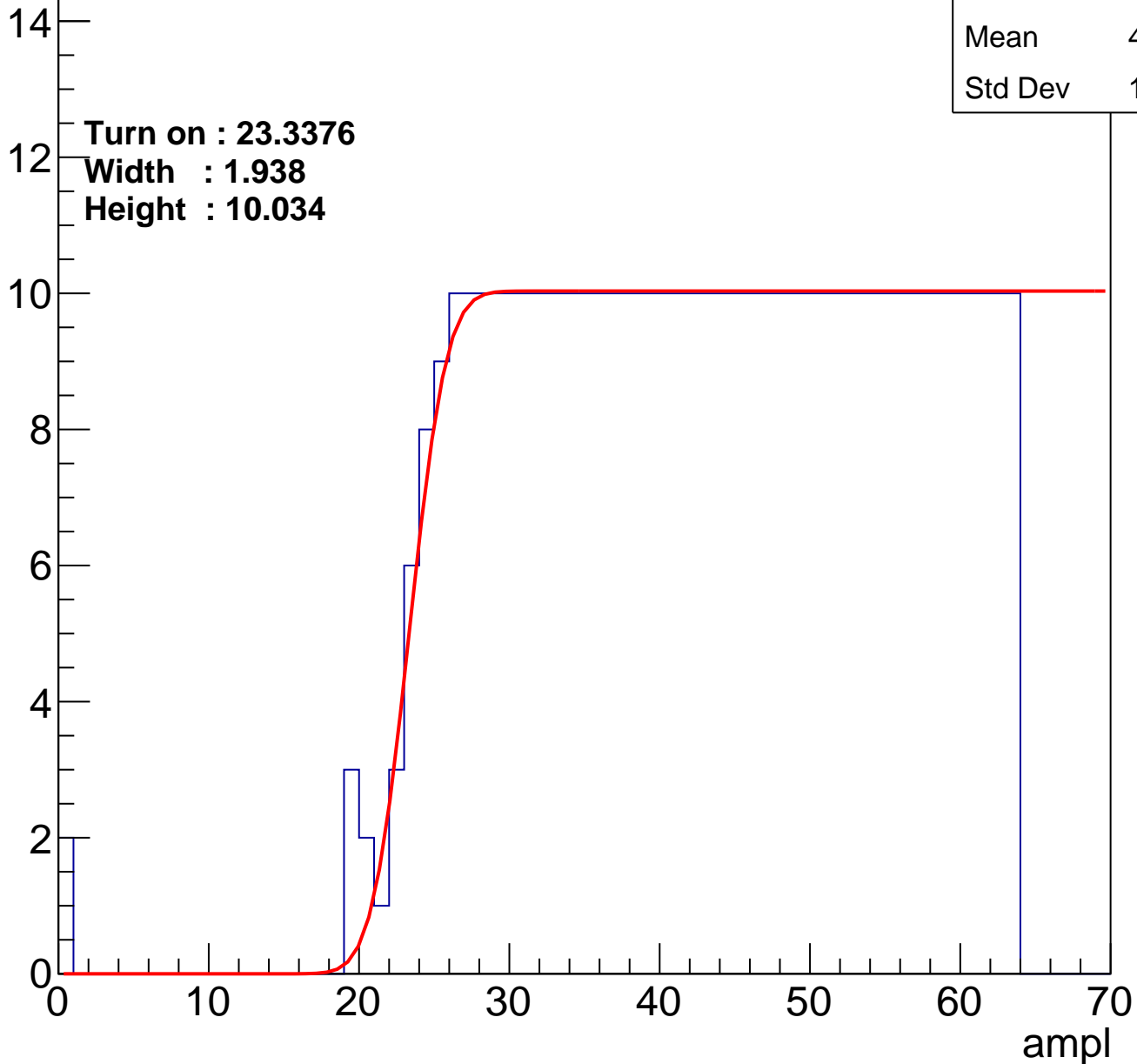
Entries	414
Mean	42.63
Std Dev	12.33

Turn on : 23.3376

Width : 1.938

Height : 10.034

Entry



B1L002S, U18-ch69

calib_packv5_042523_0143.root, FC#10, port B3

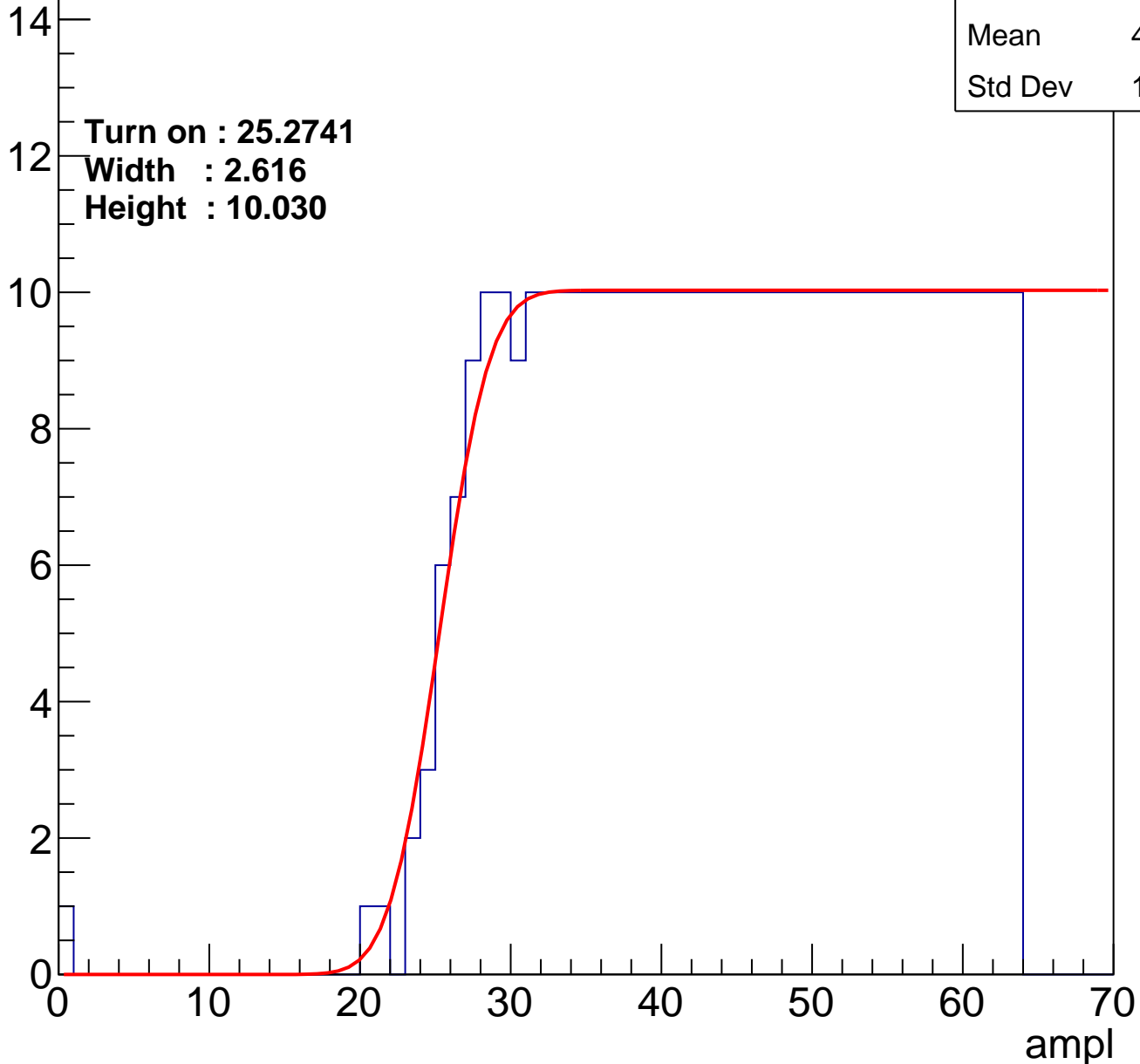
Entries	389
Mean	43.92
Std Dev	11.52

Turn on : 25.2741

Width : 2.616

Height : 10.030

Entry



B1L002S, U18-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.8

Turn on : 24.5049

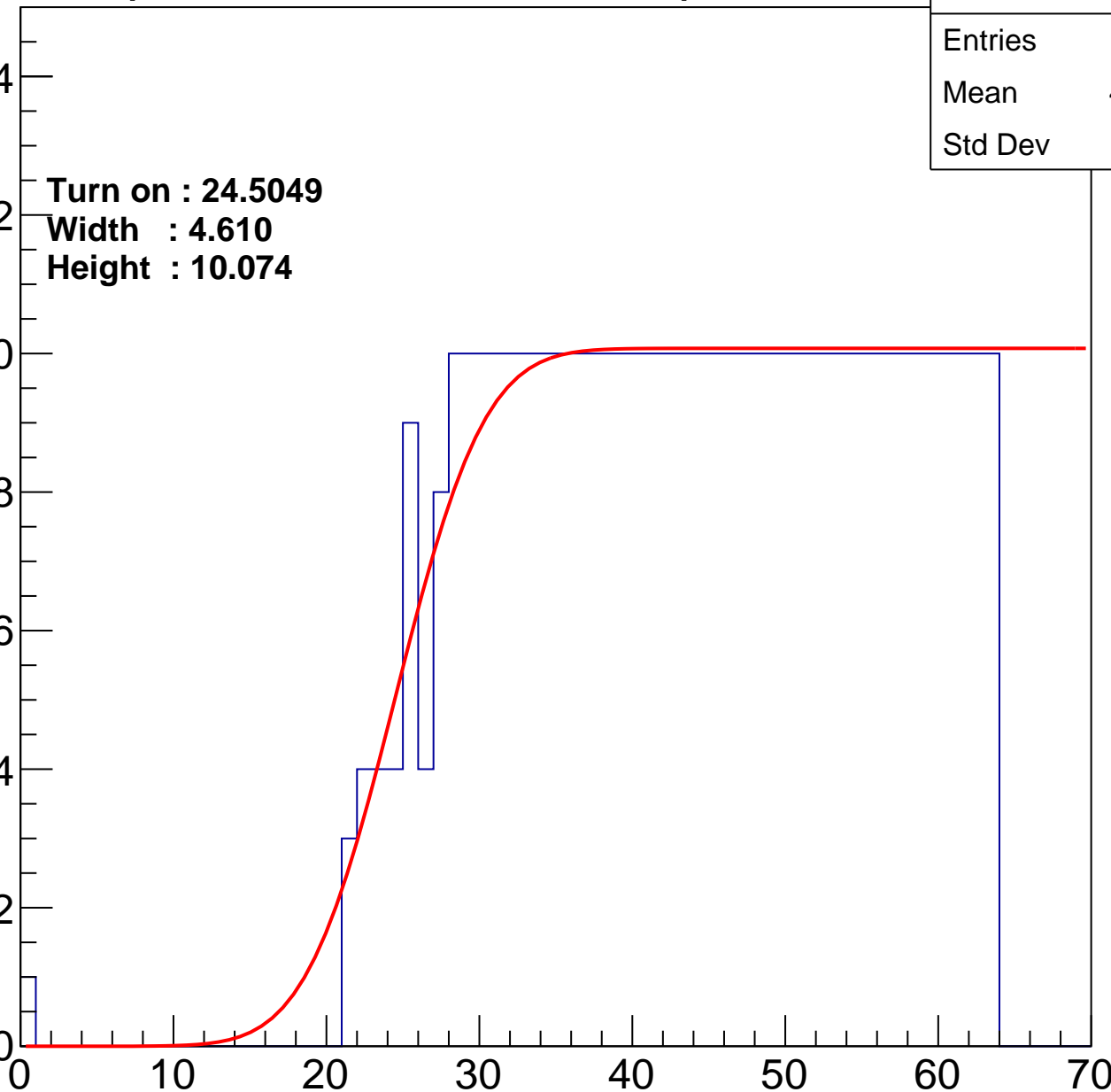
Width : 4.610

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.15
Std Dev	12.46

Turn on : 25.0312

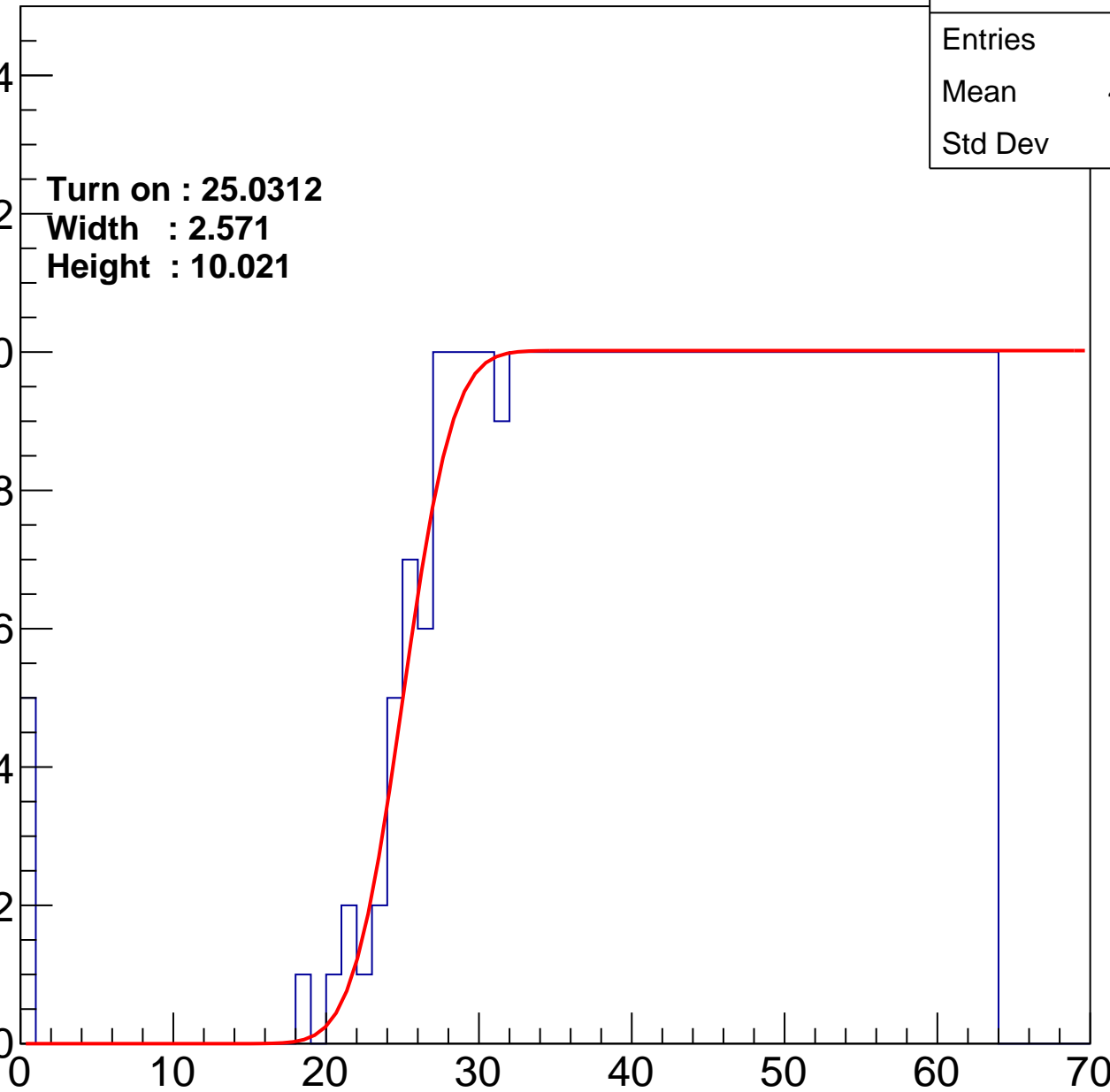
Width : 2.571

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch72

calib_packv5_042523_0143.root, FC#10, port B3

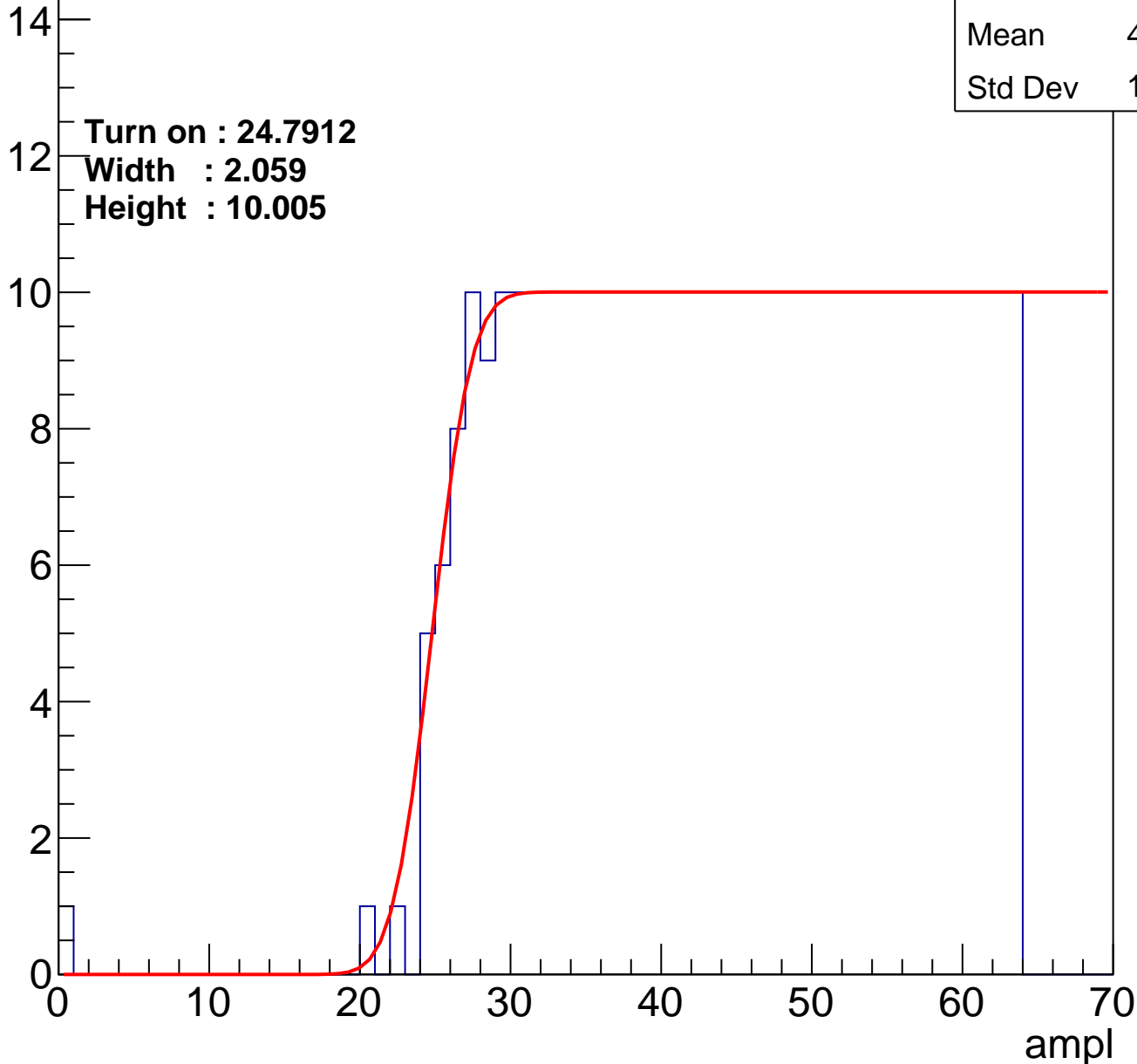
Entries	391
Mean	43.84
Std Dev	11.54

Turn on : 24.7912

Width : 2.059

Height : 10.005

Entry



B1L002S, U18-ch73

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.01
Std Dev	12.41

Turn on : 24.2409

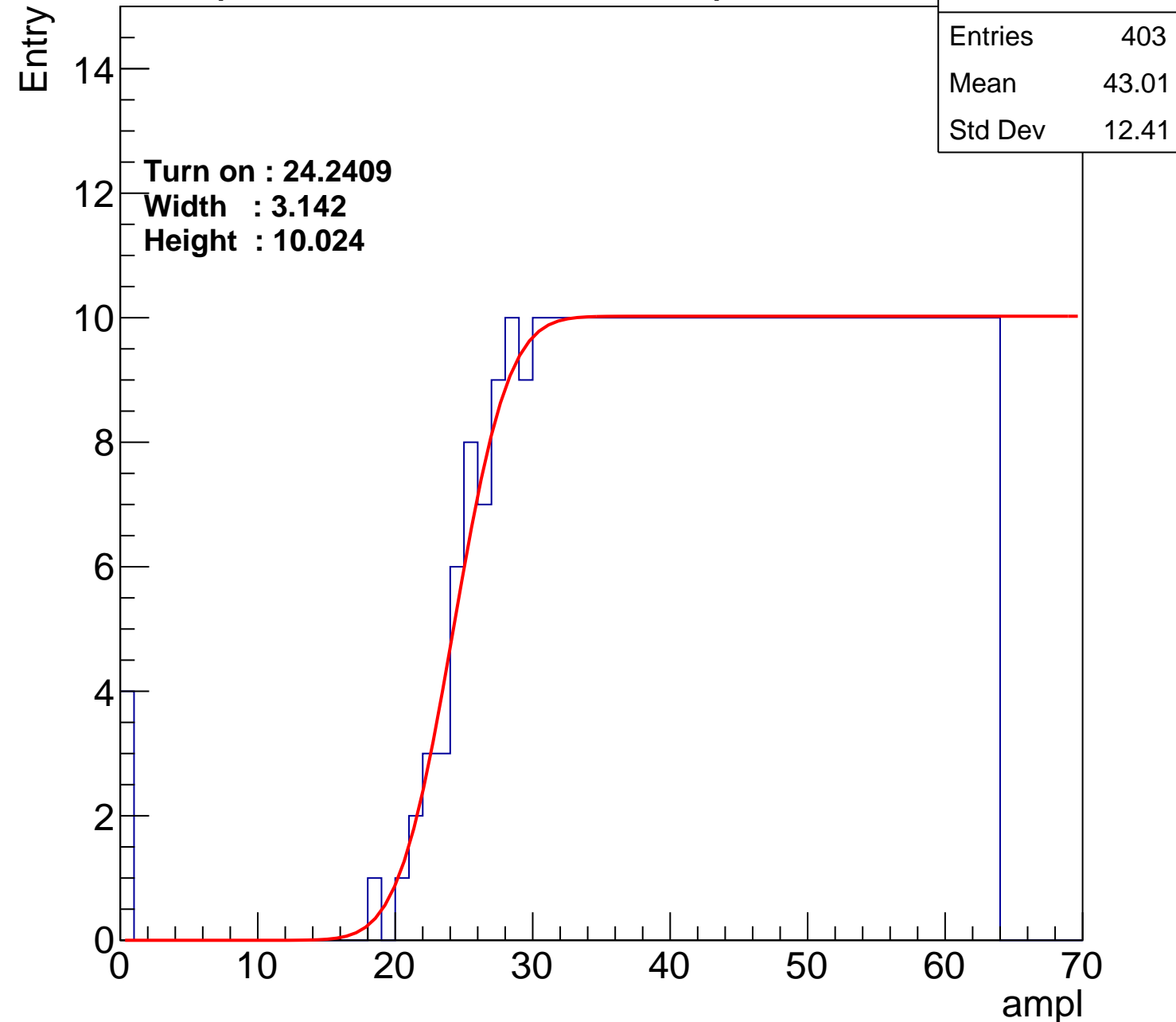
Width : 3.142

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch74

calib_packv5_042523_0143.root, FC#10, port B3

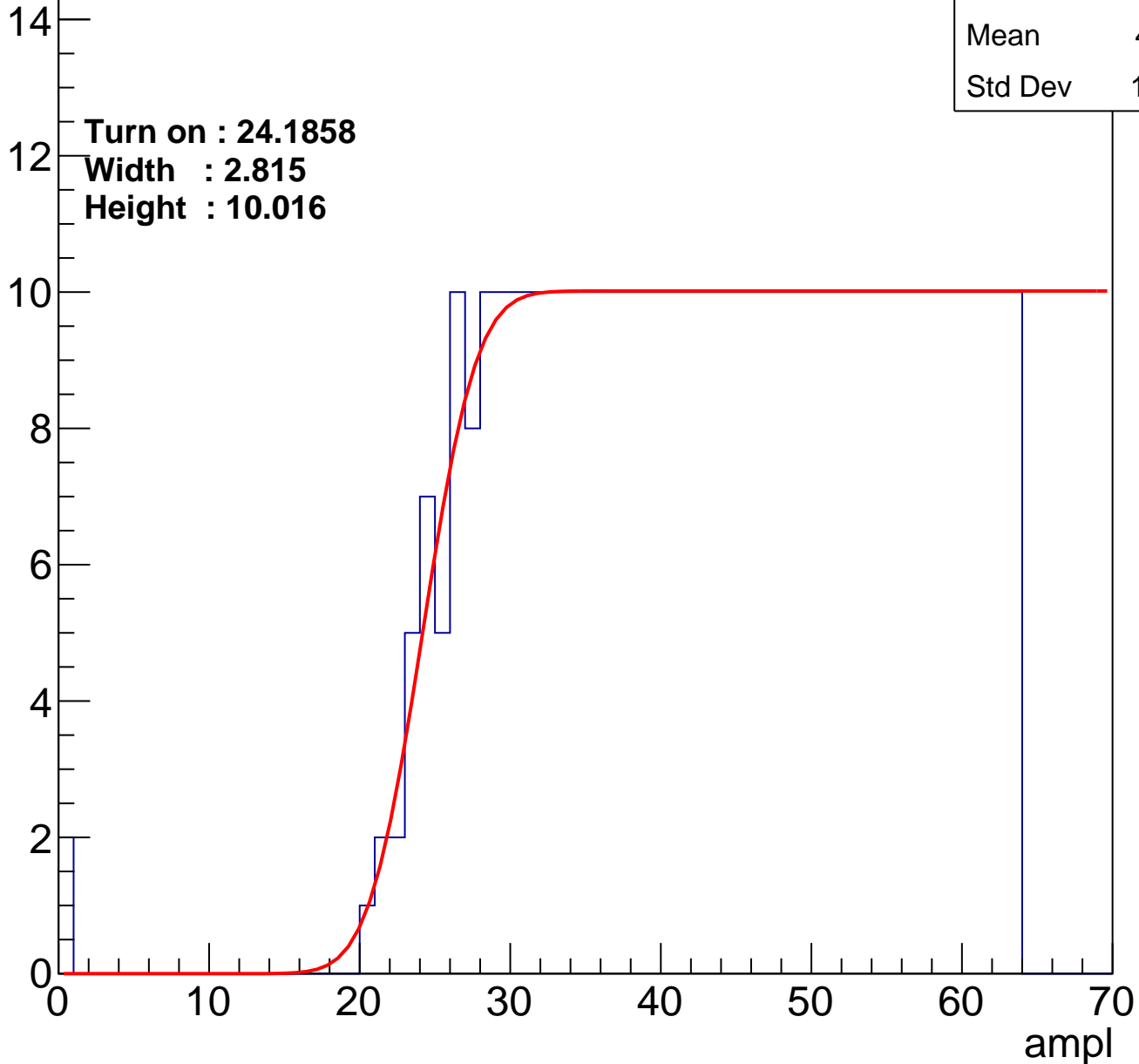
Entries	402
Mean	43.21
Std Dev	12.04

Turn on : 24.1858

Width : 2.815

Height : 10.016

Entry



B1L002S, U18-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.96
Std Dev	11.86

Turn on : 26.6116

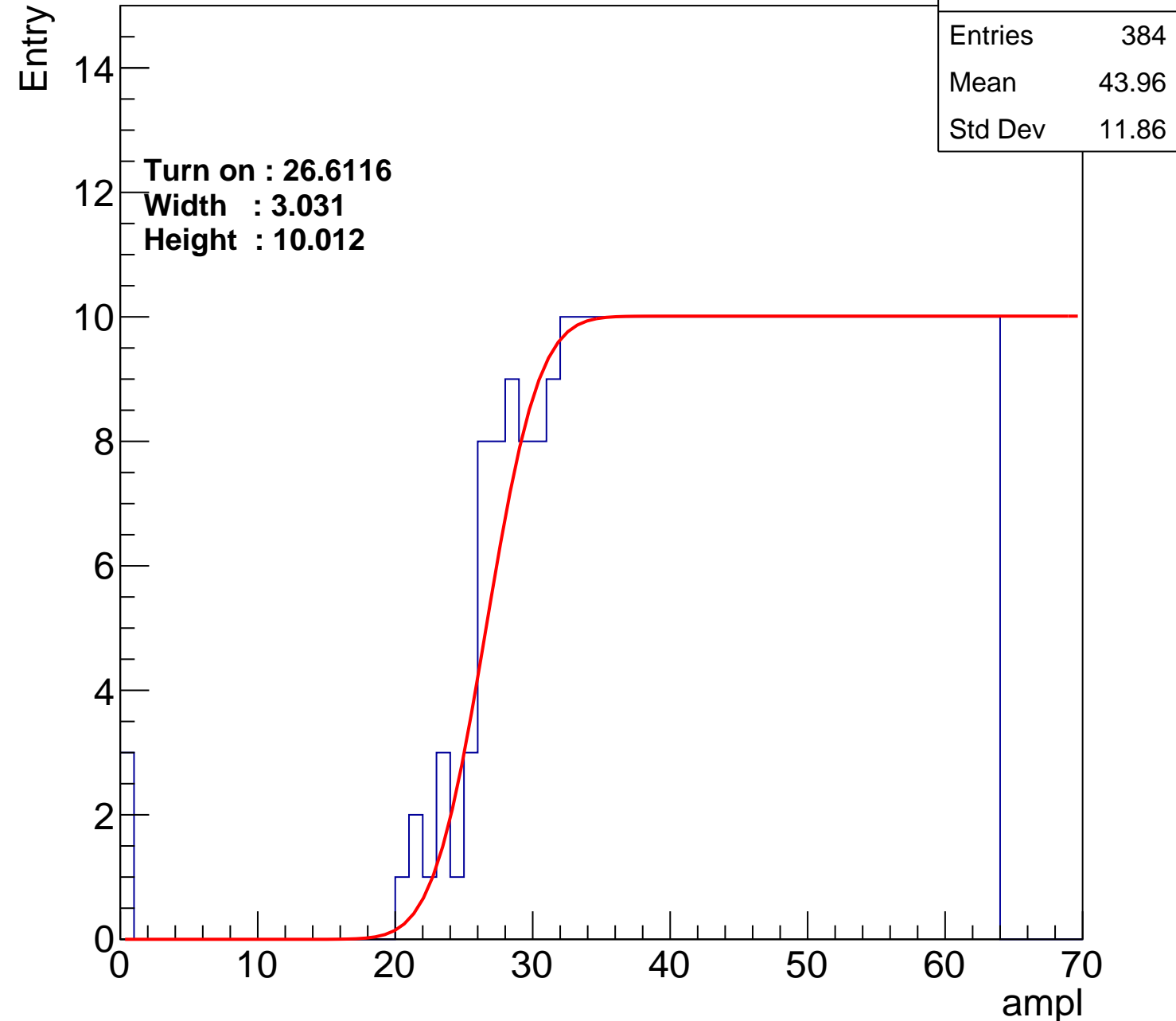
Width : 3.031

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch76

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.79
Std Dev	12.42

Turn on : 23.5528

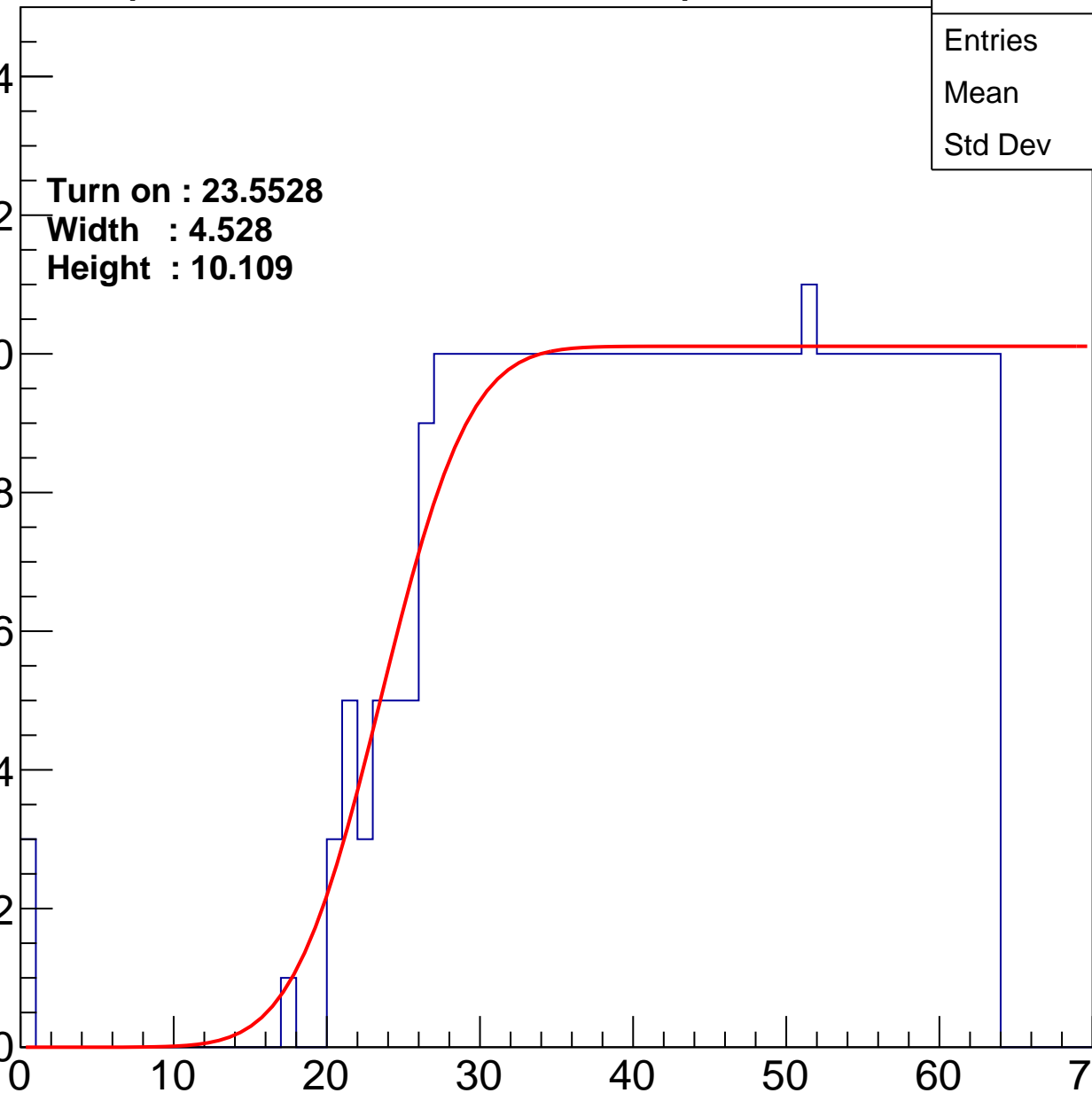
Width : 4.528

Height : 10.109

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch77

calib_packv5_042523_0143.root, FC#10, port B3

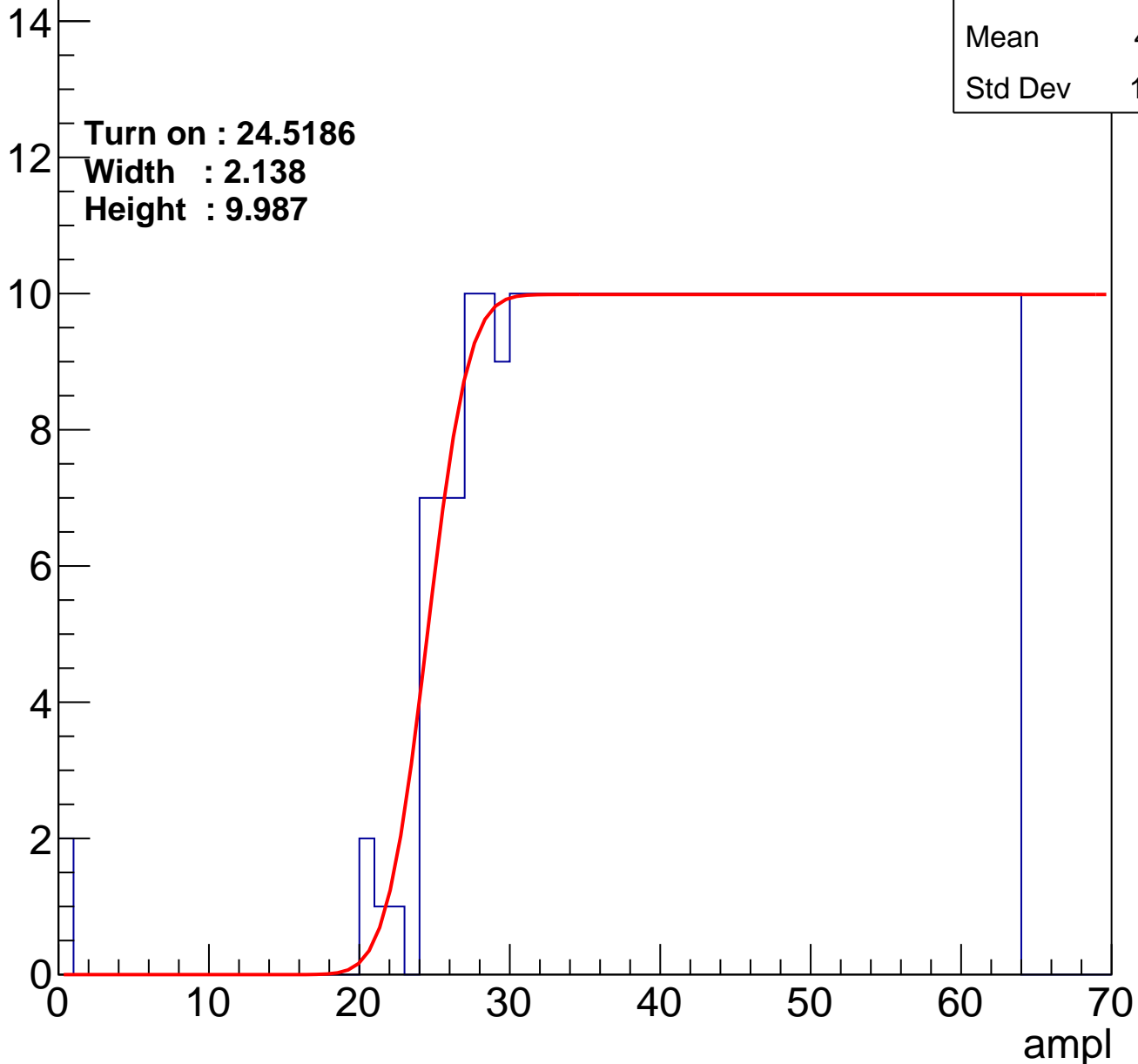
Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.5186

Width : 2.138

Height : 9.987

Entry



B1L002S, U18-ch78

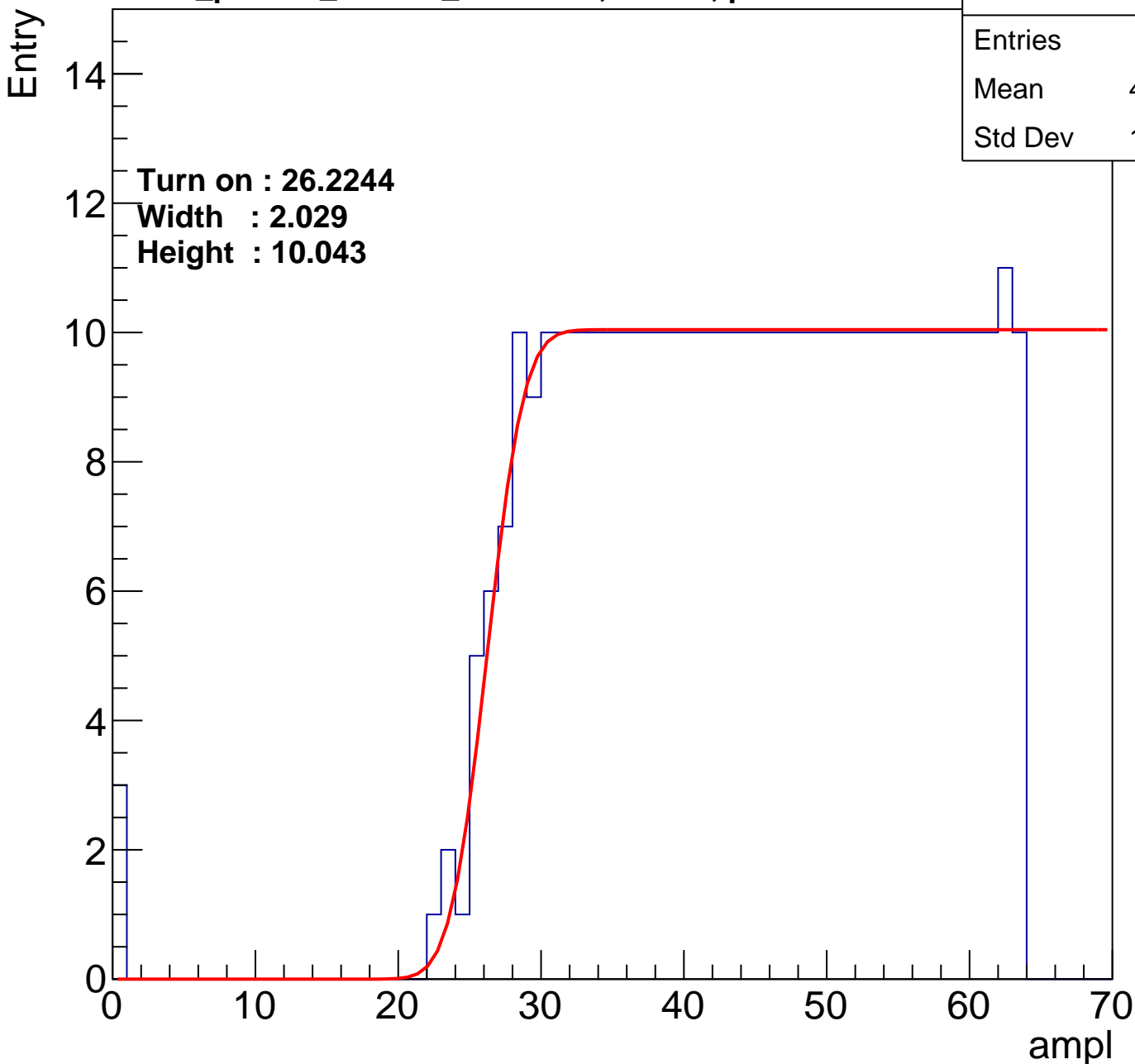
calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.09
Std Dev	11.74

Turn on : 26.2244

Width : 2.029

Height : 10.043



B1L002S, U18-ch79

calib_packv5_042523_0143.root, FC#10, port B3

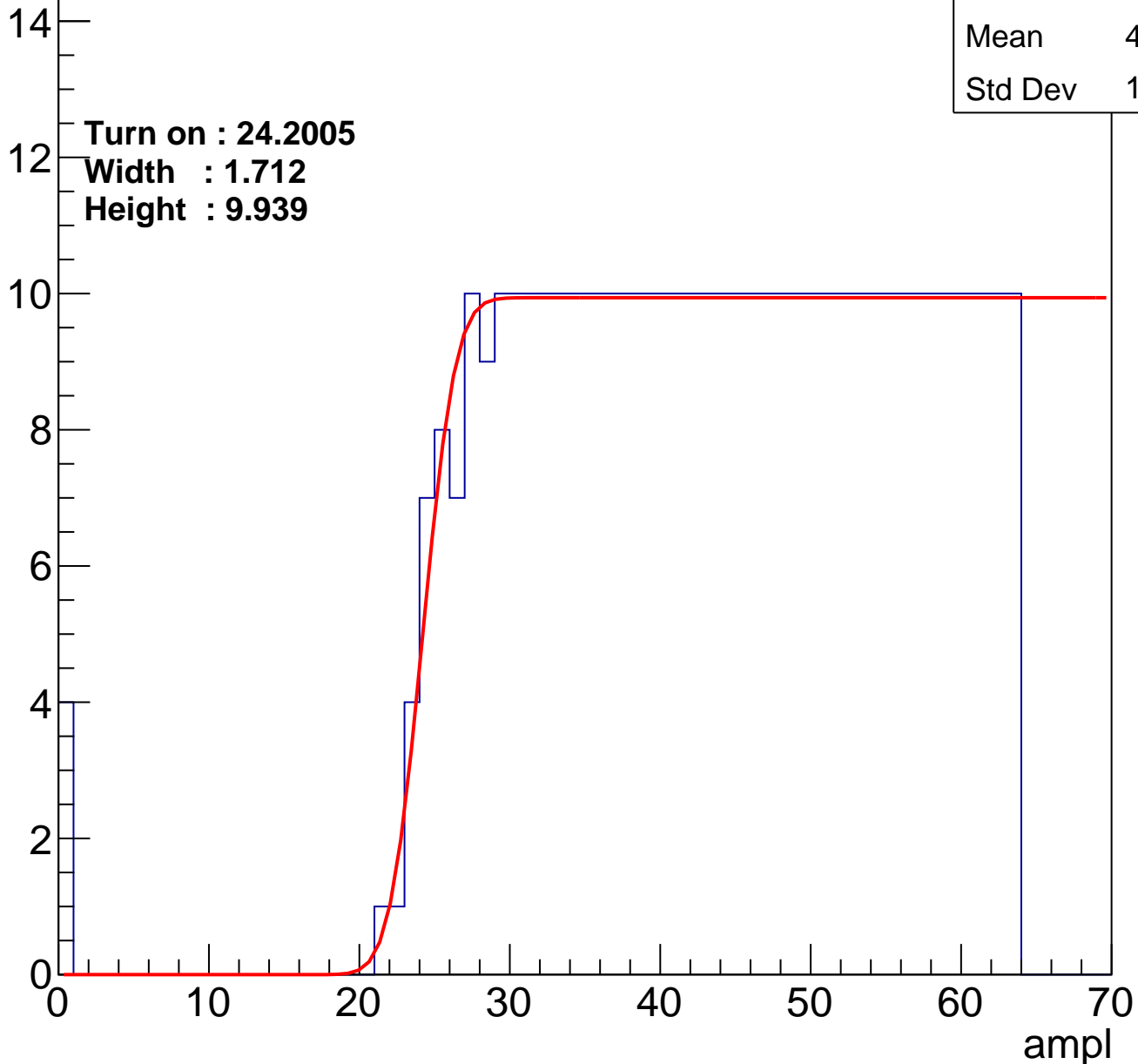
Entries	401
Mean	43.16
Std Dev	12.28

Turn on : 24.2005

Width : 1.712

Height : 9.939

Entry



B1L002S, U18-ch80

calib_packv5_042523_0143.root, FC#10, port B3

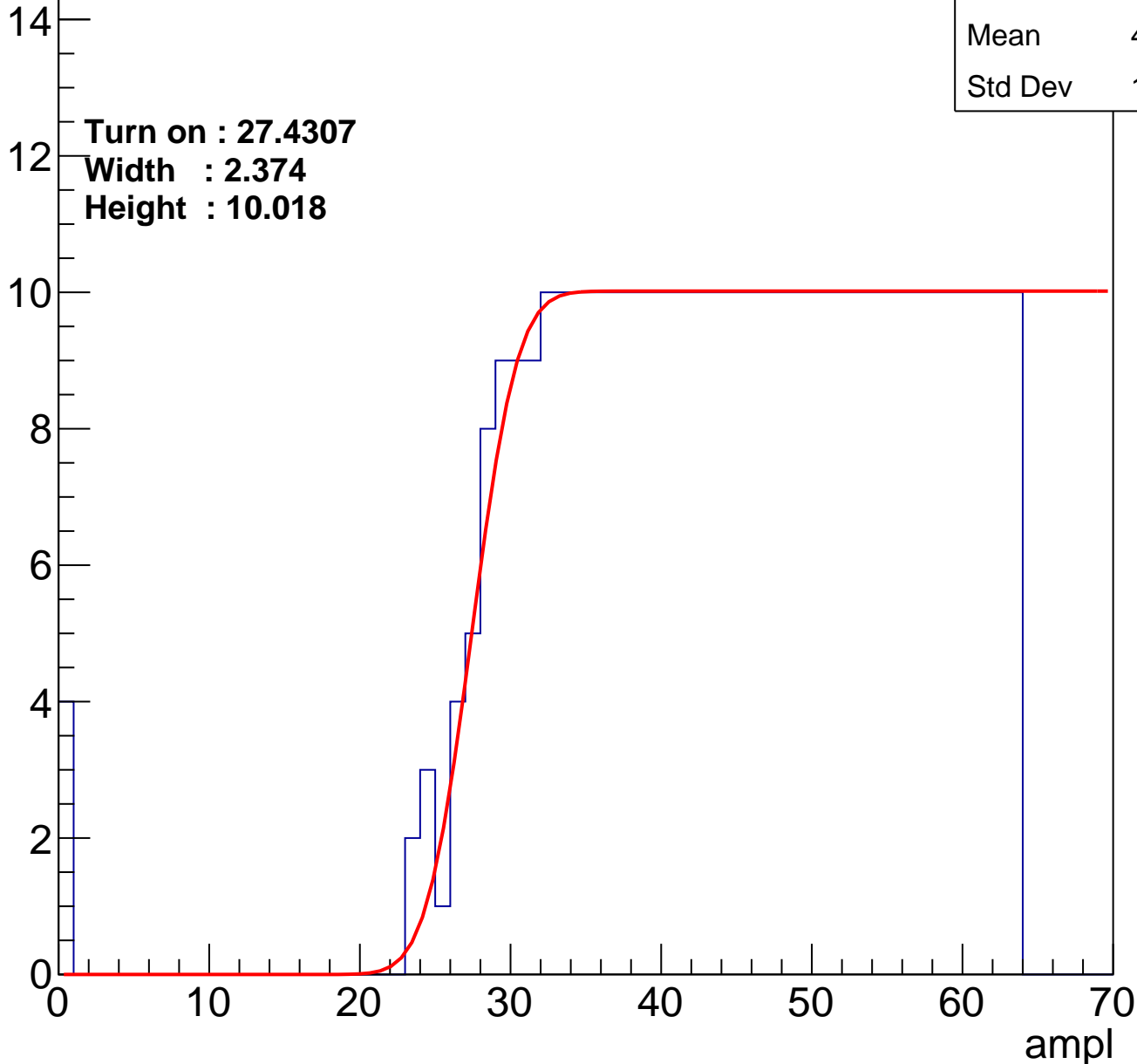
Entries	374
Mean	44.43
Std Dev	11.73

Turn on : 27.4307

Width : 2.374

Height : 10.018

Entry



B1L002S, U18-ch81

calib_packv5_042523_0143.root, FC#10, port B3

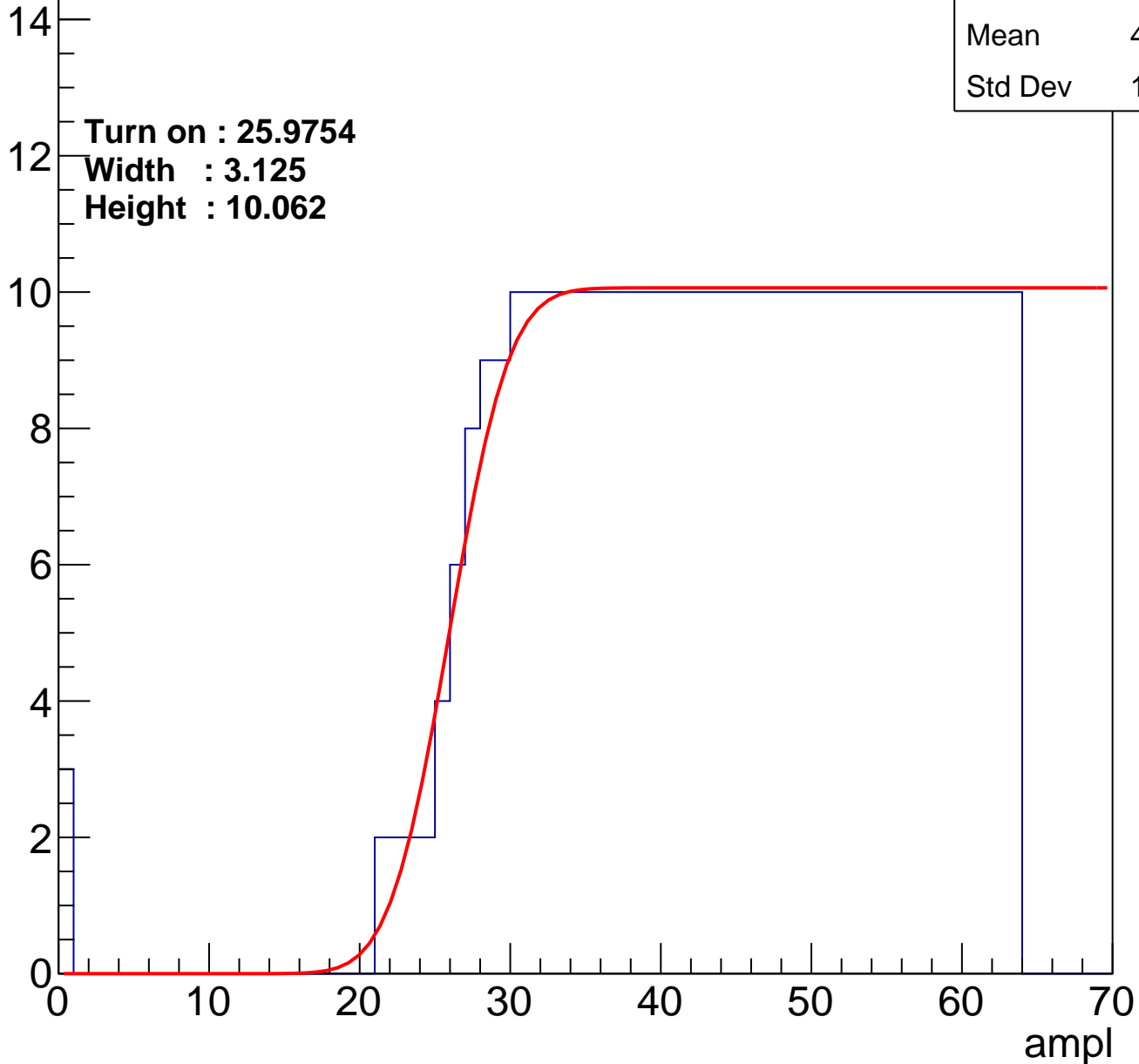
Entries	387
Mean	43.86
Std Dev	11.85

Turn on : 25.9754

Width : 3.125

Height : 10.062

Entry



B1L002S, U18-ch82

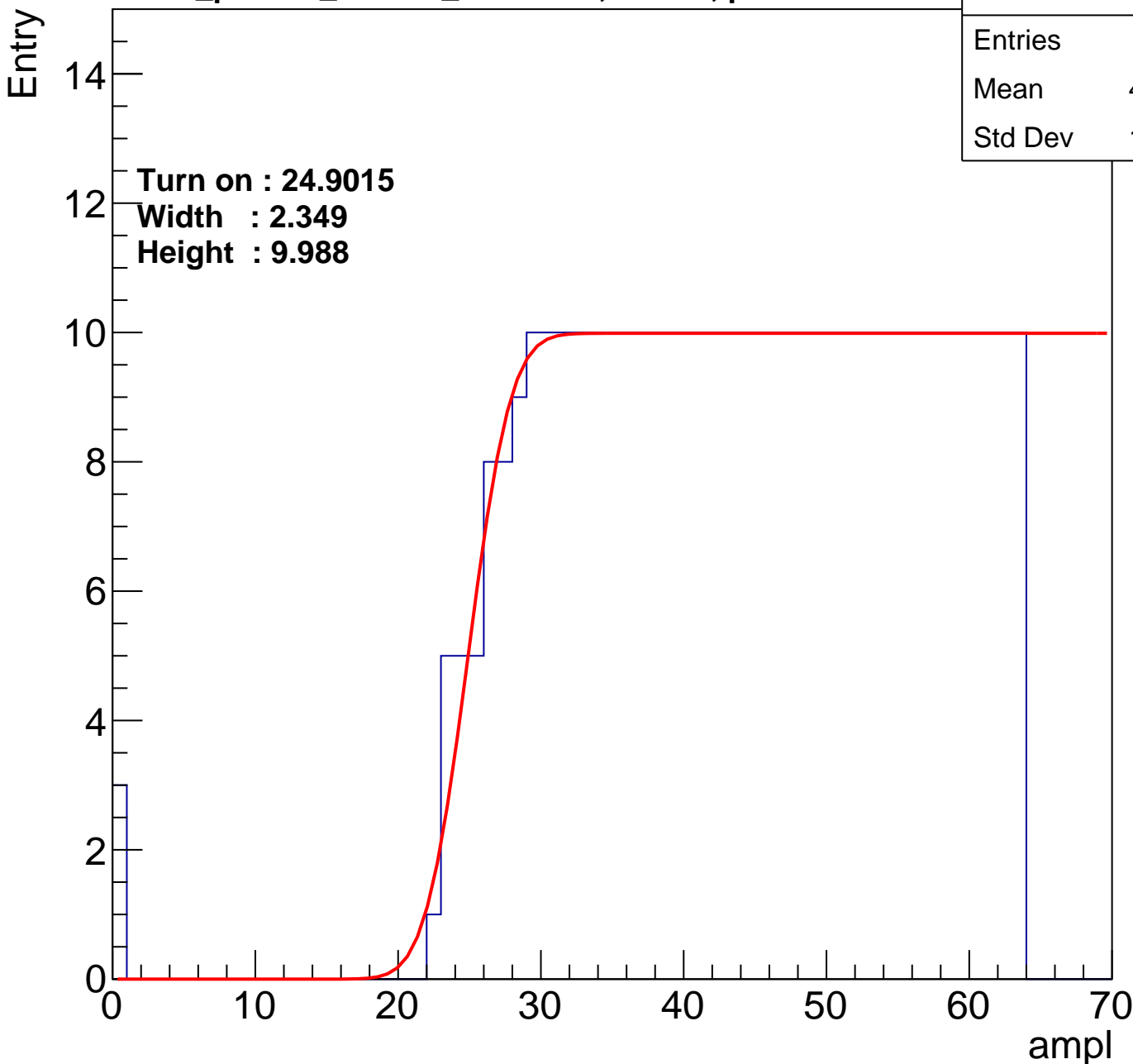
calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.55
Std Dev	11.98

Turn on : 24.9015

Width : 2.349

Height : 9.988



B1L002S, U18-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.32

Turn on : 27.0972

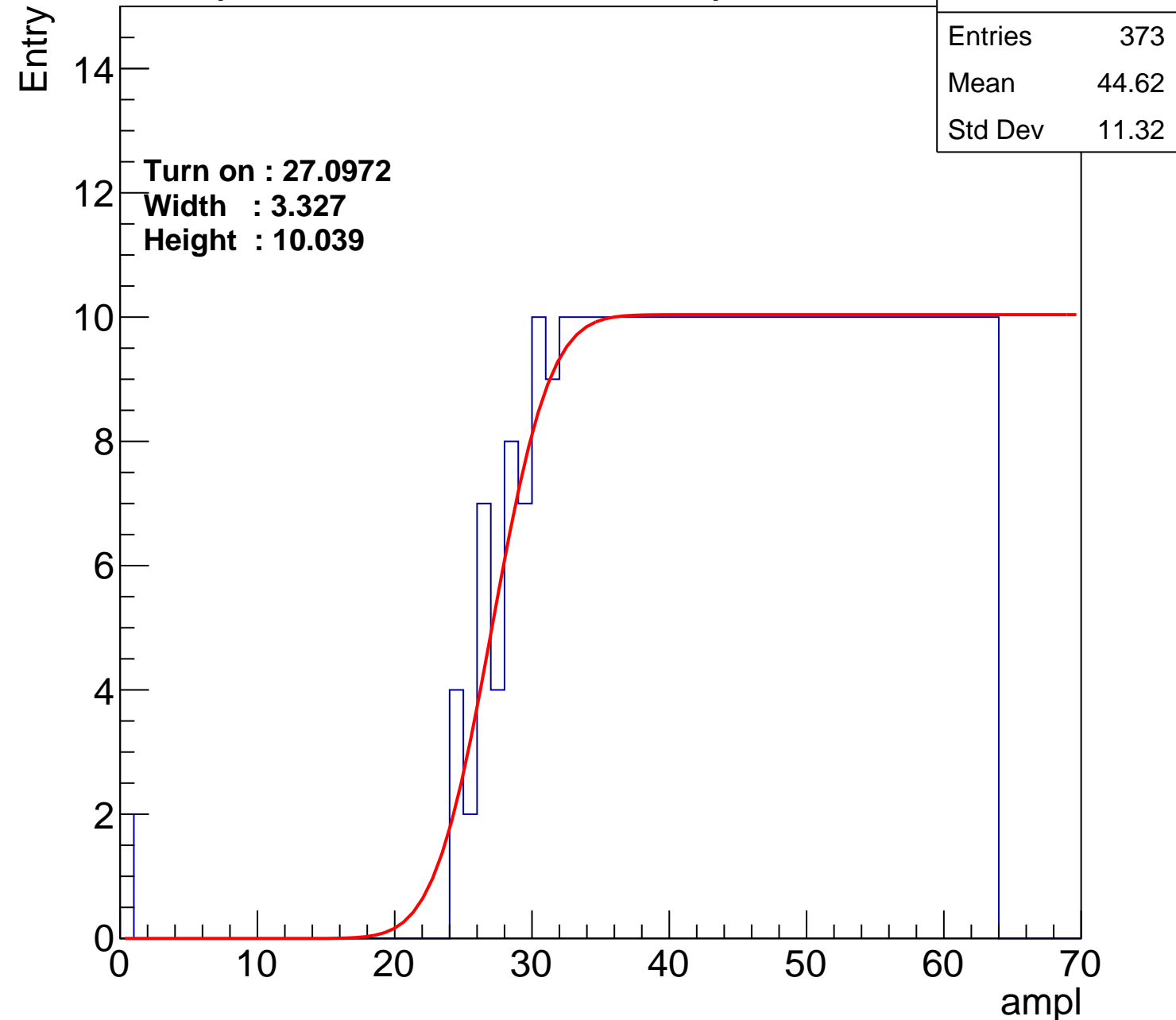
Width : 3.327

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.48
Std Dev	12.06

Turn on : 25.2118

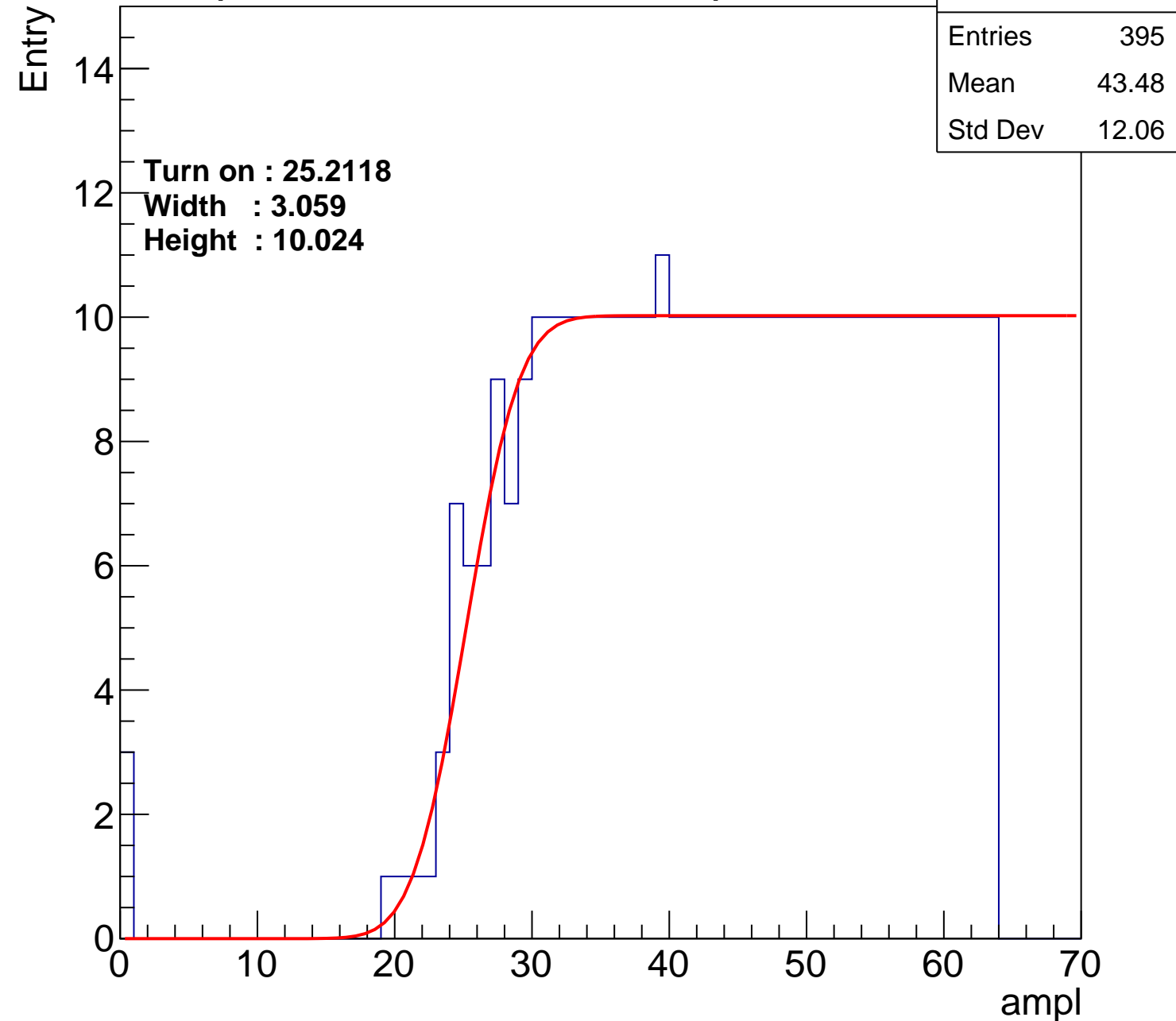
Width : 3.059

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	382
Mean	44.21
Std Dev	11.42

Turn on : 26.0305

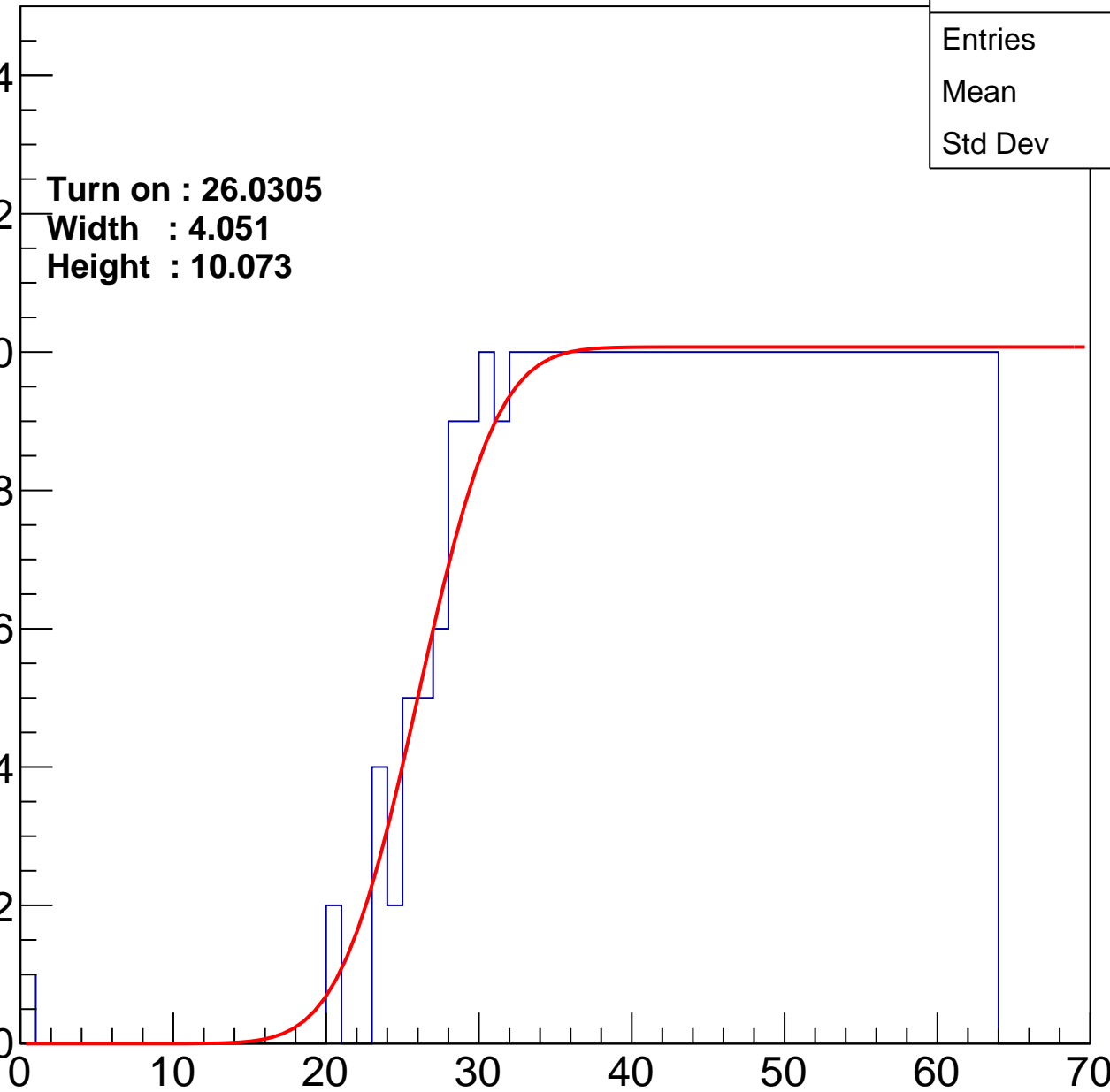
Width : 4.051

Height : 10.073

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	410
Mean	42.83
Std Dev	12.22

Turn on : 23.3965

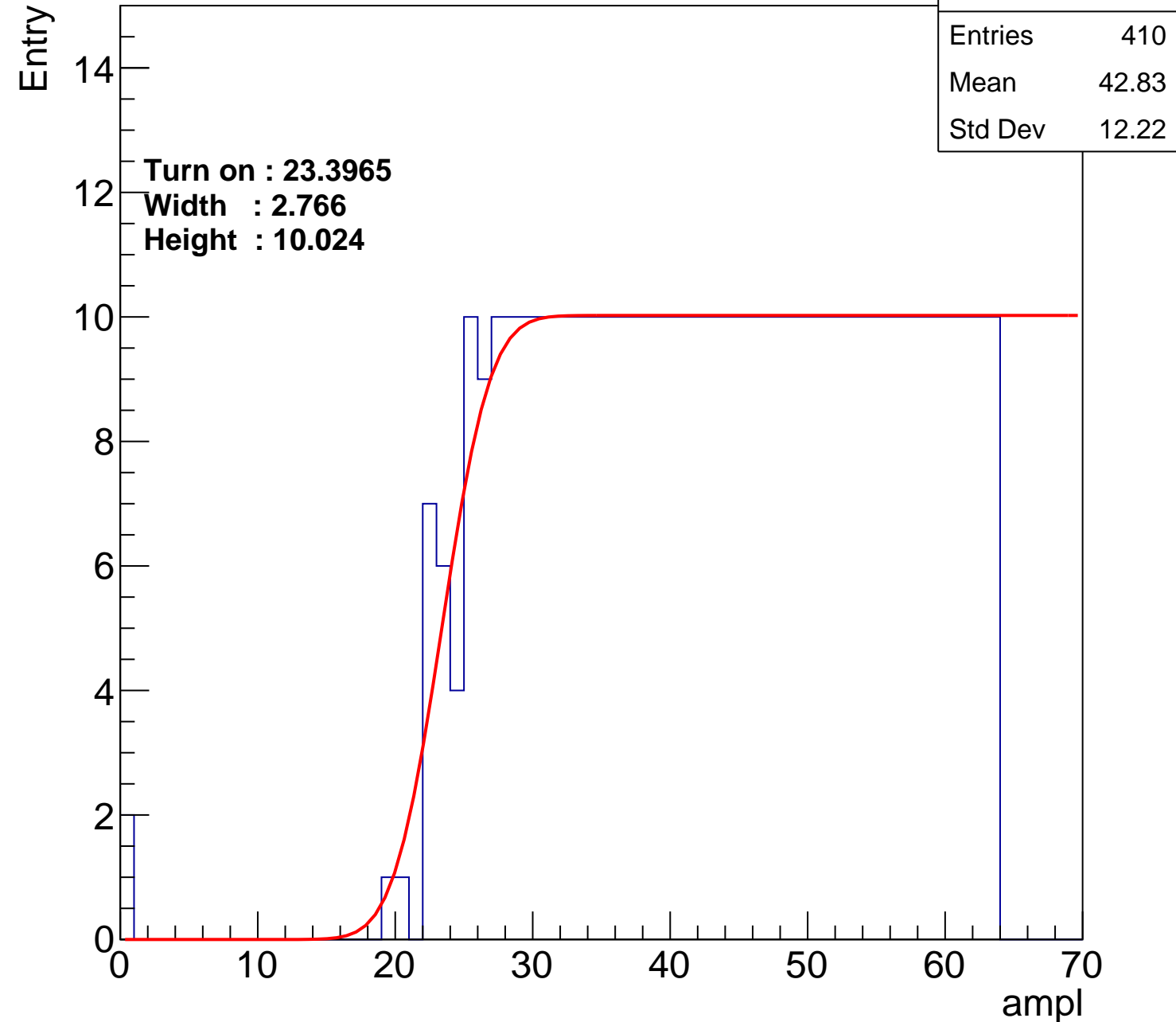
Width : 2.766

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch87

calib_packv5_042523_0143.root, FC#10, port B3

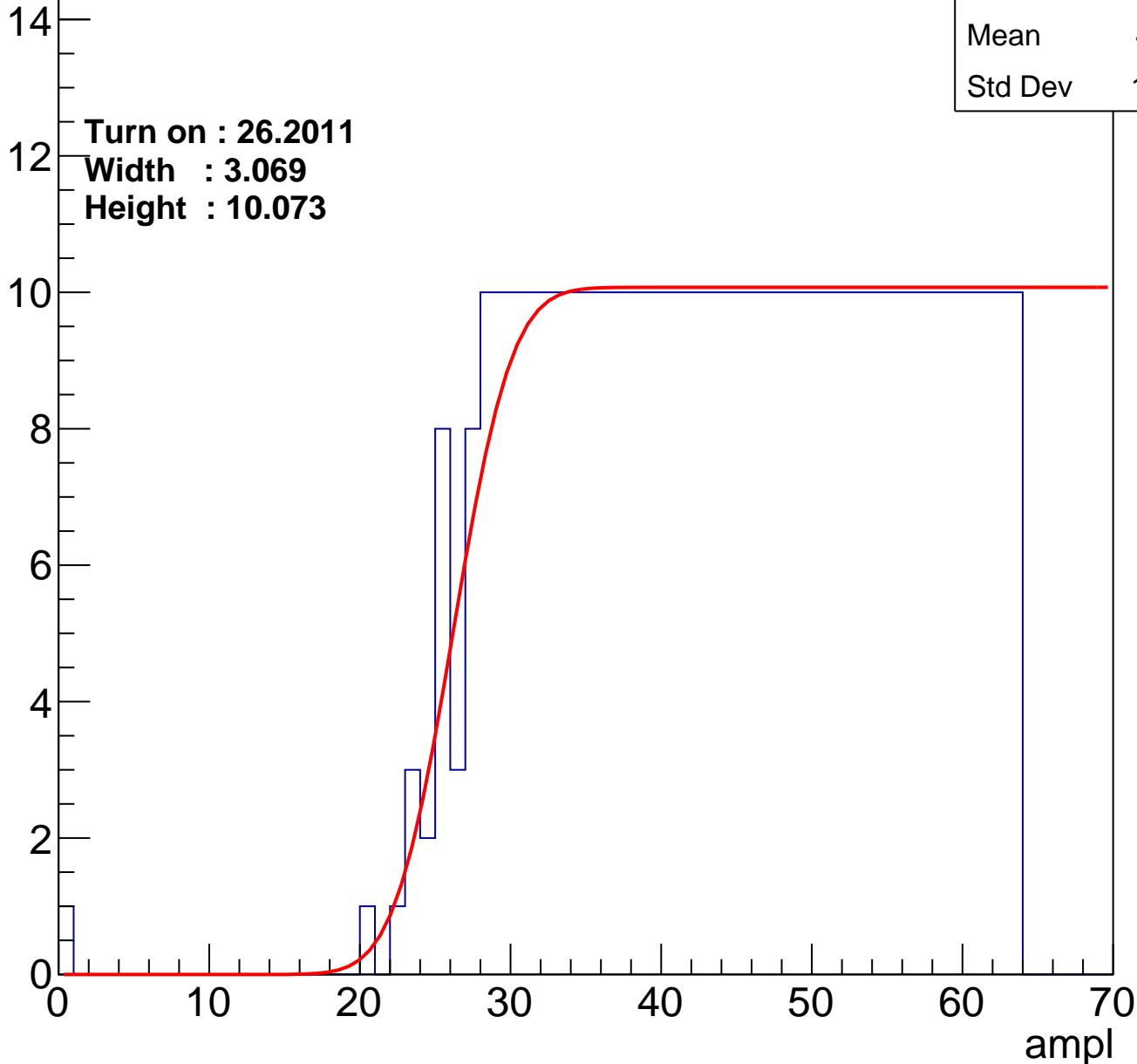
Entries	387
Mean	44.01
Std Dev	11.47

Turn on : 26.2011

Width : 3.069

Height : 10.073

Entry



B1L002S, U18-ch88

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.6
Std Dev	11.71

Turn on : 24.1586

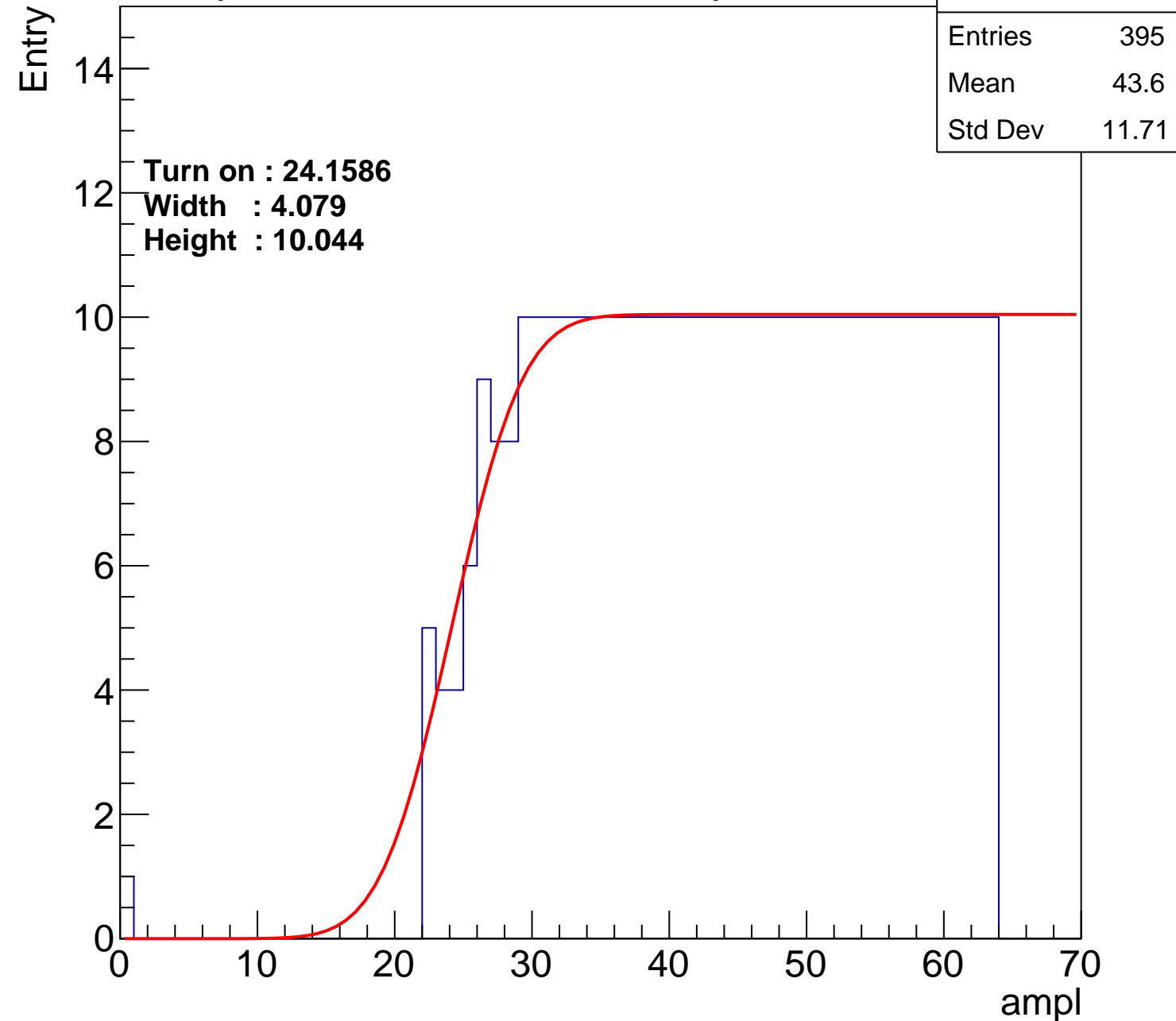
Width : 4.079

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch89

calib_packv5_042523_0143.root, FC#10, port B3

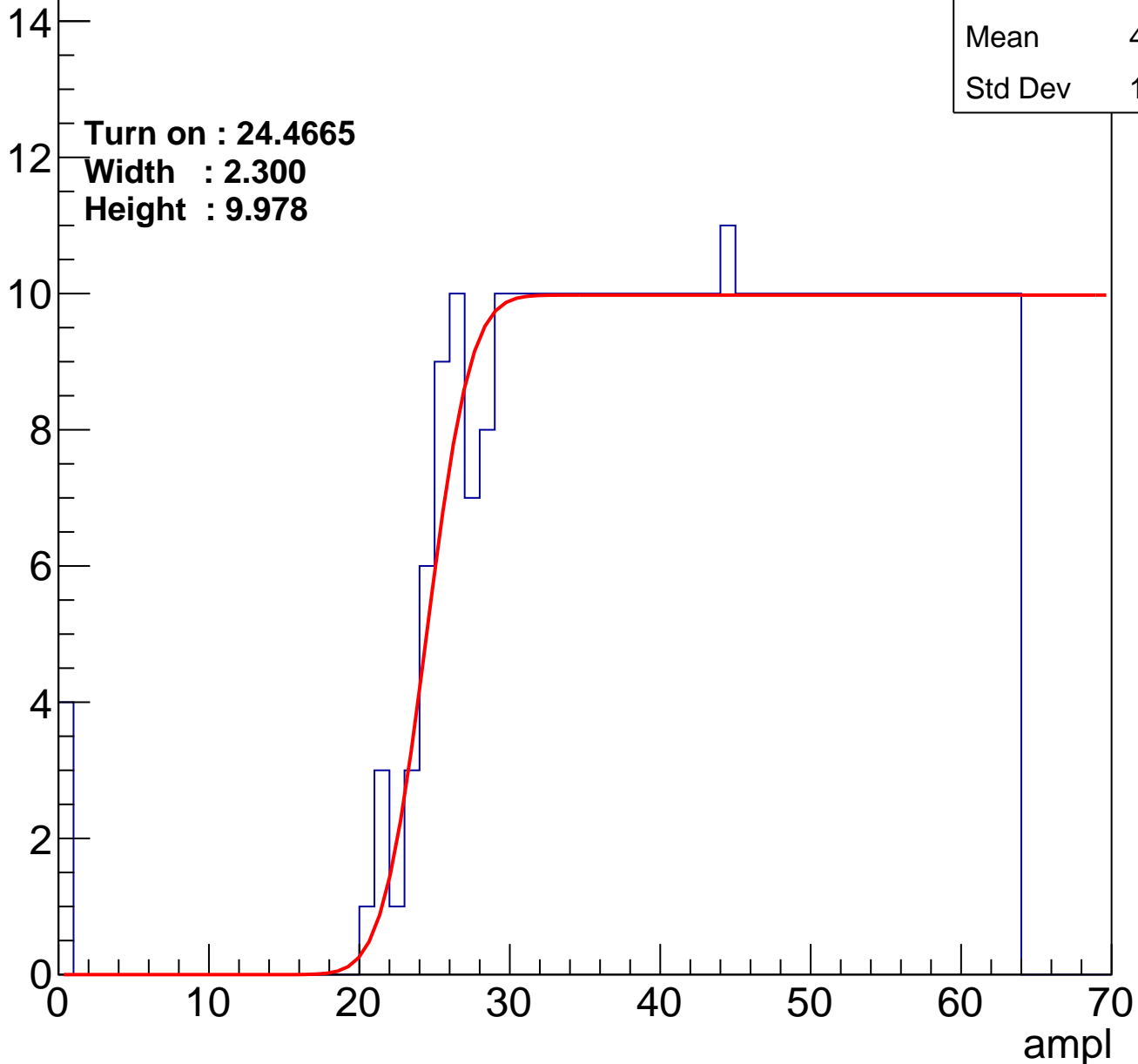
Entries	403
Mean	43.08
Std Dev	12.35

Turn on : 24.4665

Width : 2.300

Height : 9.978

Entry



B1L002S, U18-ch90

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.21
Std Dev	12.06

Turn on : 24.5939

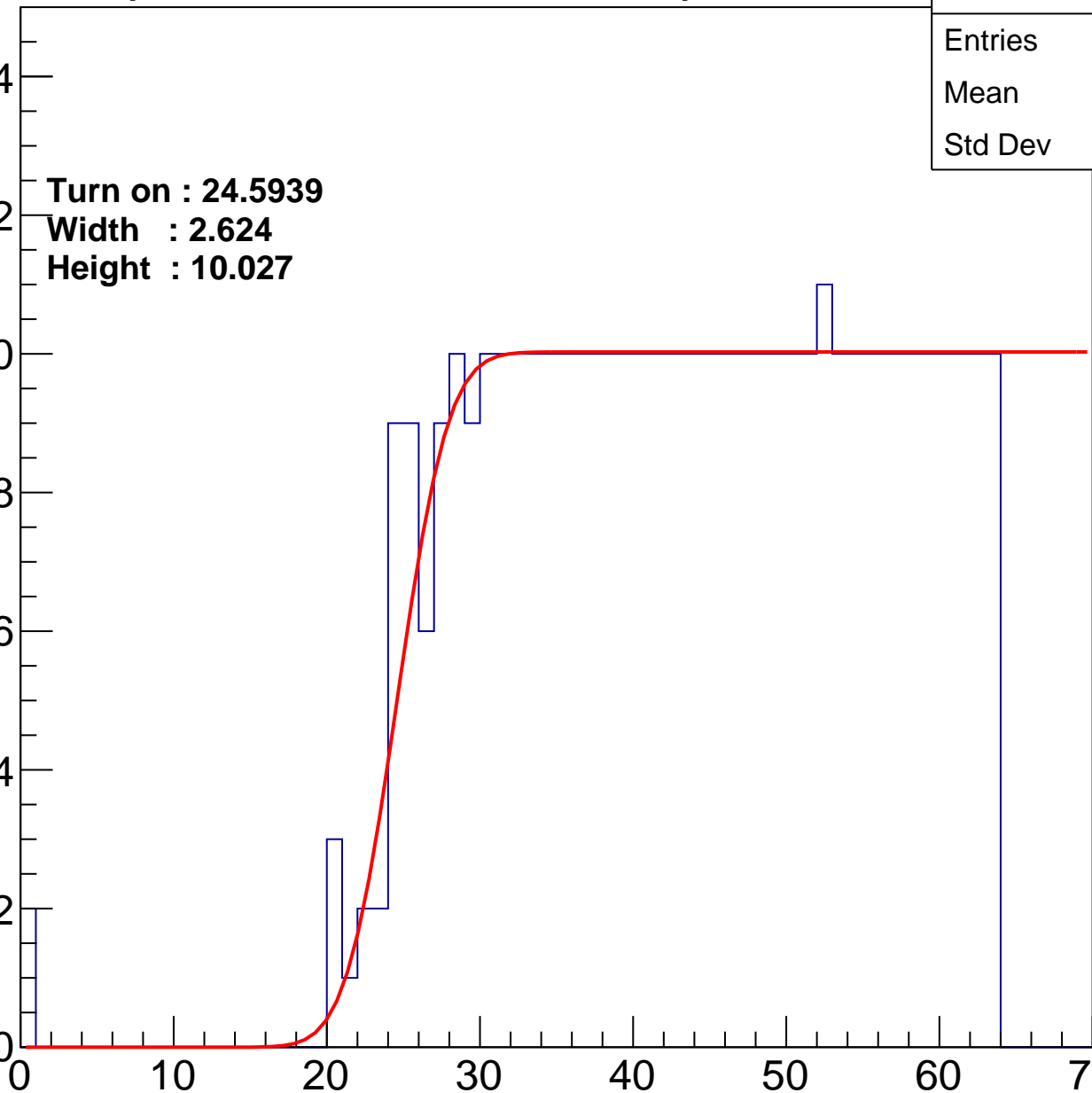
Width : 2.624

Height : 10.027

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.33

Turn on : 26.4134

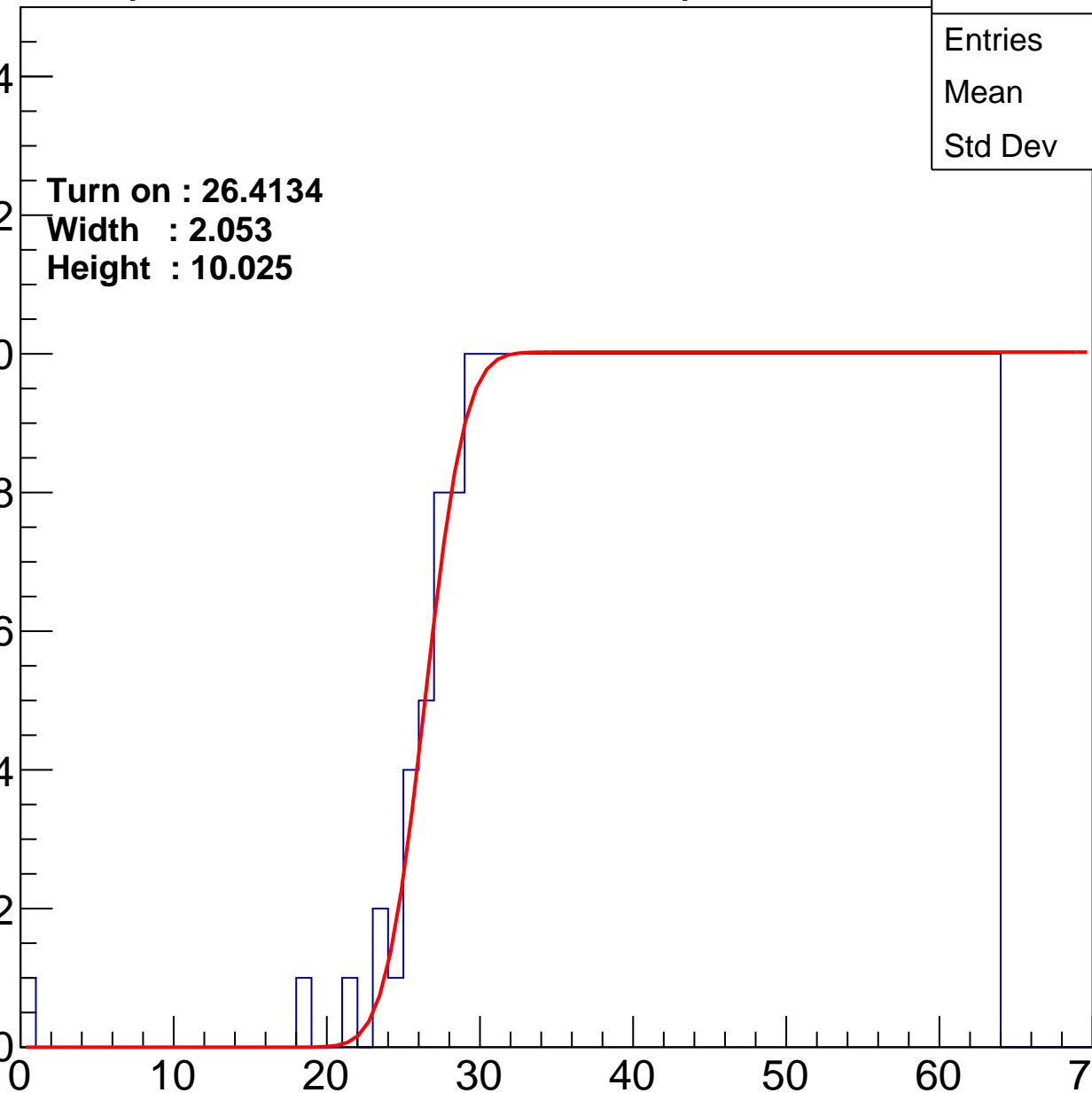
Width : 2.053

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch92

calib_packv5_042523_0143.root, FC#10, port B3

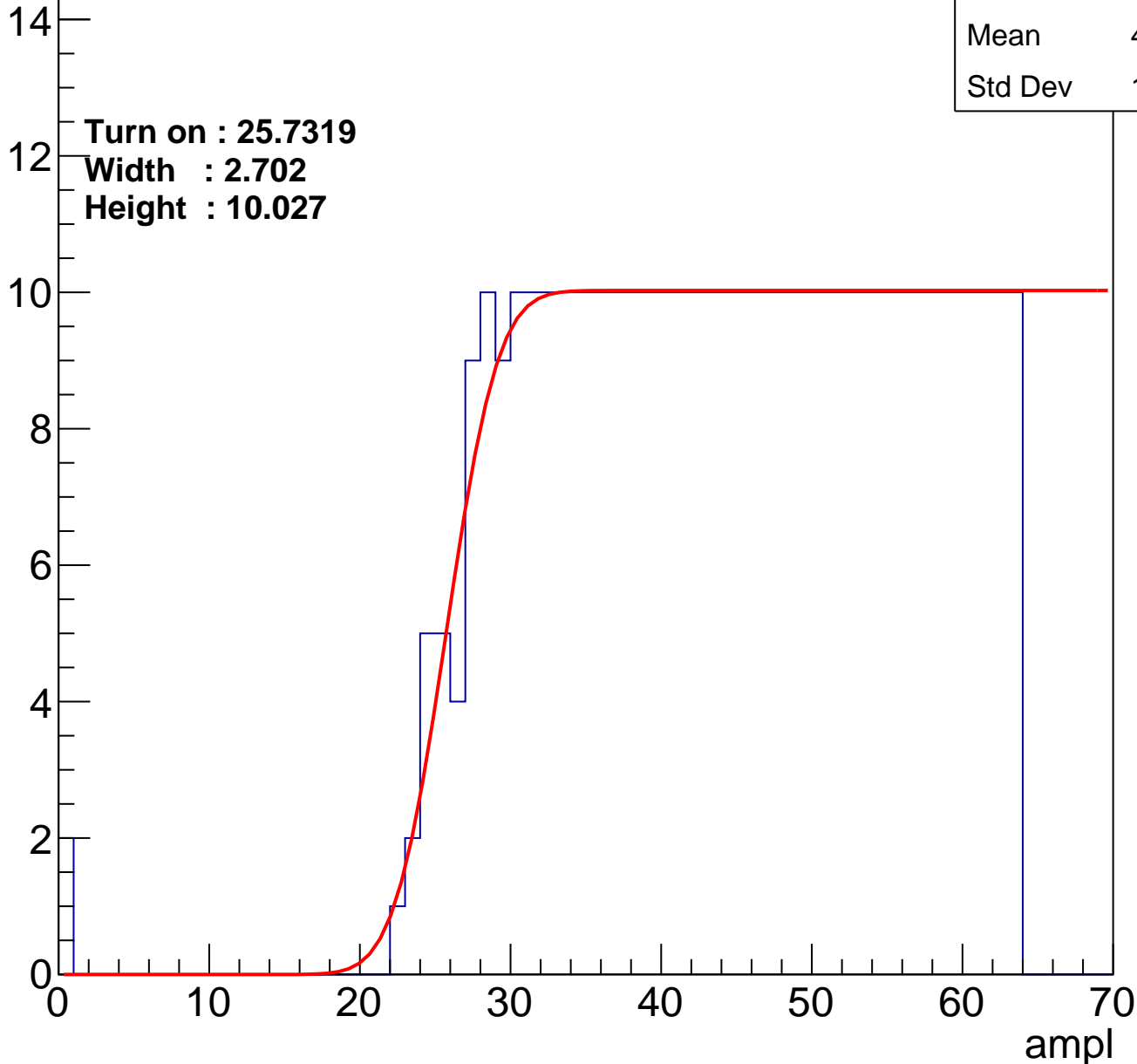
Entries	387
Mean	43.96
Std Dev	11.63

Turn on : 25.7319

Width : 2.702

Height : 10.027

Entry



B1L002S, U18-ch93

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.5
Std Dev	11.22

Turn on : 27.2750

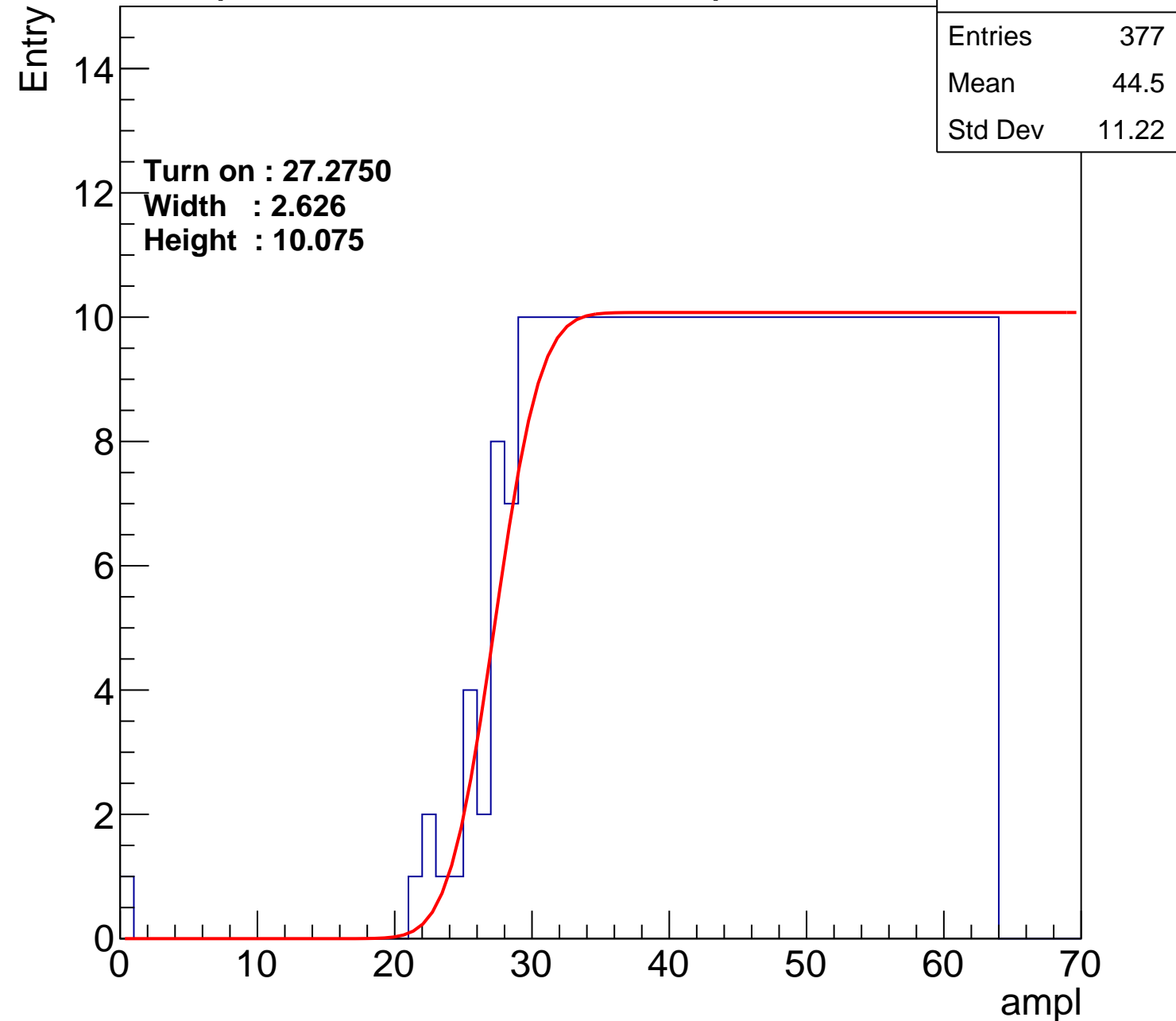
Width : 2.626

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch94

calib_packv5_042523_0143.root, FC#10, port B3

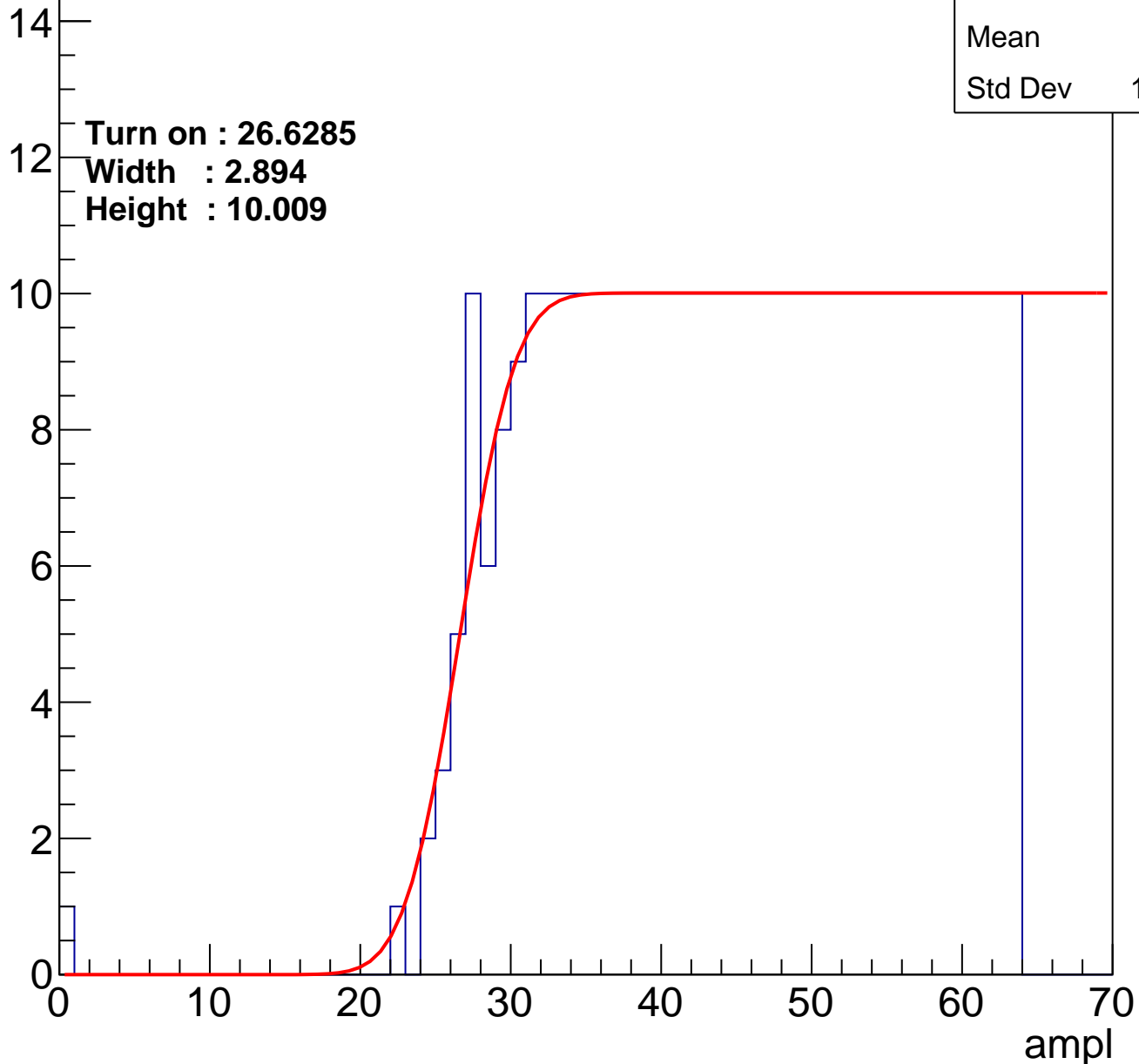
Entries	375
Mean	44.6
Std Dev	11.16

Turn on : 26.6285

Width : 2.894

Height : 10.009

Entry



B1L002S, U18-ch95

calib_packv5_042523_0143.root, FC#10, port B3

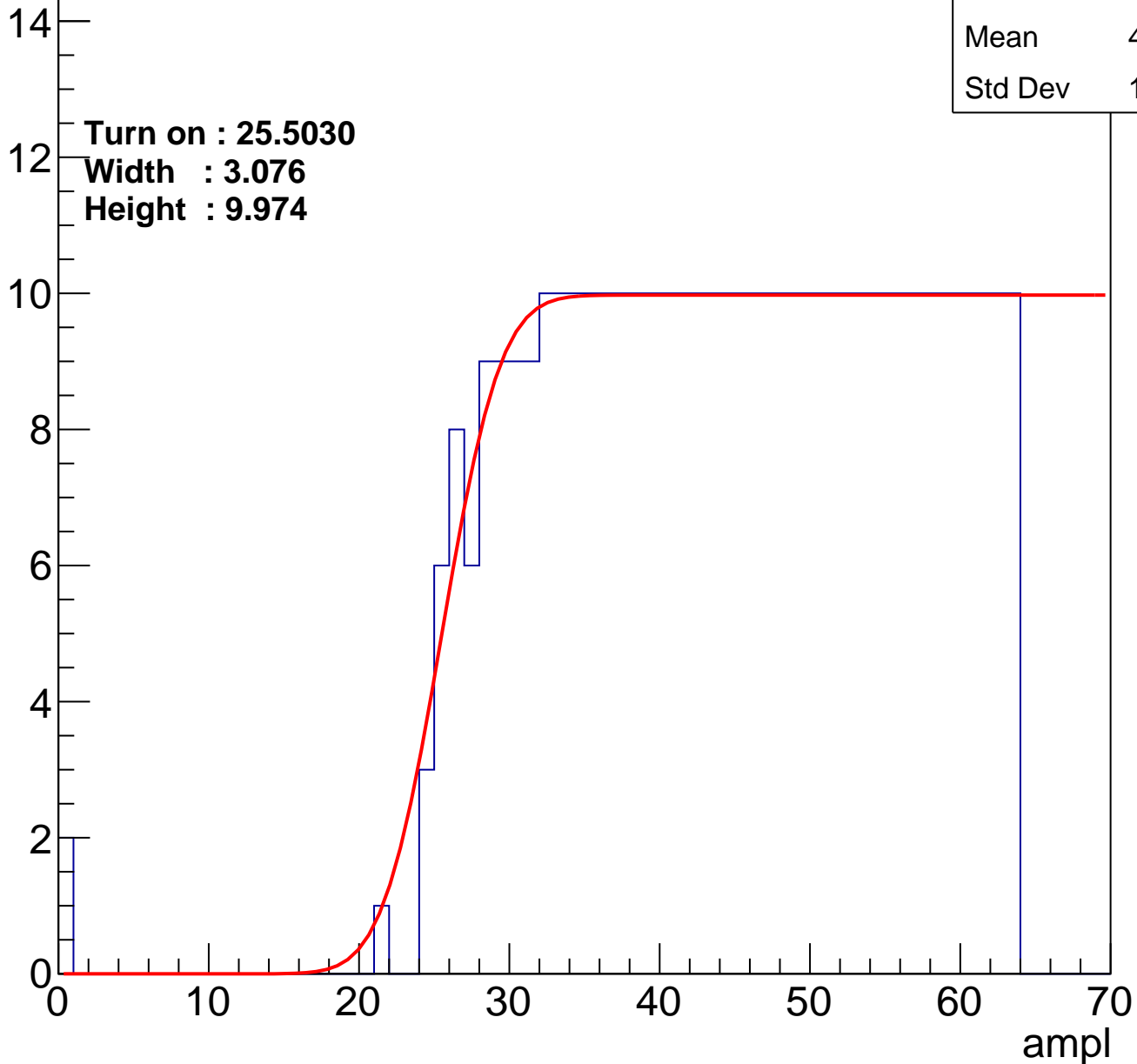
Entries	382
Mean	44.18
Std Dev	11.54

Turn on : 25.5030

Width : 3.076

Height : 9.974

Entry



B1L002S, U18-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.35
Std Dev	12.08

Turn on : 24.3810

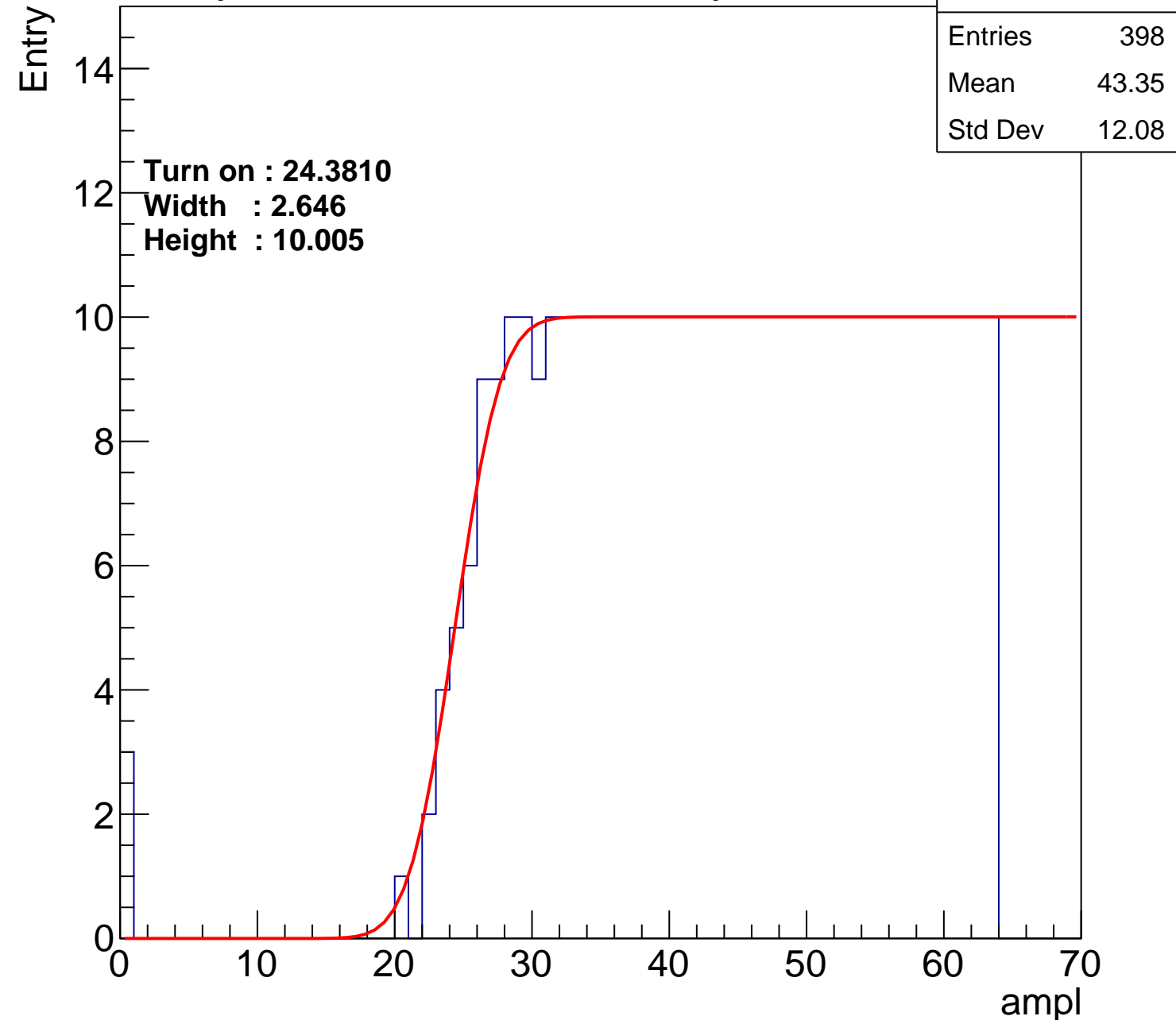
Width : 2.646

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch97

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.4
Std Dev	12.22

Turn on : 24.9494

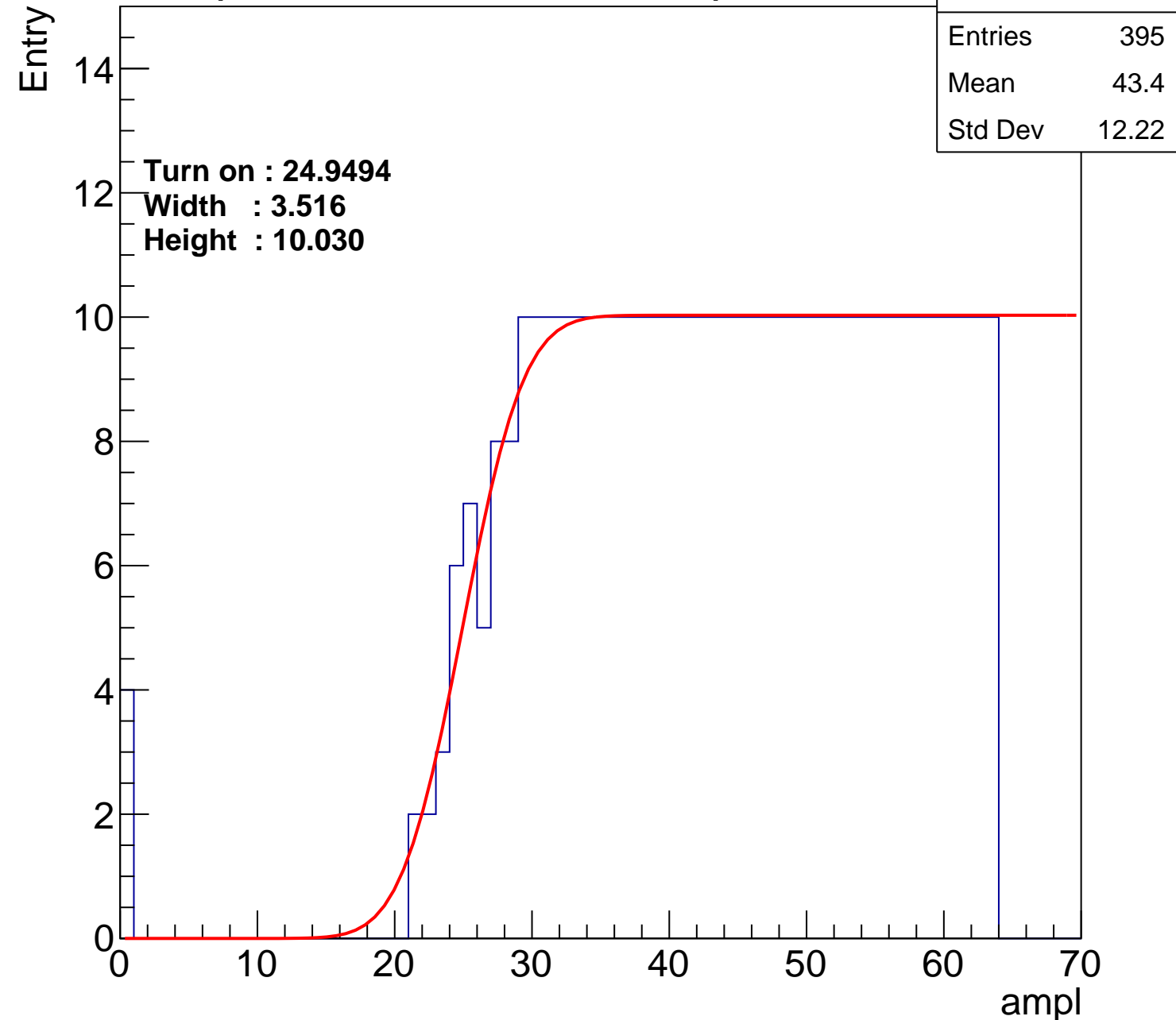
Width : 3.516

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	43.04
Std Dev	11.99

Turn on : 23.3674

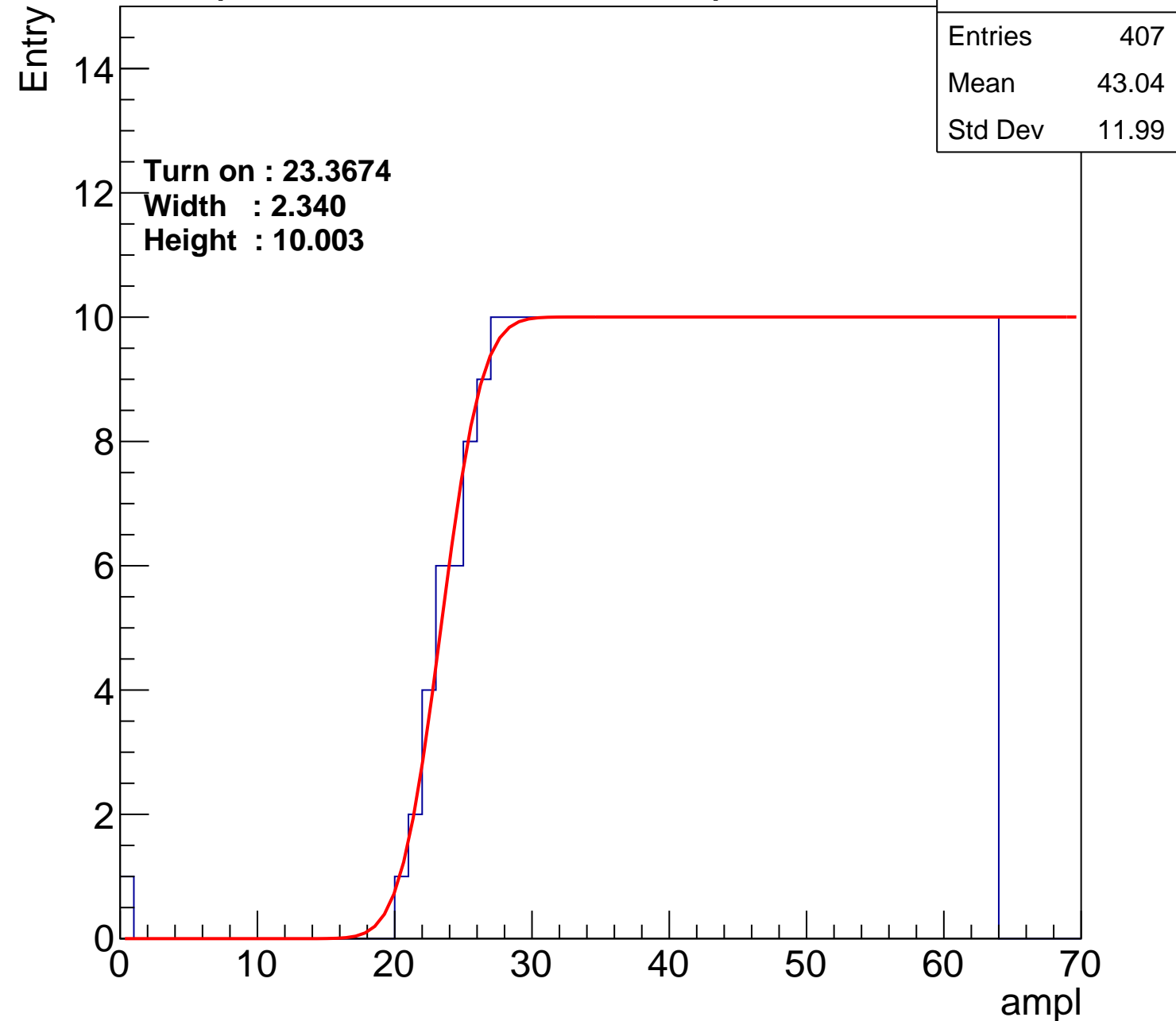
Width : 2.340

Height : 10.003

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.58
Std Dev	11.86

Turn on : 25.1116

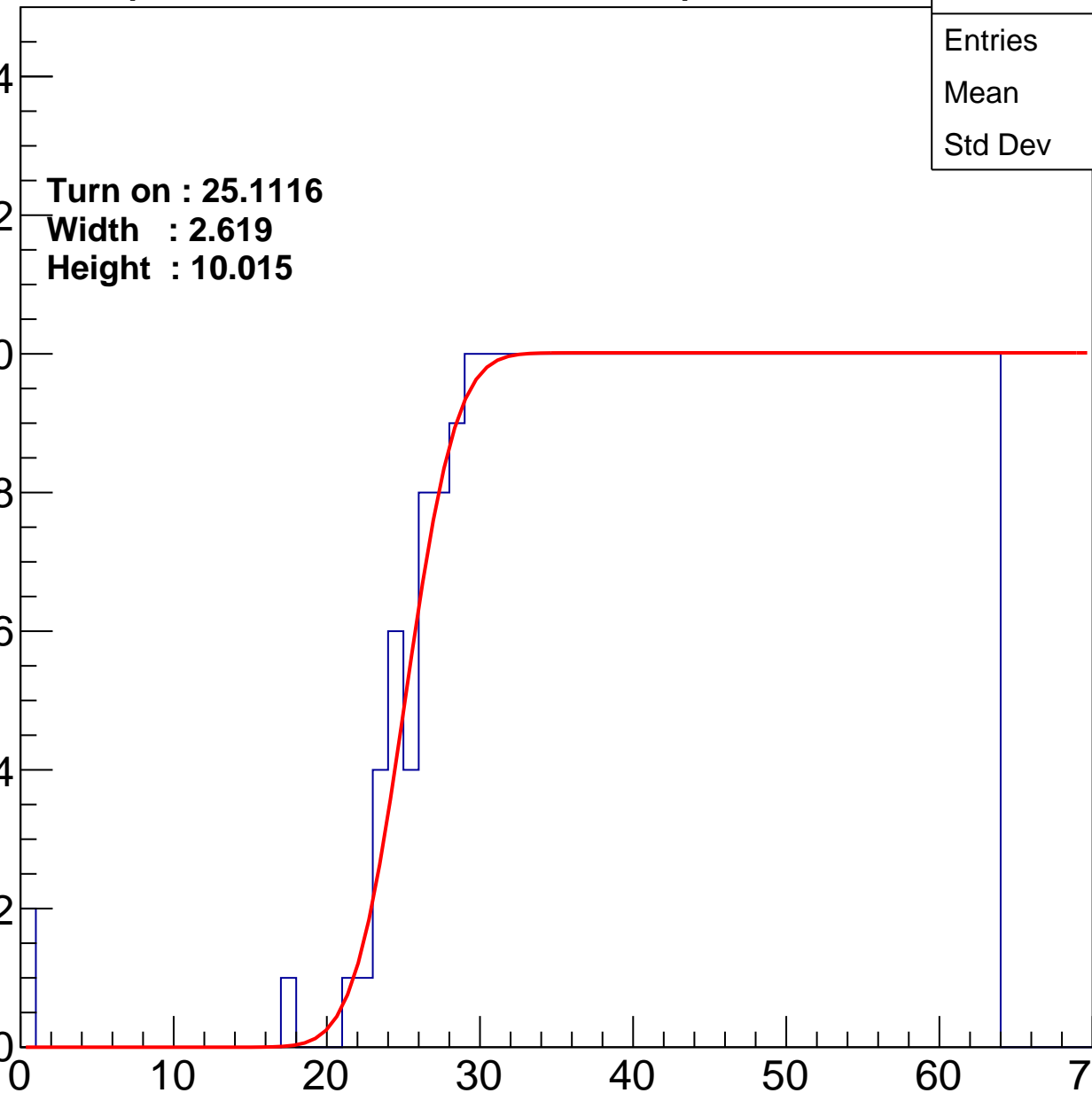
Width : 2.619

Height : 10.015

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch100

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.78
Std Dev	11.59

Turn on : 25.3434

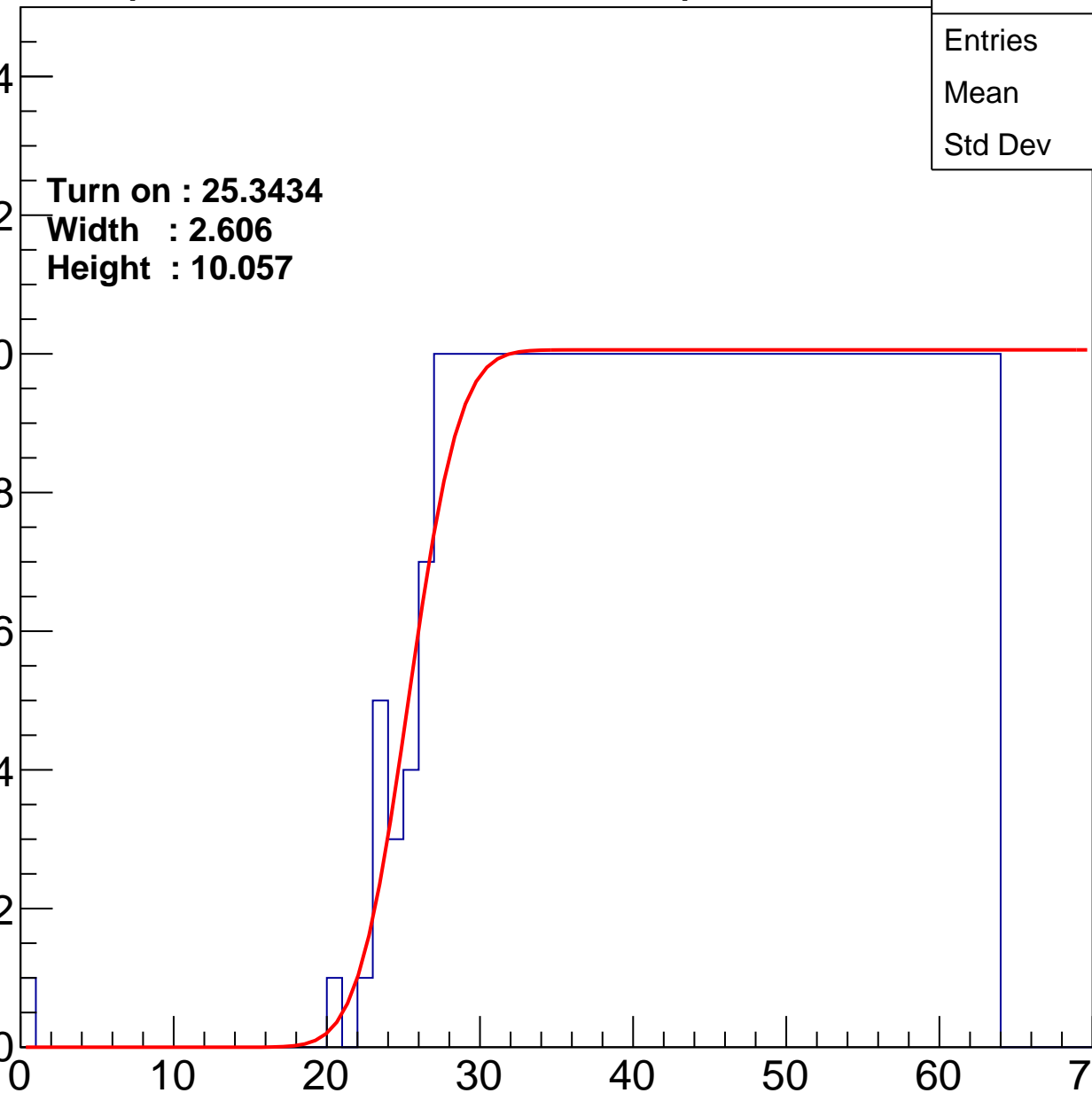
Width : 2.606

Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch101

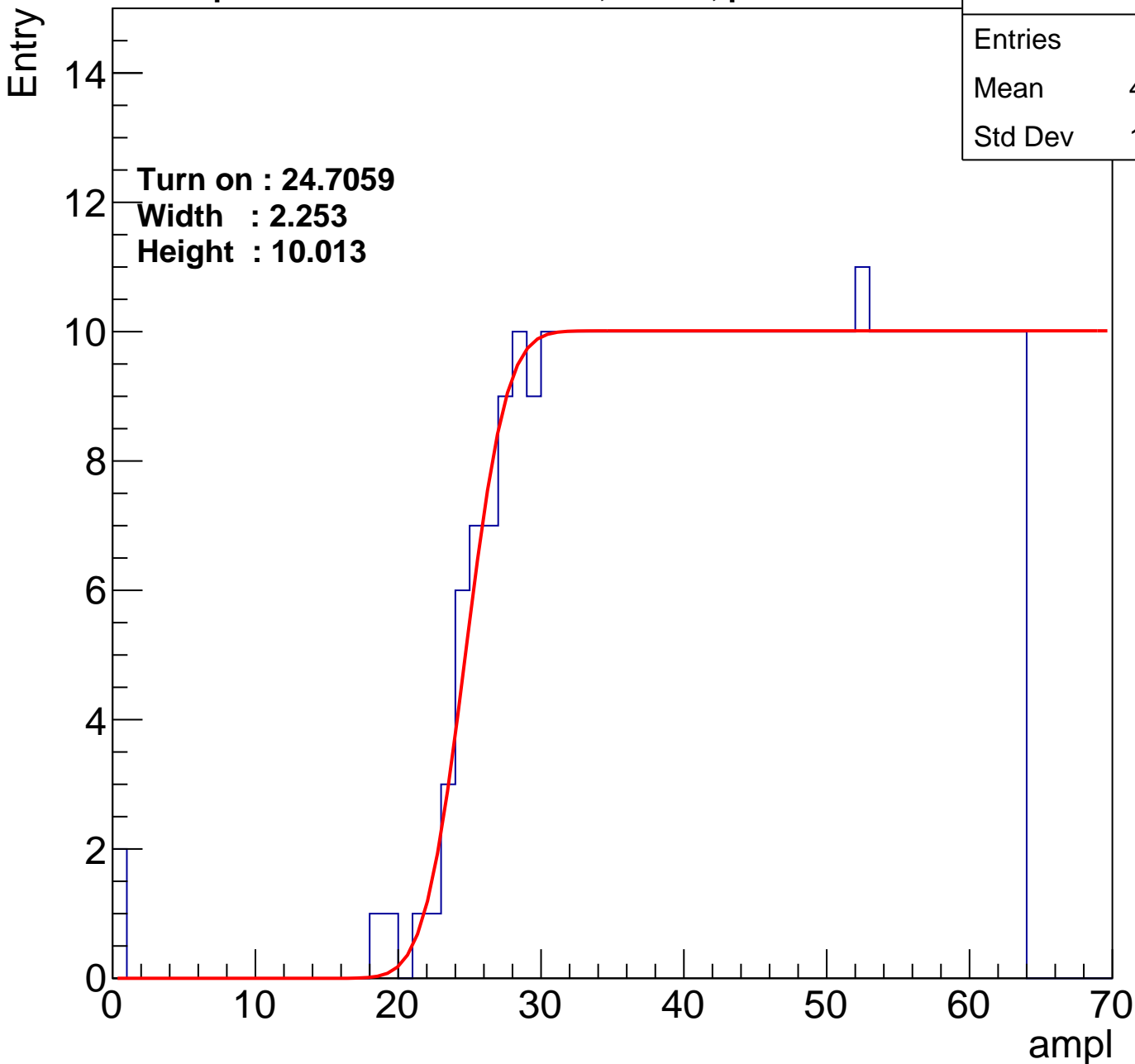
calib_packv5_042523_0143.root, FC#10, port B3

Entries	398
Mean	43.46
Std Dev	11.93

Turn on : 24.7059

Width : 2.253

Height : 10.013



B1L002S, U18-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.56
Std Dev	11.9

Turn on : 25.2288

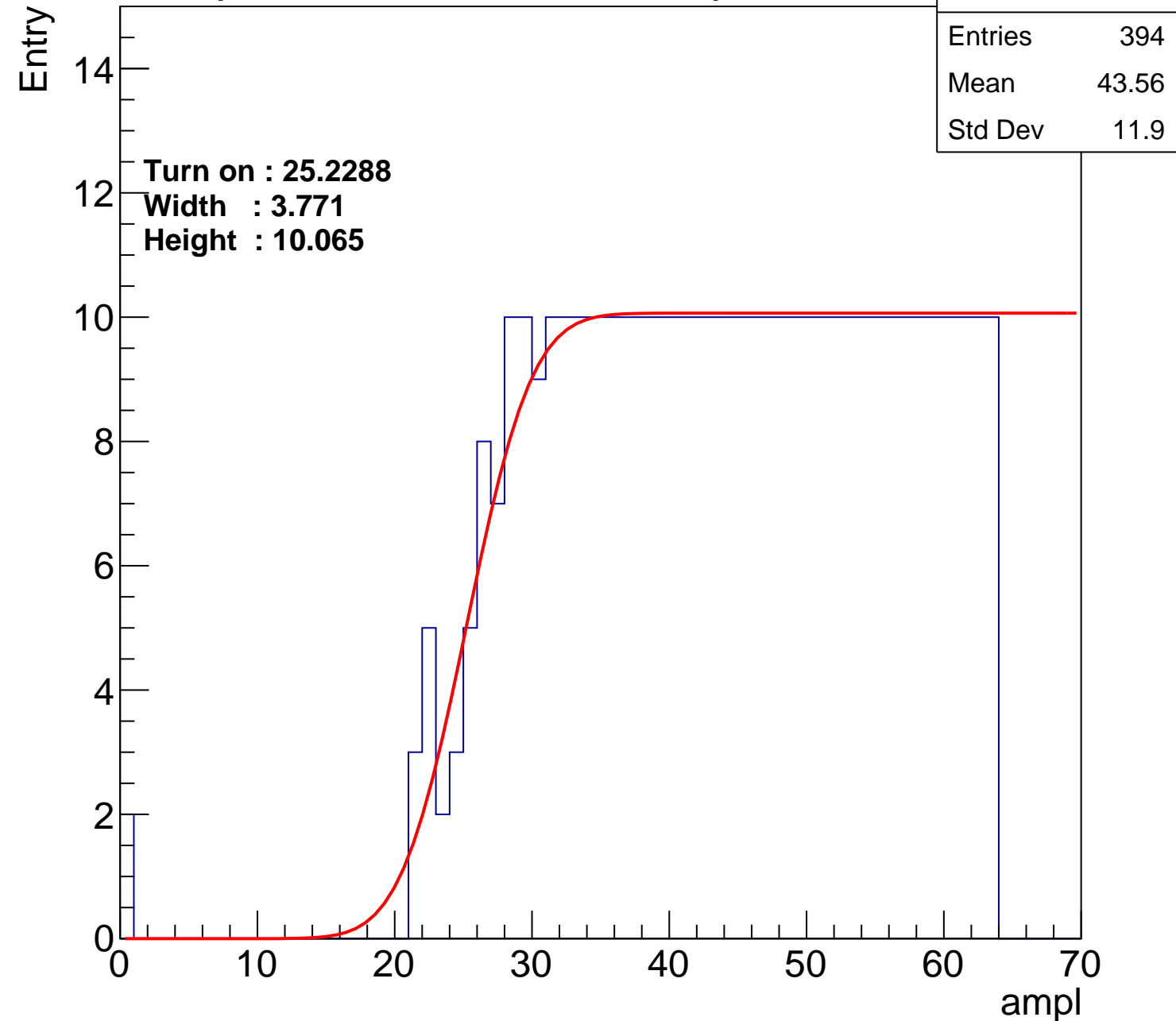
Width : 3.771

Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	411
Mean	42.73
Std Dev	12.32

Turn on : 22.9122

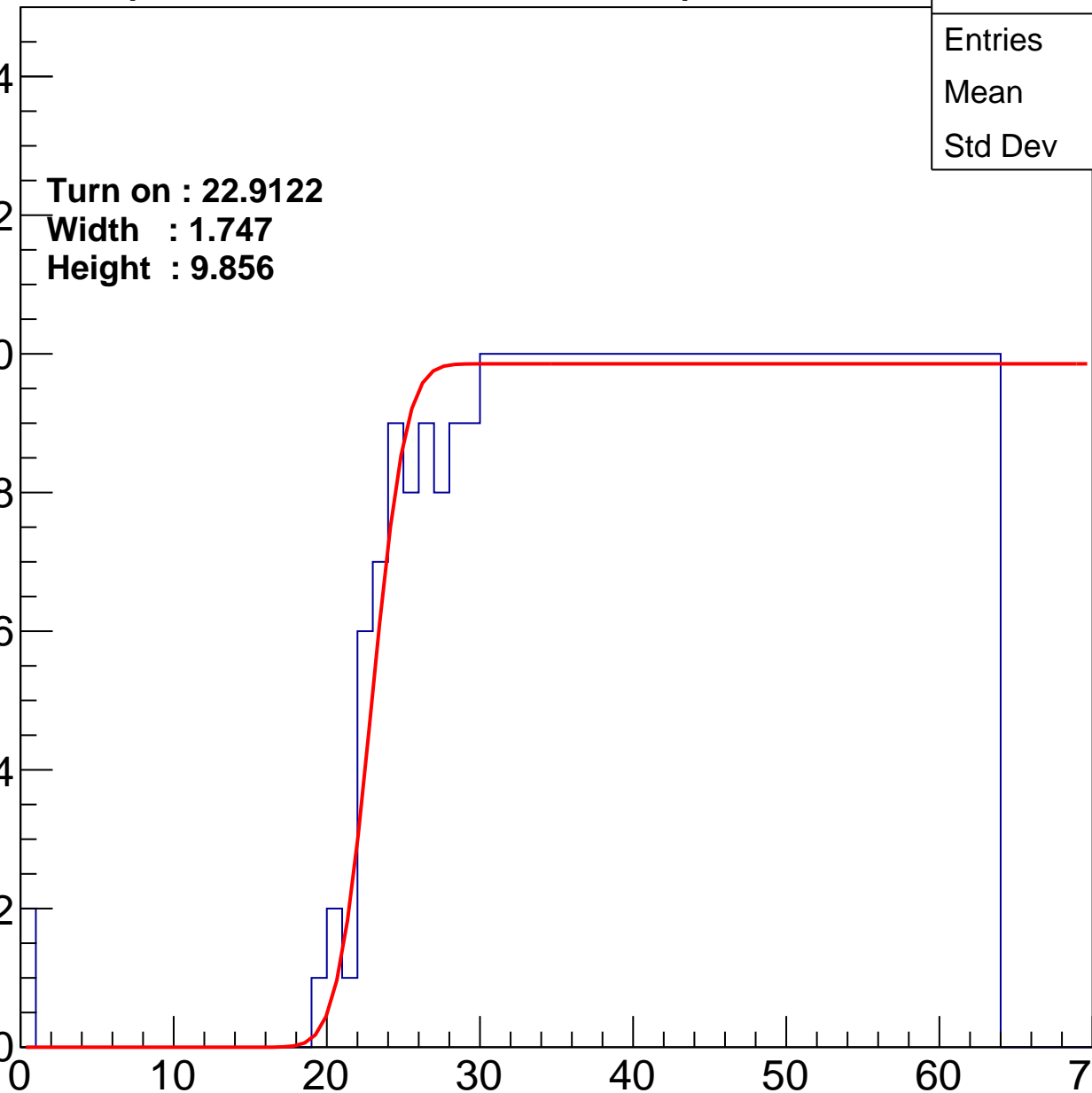
Width : 1.747

Height : 9.856

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	44.03
Std Dev	11.8

Turn on : 26.0554

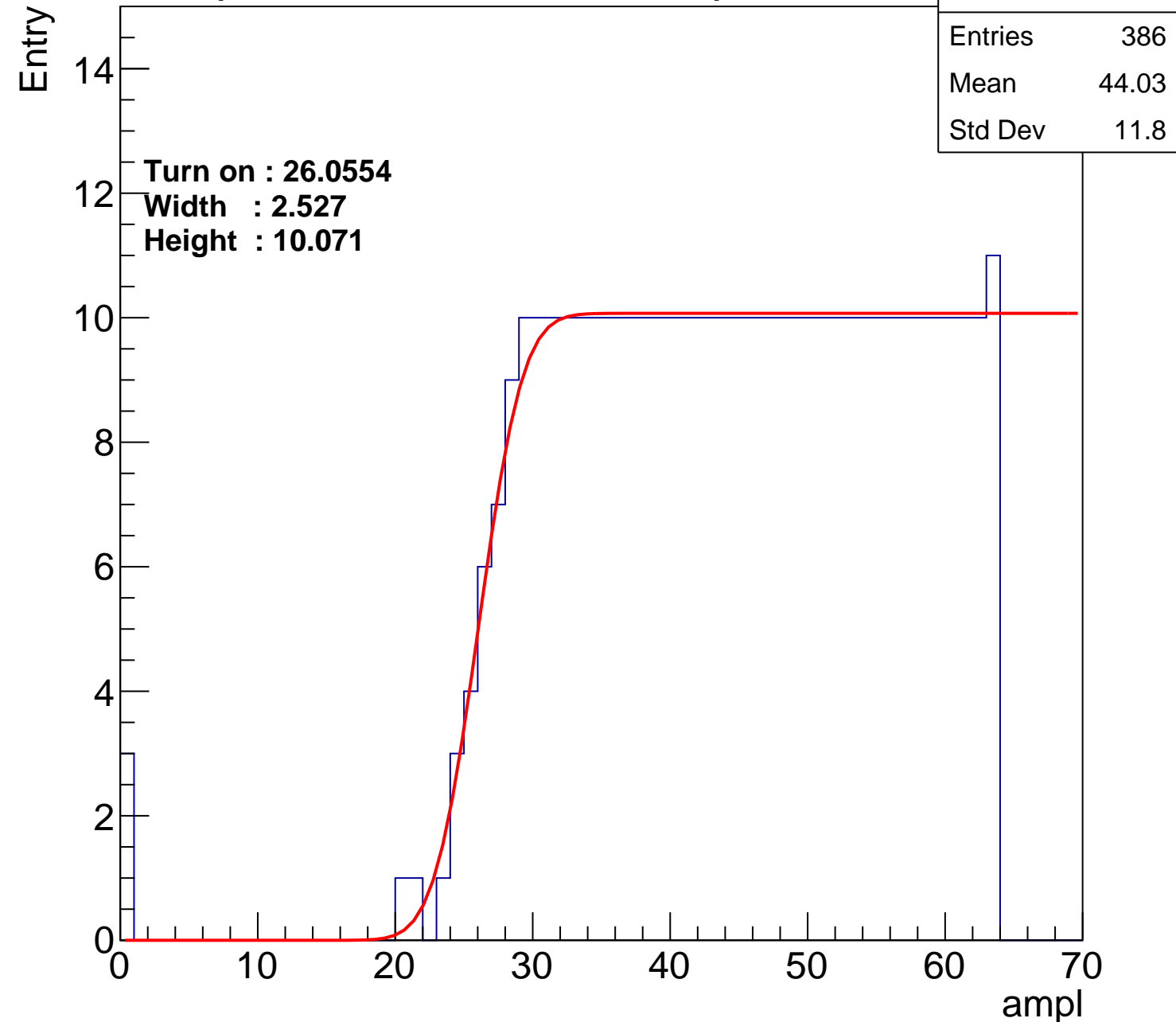
Width : 2.527

Height : 10.071

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.36
Std Dev	11.28

Turn on : 26.4826

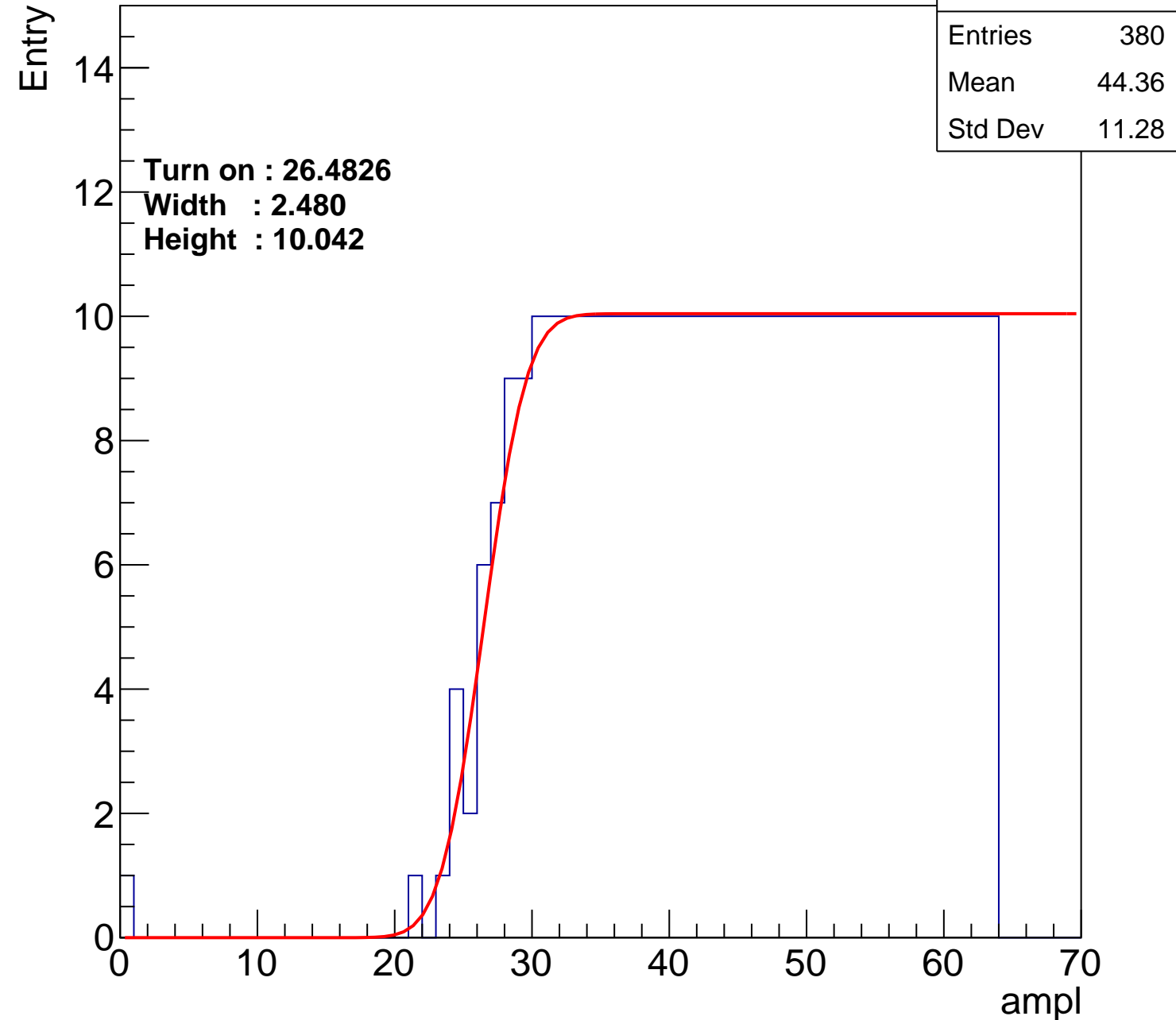
Width : 2.480

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.34
Std Dev	11.82

Turn on : 23.9008

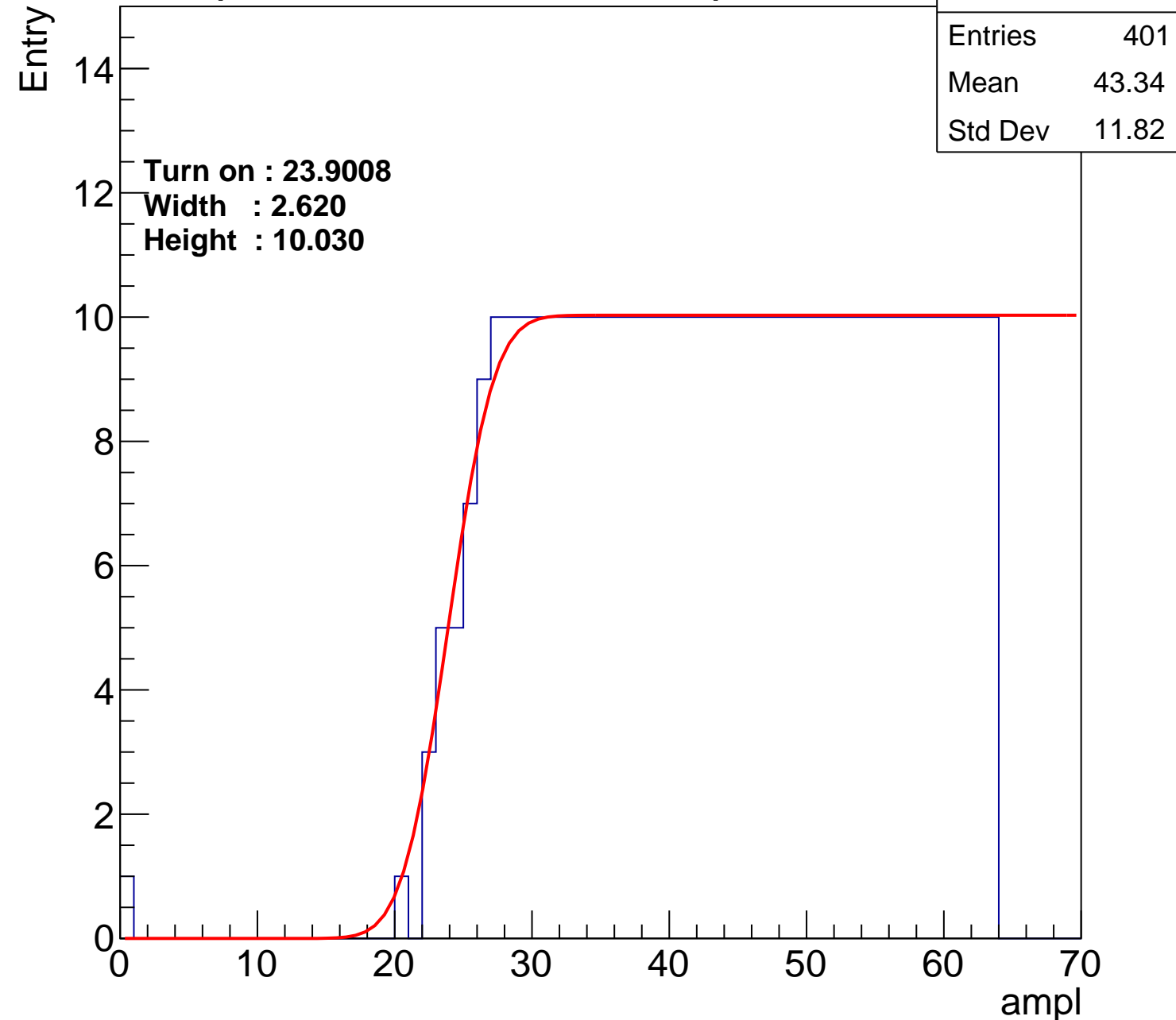
Width : 2.620

Height : 10.030

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.38
Std Dev	11.81

Turn on : 24.3503

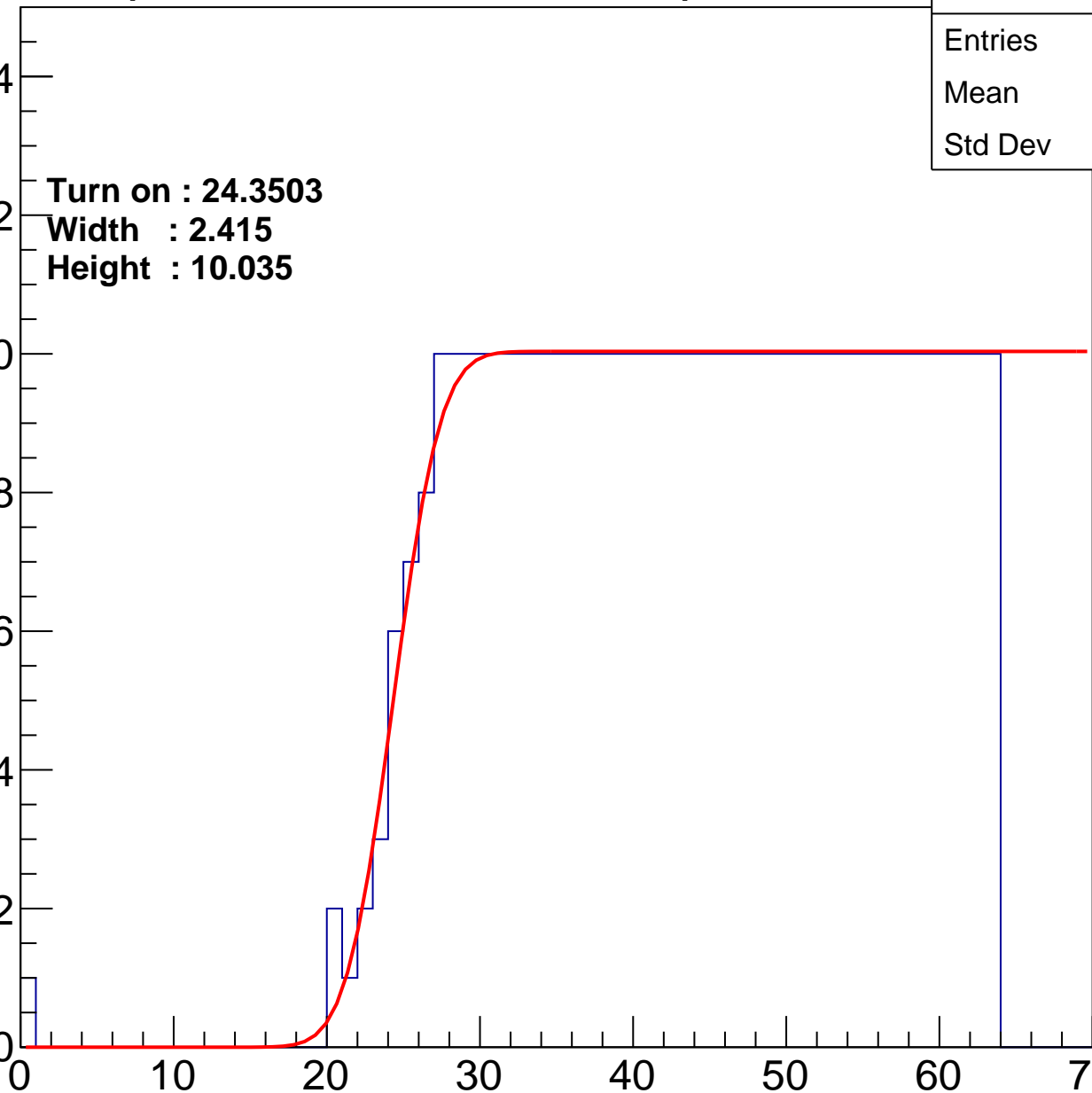
Width : 2.415

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch108

calib_packv5_042523_0143.root, FC#10, port B3

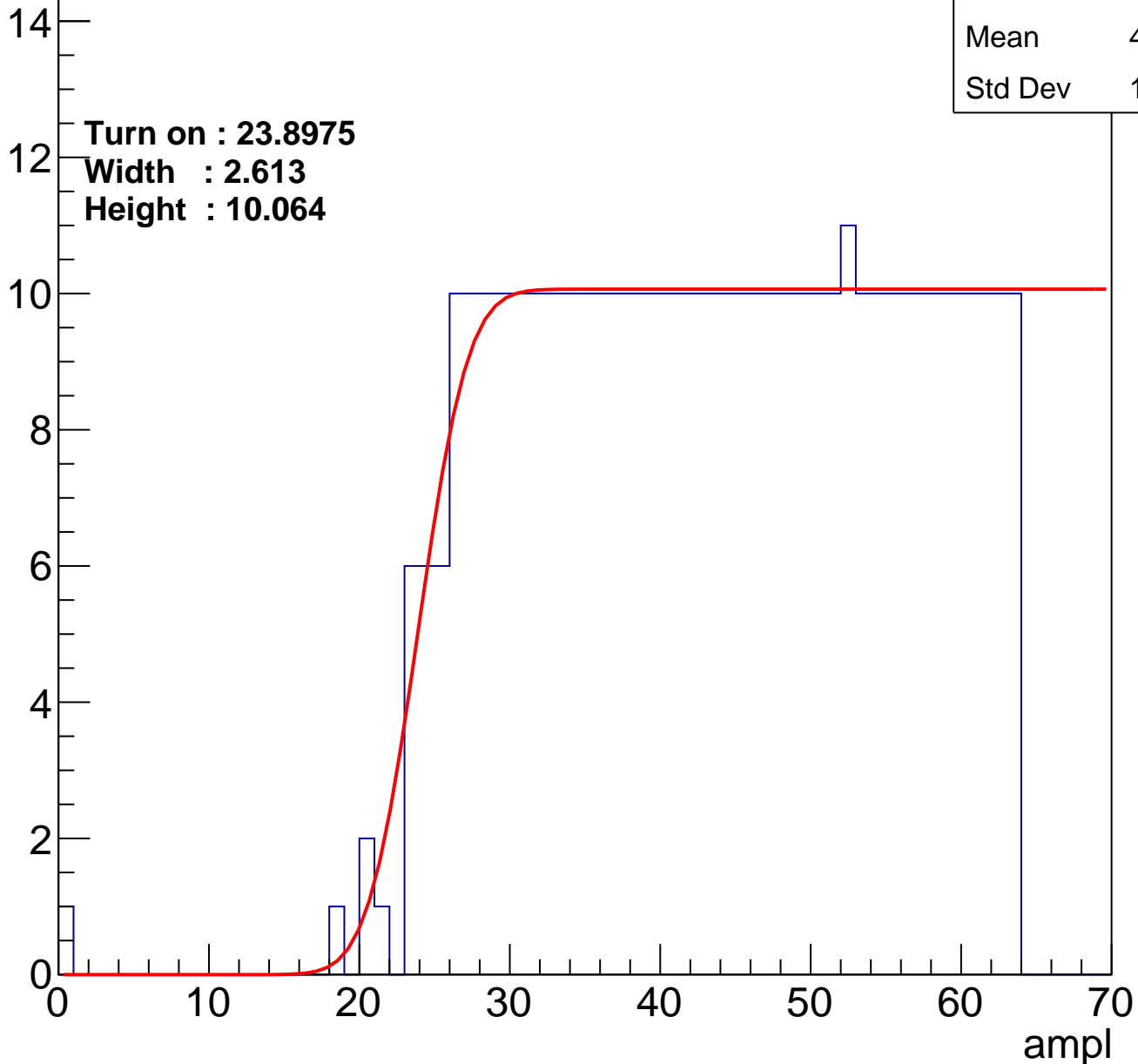
Entries	404
Mean	43.25
Std Dev	11.89

Turn on : 23.8975

Width : 2.613

Height : 10.064

Entry



B1L002S, U18-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.9
Std Dev	11.72

Turn on : 25.6936

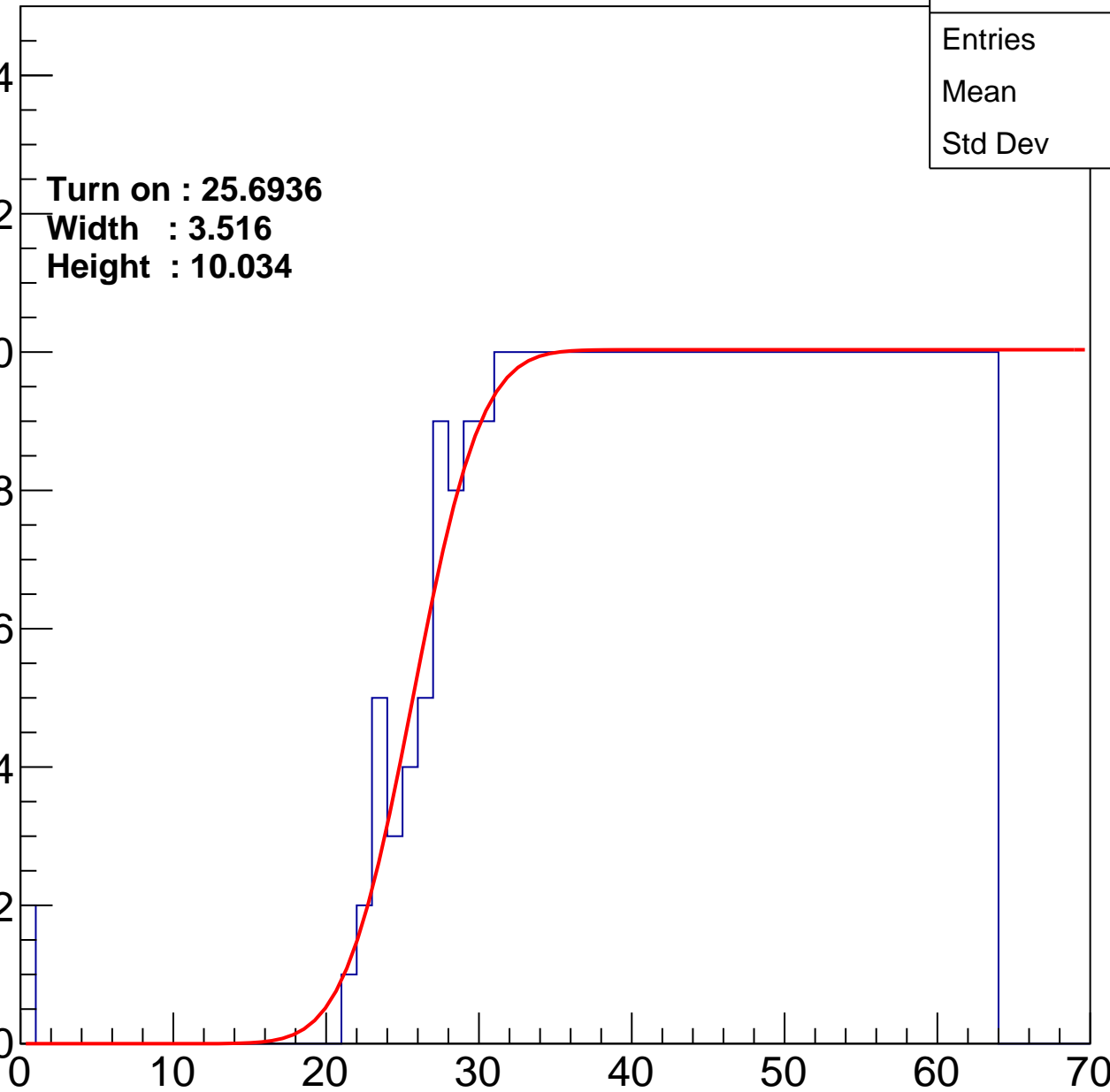
Width : 3.516

Height : 10.034

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.53
Std Dev	11.89

Turn on : 25.1958

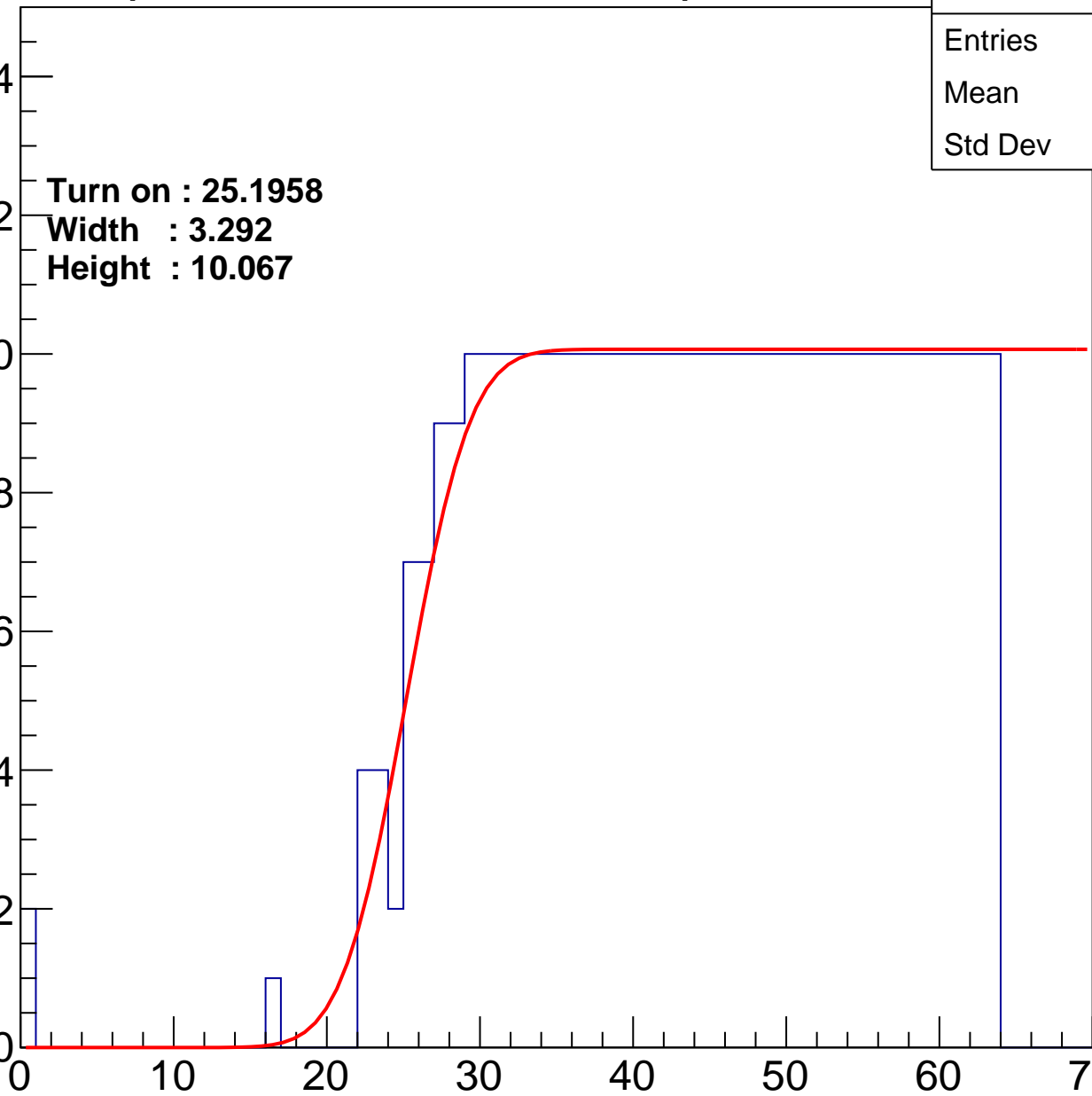
Width : 3.292

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch111

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.46
Std Dev	12.08

Turn on : 25.9005

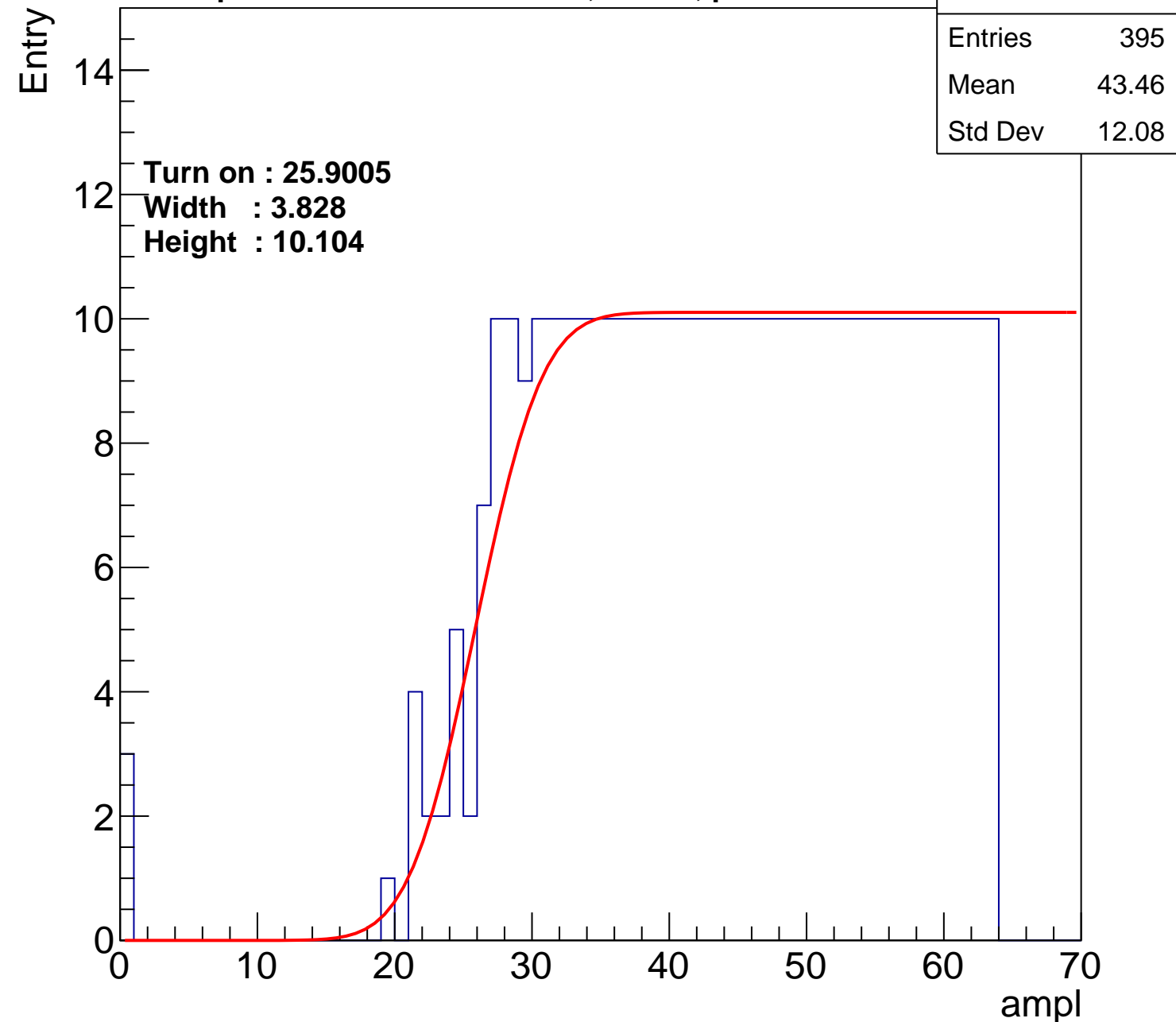
Width : 3.828

Height : 10.104

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch112

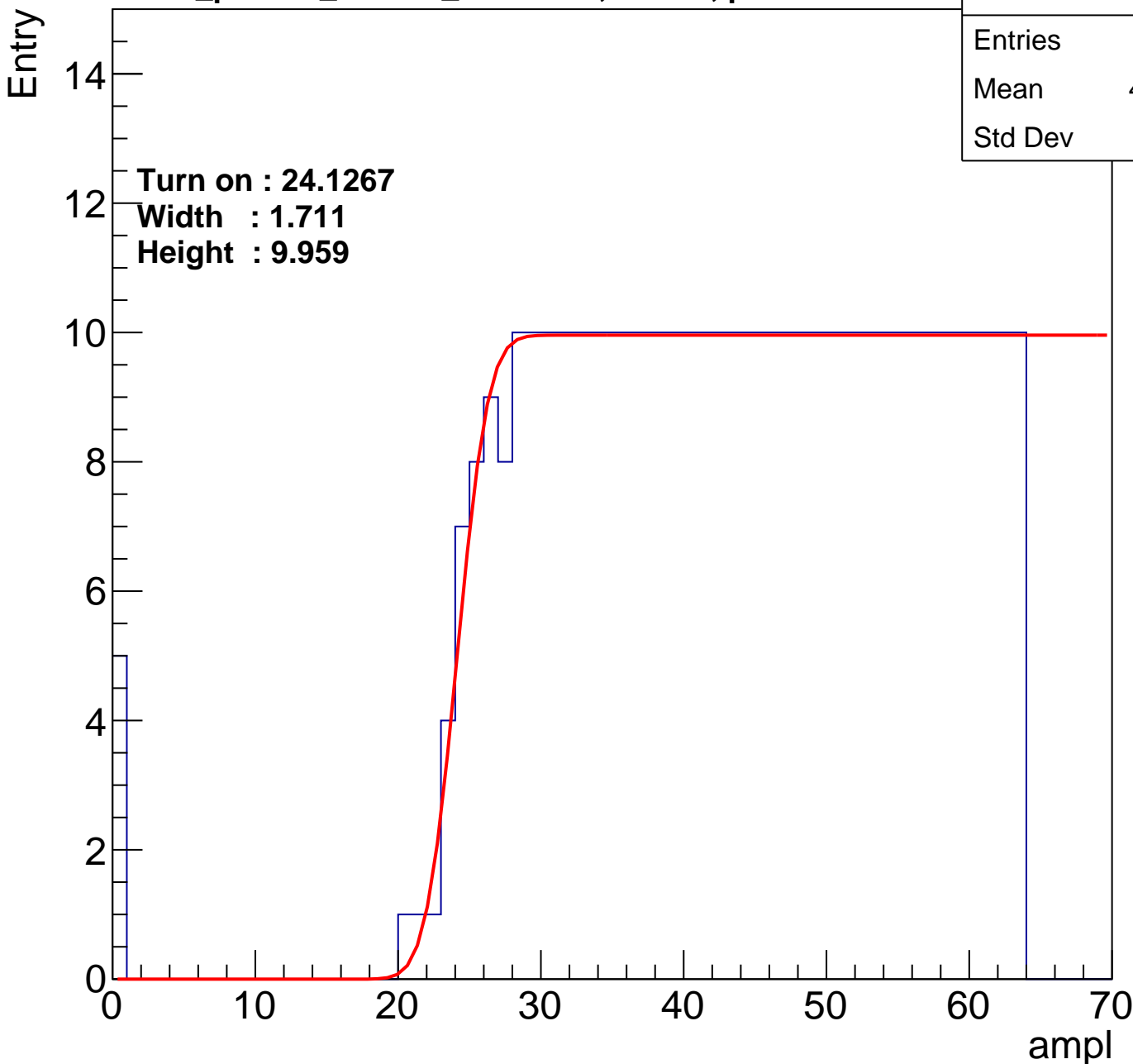
calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	42.95
Std Dev	12.5

Turn on : 24.1267

Width : 1.711

Height : 9.959



B1L002S, U18-ch113

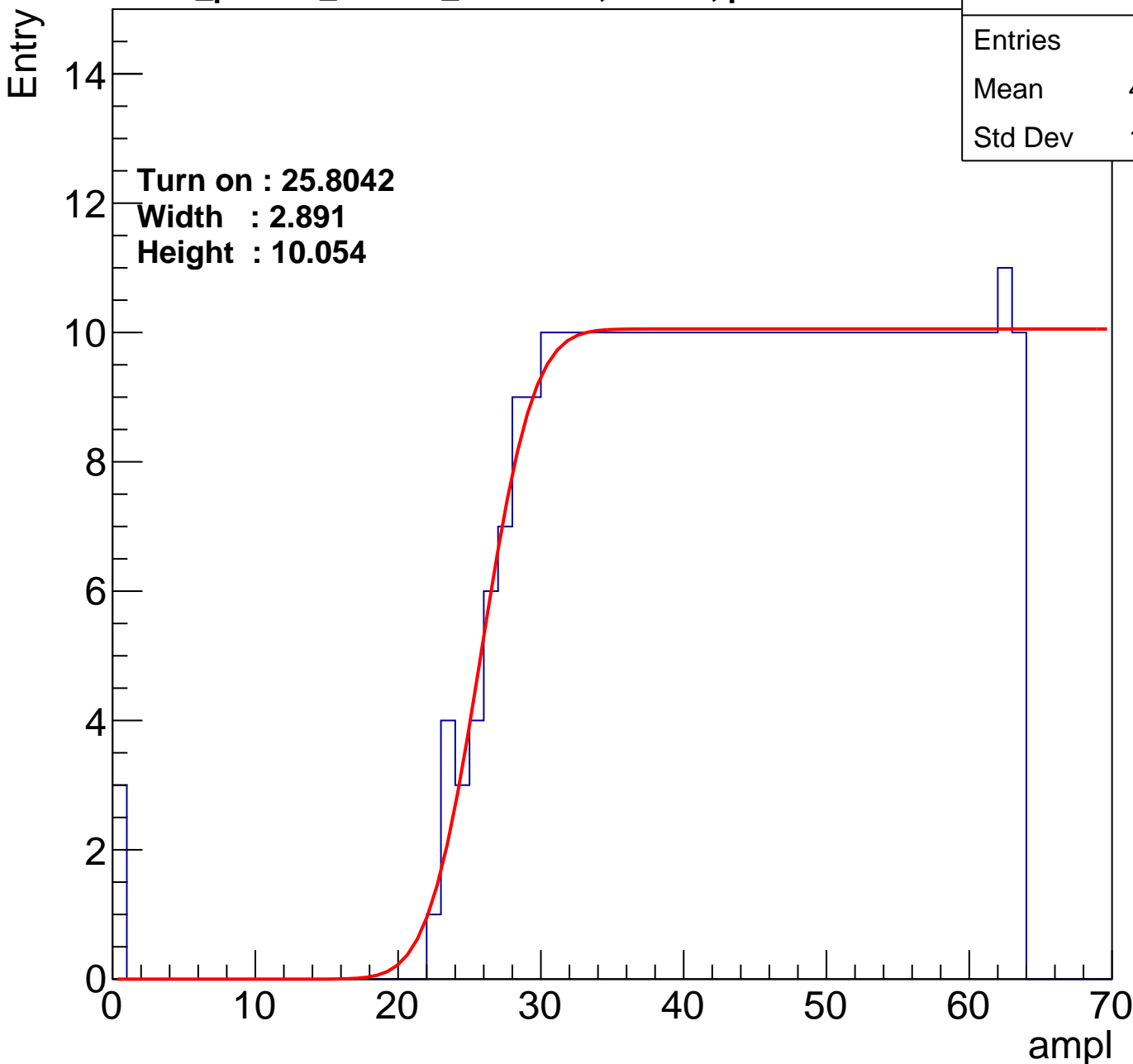
calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.97
Std Dev	11.83

Turn on : 25.8042

Width : 2.891

Height : 10.054



B1L002S, U18-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.06
Std Dev	11.75

Turn on : 26.0343

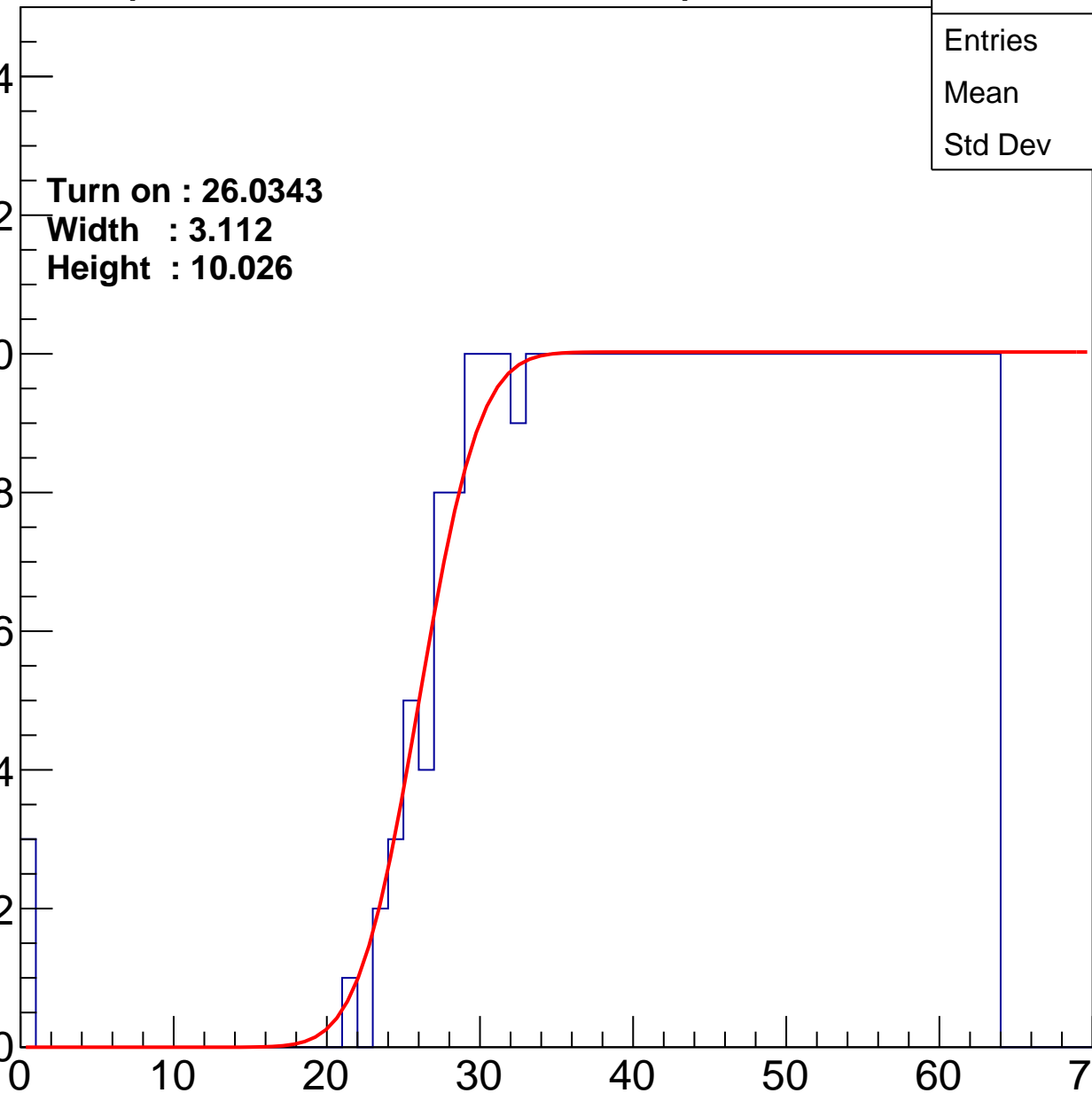
Width : 3.112

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	371
Mean	44.56
Std Dev	11.69

Turn on : 27.7862

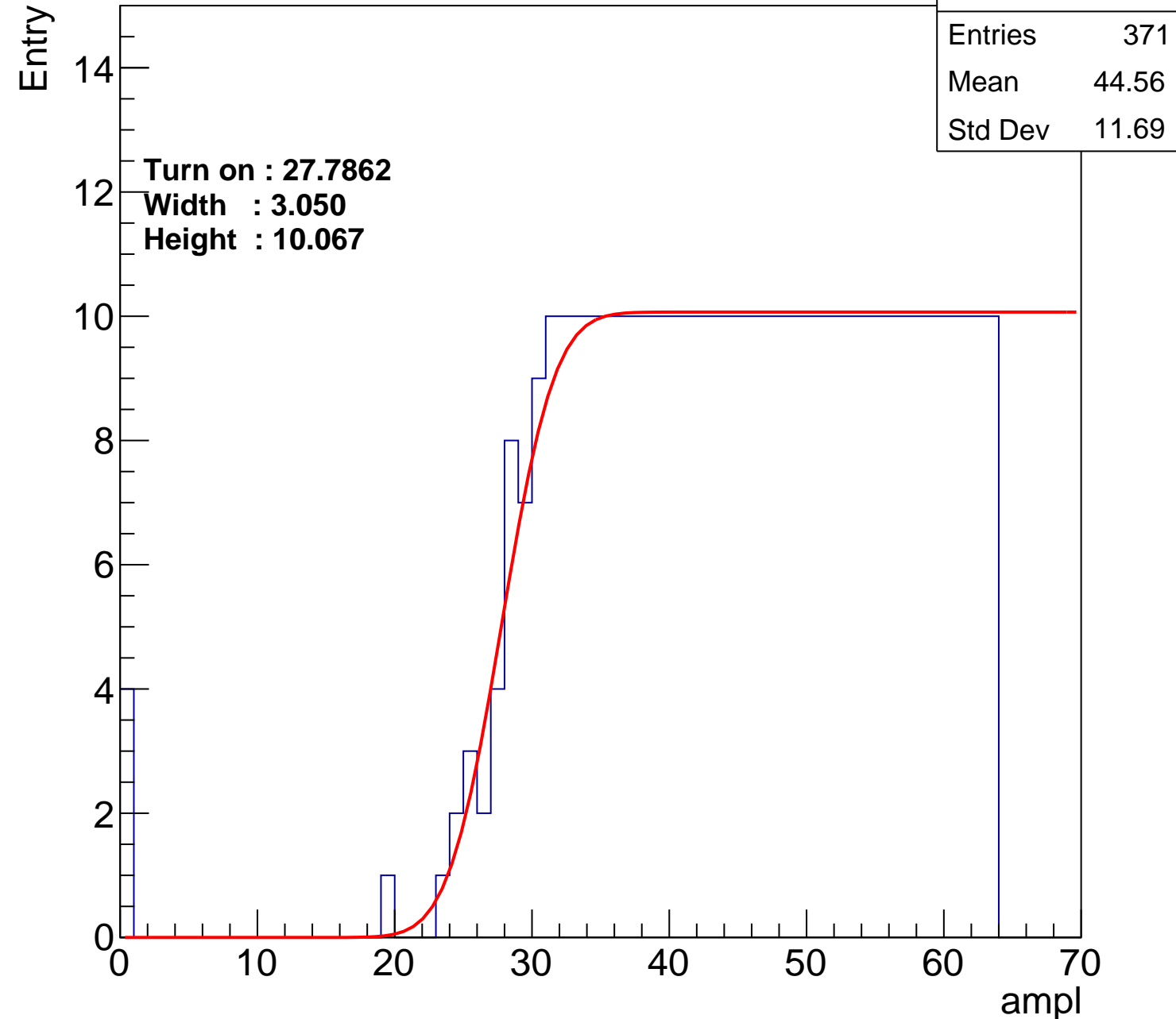
Width : 3.050

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.05
Std Dev	12.24

Turn on : 24.0125

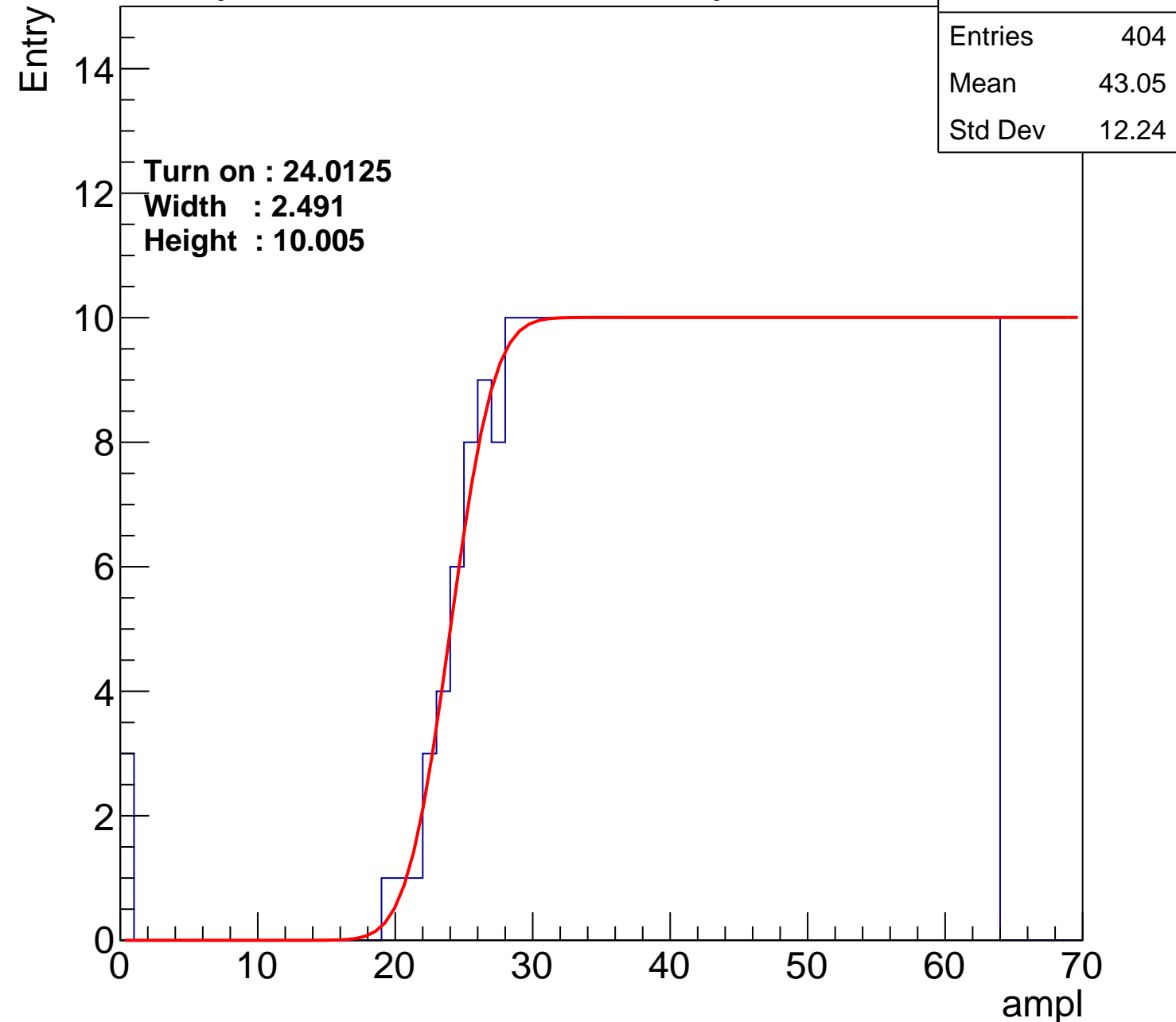
Width : 2.491

Height : 10.005

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch117

calib_packv5_042523_0143.root, FC#10, port B3

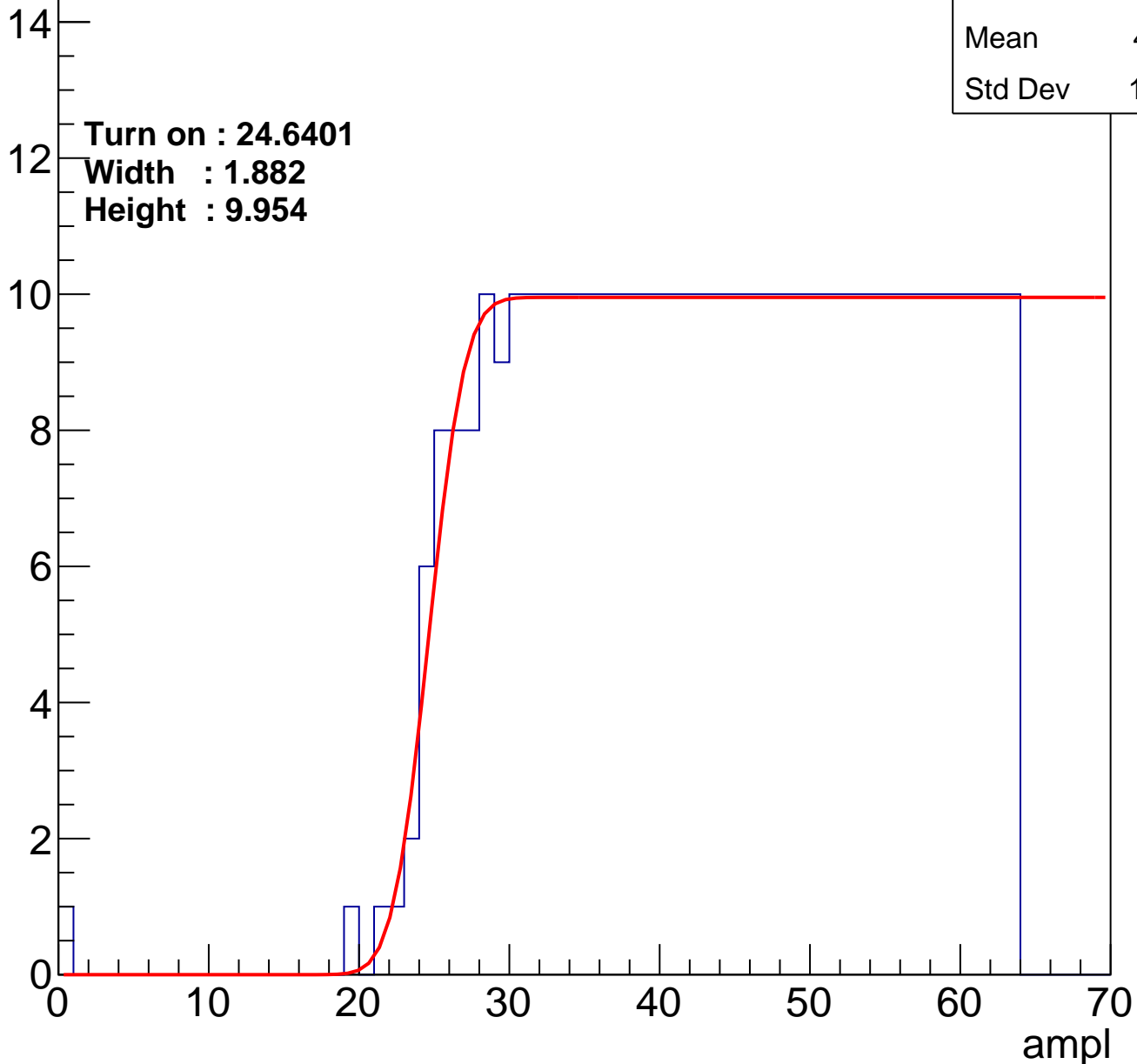
Entries	395
Mean	43.61
Std Dev	11.69

Turn on : 24.6401

Width : 1.882

Height : 9.954

Entry



B1L002S, U18-ch118

calib_packv5_042523_0143.root, FC#10, port B3

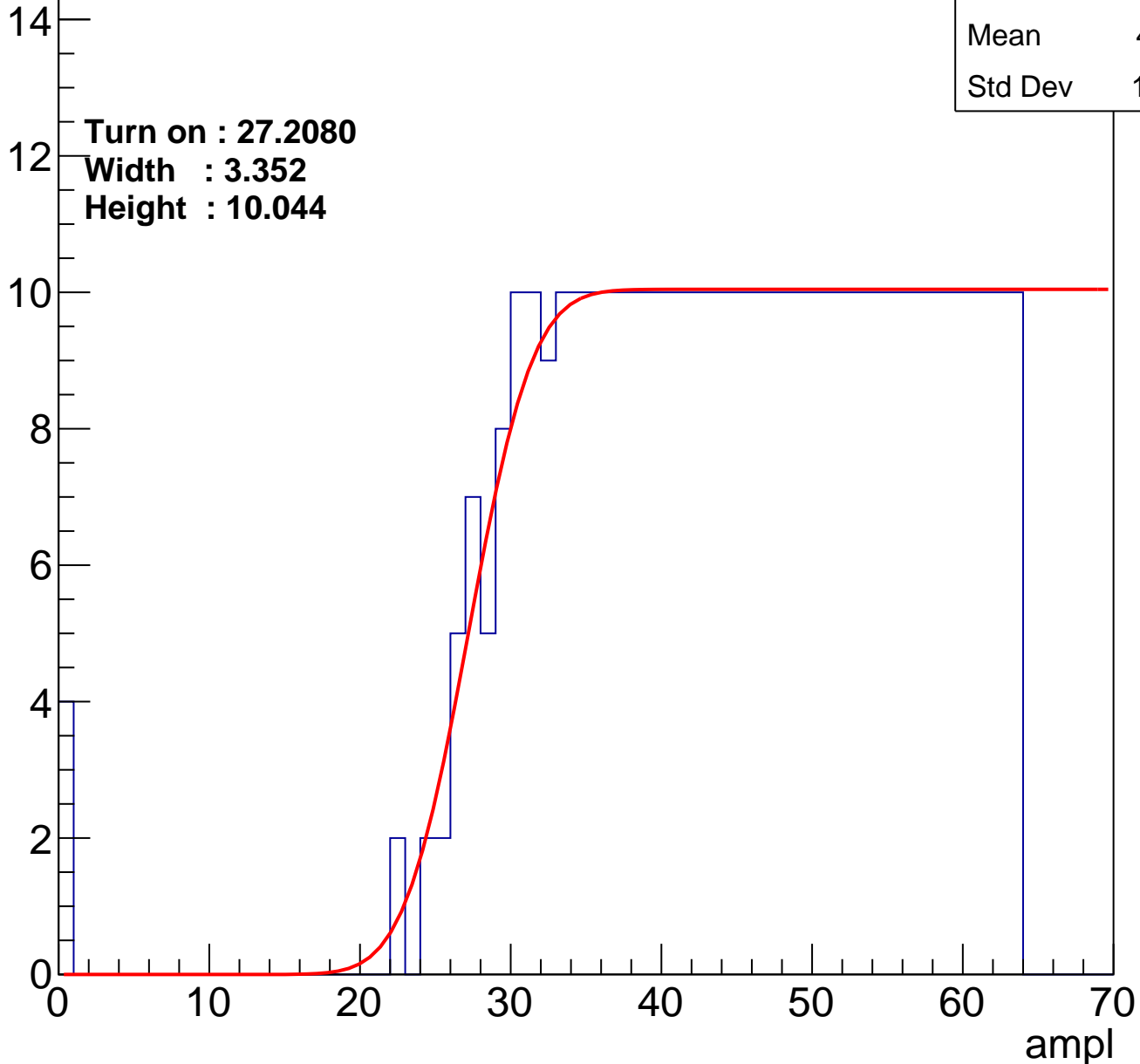
Entries	374
Mean	44.41
Std Dev	11.75

Turn on : 27.2080

Width : 3.352

Height : 10.044

Entry



B1L002S, U18-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.42
Std Dev	12.2

Turn on : 25.2528

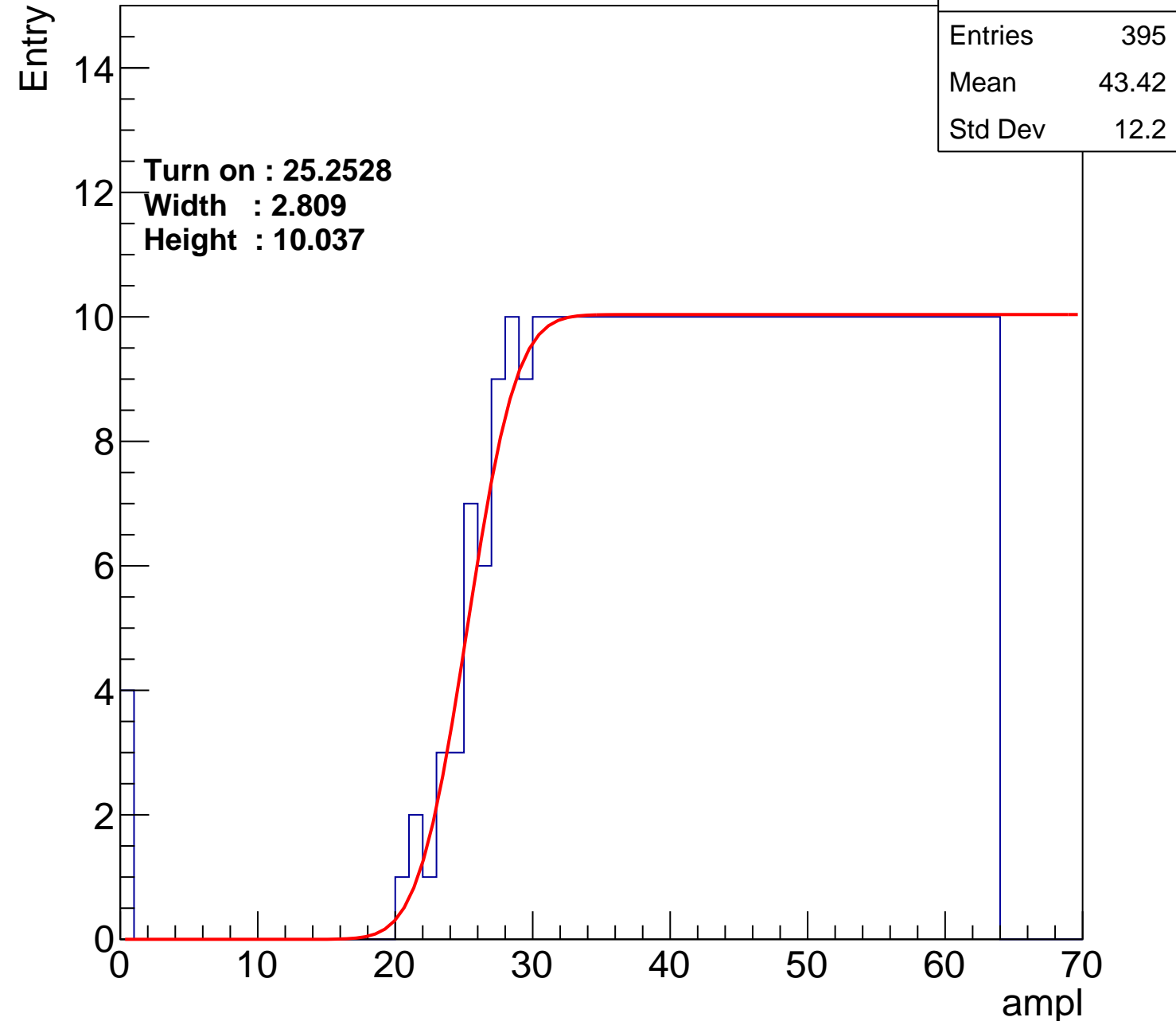
Width : 2.809

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.62
Std Dev	11.86

Turn on : 24.8451

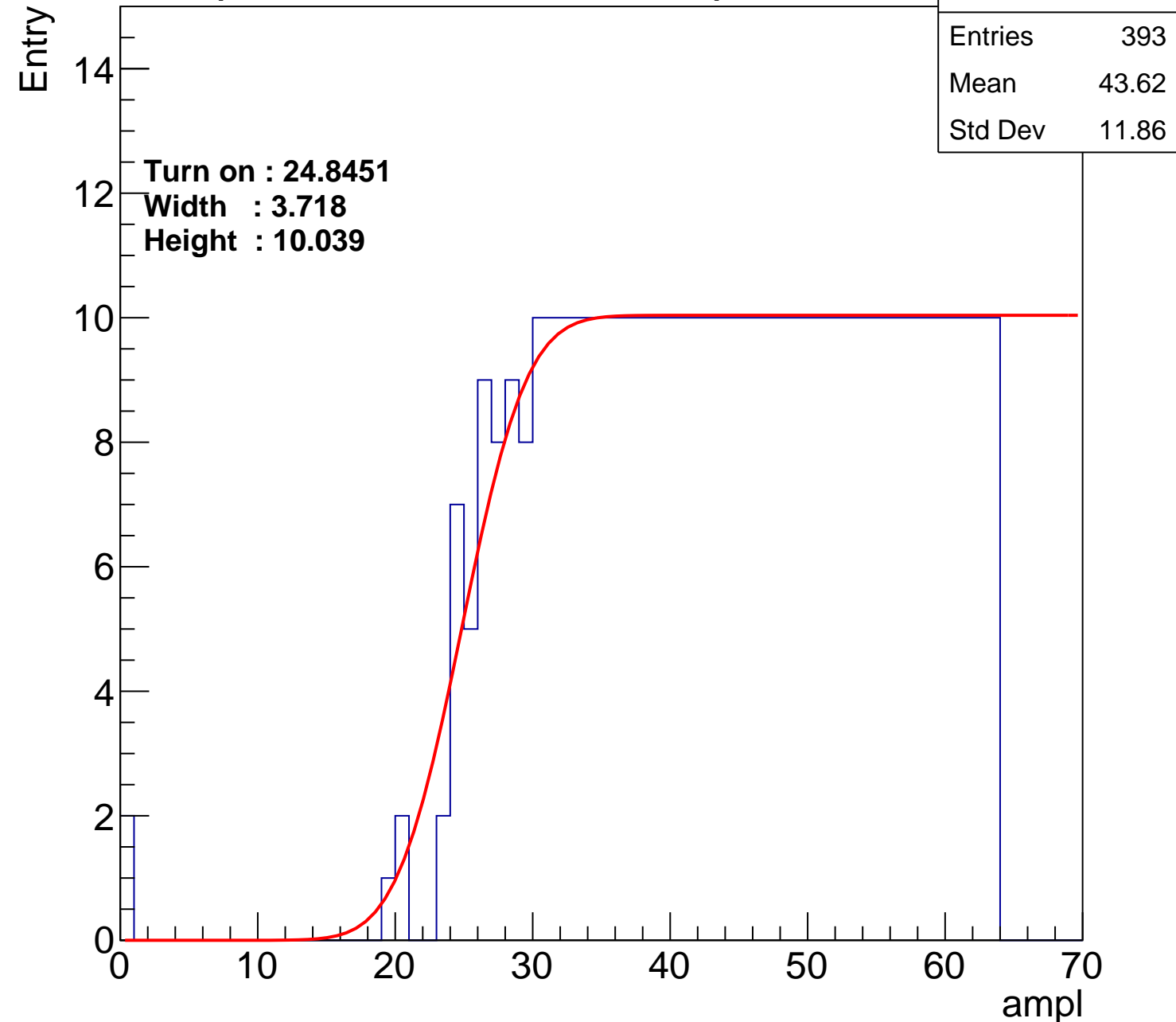
Width : 3.718

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch121

calib_packv5_042523_0143.root, FC#10, port B3

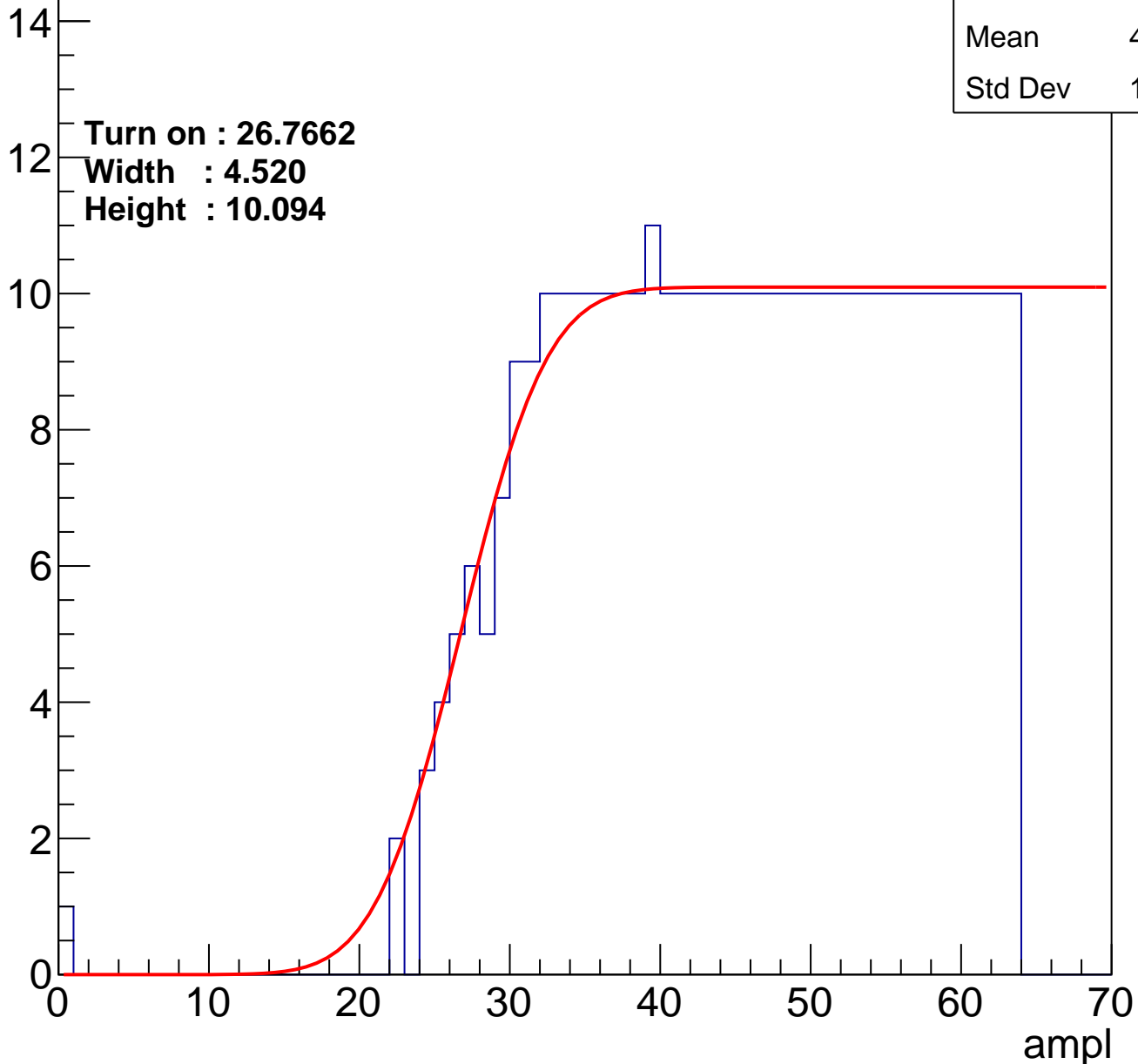
Entries	372
Mean	44.73
Std Dev	11.13

Turn on : 26.7662

Width : 4.520

Height : 10.094

Entry



B1L002S, U18-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.31
Std Dev	12.25

Turn on : 25.1892

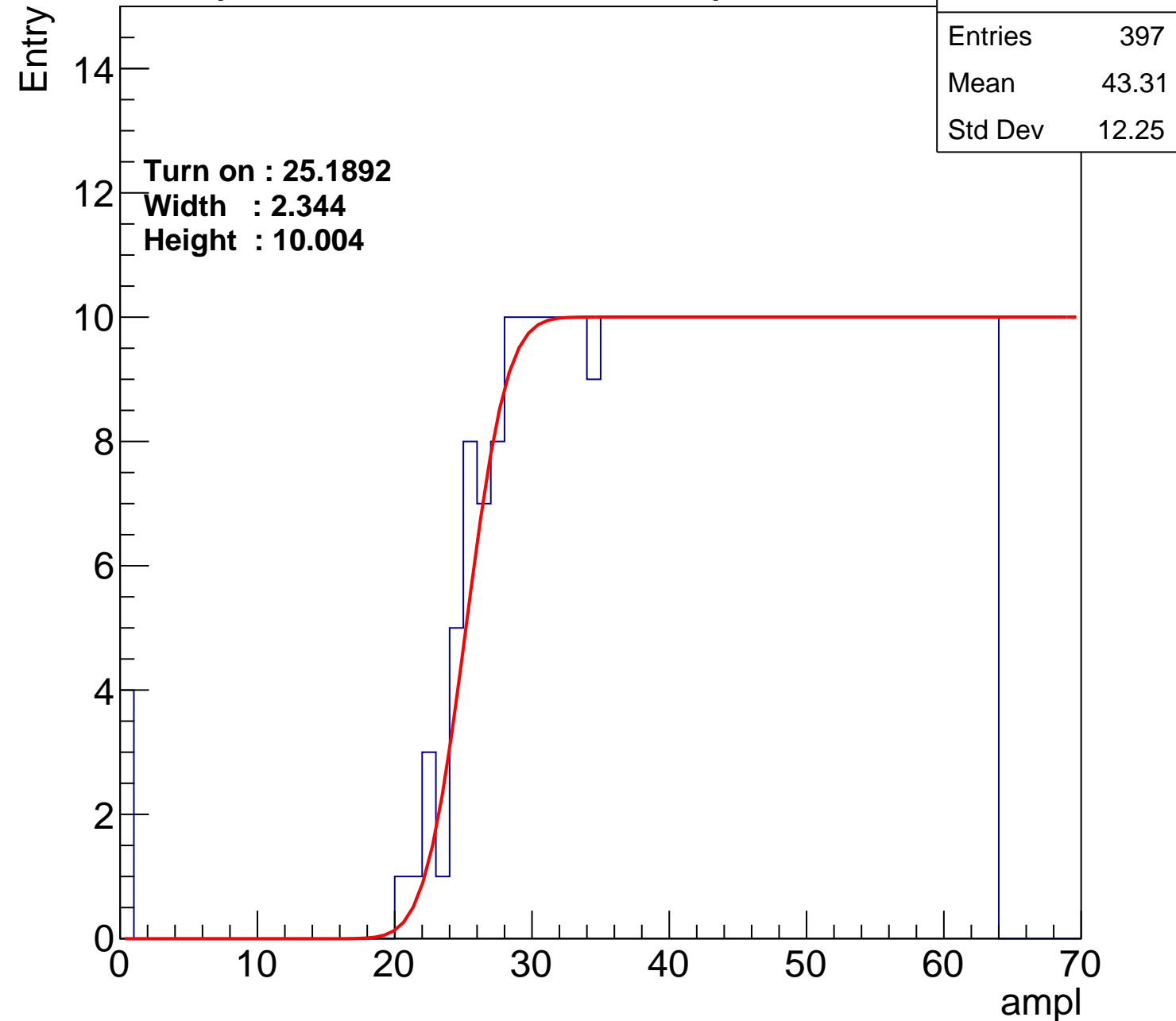
Width : 2.344

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	43.98
Std Dev	11.82

Turn on : 25.6056

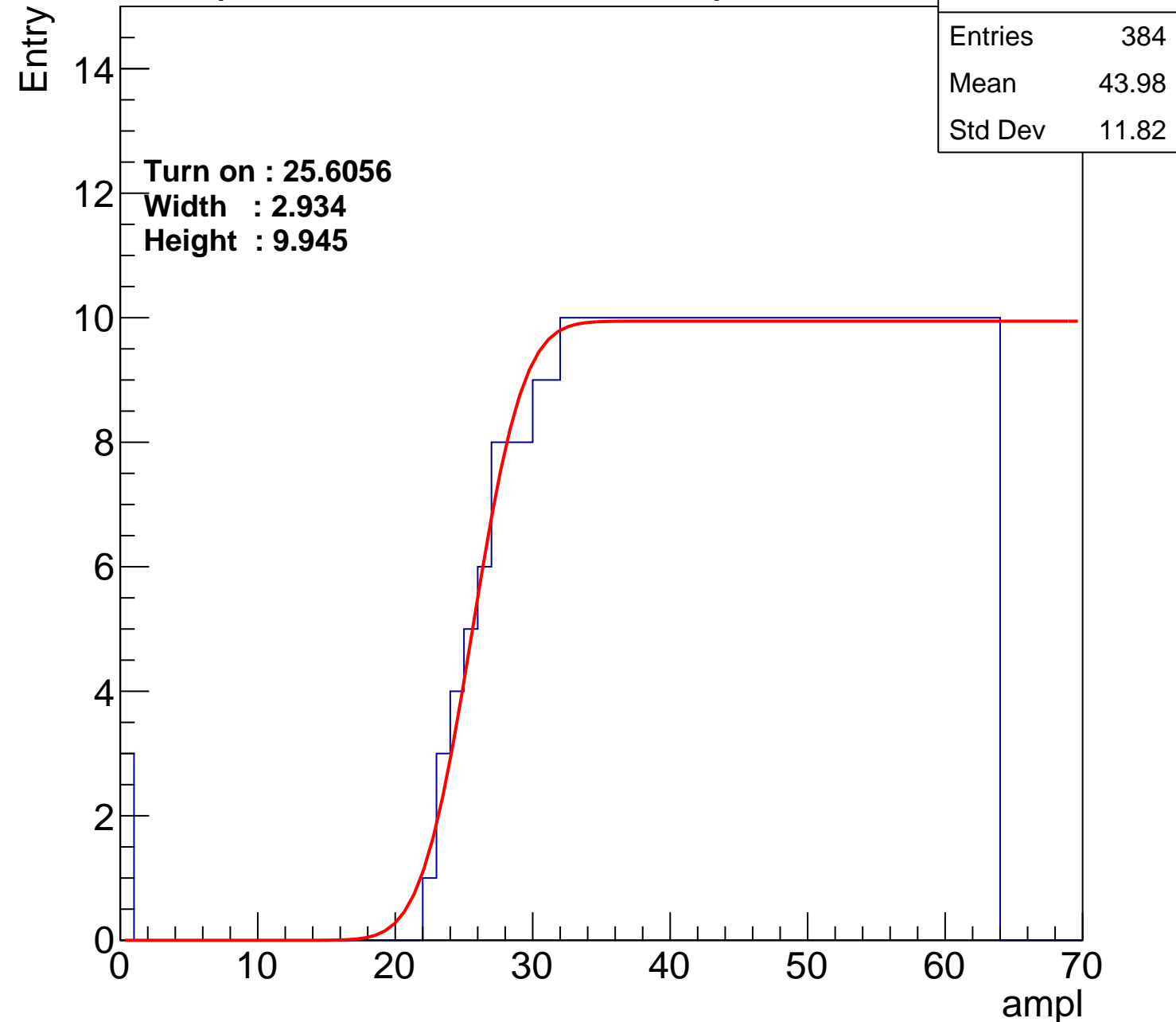
Width : 2.934

Height : 9.945

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch124

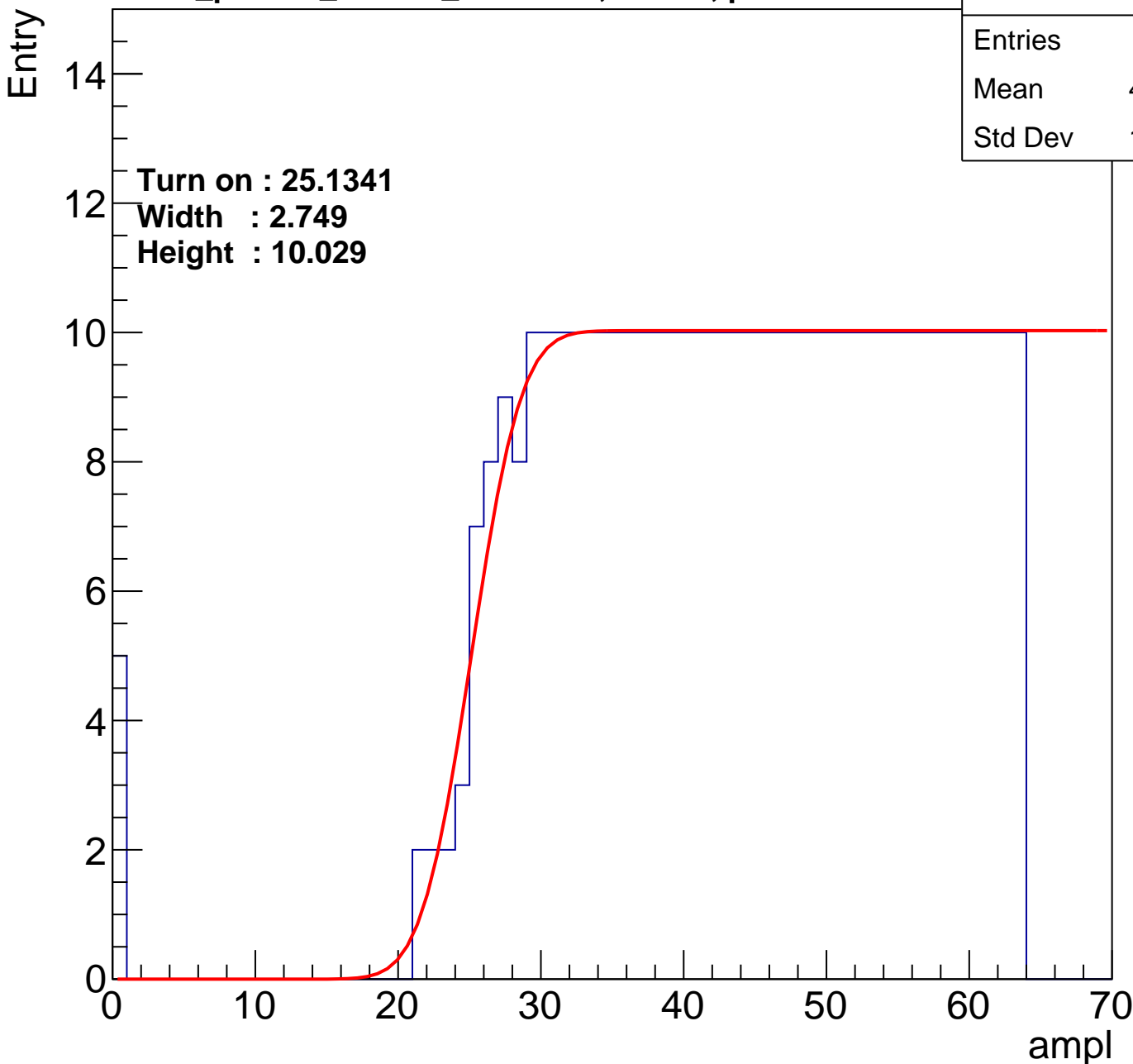
calib_packv5_042523_0143.root, FC#10, port B3

Turn on : 25.1341

Width : 2.749

Height : 10.029

Entries	396
Mean	43.32
Std Dev	12.36



B1L002S, U18-ch125

calib_packv5_042523_0143.root, FC#10, port B3

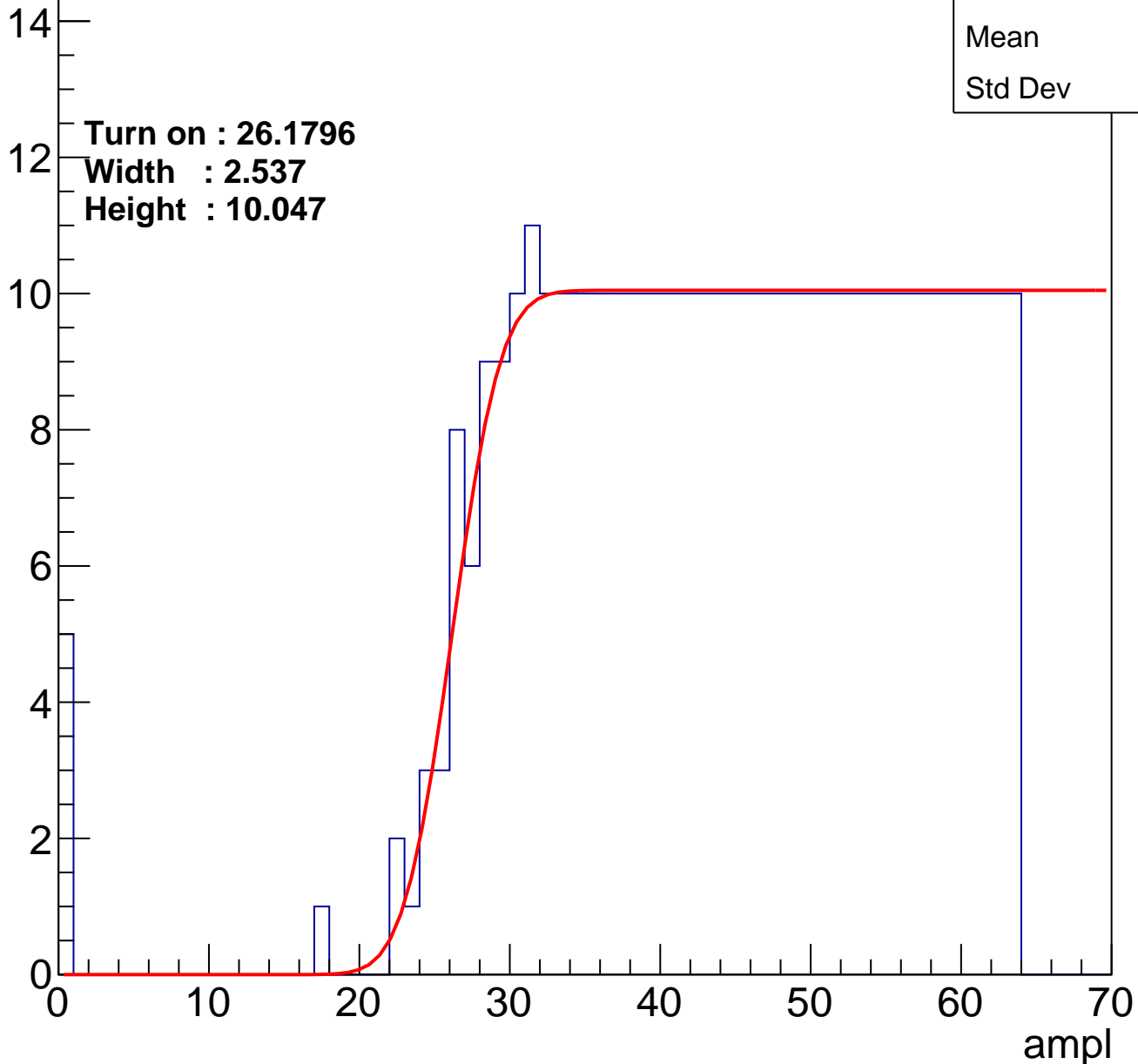
Entries	388
Mean	43.7
Std Dev	12.2

Turn on : 26.1796

Width : 2.537

Height : 10.047

Entry



B1L002S, U18-ch126

calib_packv5_042523_0143.root, FC#10, port B3

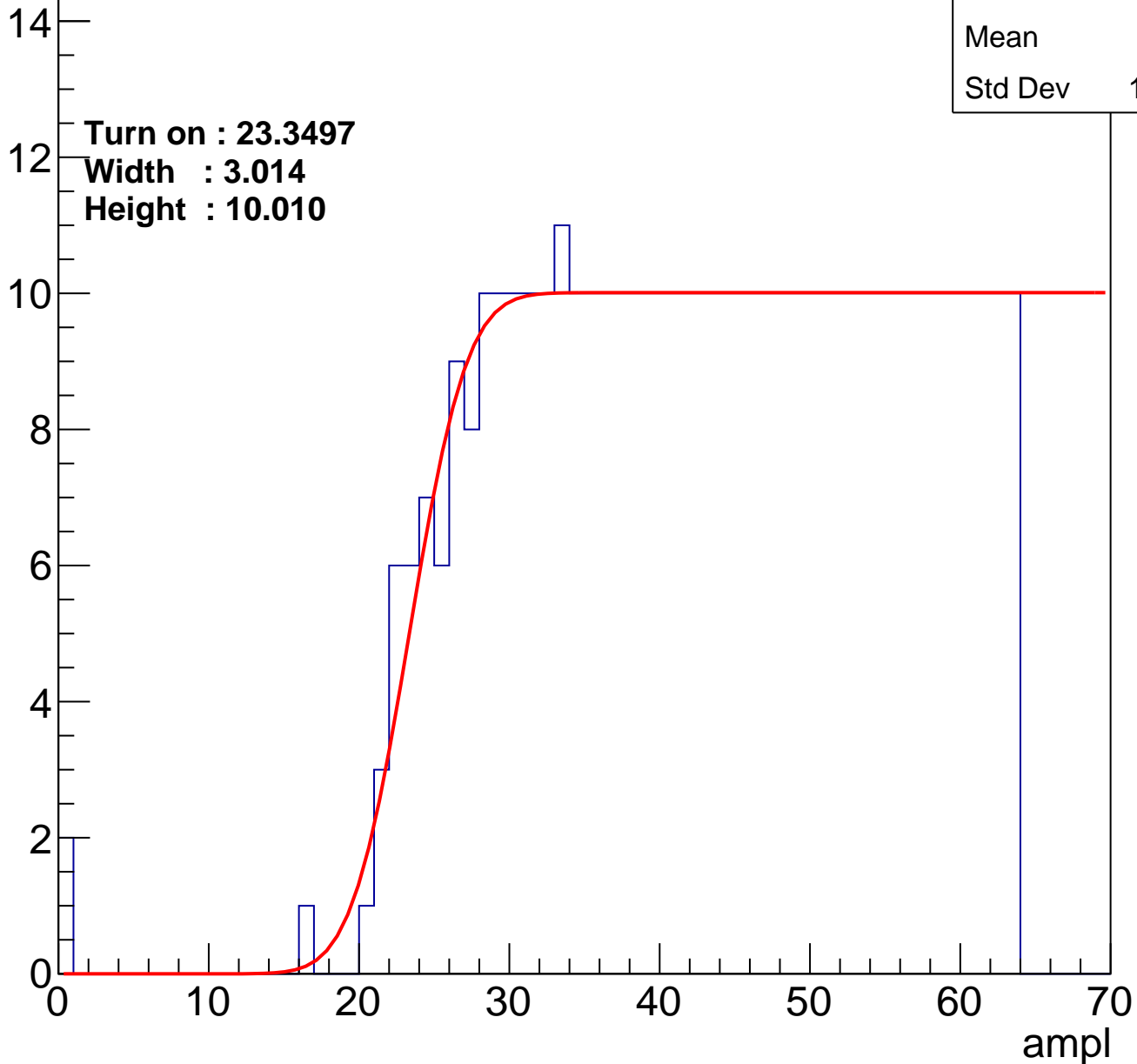
Entries	410
Mean	42.8
Std Dev	12.27

Turn on : 23.3497

Width : 3.014

Height : 10.010

Entry



B1L002S, U18-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.37
Std Dev	11.94

Turn on : 24.7337

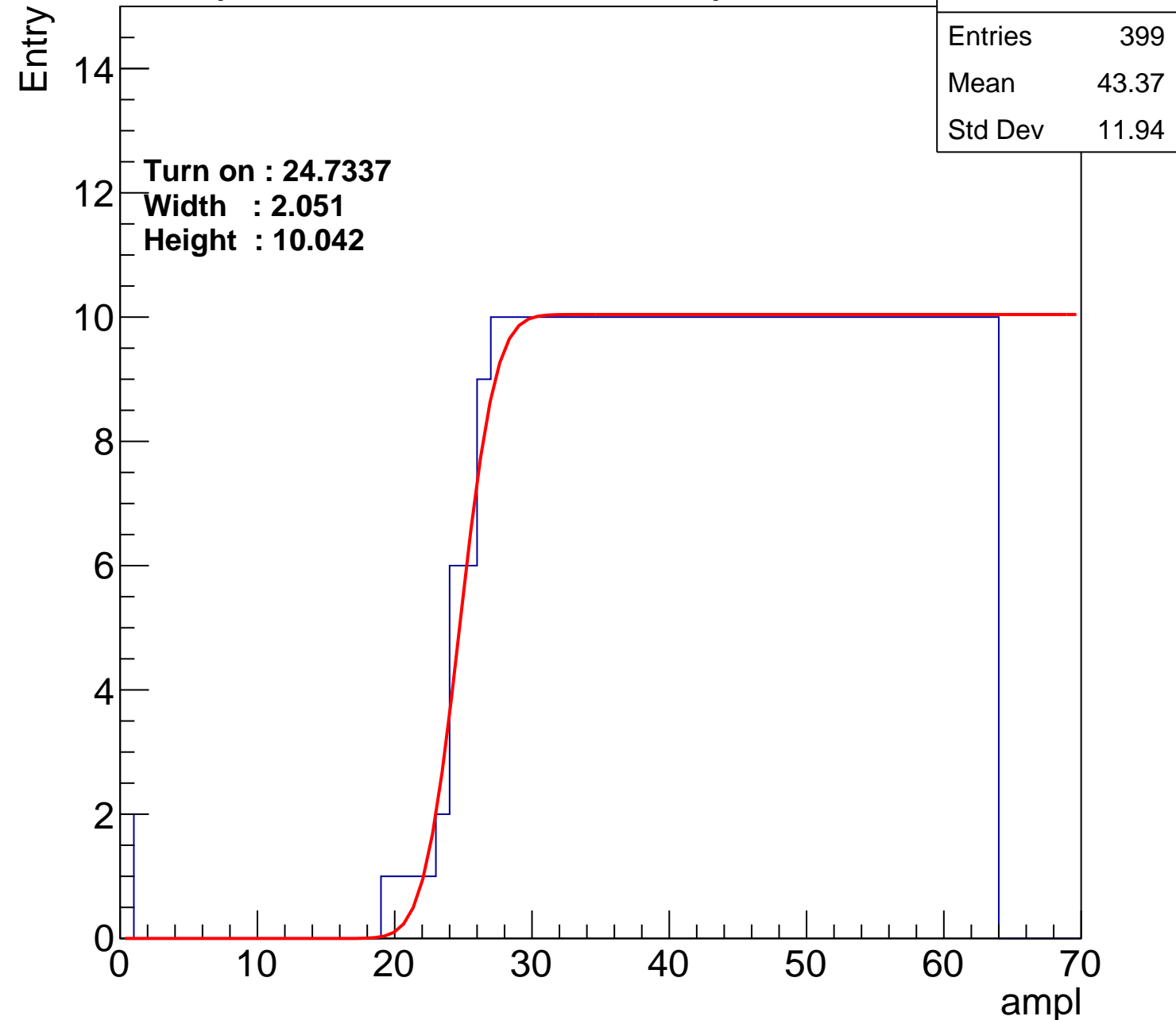
Width : 2.051

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U18-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.37
Std Dev	11.94

Turn on : 24.7337

Width : 2.051

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl

