

B1L002S, U20-ch0

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.49
Std Dev	11.79

Turn on : 25.2628

Width : 2.646

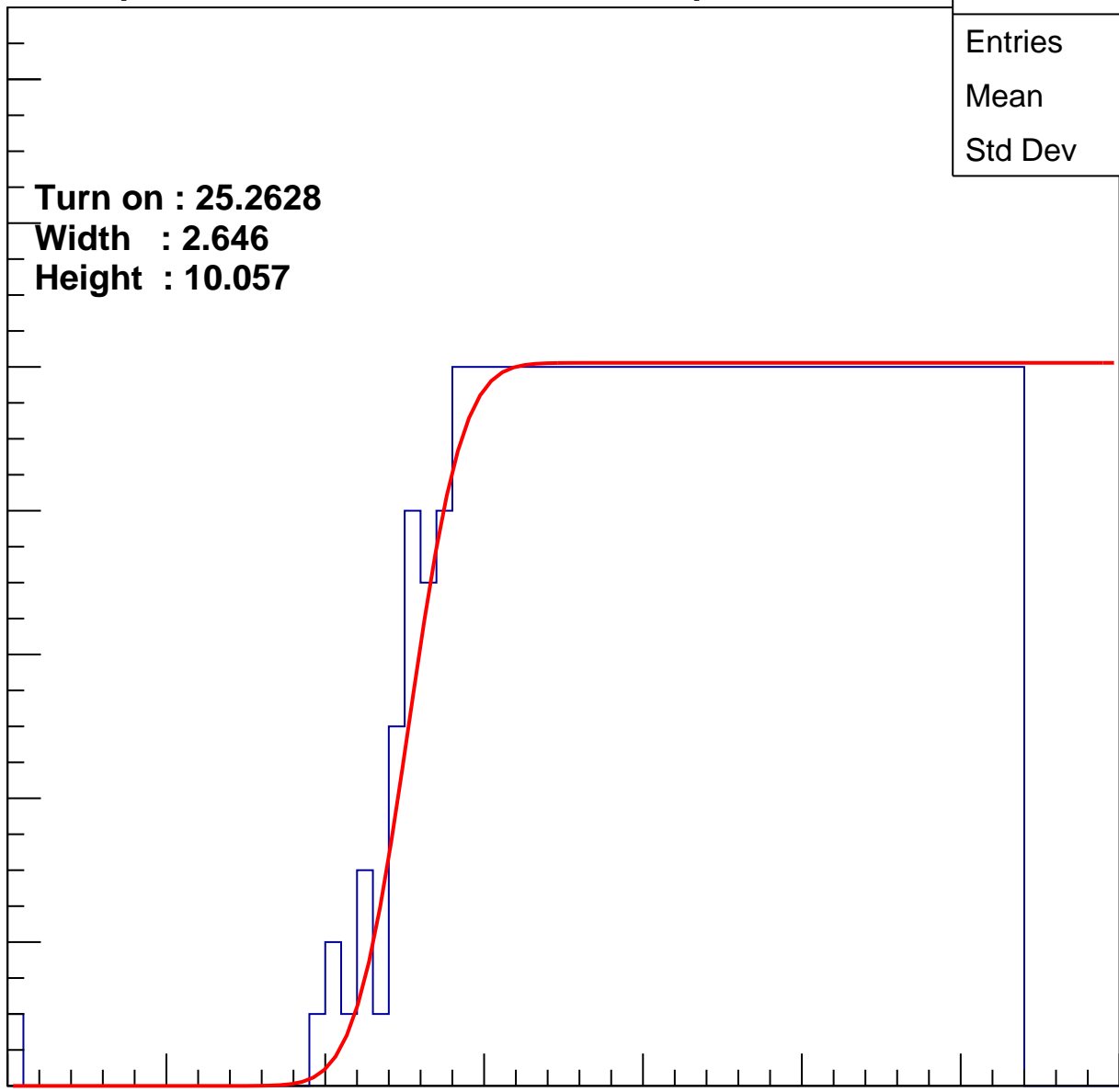
Height : 10.057

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch1

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.4
Std Dev	11.28

Turn on : 26.2744

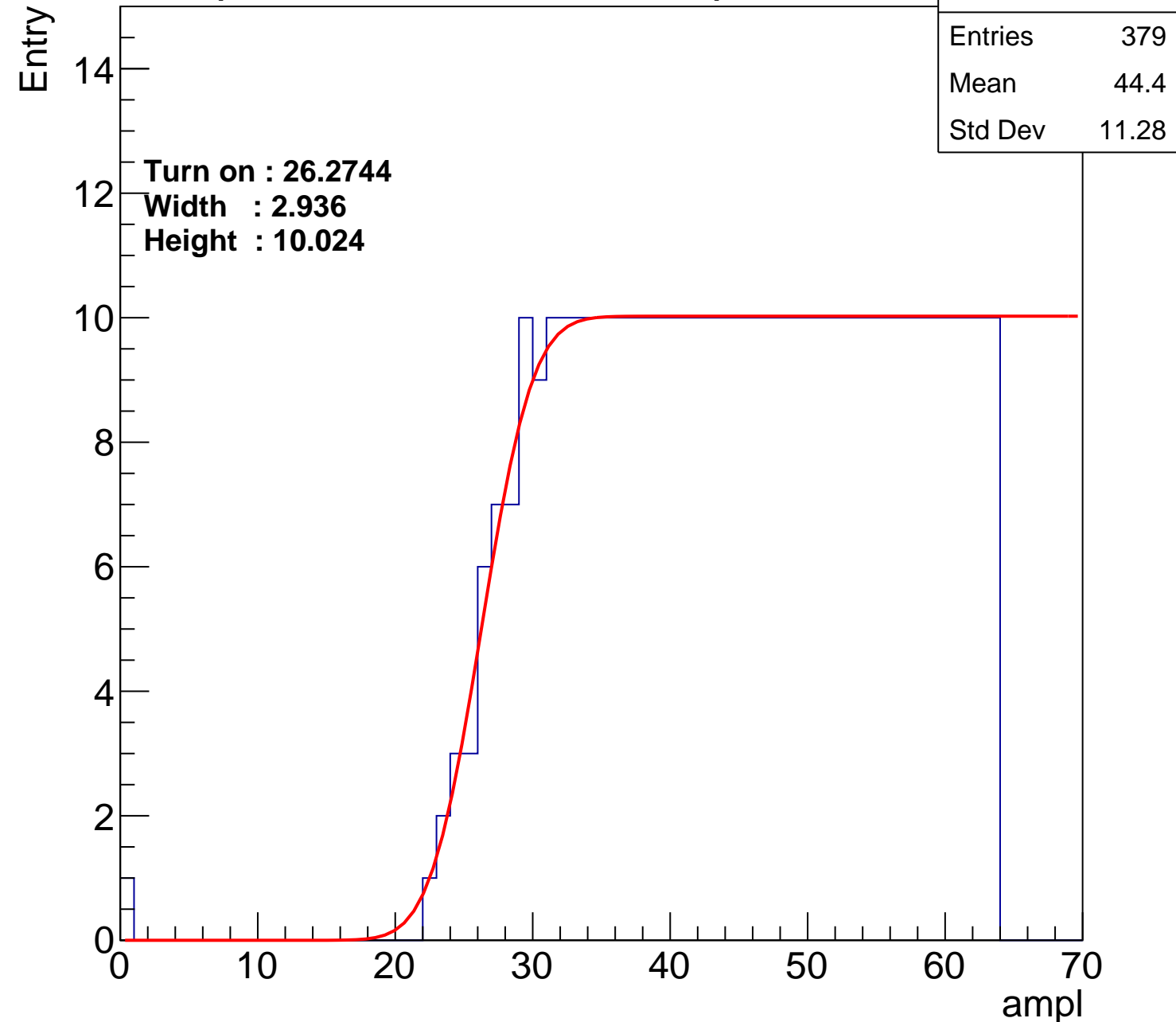
Width : 2.936

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch2

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.13
Std Dev	12.17

Turn on : 24.0081

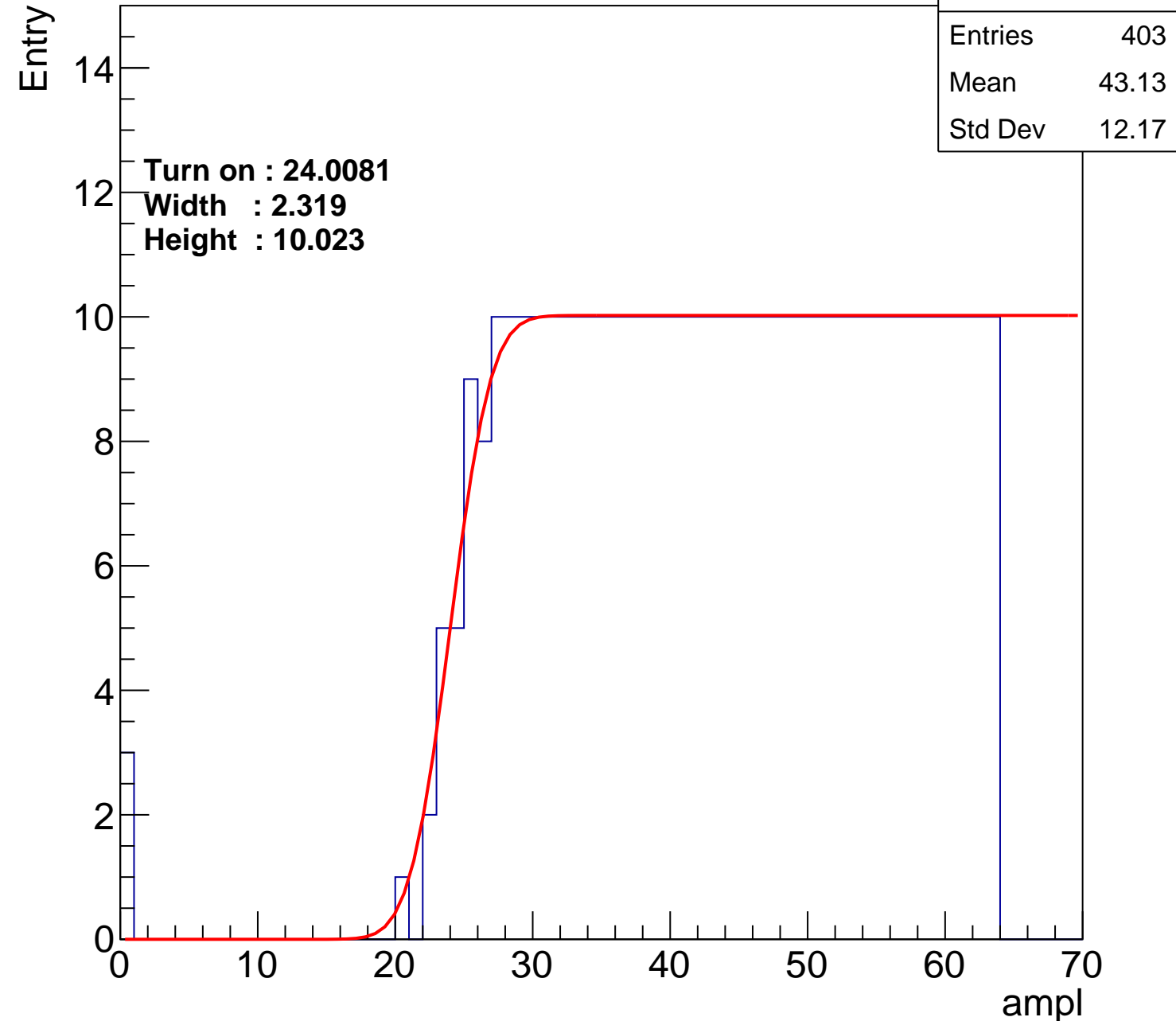
Width : 2.319

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch3

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.56
Std Dev	11.18

Turn on : 26.4932

Width : 3.138

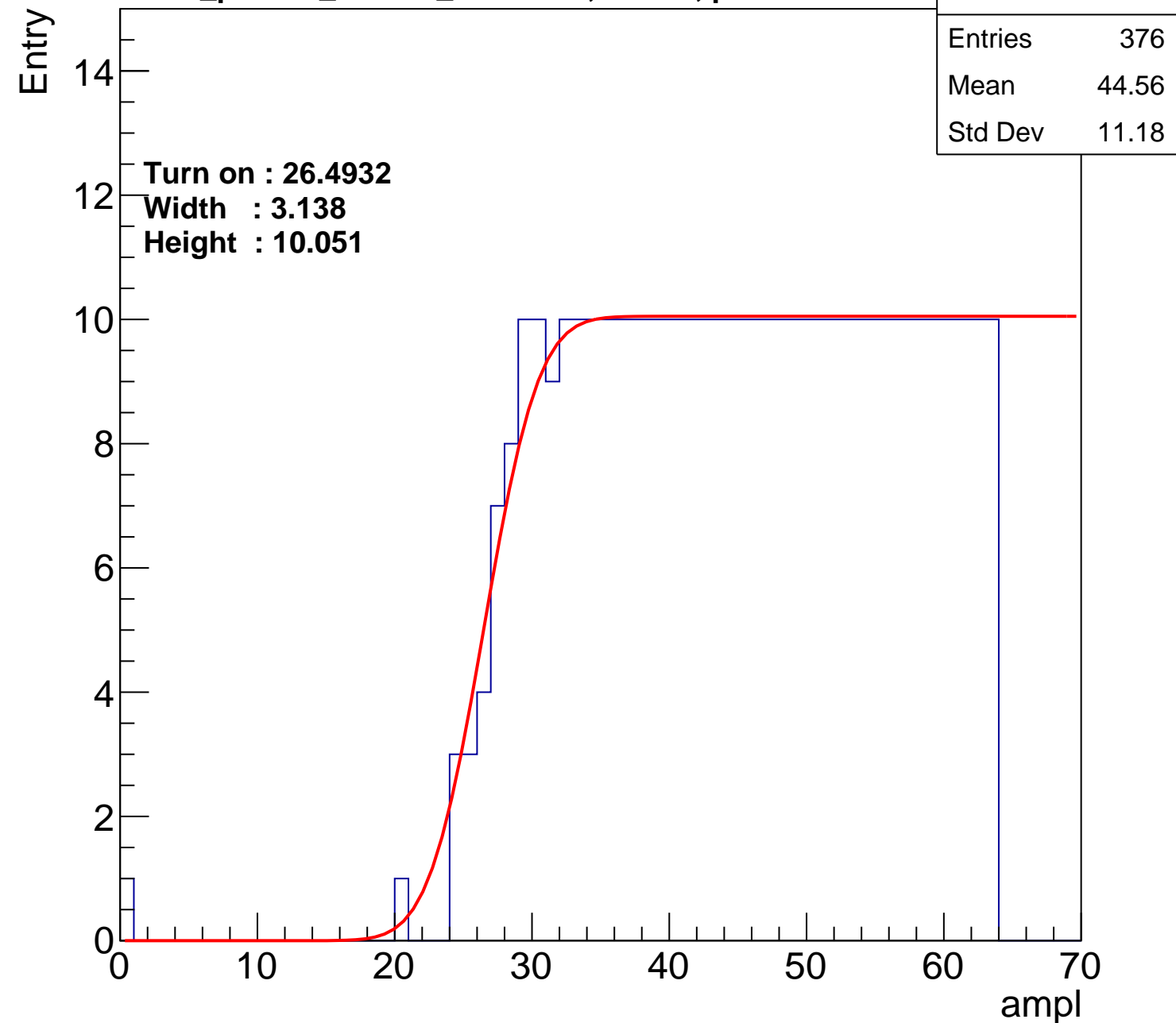
Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch4

calib_packv5_042523_0143.root, FC#10, port B3

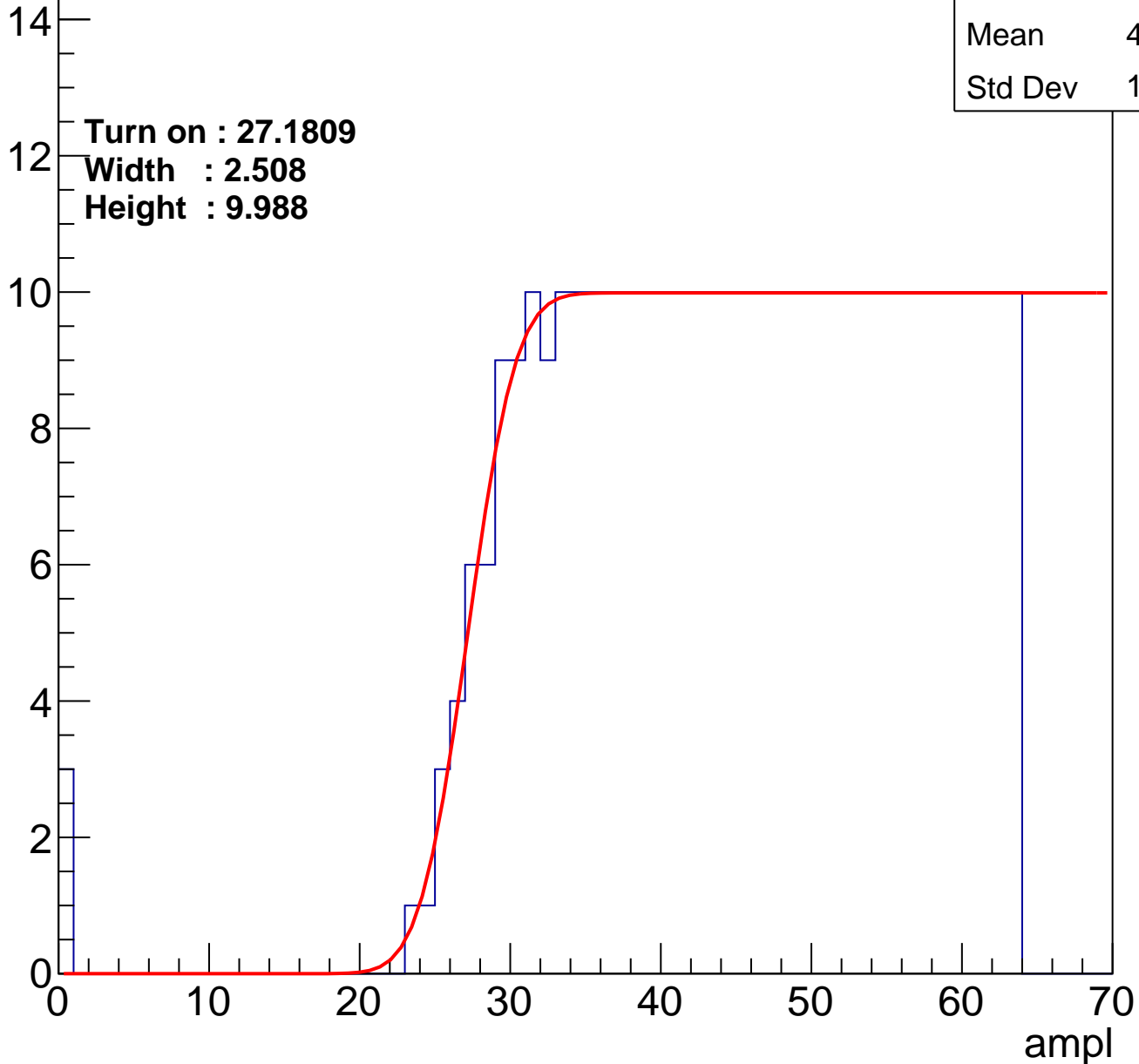
Entries	371
Mean	44.65
Std Dev	11.45

Turn on : 27.1809

Width : 2.508

Height : 9.988

Entry



B1L002S, U20-ch5

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.65
Std Dev	11.26

Turn on : 26.9028

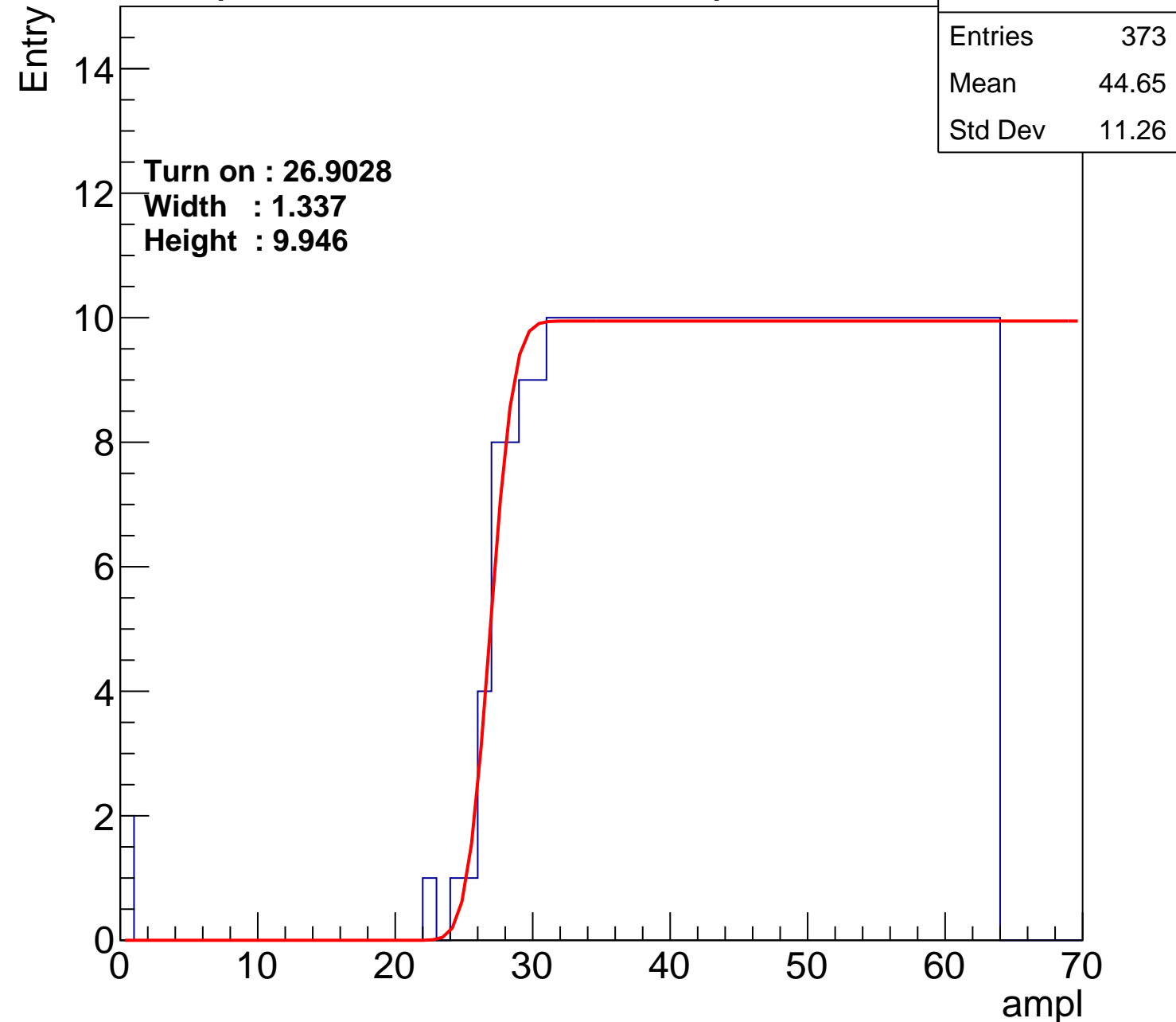
Width : 1.337

Height : 9.946

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch6

calib_packv5_042523_0143.root, FC#10, port B3

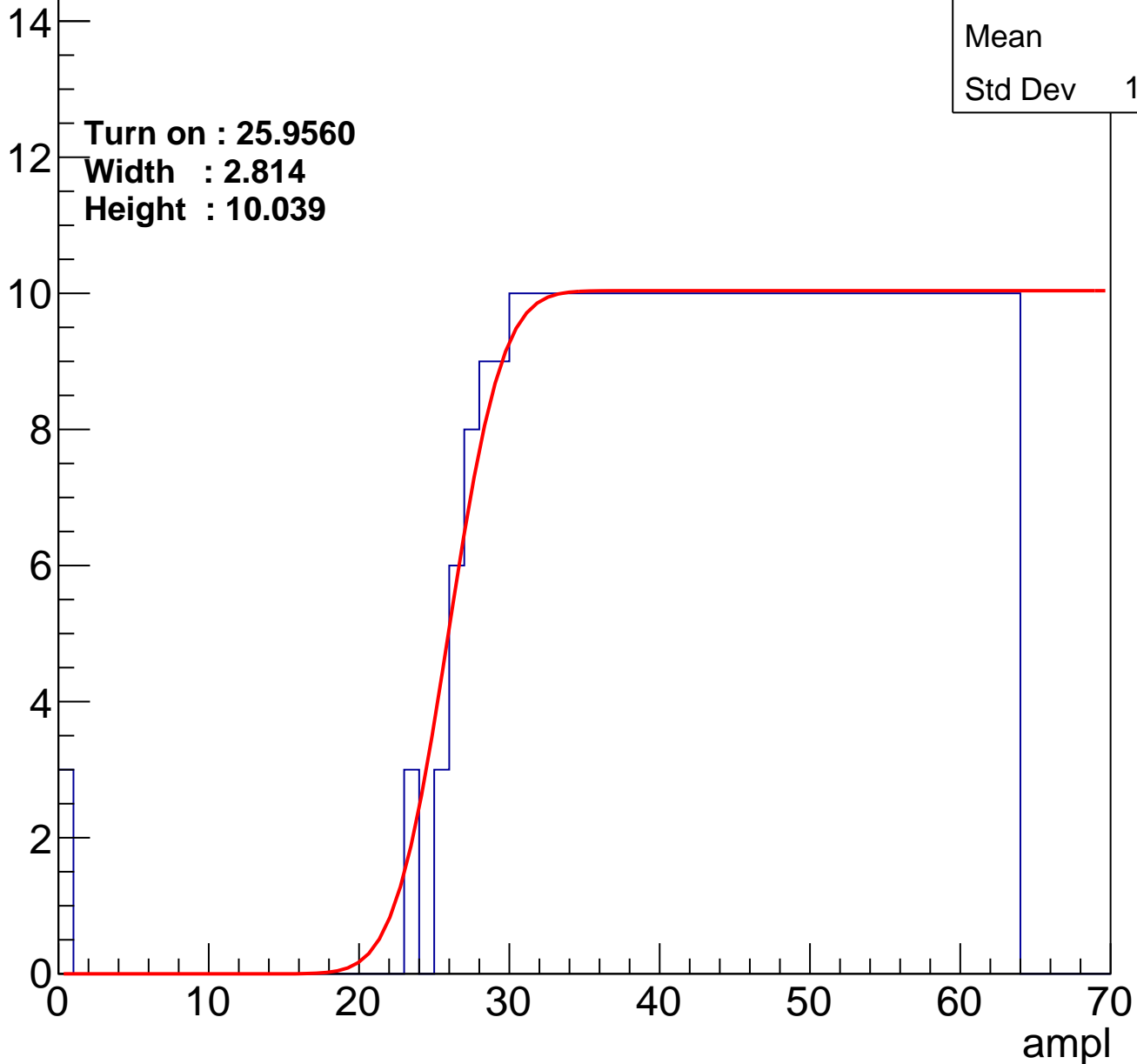
Entries	381
Mean	44.2
Std Dev	11.64

Turn on : 25.9560

Width : 2.814

Height : 10.039

Entry



B1L002S, U20-ch7

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 26.7328

Width : 2.543

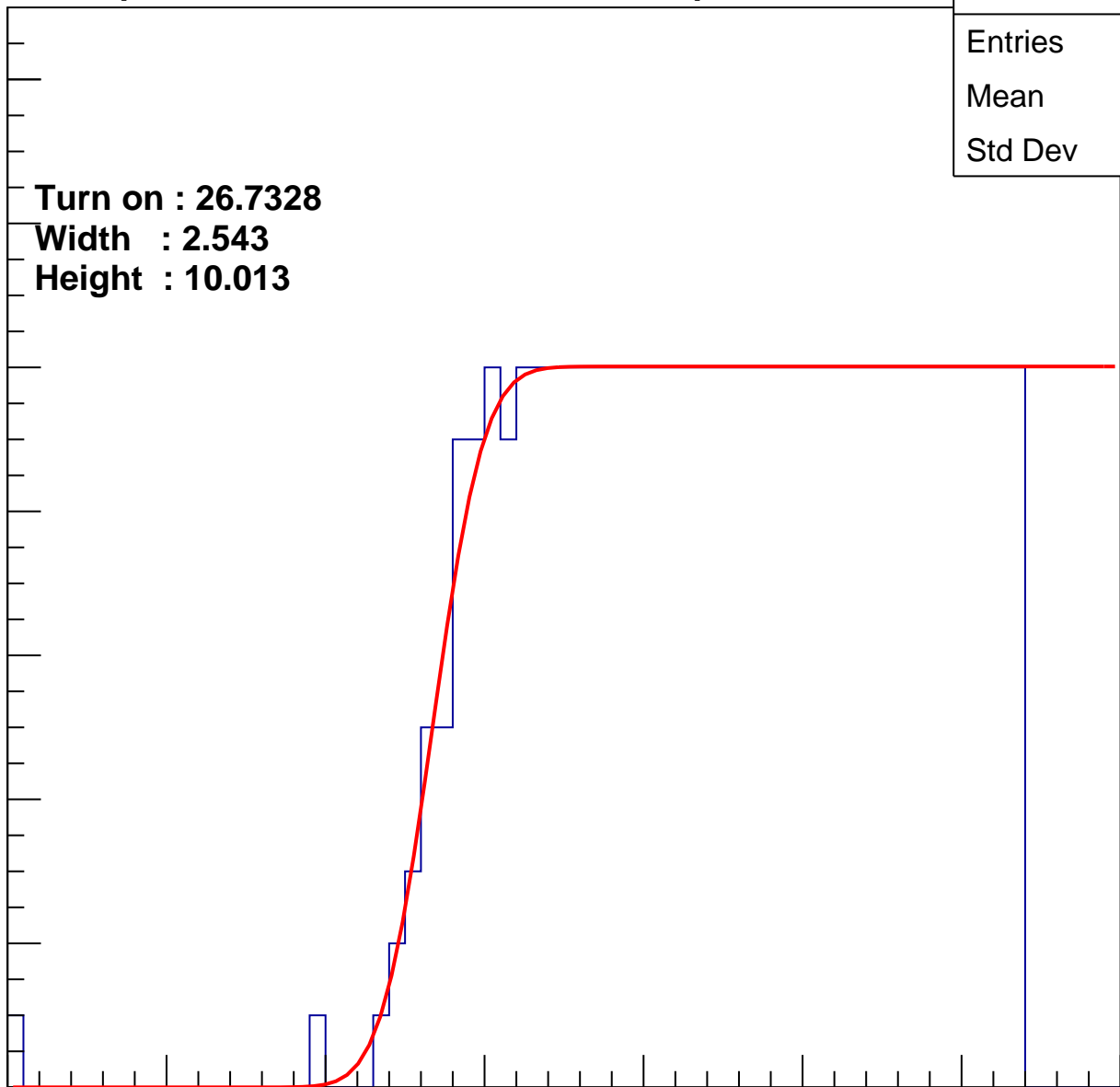
Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch8

calib_packv5_042523_0143.root, FC#10, port B3

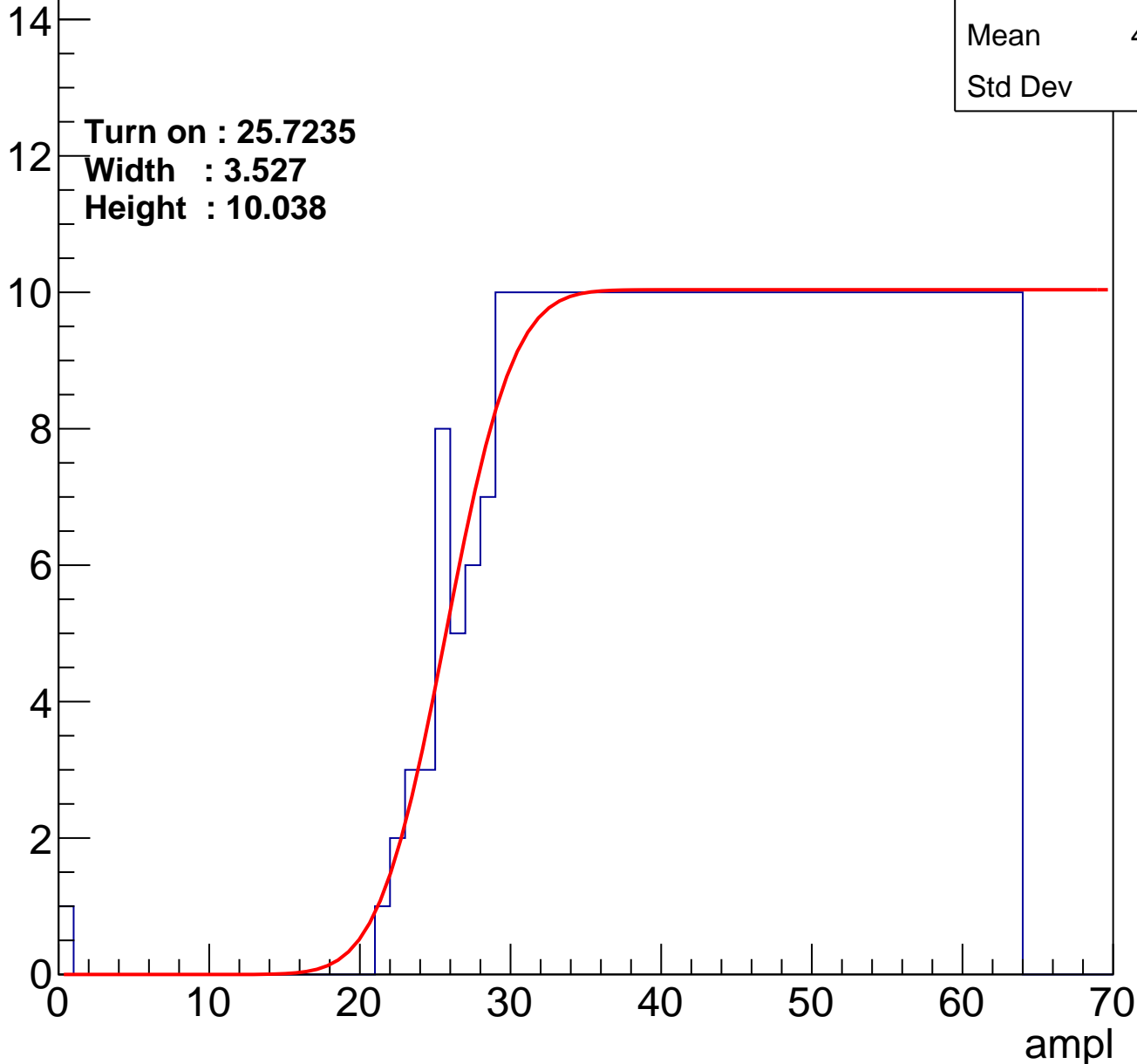
Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 25.7235

Width : 3.527

Height : 10.038

Entry



B1L002S, U20-ch9

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.22
Std Dev	12.03

Turn on : 24.2416

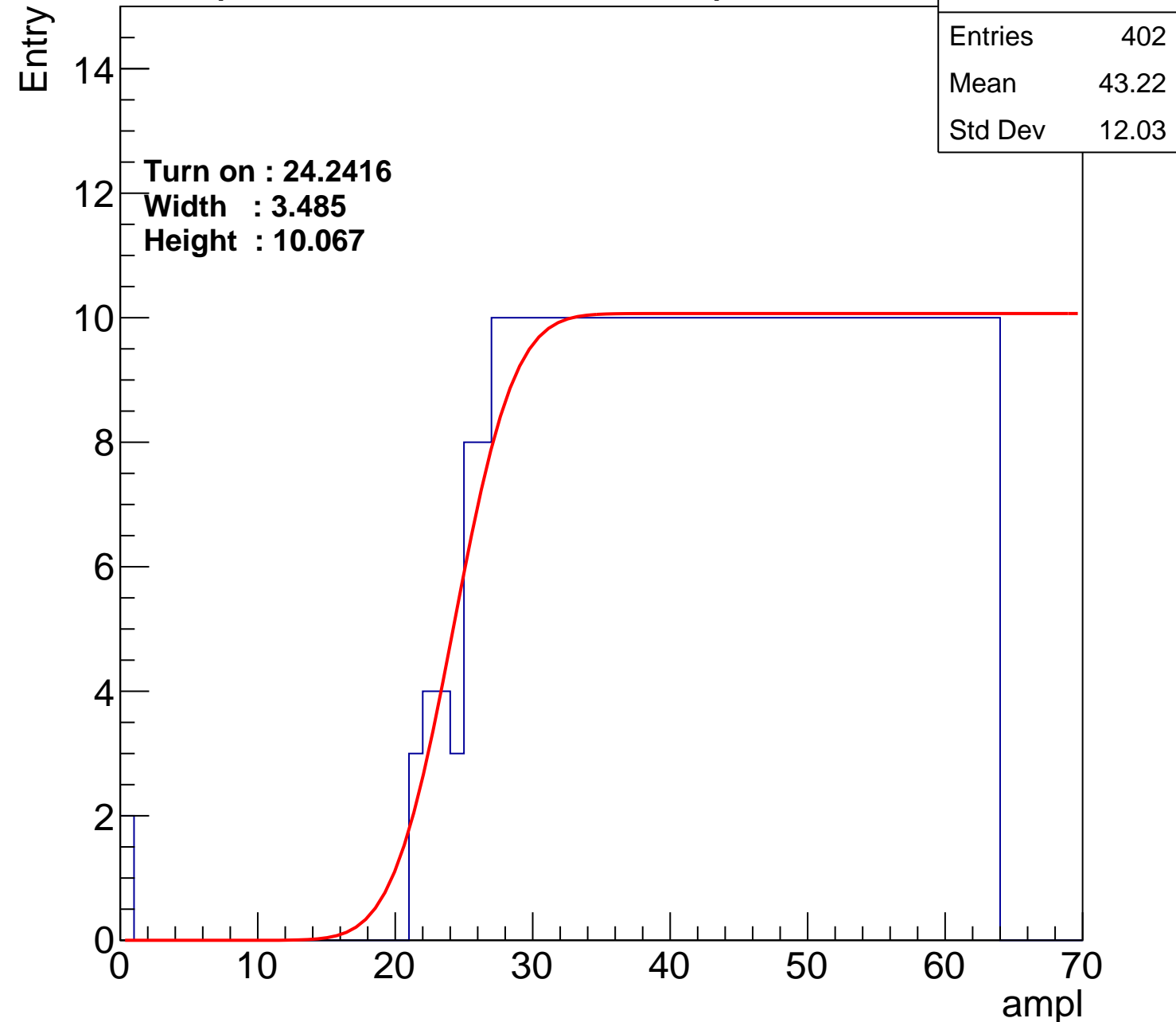
Width : 3.485

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch10

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.72
Std Dev	12.03

Turn on : 26.0489

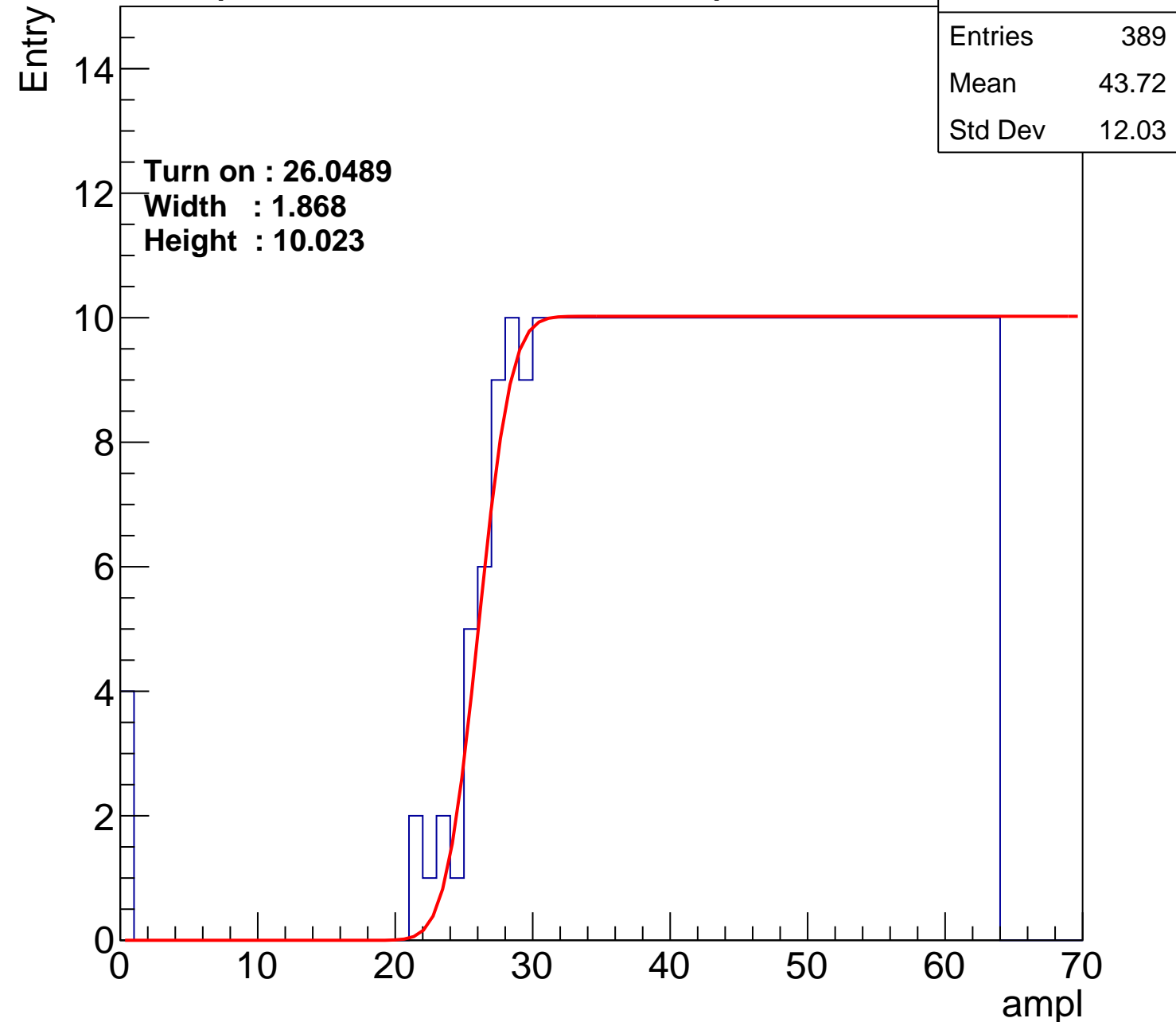
Width : 1.868

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch11

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.61
Std Dev	11.47

Turn on : 26.9419

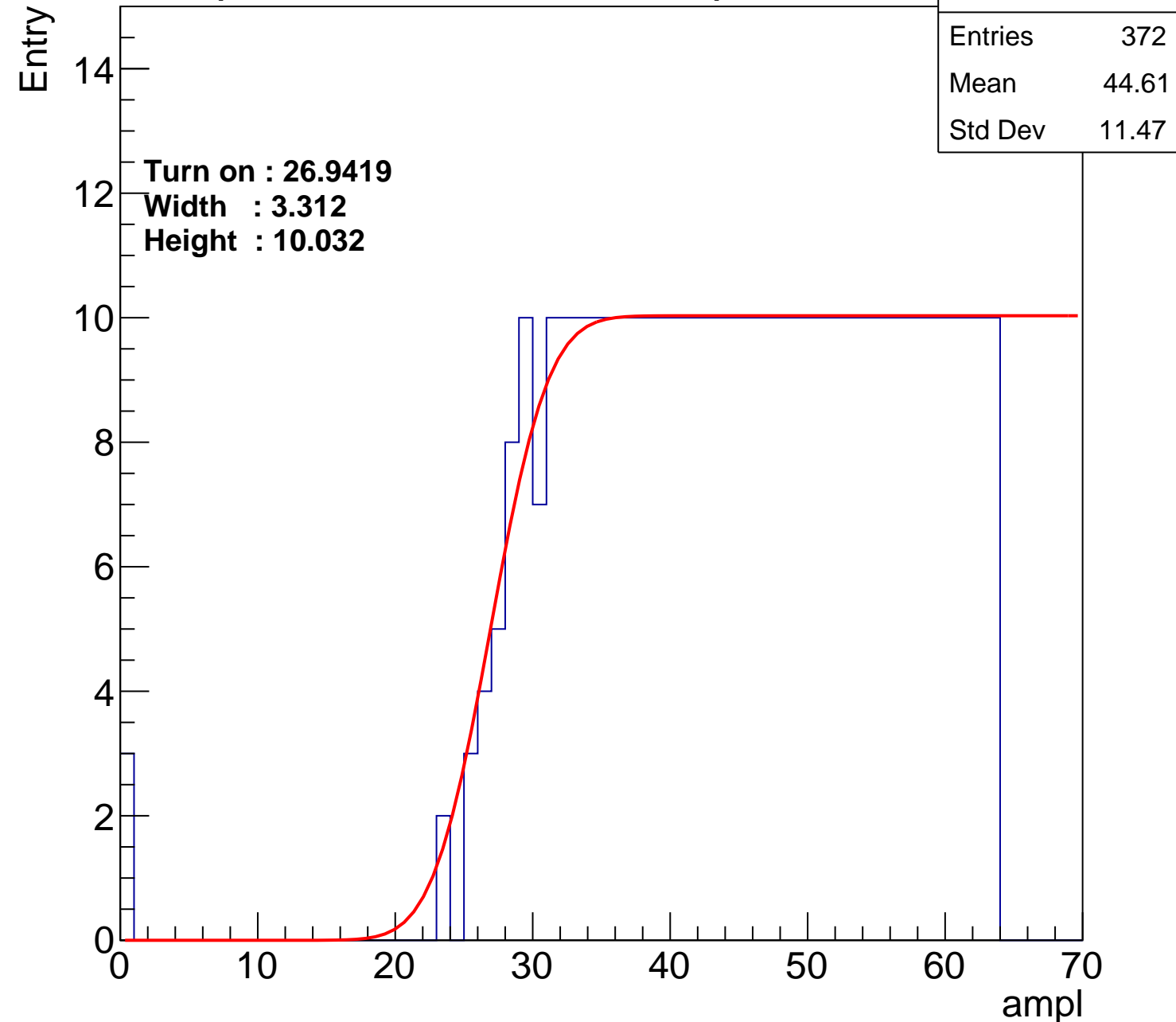
Width : 3.312

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch12

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.84
Std Dev	11.55

Turn on : 25.1103

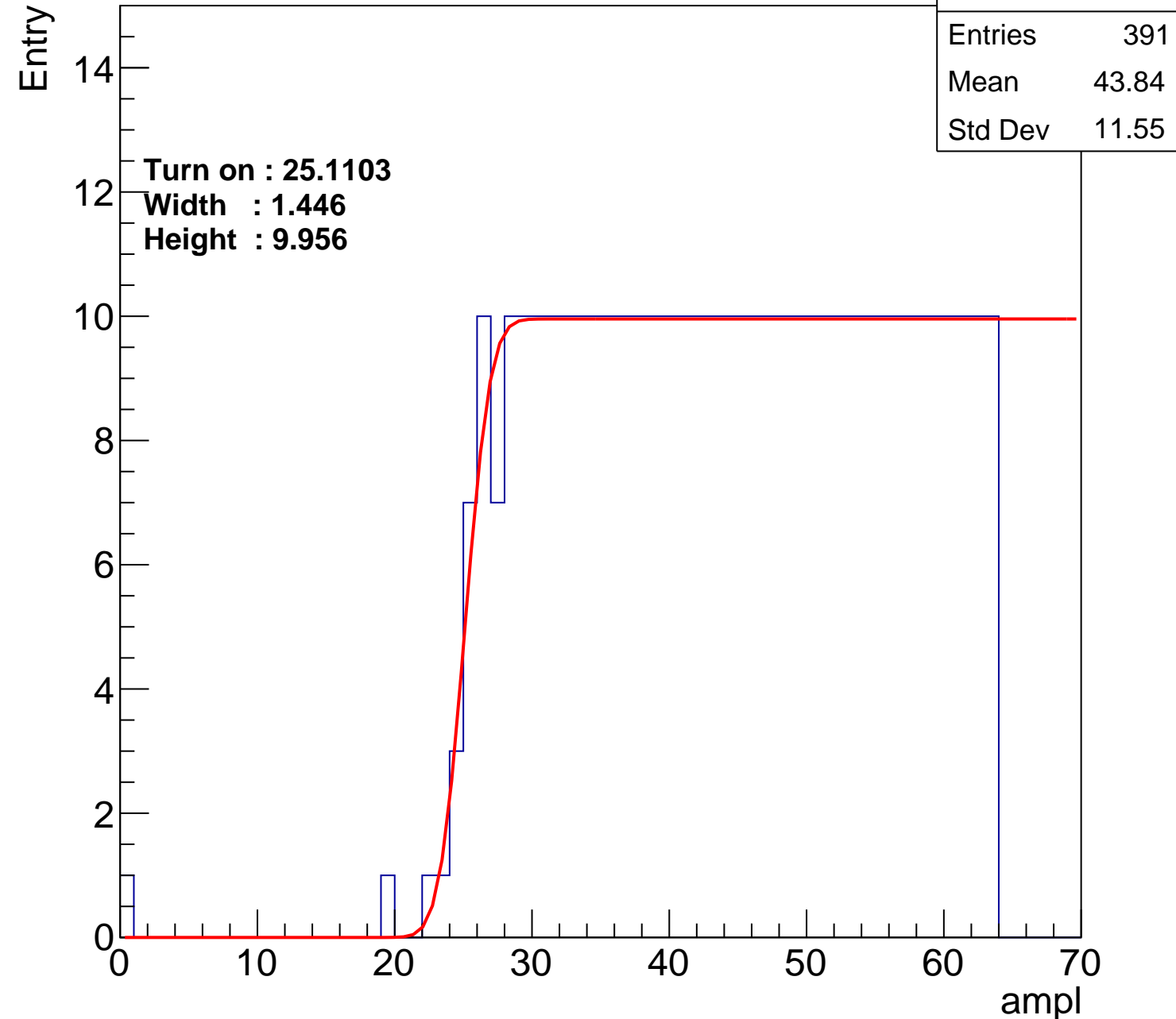
Width : 1.446

Height : 9.956

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch13

calib_packv5_042523_0143.root, FC#10, port B3

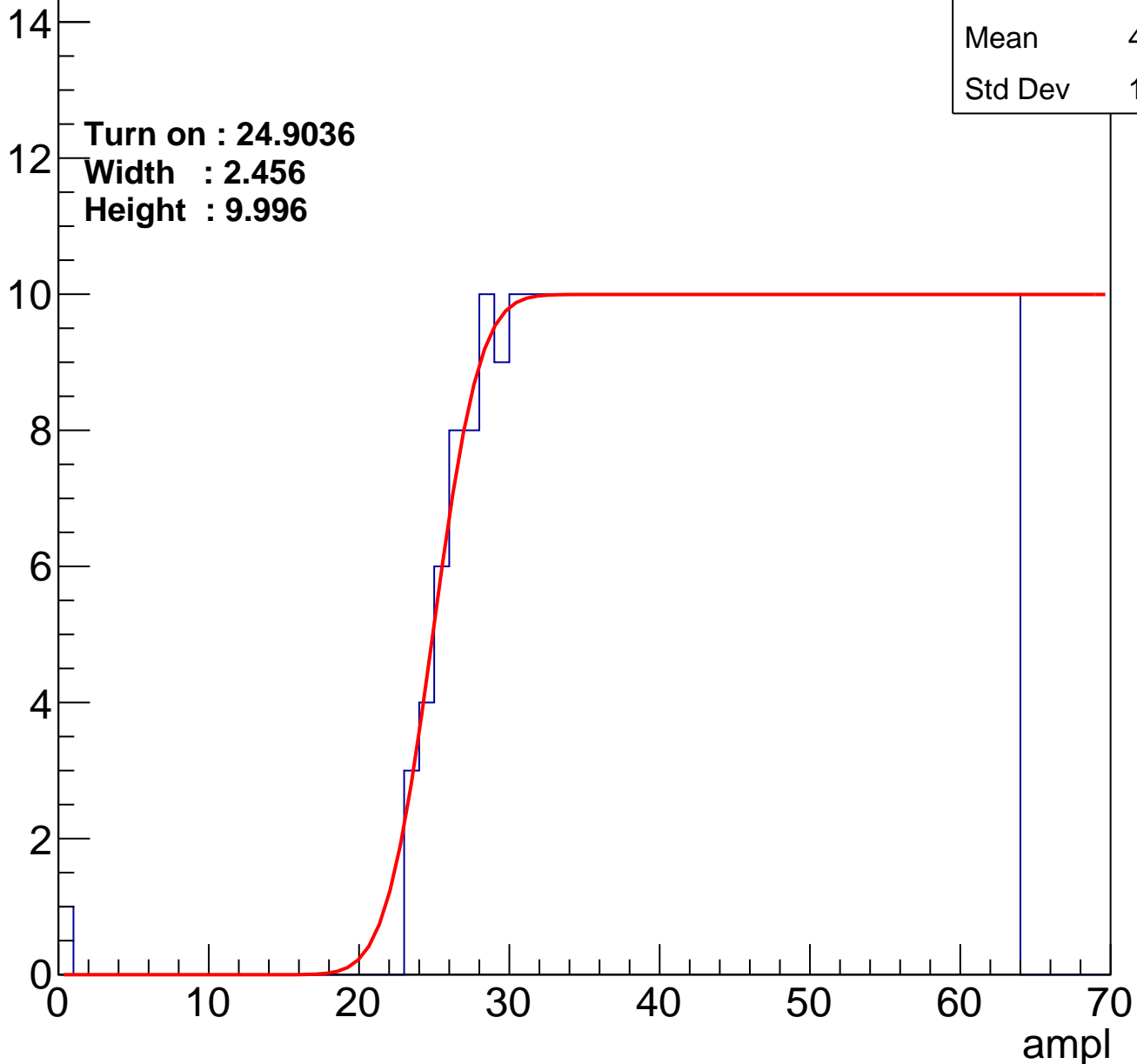
Entries	389
Mean	43.93
Std Dev	11.49

Turn on : 24.9036

Width : 2.456

Height : 9.996

Entry



B1L002S, U20-ch14

calib_packv5_042523_0143.root, FC#10, port B3

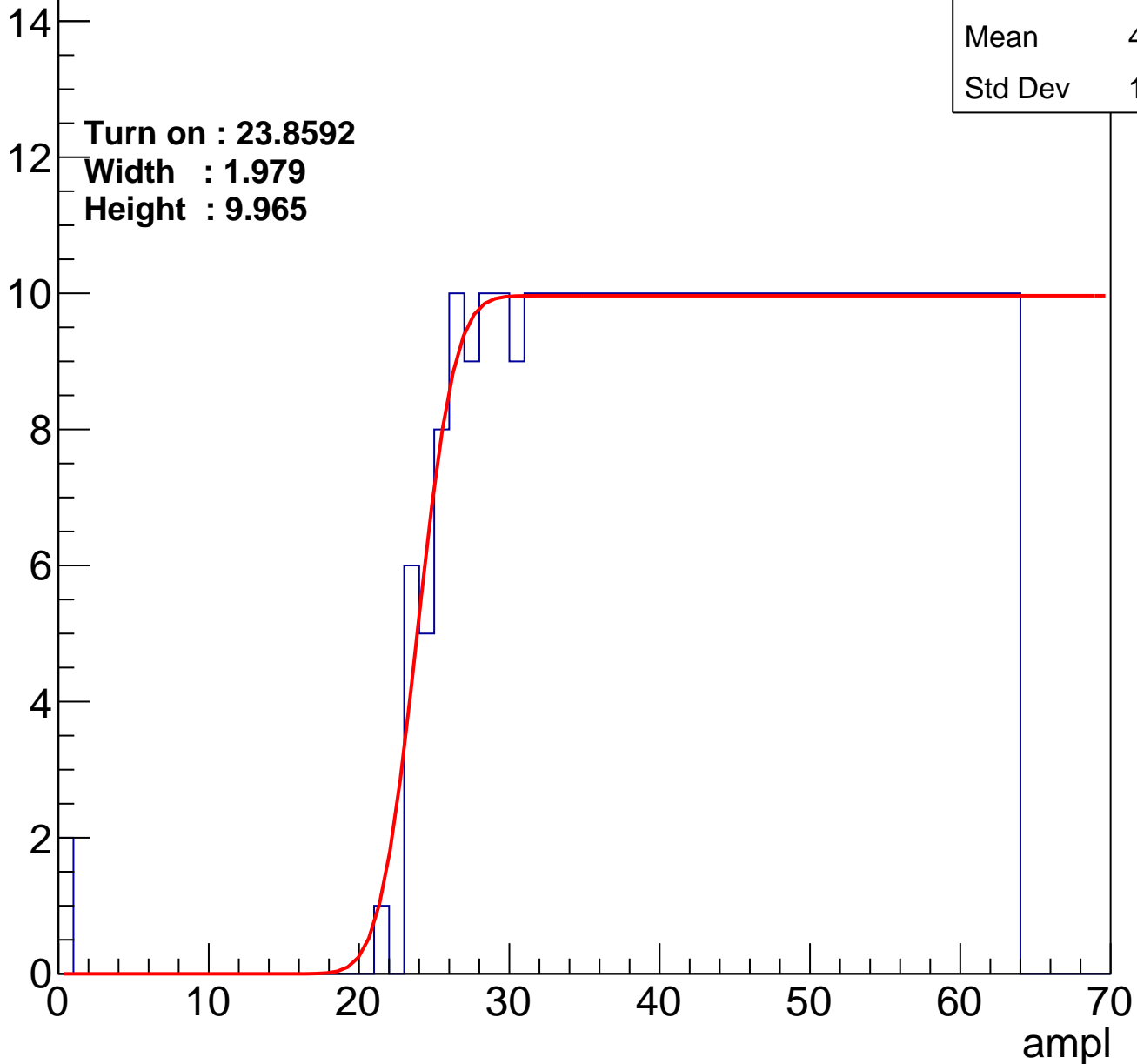
Entries	400
Mean	43.33
Std Dev	11.94

Turn on : 23.8592

Width : 1.979

Height : 9.965

Entry



B1L002S, U20-ch15

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.72
Std Dev	11.06

Turn on : 26.9303

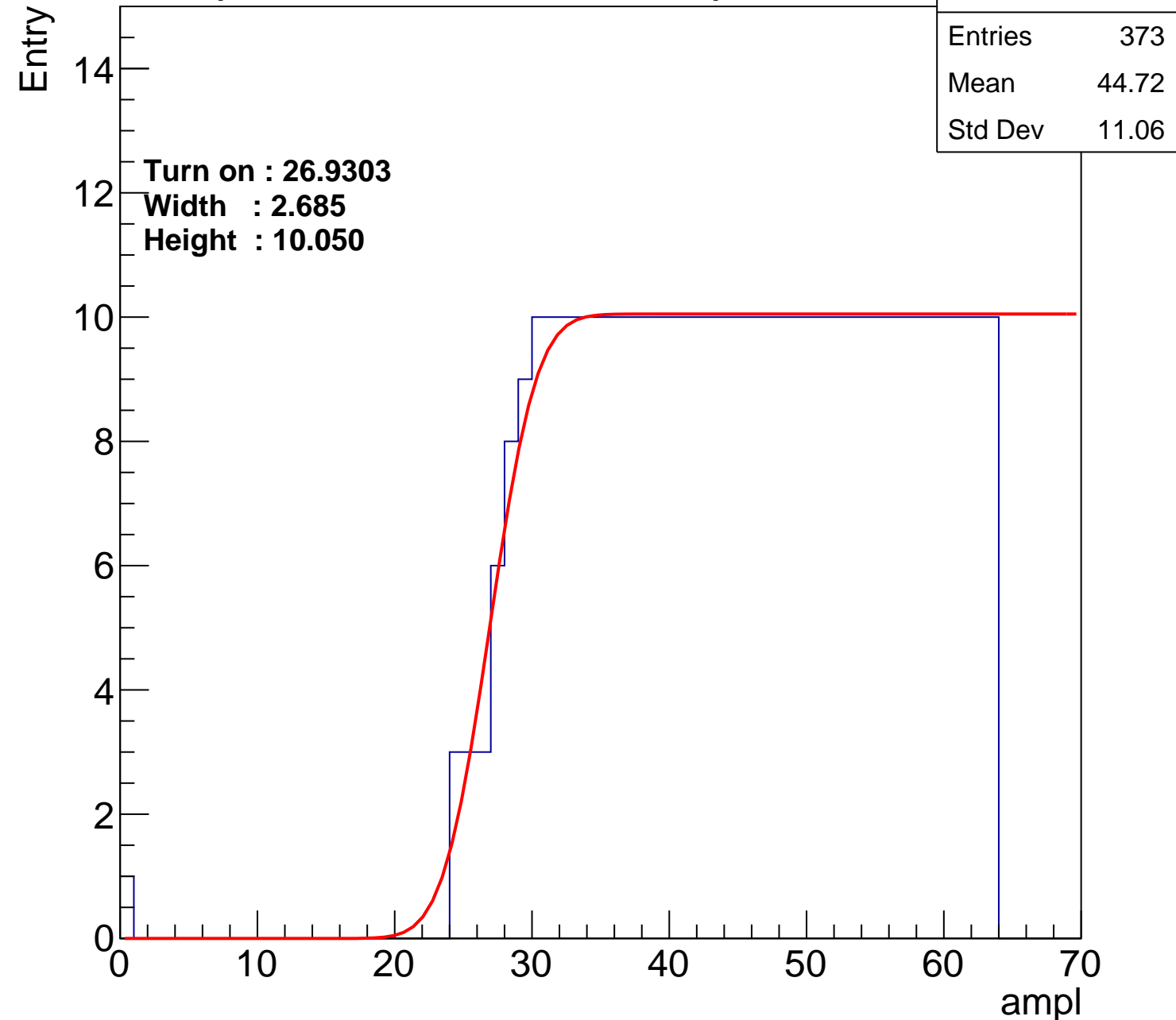
Width : 2.685

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch16

calib_packv5_042523_0143.root, FC#10, port B3

Entries	376
Mean	44.55
Std Dev	11.18

Turn on : 26.4731

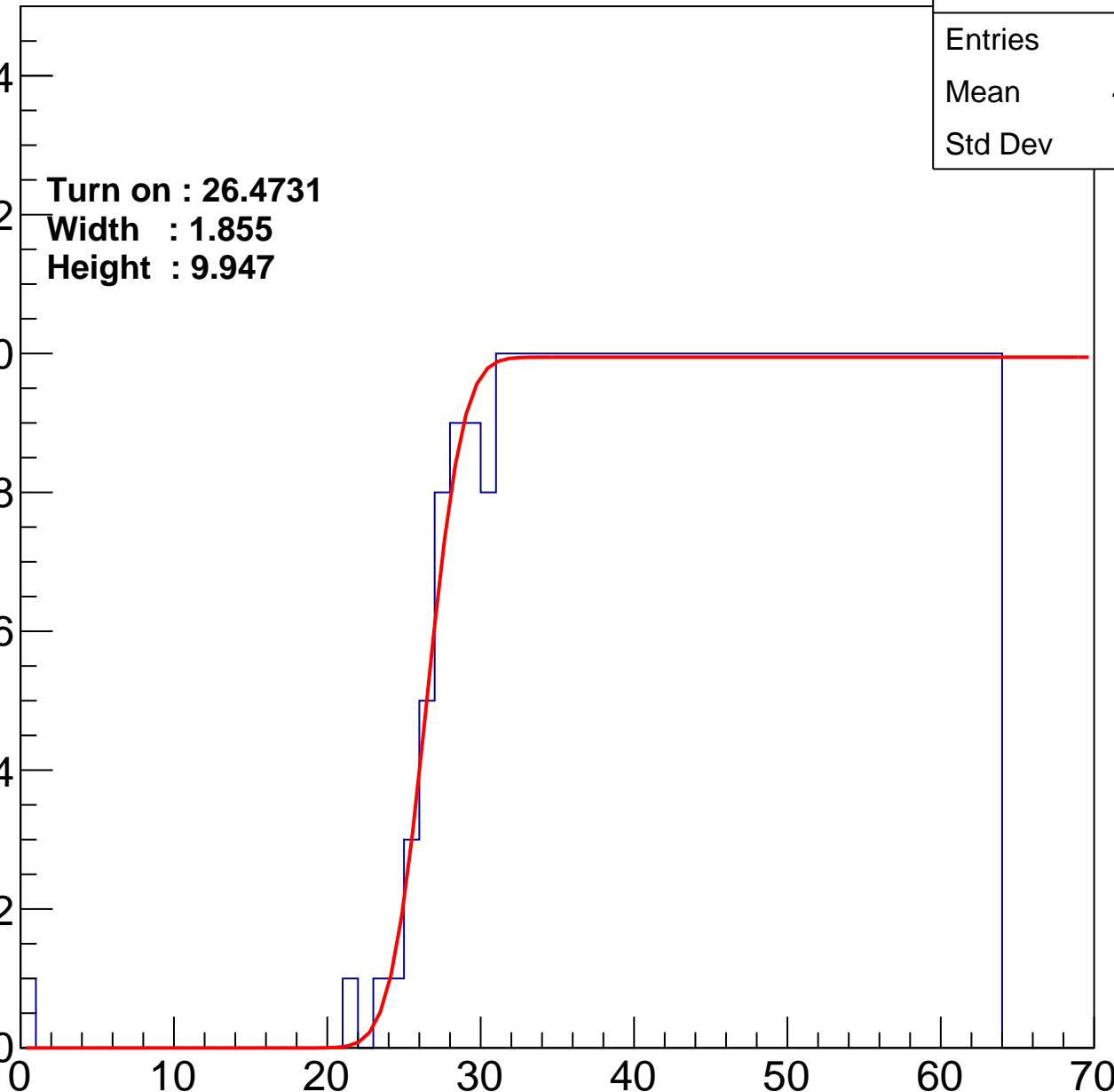
Width : 1.855

Height : 9.947

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch17

calib_packv5_042523_0143.root, FC#10, port B3

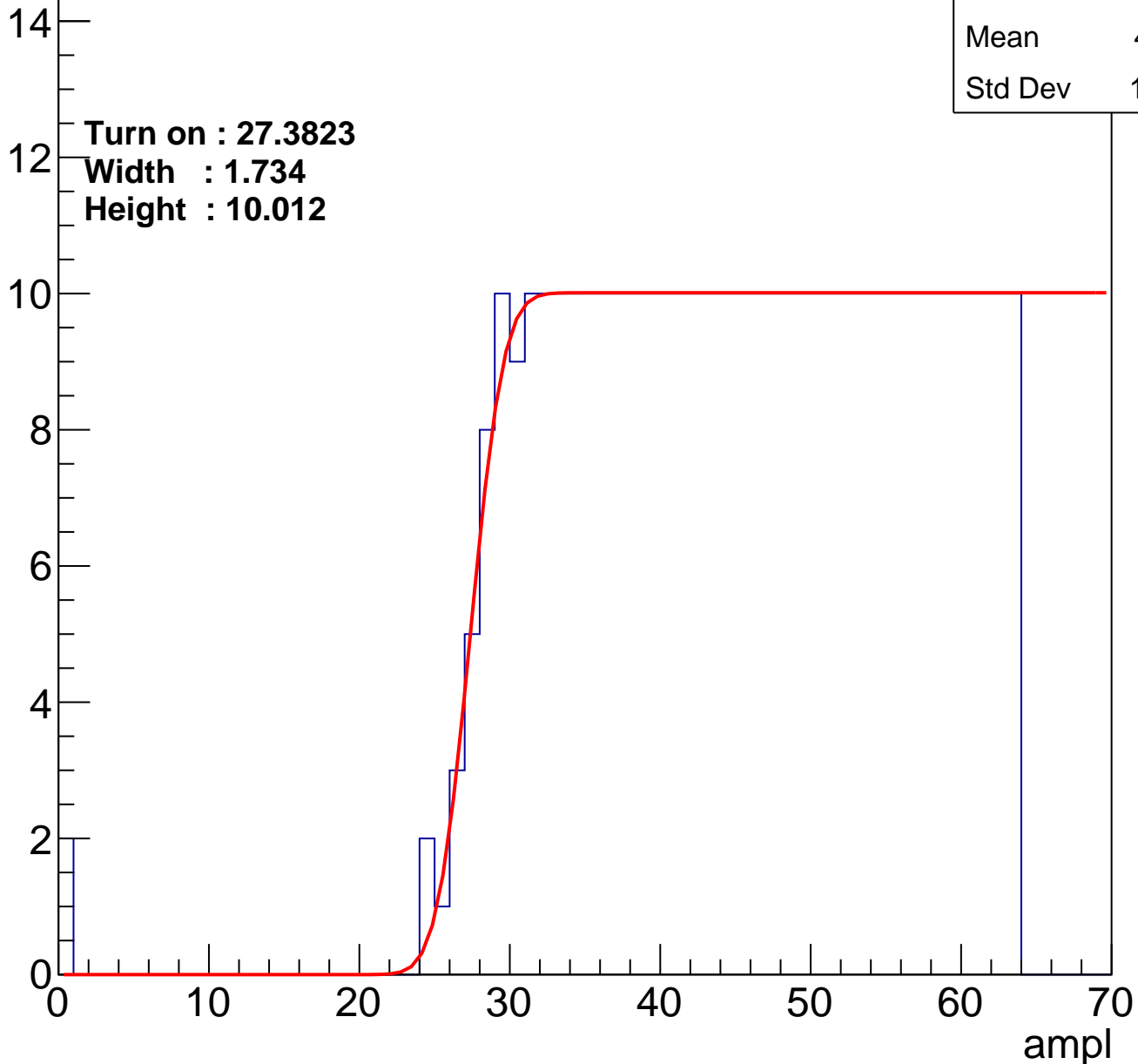
Entries	370
Mean	44.81
Std Dev	11.17

Turn on : 27.3823

Width : 1.734

Height : 10.012

Entry



B1L002S, U20-ch18

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.26
Std Dev	11.47

Turn on : 26.1127

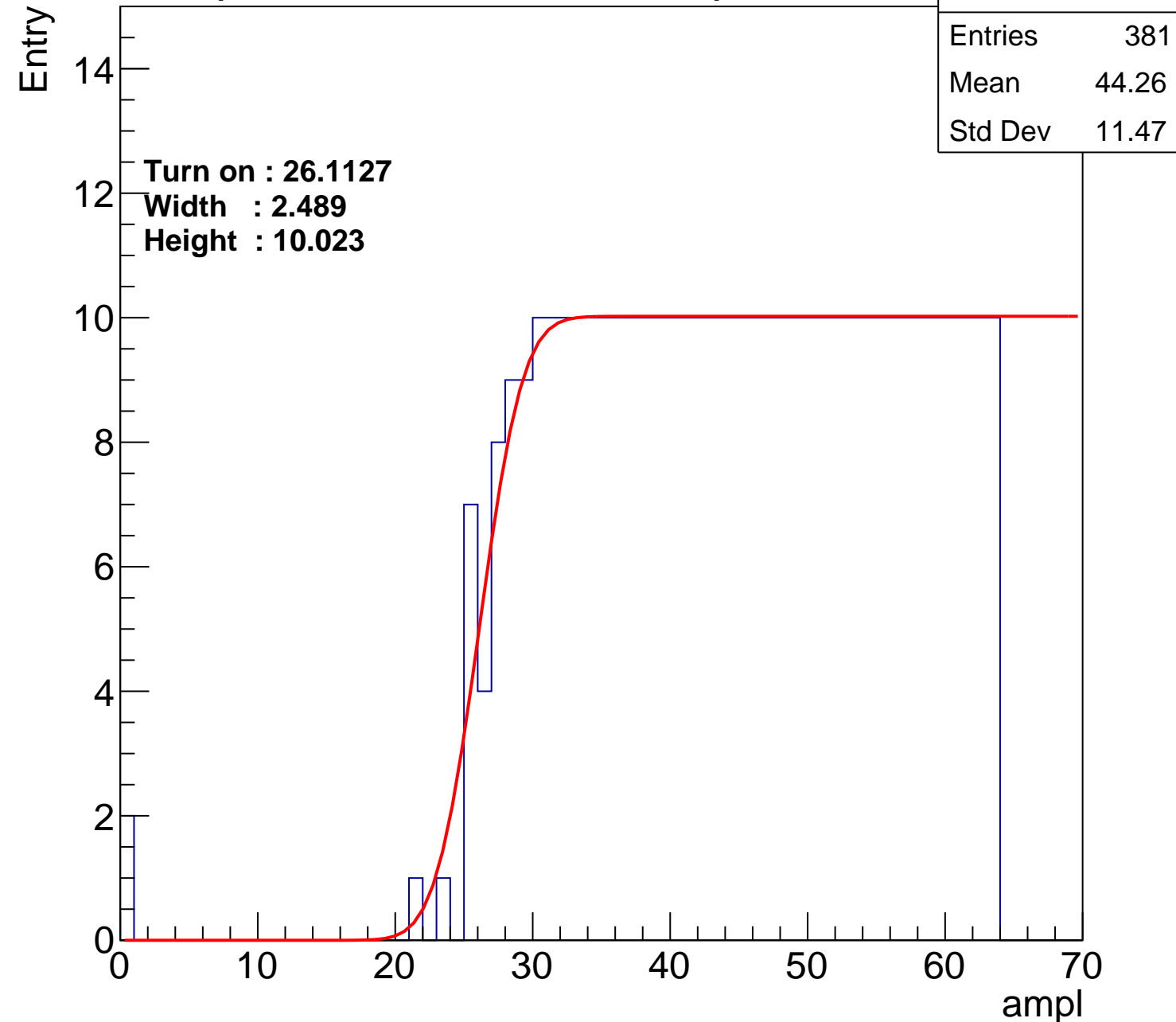
Width : 2.489

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch19

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.76
Std Dev	11.06

Turn on : 26.8752

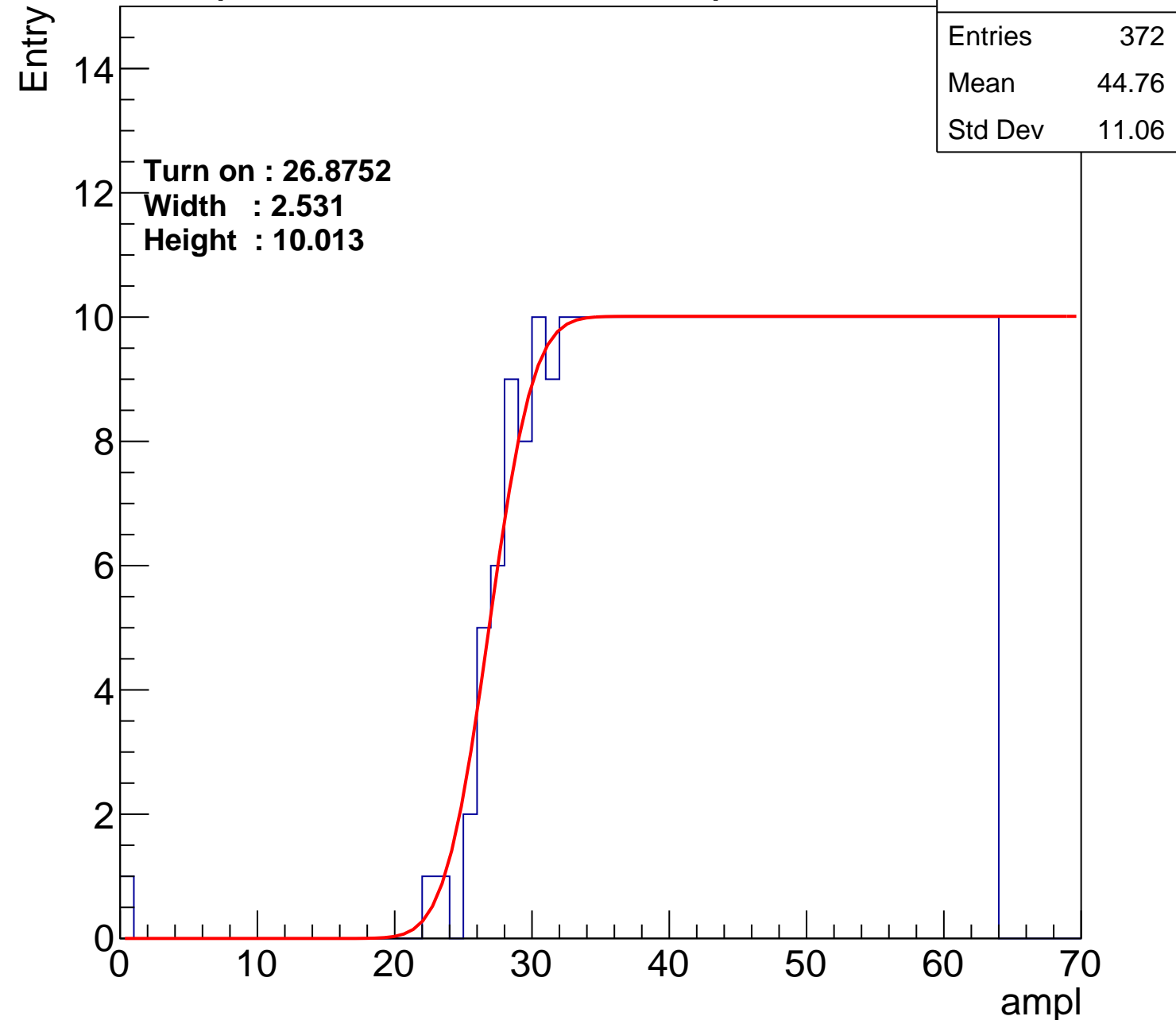
Width : 2.531

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch20

calib_packv5_042523_0143.root, FC#10, port B3

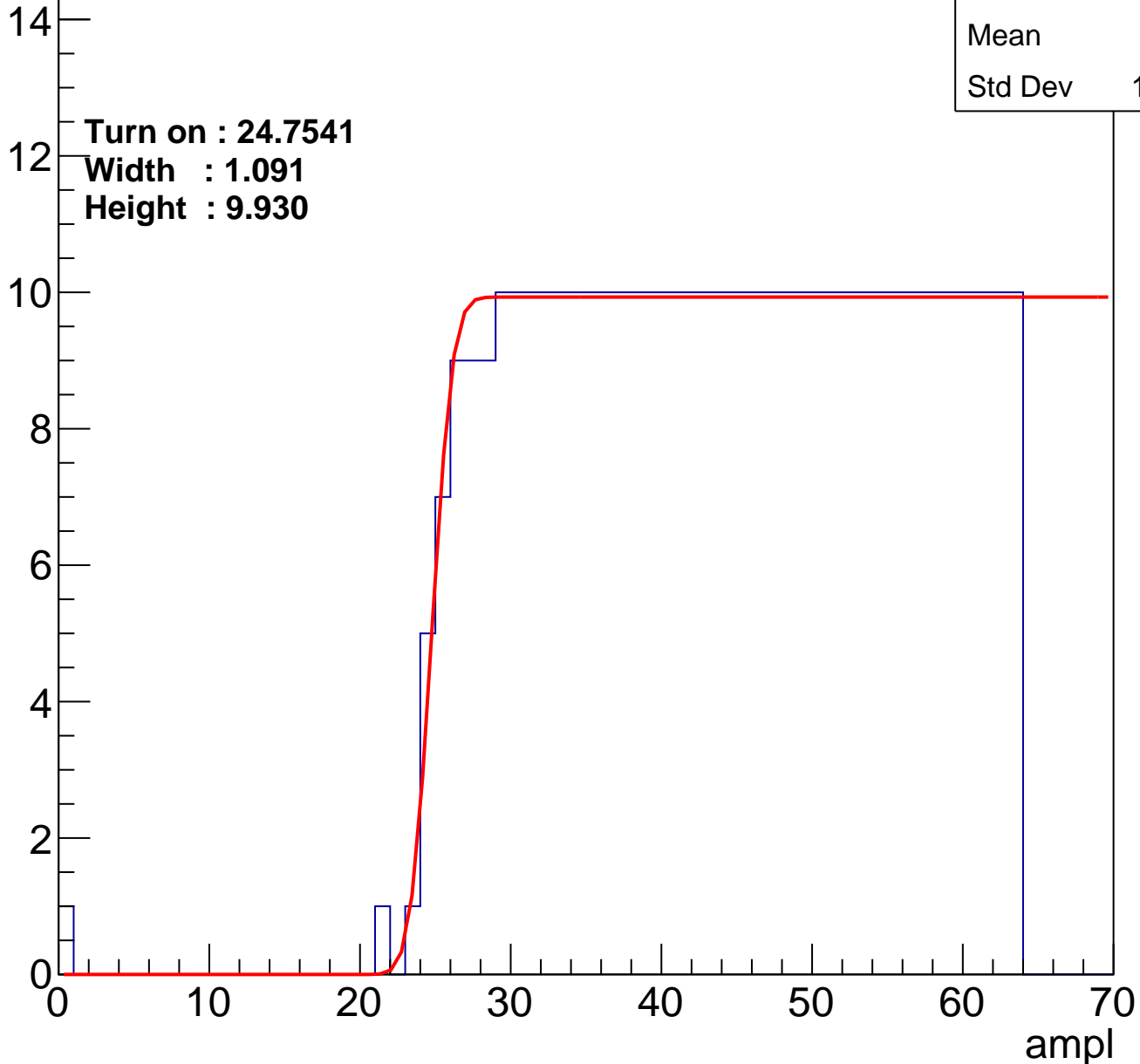
Entries	392
Mean	43.8
Std Dev	11.55

Turn on : 24.7541

Width : 1.091

Height : 9.930

Entry



B1L002S, U20-ch21

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.23
Std Dev	11.53

Turn on : 26.4062

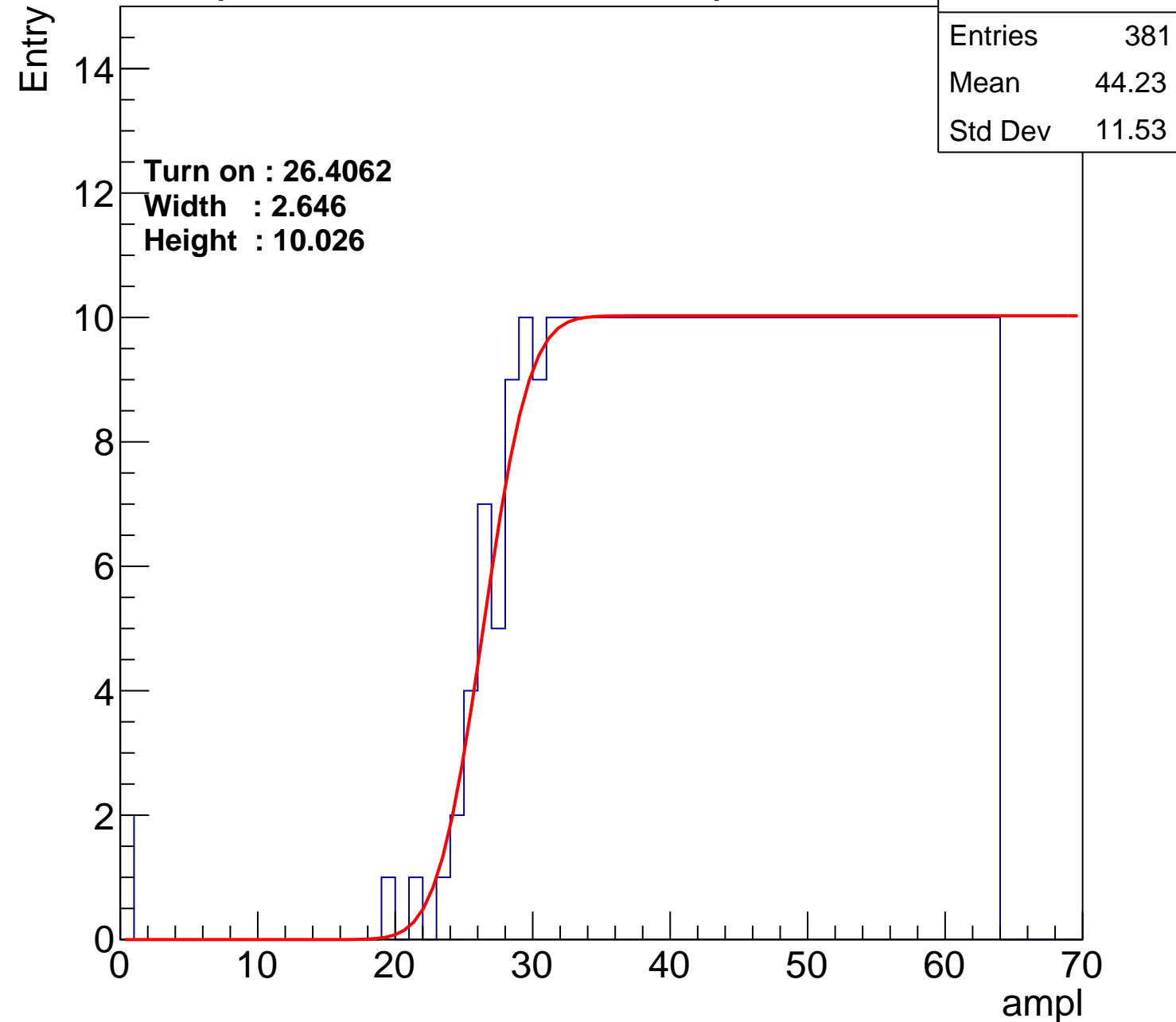
Width : 2.646

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch22

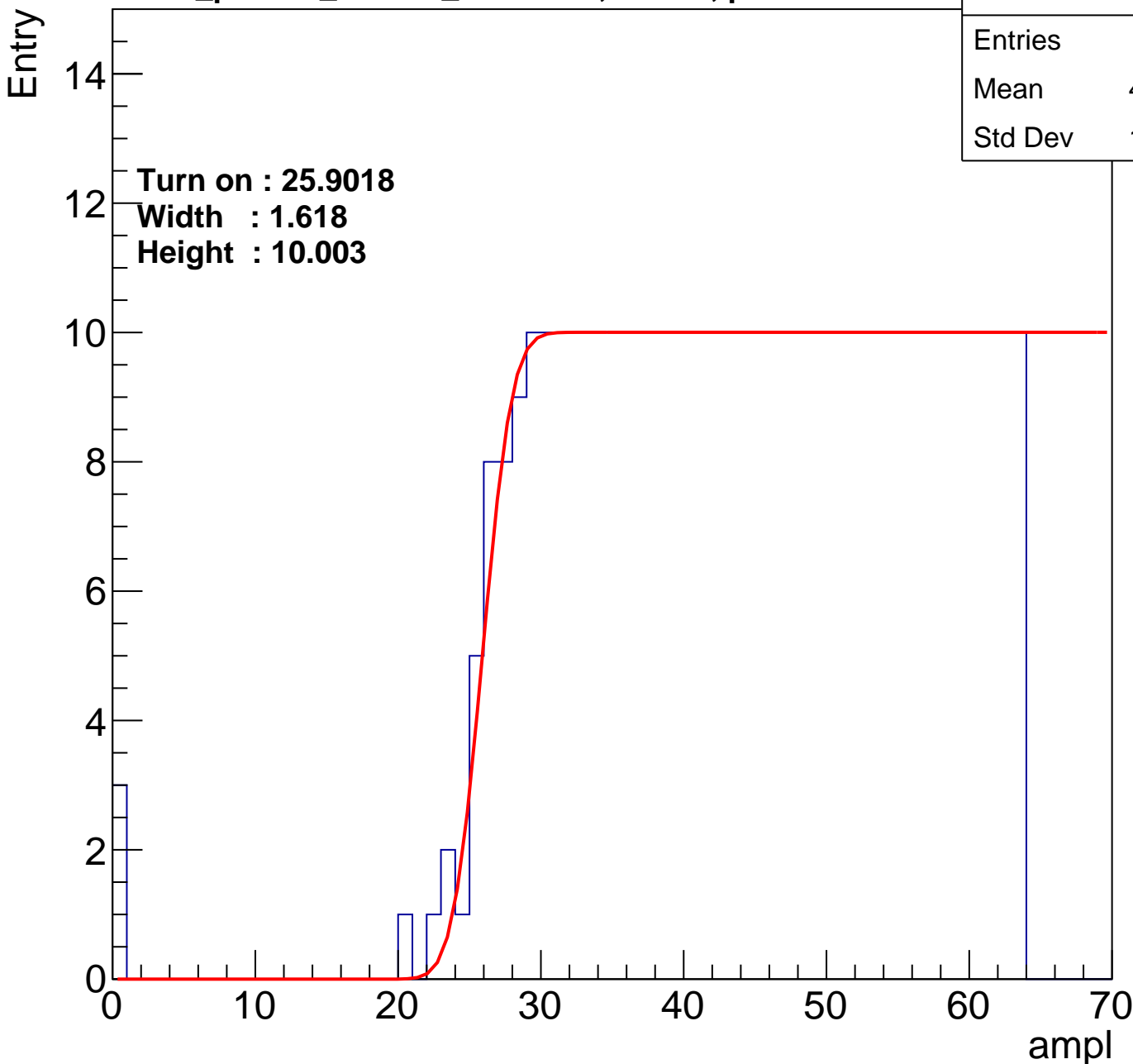
calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.85
Std Dev	11.82

Turn on : 25.9018

Width : 1.618

Height : 10.003



B1L002S, U20-ch23

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.34
Std Dev	11.44

Turn on : 26.1519

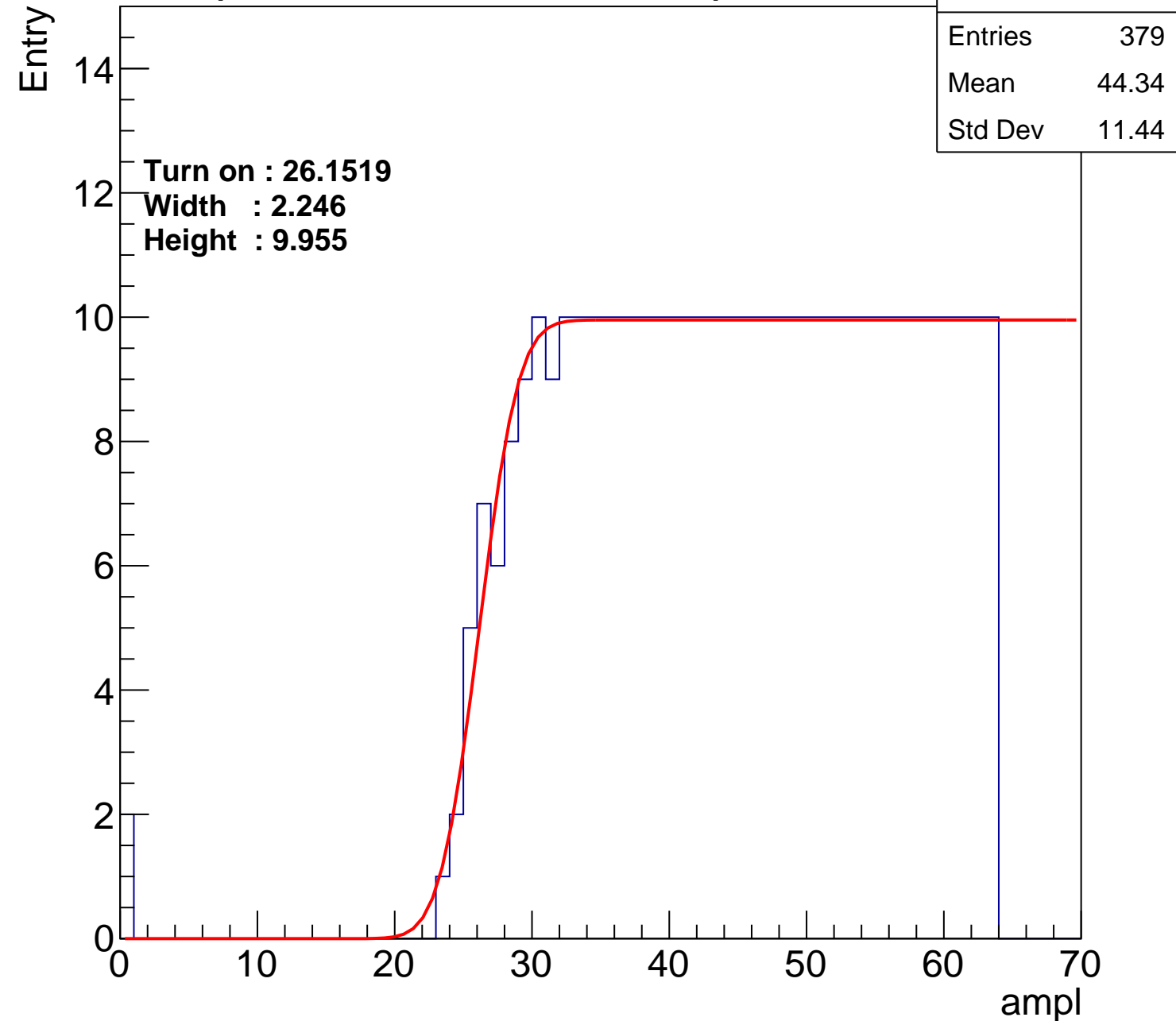
Width : 2.246

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch24

calib_packv5_042523_0143.root, FC#10, port B3

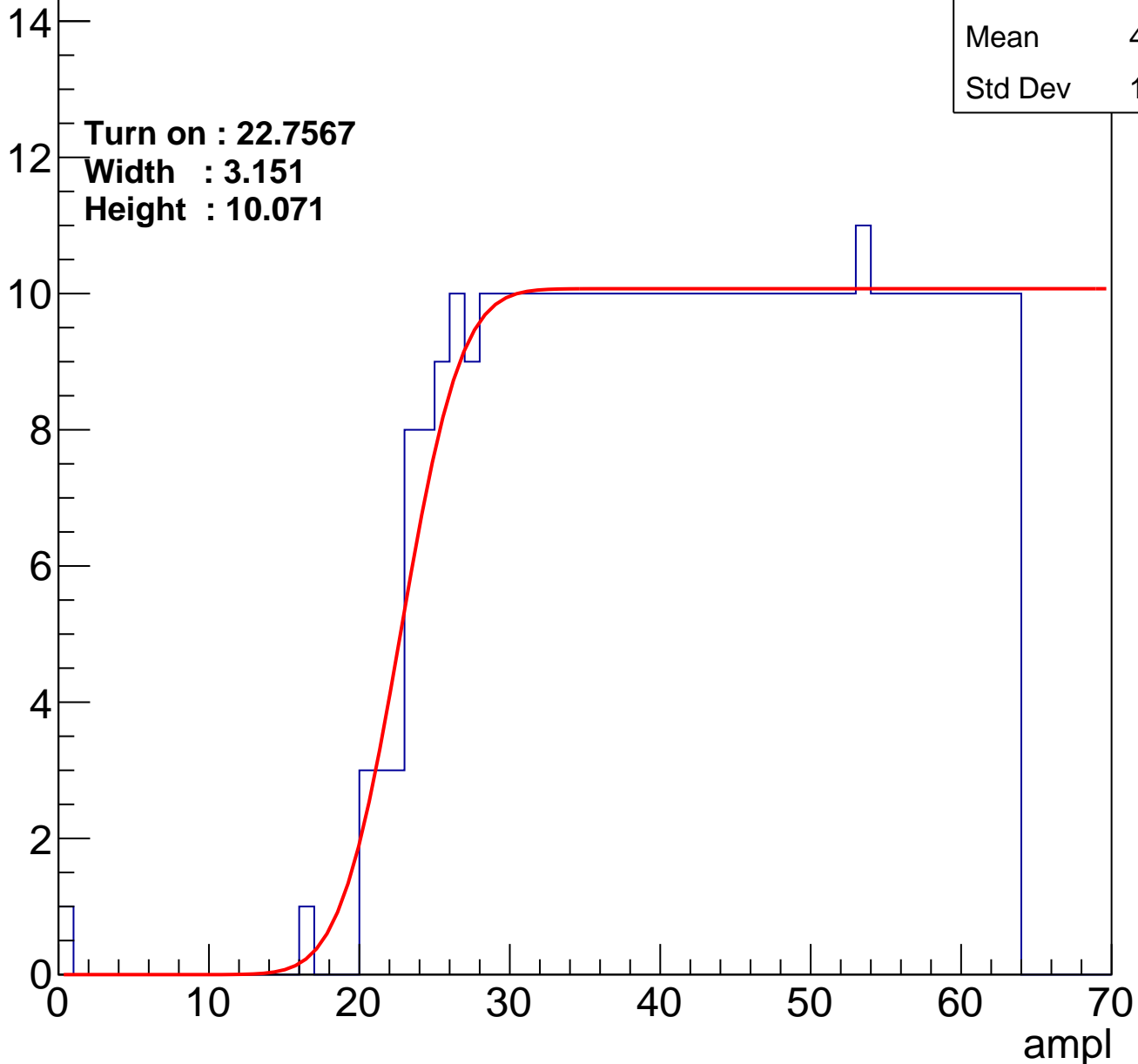
Entries	416
Mean	42.65
Std Dev	12.24

Turn on : 22.7567

Width : 3.151

Height : 10.071

Entry



B1L002S, U20-ch25

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.69
Std Dev	11.07

Turn on : 26.6028

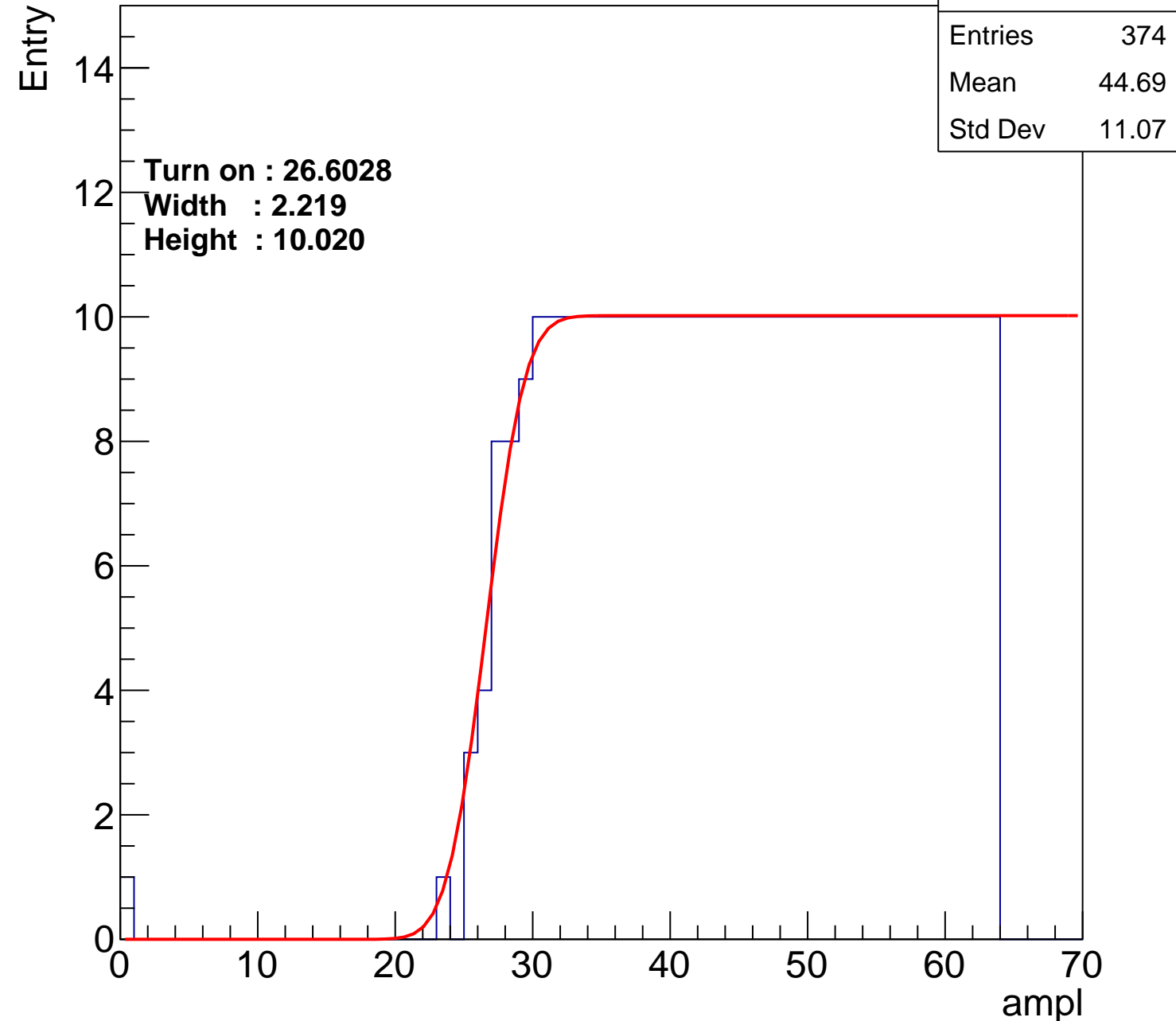
Width : 2.219

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch26

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.79
Std Dev	11.98

Turn on : 25.3087

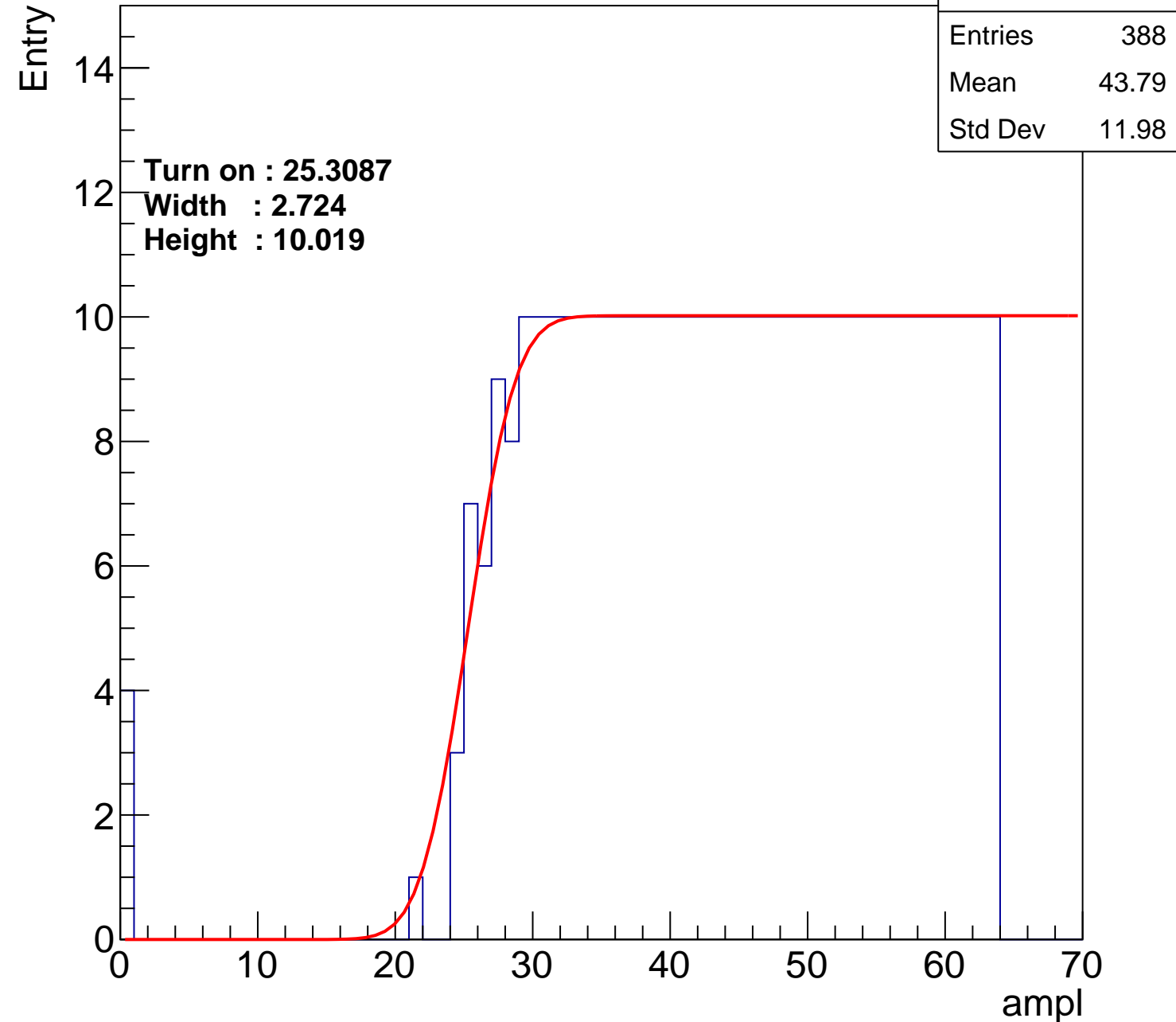
Width : 2.724

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch27

calib_packv5_042523_0143.root, FC#10, port B3

Entries	396
Mean	43.38
Std Dev	12.21

Turn on : 24.7000

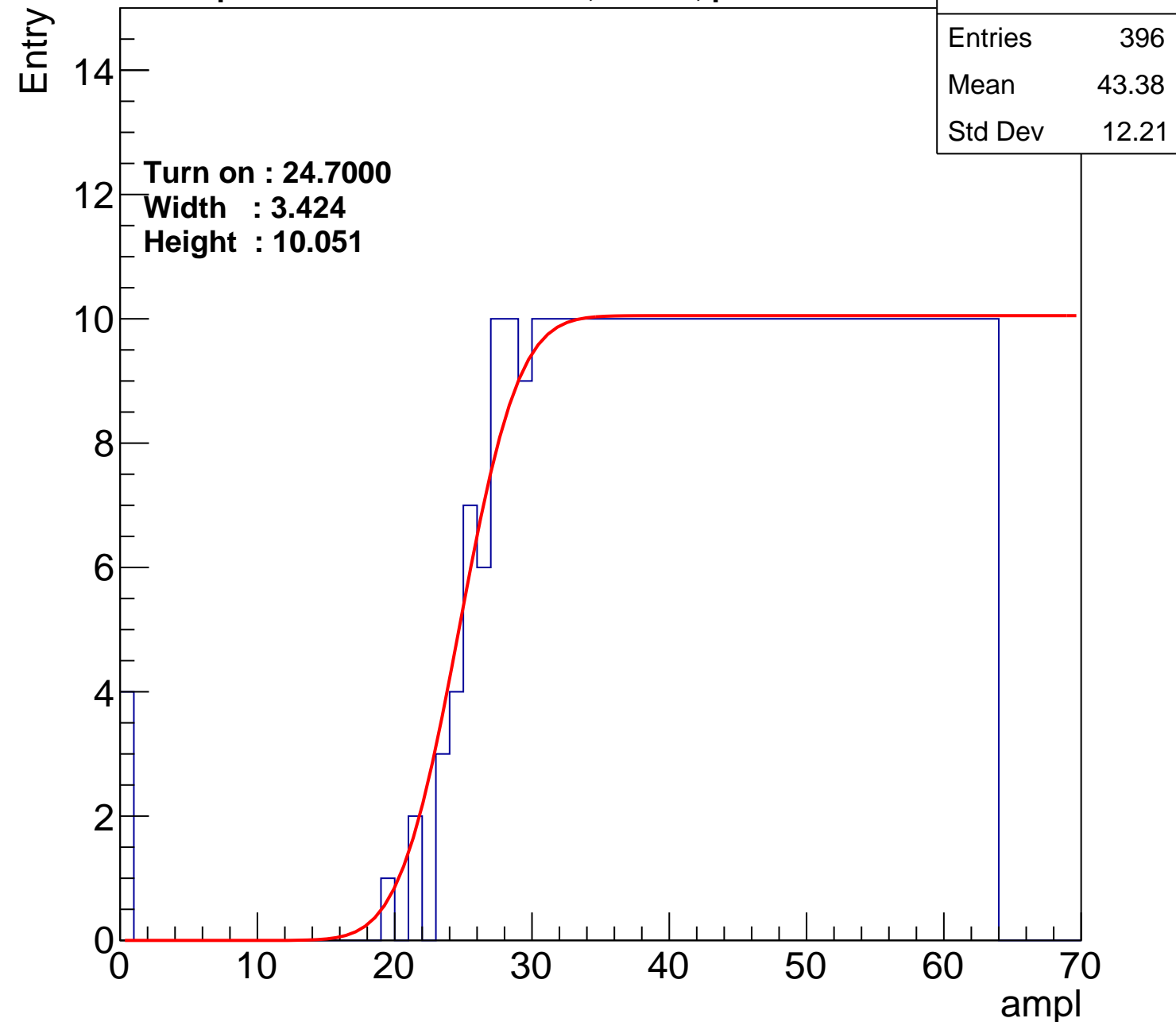
Width : 3.424

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch28

calib_packv5_042523_0143.root, FC#10, port B3

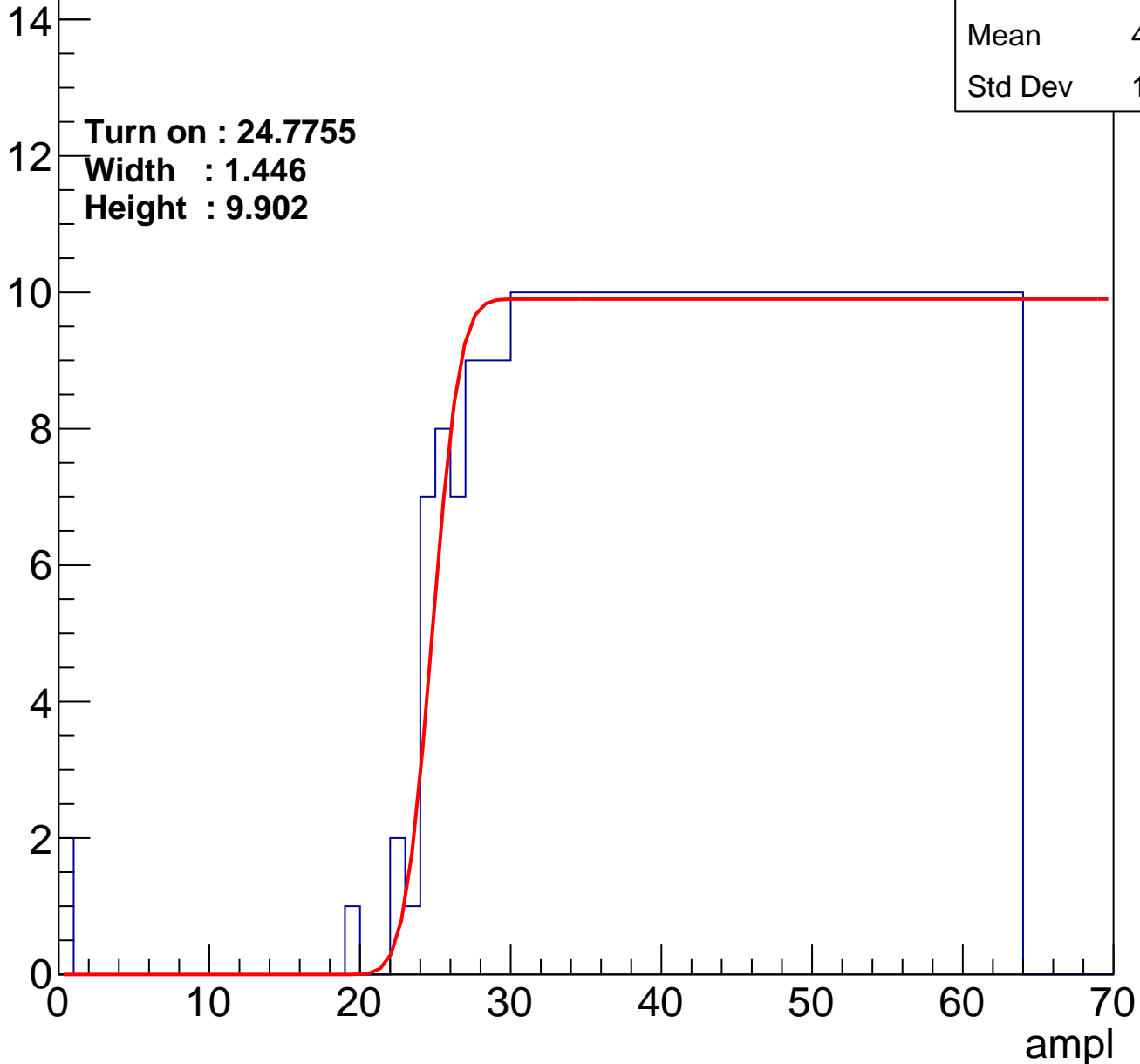
Entries	395
Mean	43.55
Std Dev	11.86

Turn on : 24.7755

Width : 1.446

Height : 9.902

Entry



B1L002S, U20-ch29

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.95
Std Dev	11.65

Turn on : 25.3418

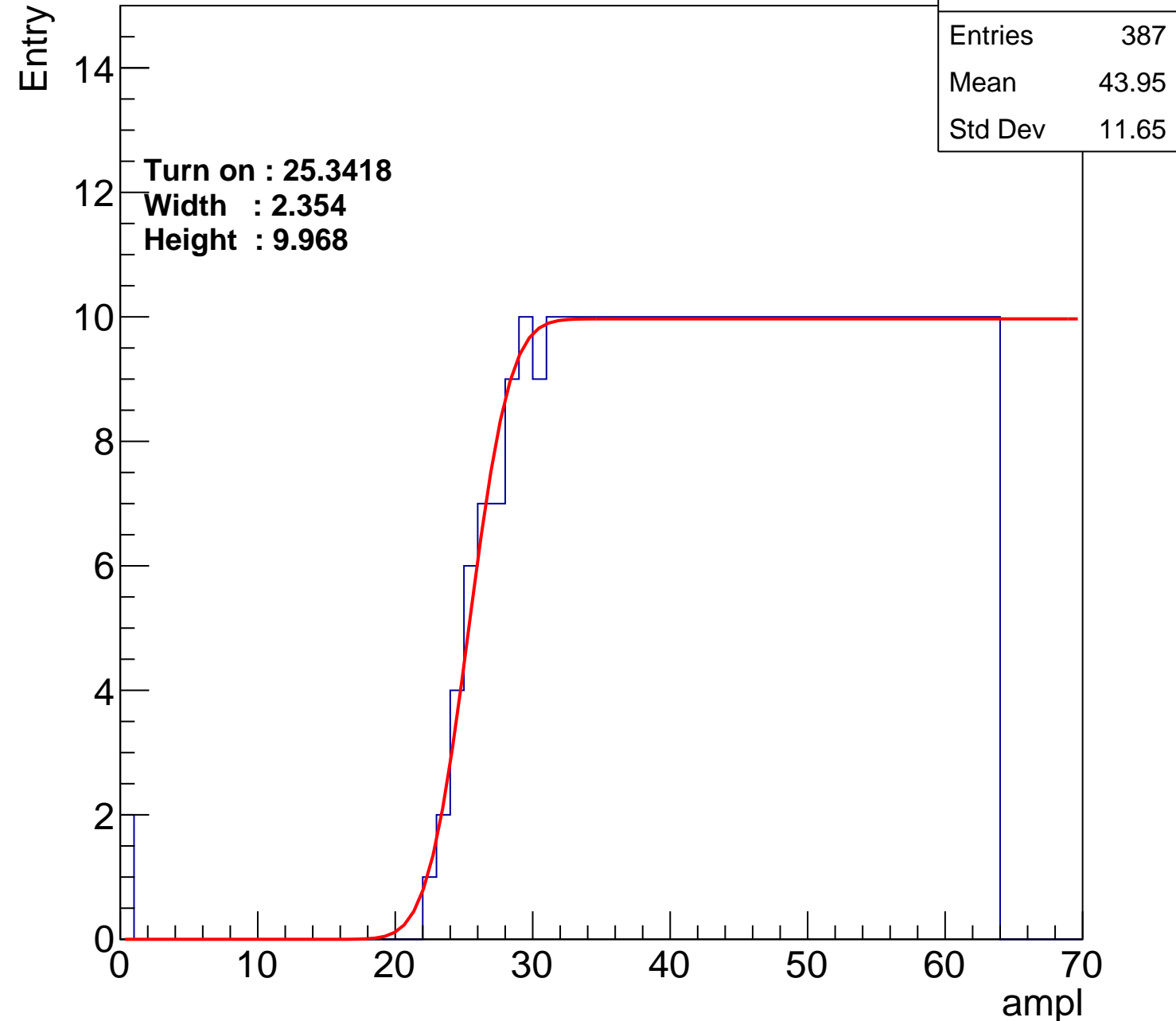
Width : 2.354

Height : 9.968

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch30

calib_packv5_042523_0143.root, FC#10, port B3

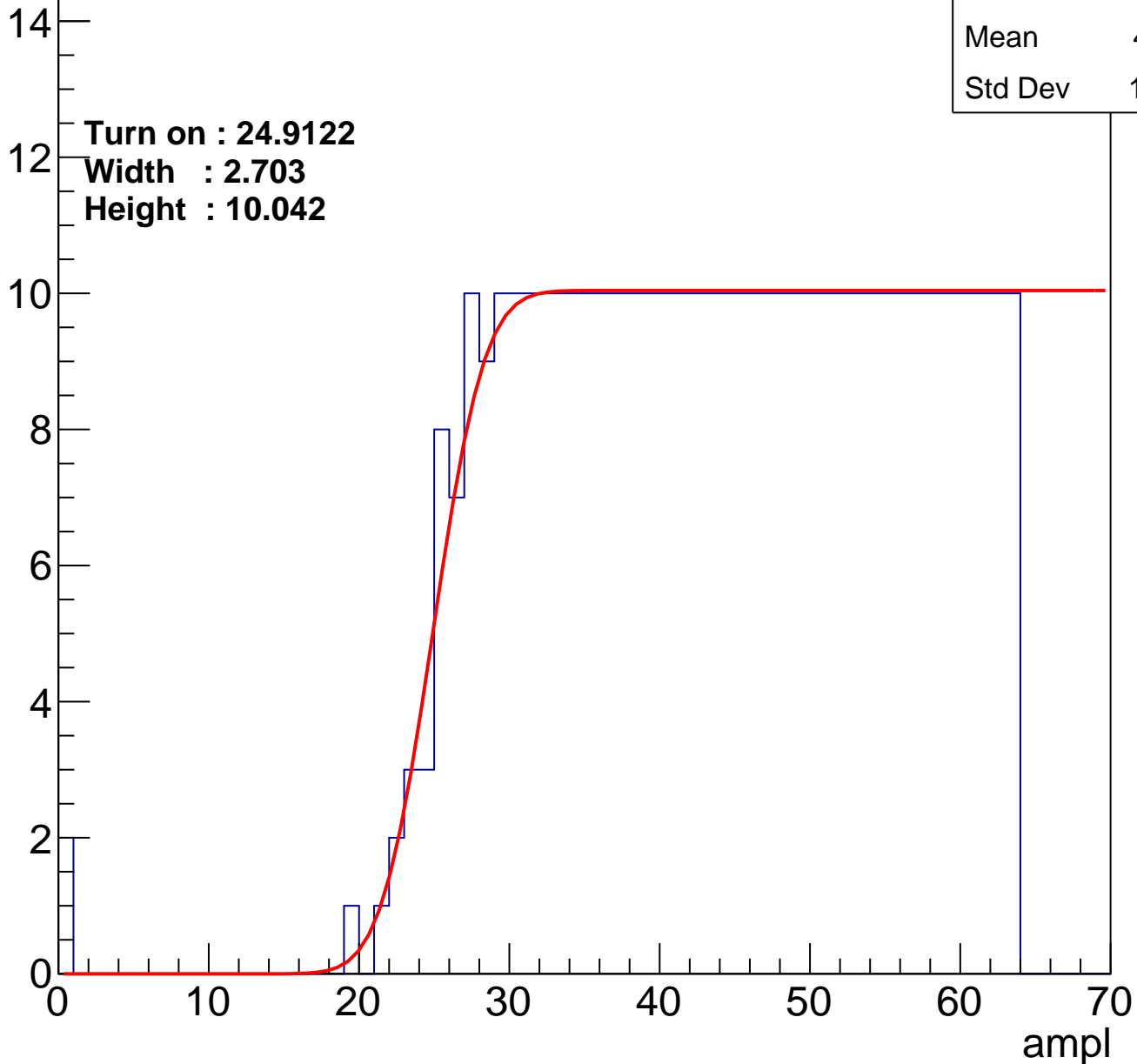
Entries	396
Mean	43.51
Std Dev	11.88

Turn on : 24.9122

Width : 2.703

Height : 10.042

Entry



B1L002S, U20-ch31

calib_packv5_042523_0143.root, FC#10, port B3

Entries	378
Mean	44.37
Std Dev	11.45

Turn on : 26.4217

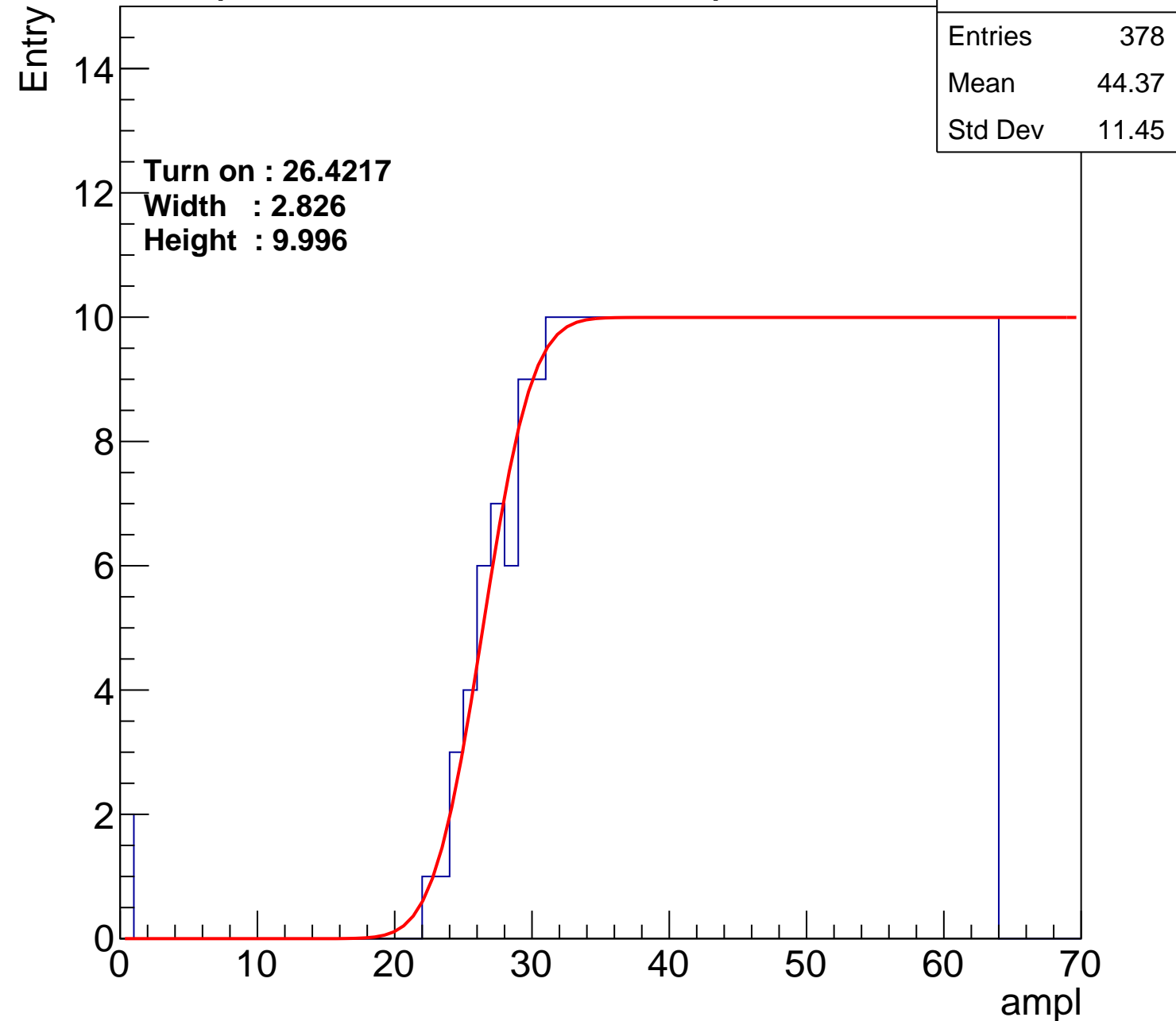
Width : 2.826

Height : 9.996

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch32

calib_packv5_042523_0143.root, FC#10, port B3

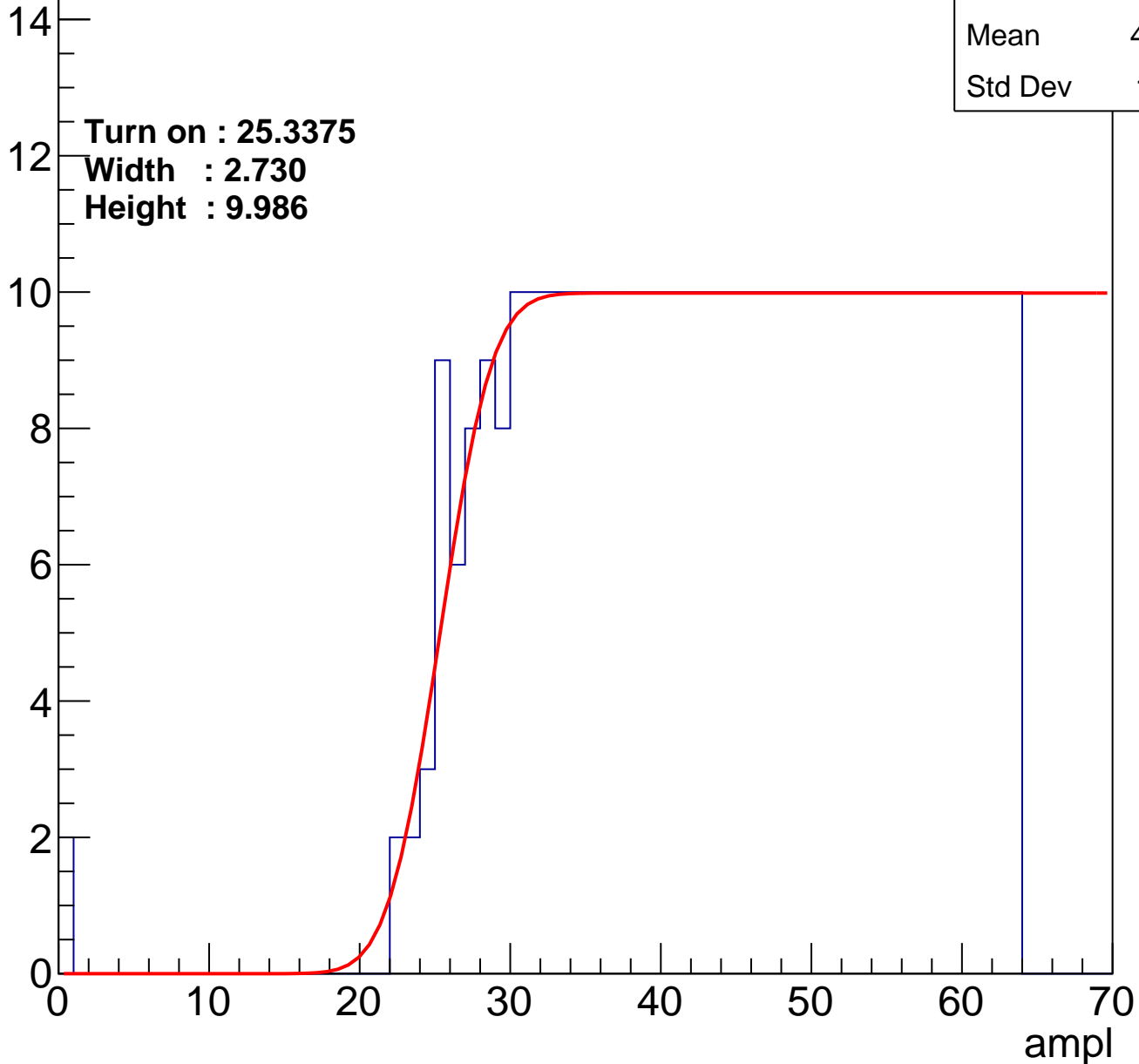
Entries	389
Mean	43.84
Std Dev	11.71

Turn on : 25.3375

Width : 2.730

Height : 9.986

Entry



B1L002S, U20-ch33

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.86
Std Dev	11.67

Turn on : 25.6712

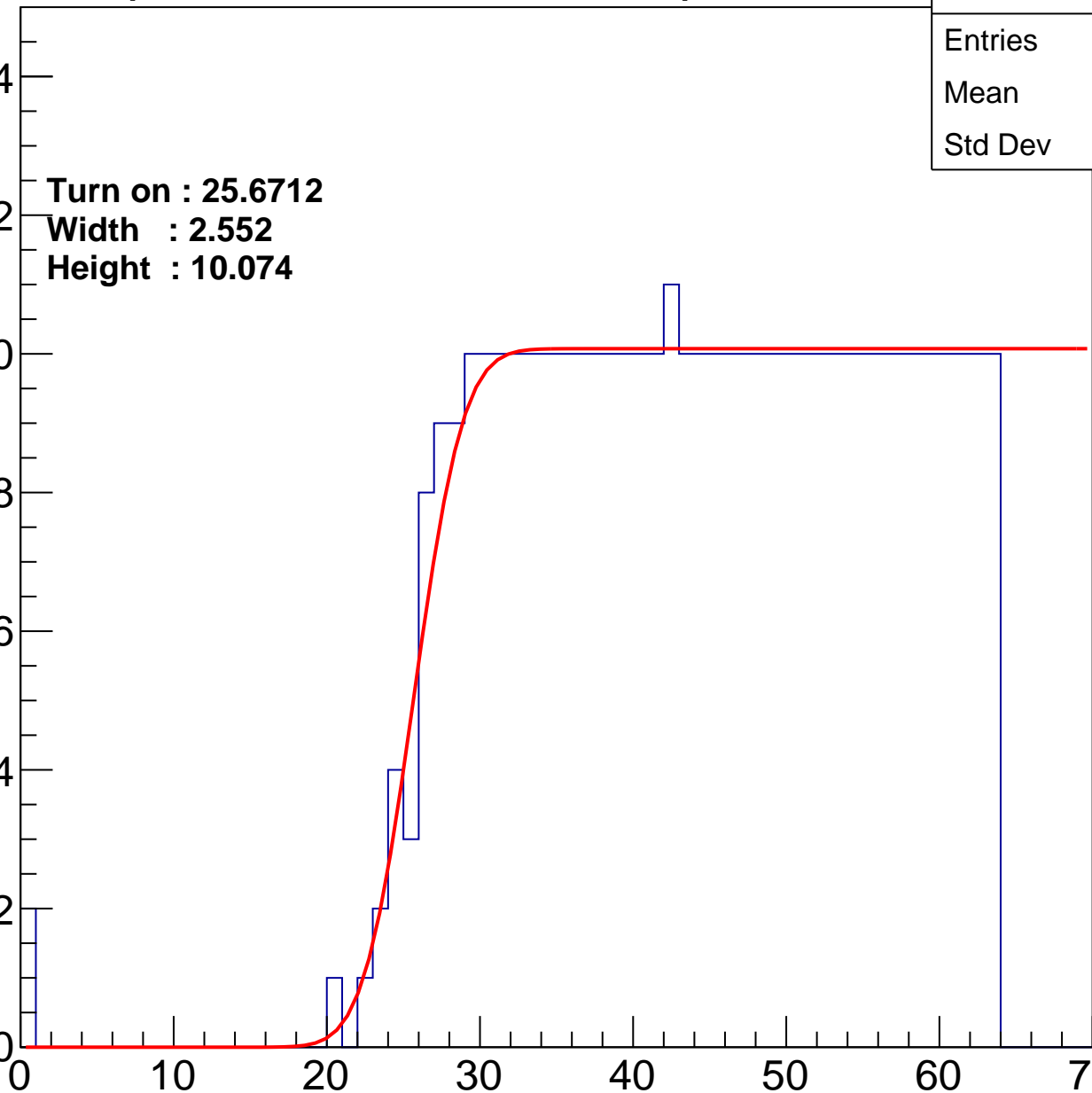
Width : 2.552

Height : 10.074

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch34

calib_packv5_042523_0143.root, FC#10, port B3

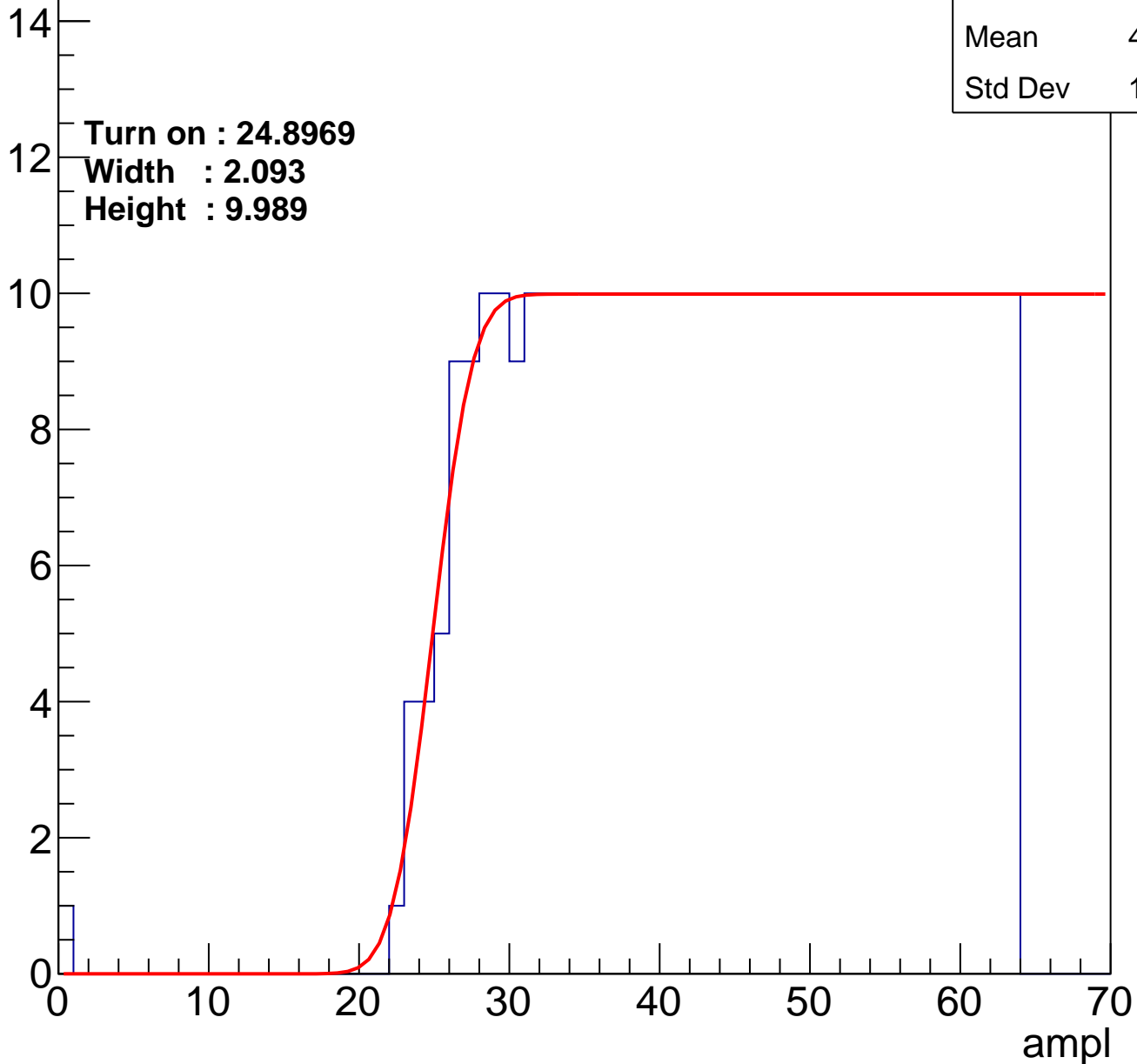
Entries	392
Mean	43.78
Std Dev	11.58

Turn on : 24.8969

Width : 2.093

Height : 9.989

Entry



B1L002S, U20-ch35

calib_packv5_042523_0143.root, FC#10, port B3

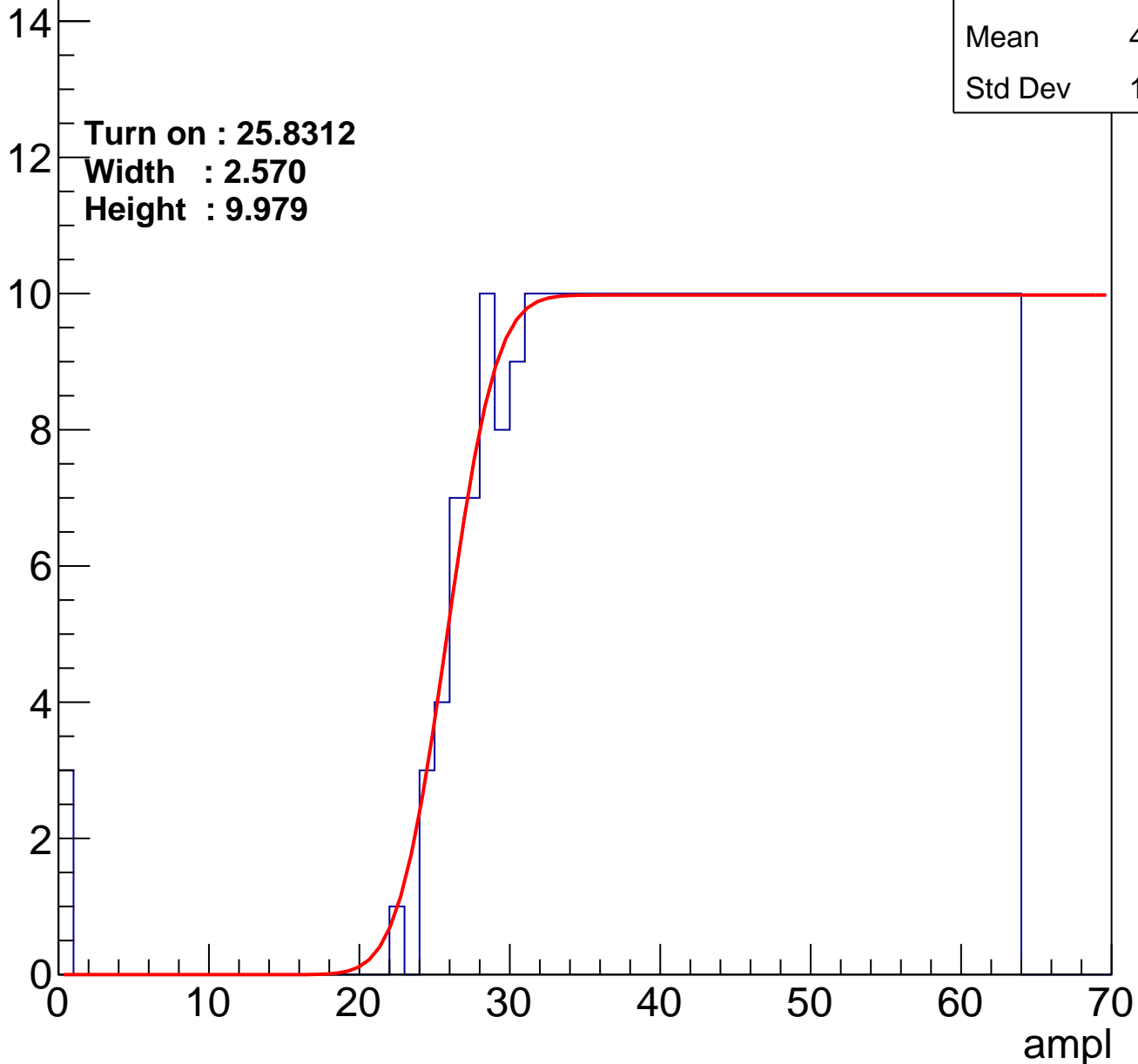
Entries	382
Mean	44.13
Std Dev	11.69

Turn on : 25.8312

Width : 2.570

Height : 9.979

Entry



B1L002S, U20-ch36

calib_packv5_042523_0143.root, FC#10, port B3

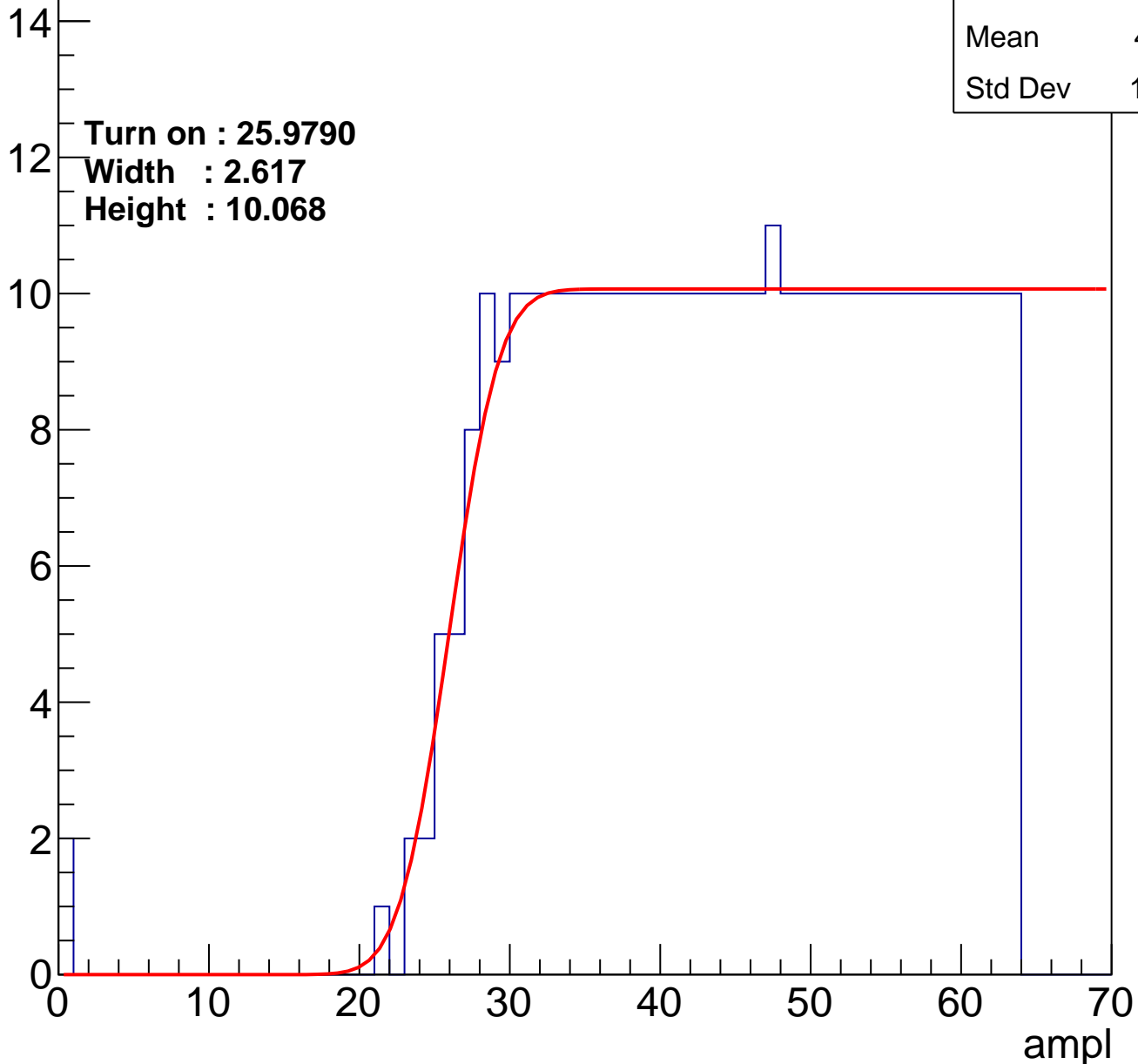
Entries	385
Mean	44.11
Std Dev	11.54

Turn on : 25.9790

Width : 2.617

Height : 10.068

Entry



B1L002S, U20-ch37

calib_packv5_042523_0143.root, FC#10, port B3

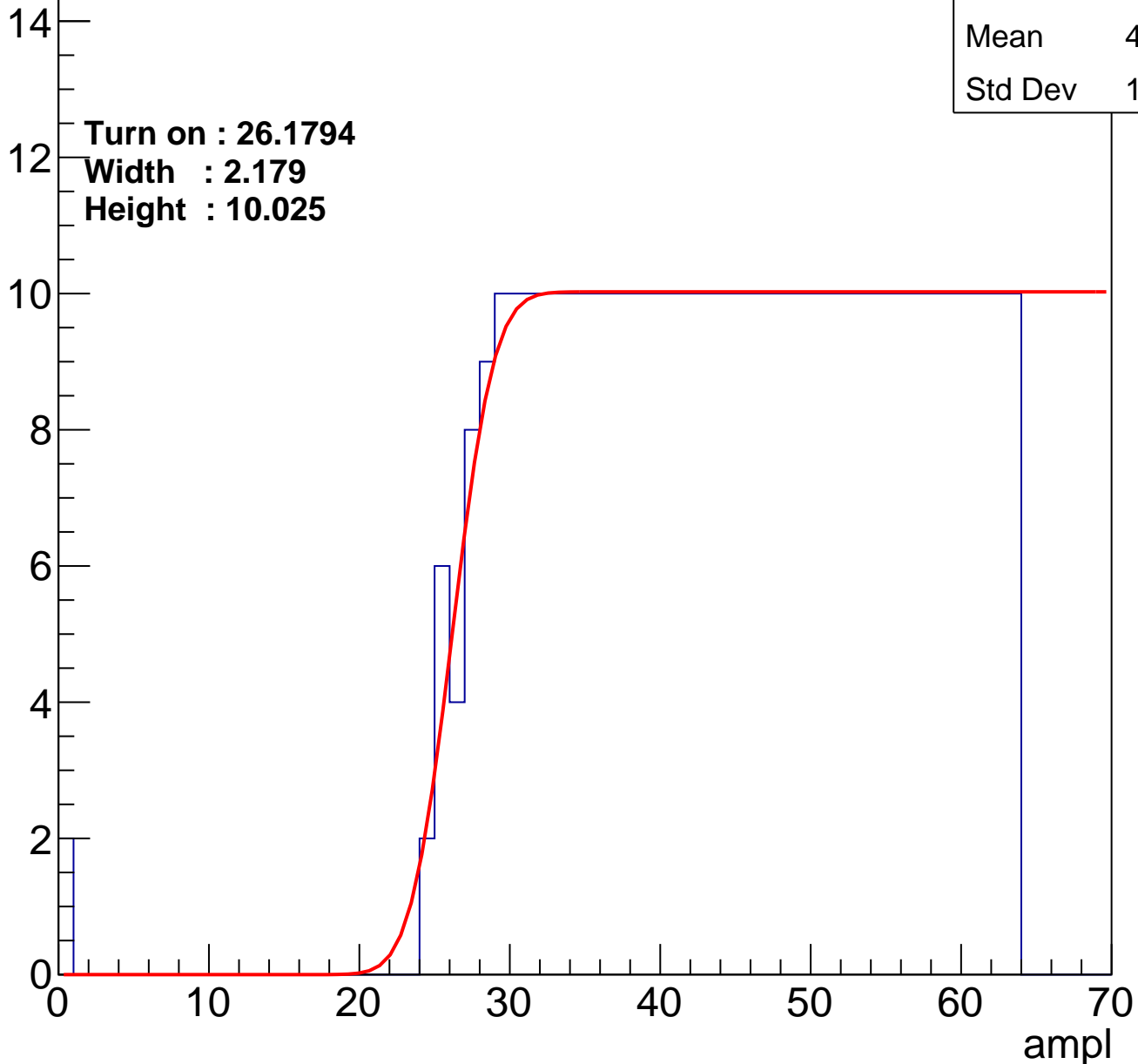
Entries	381
Mean	44.28
Std Dev	11.44

Turn on : 26.1794

Width : 2.179

Height : 10.025

Entry



B1L002S, U20-ch38

calib_packv5_042523_0143.root, FC#10, port B3

Entries	364
Mean	45.08
Std Dev	11.06

Turn on : 28.2625

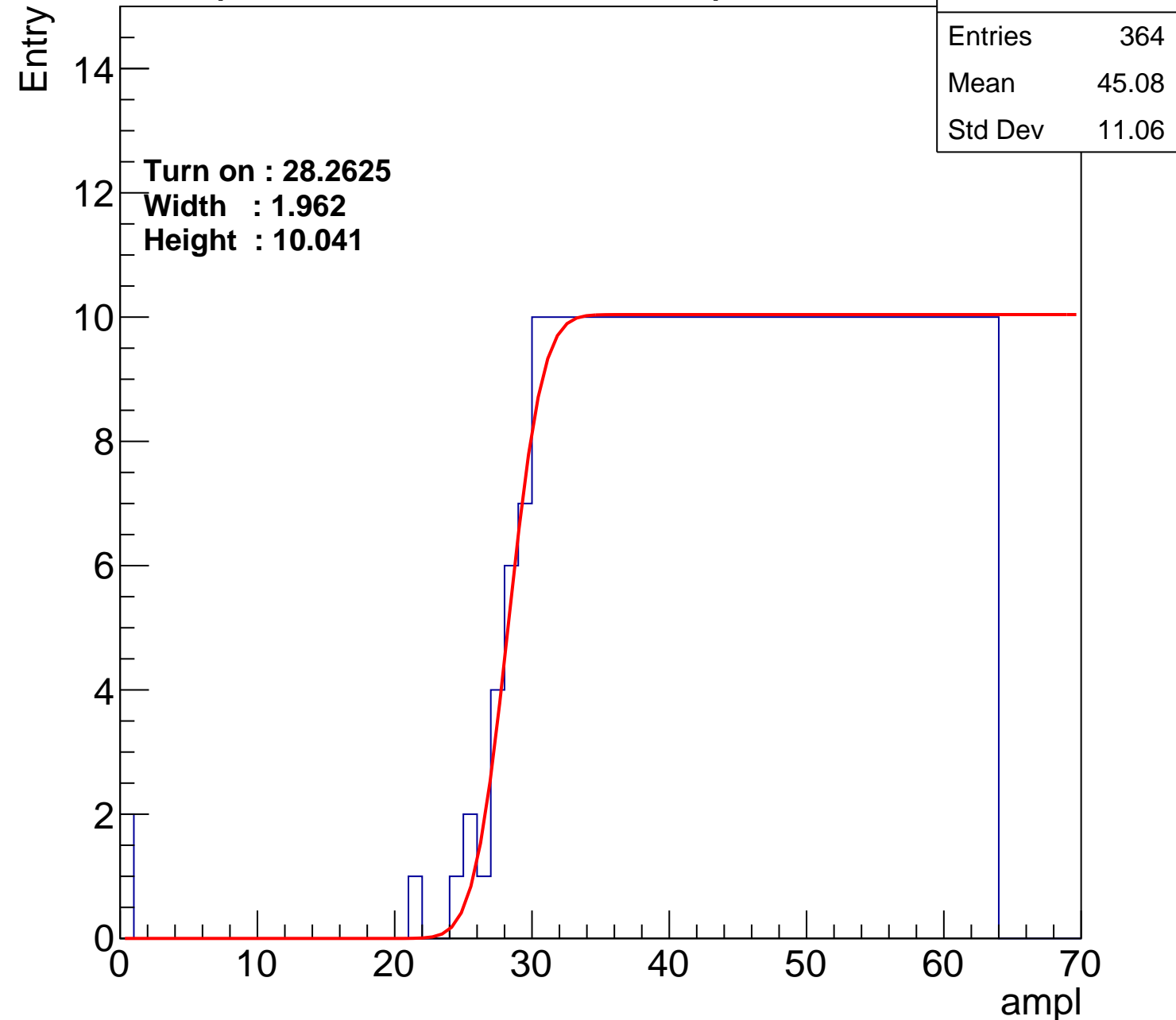
Width : 1.962

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch39

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.58
Std Dev	12.1

Turn on : 25.2748

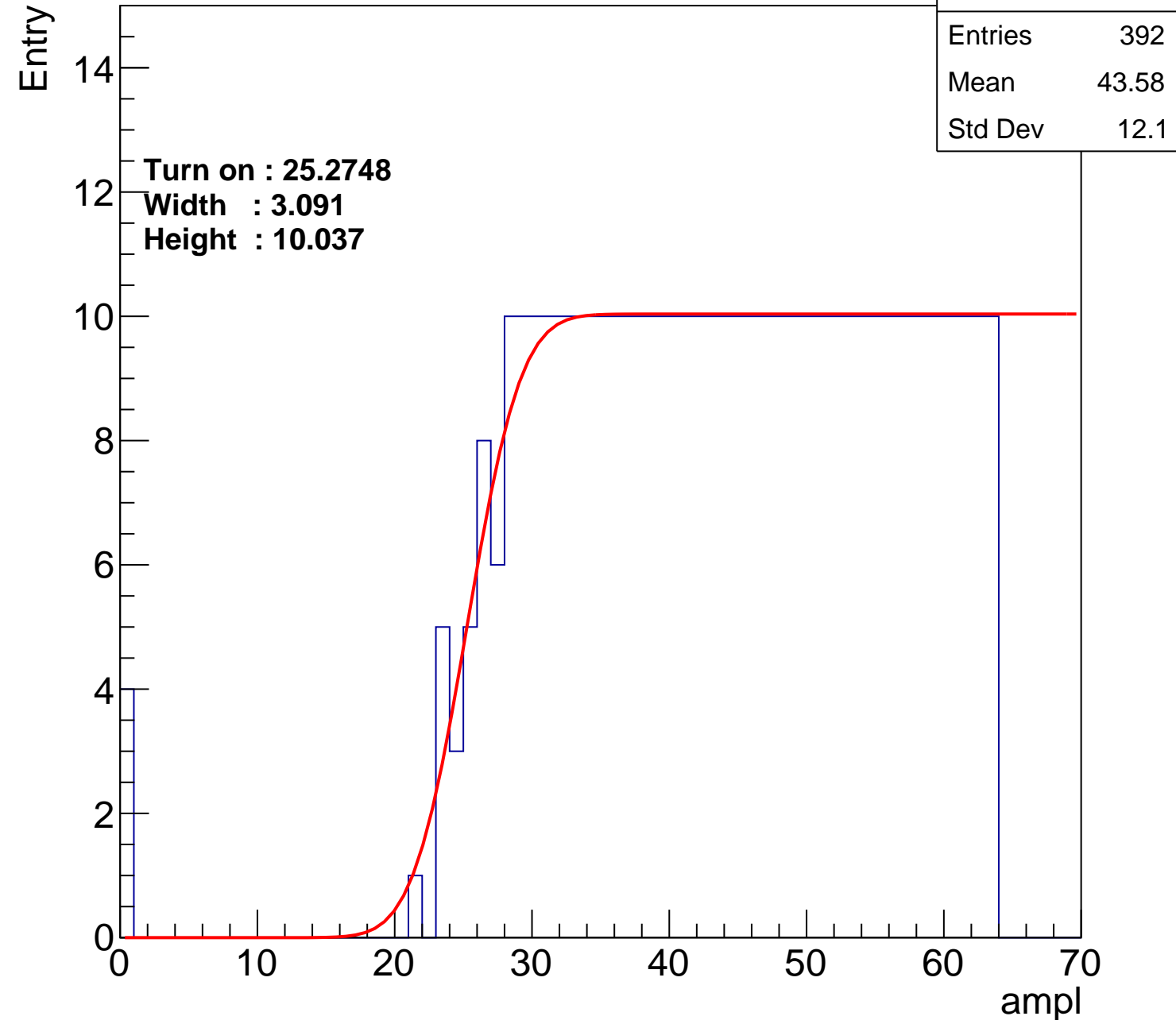
Width : 3.091

Height : 10.037

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch40

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.76
Std Dev	11.62

Turn on : 25.4347

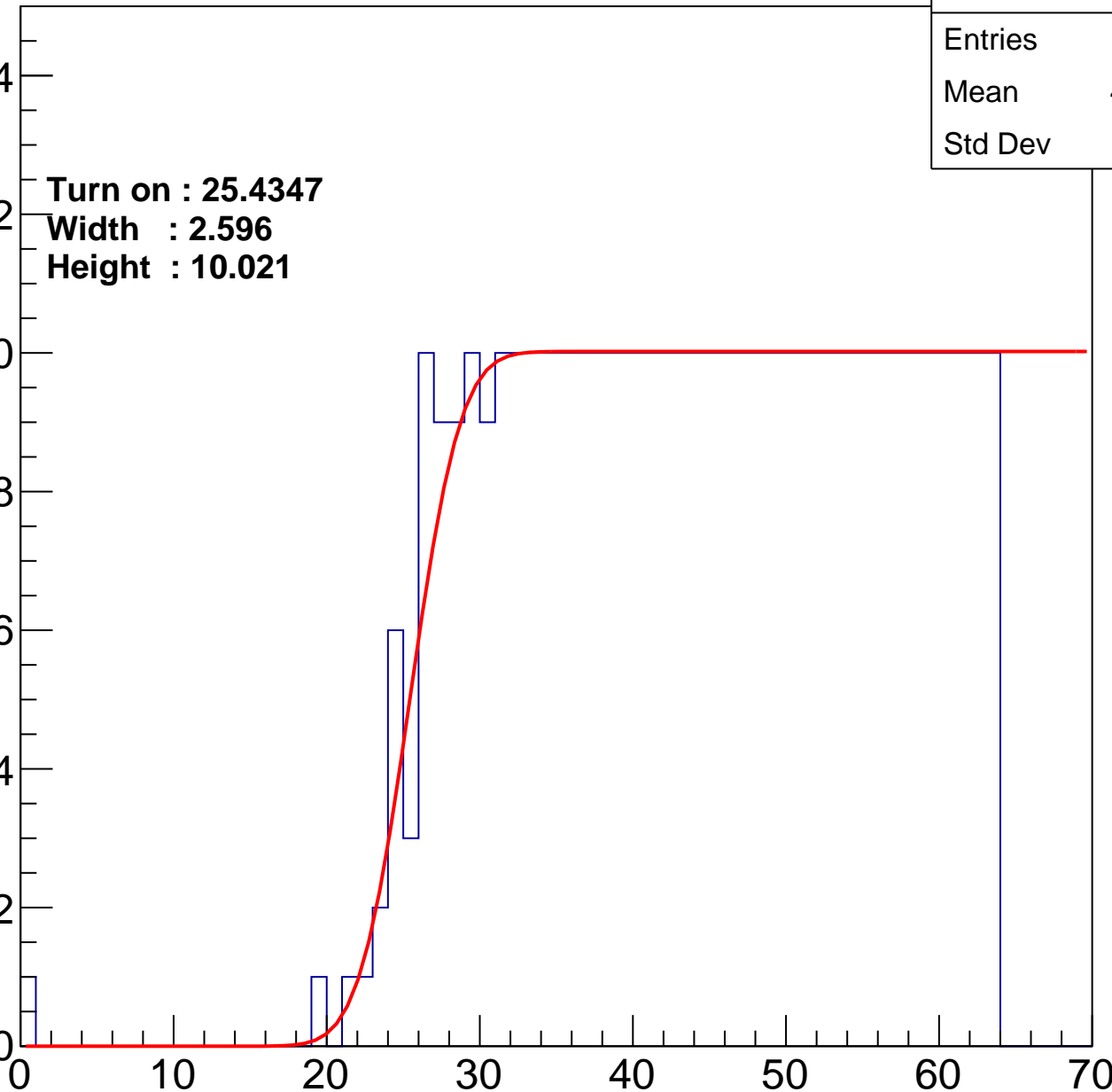
Width : 2.596

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch41

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.86
Std Dev	11.65

Turn on : 25.4925

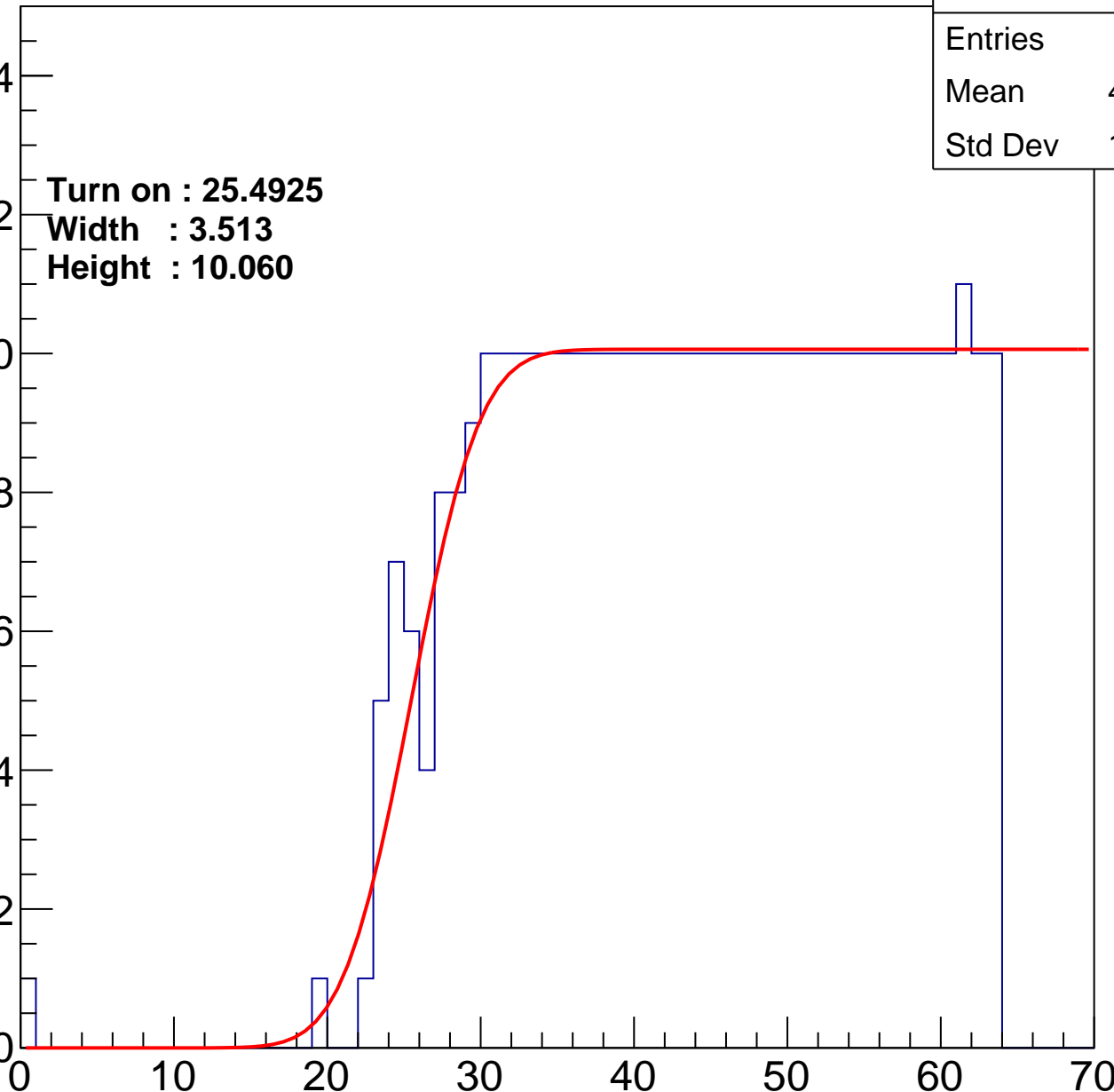
Width : 3.513

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch42

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.97
Std Dev	11.48

Turn on : 24.7470

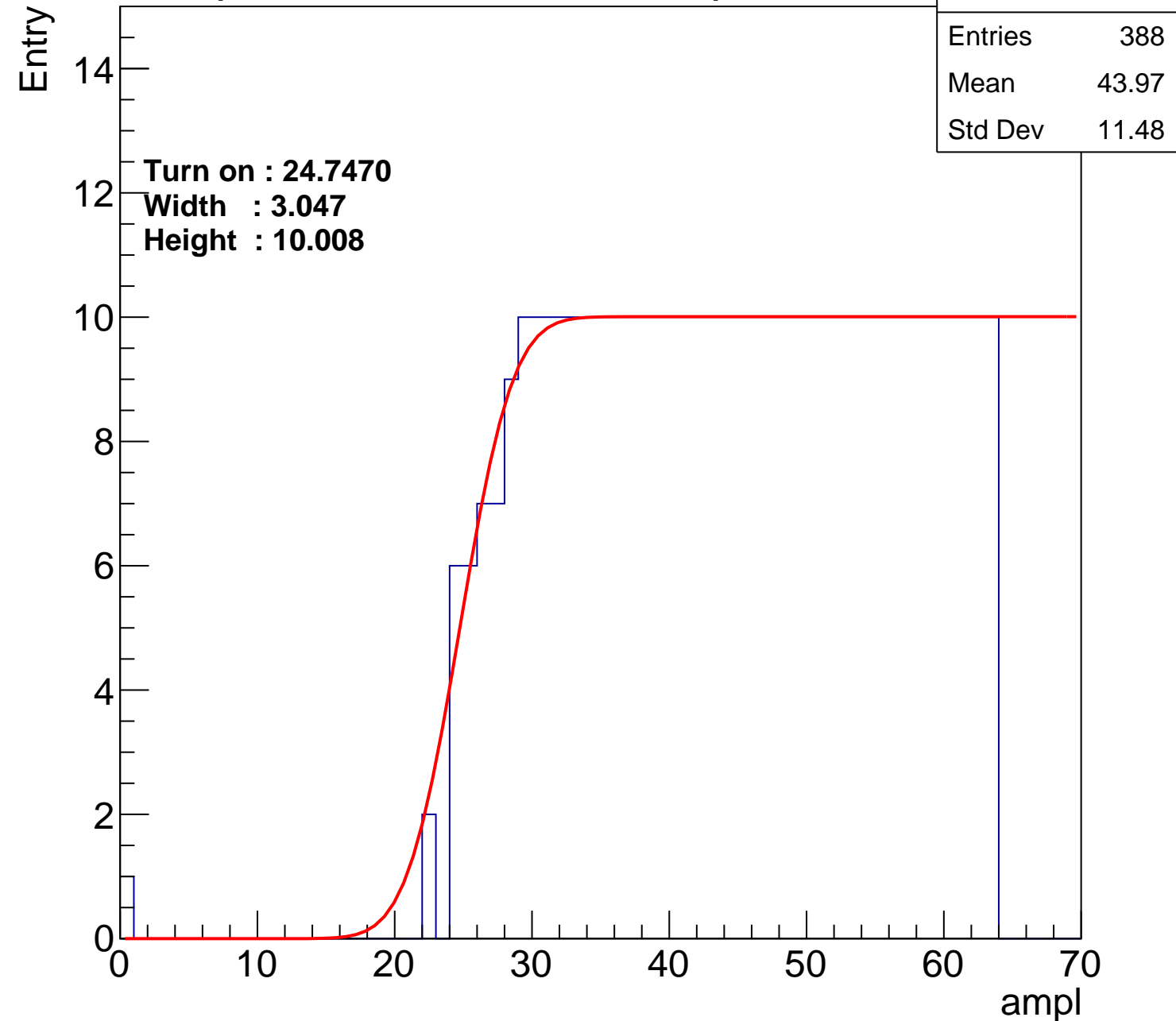
Width : 3.047

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch43

calib_packv5_042523_0143.root, FC#10, port B3

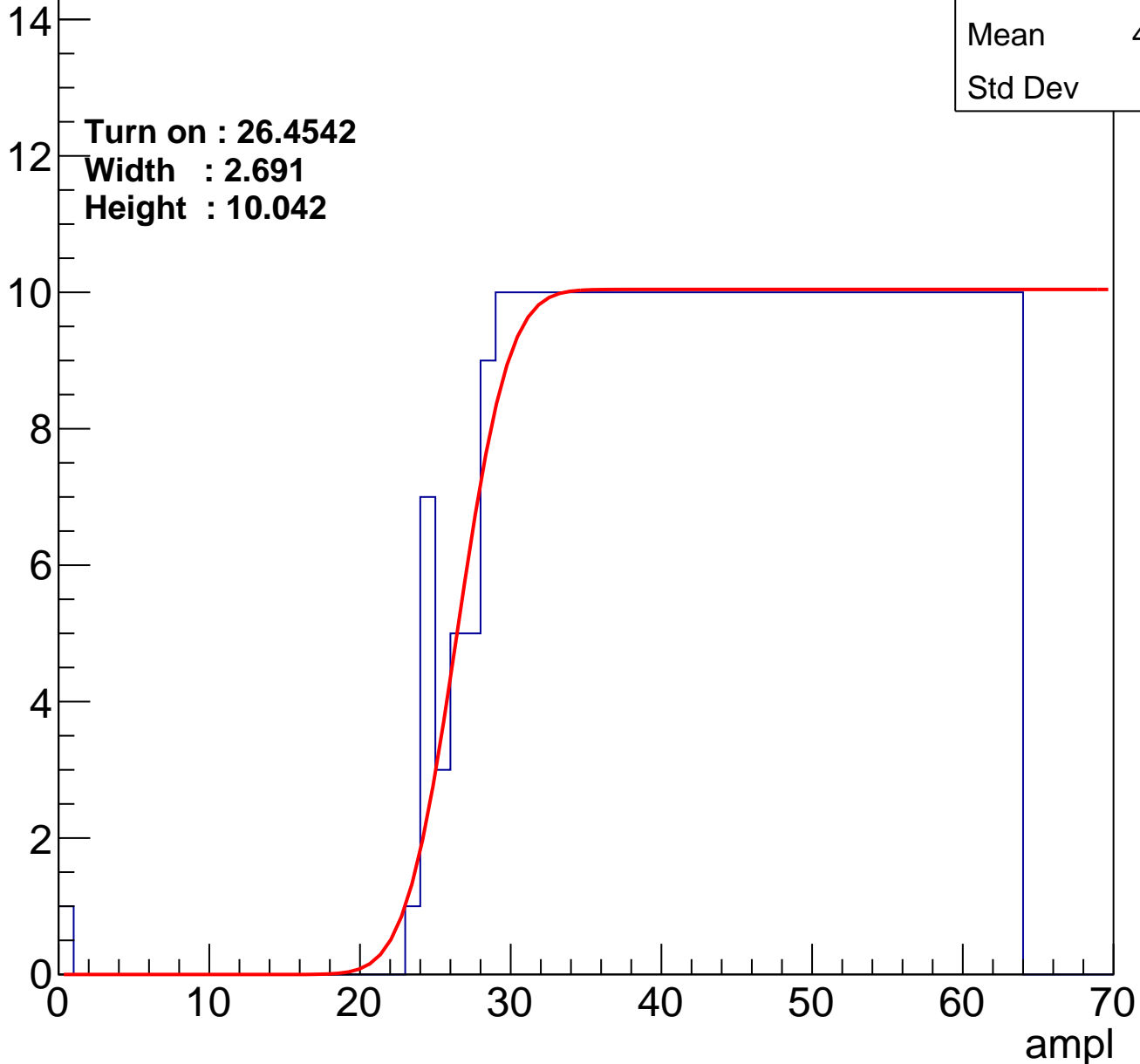
Entries	381
Mean	44.31
Std Dev	11.3

Turn on : 26.4542

Width : 2.691

Height : 10.042

Entry



B1L002S, U20-ch44

calib_packv5_042523_0143.root, FC#10, port B3

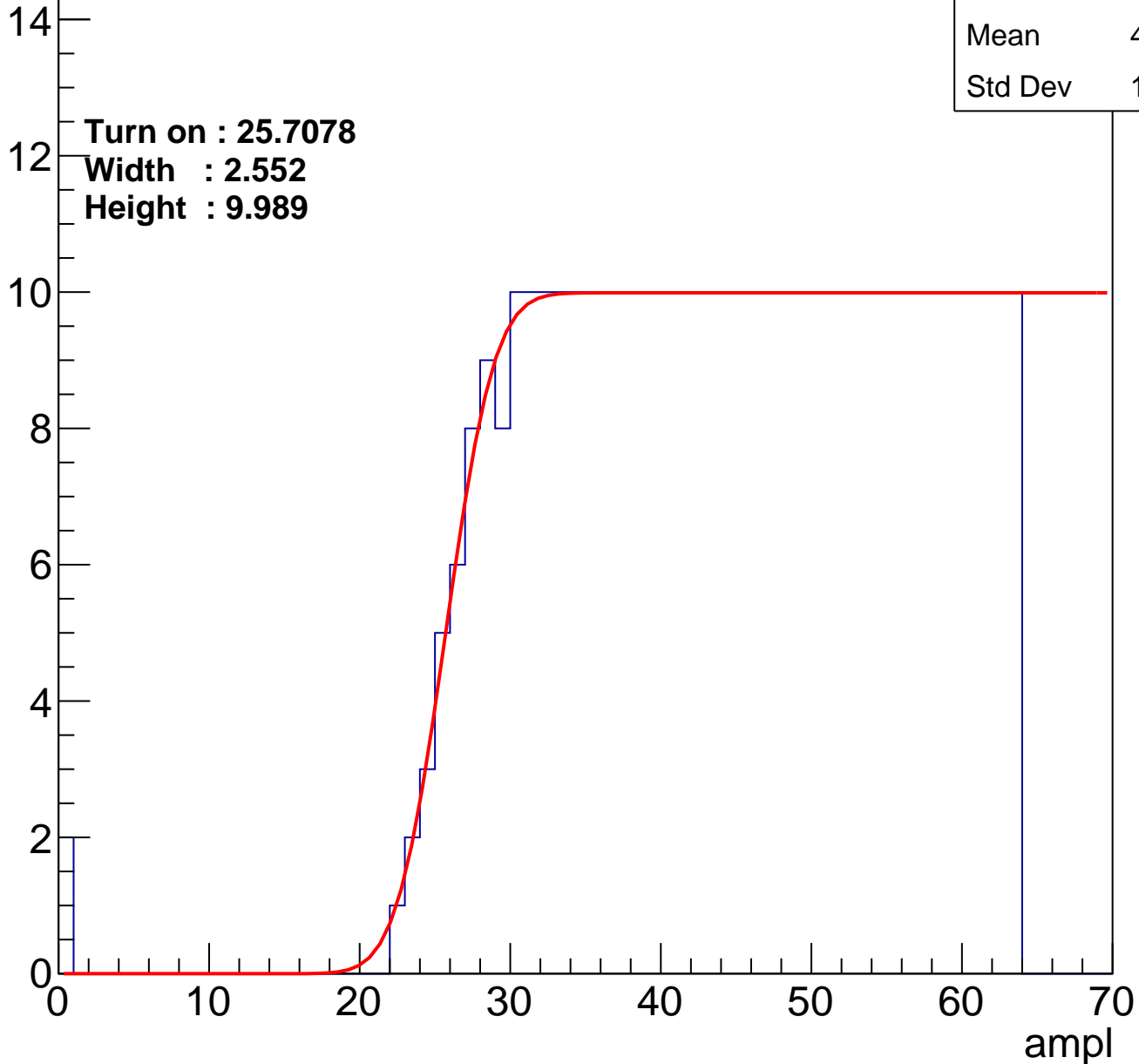
Entries	384
Mean	44.09
Std Dev	11.58

Turn on : 25.7078

Width : 2.552

Height : 9.989

Entry



B1L002S, U20-ch45

calib_packv5_042523_0143.root, FC#10, port B3

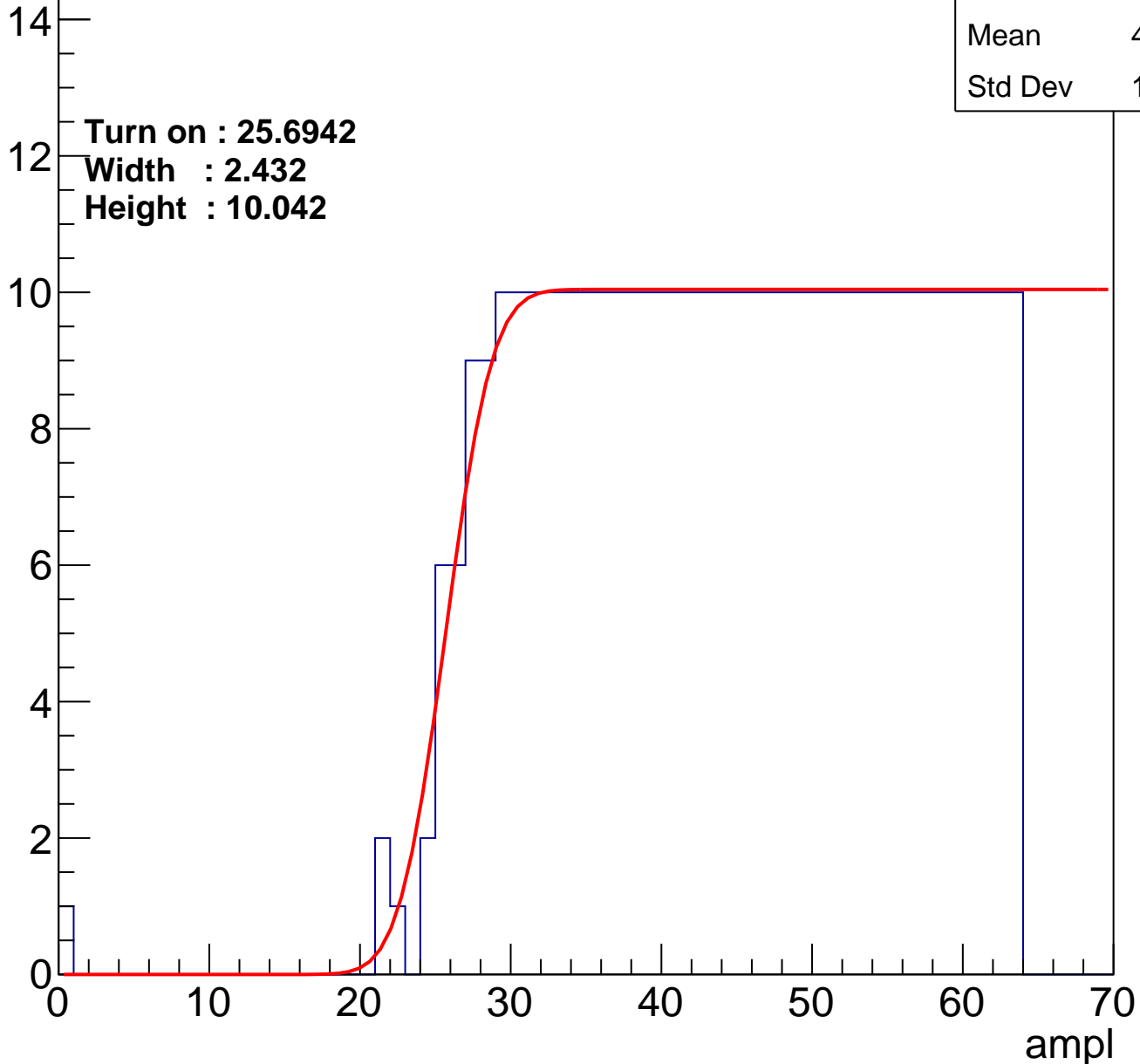
Entries	386
Mean	44.08
Std Dev	11.43

Turn on : 25.6942

Width : 2.432

Height : 10.042

Entry



B1L002S, U20-ch46

calib_packv5_042523_0143.root, FC#10, port B3

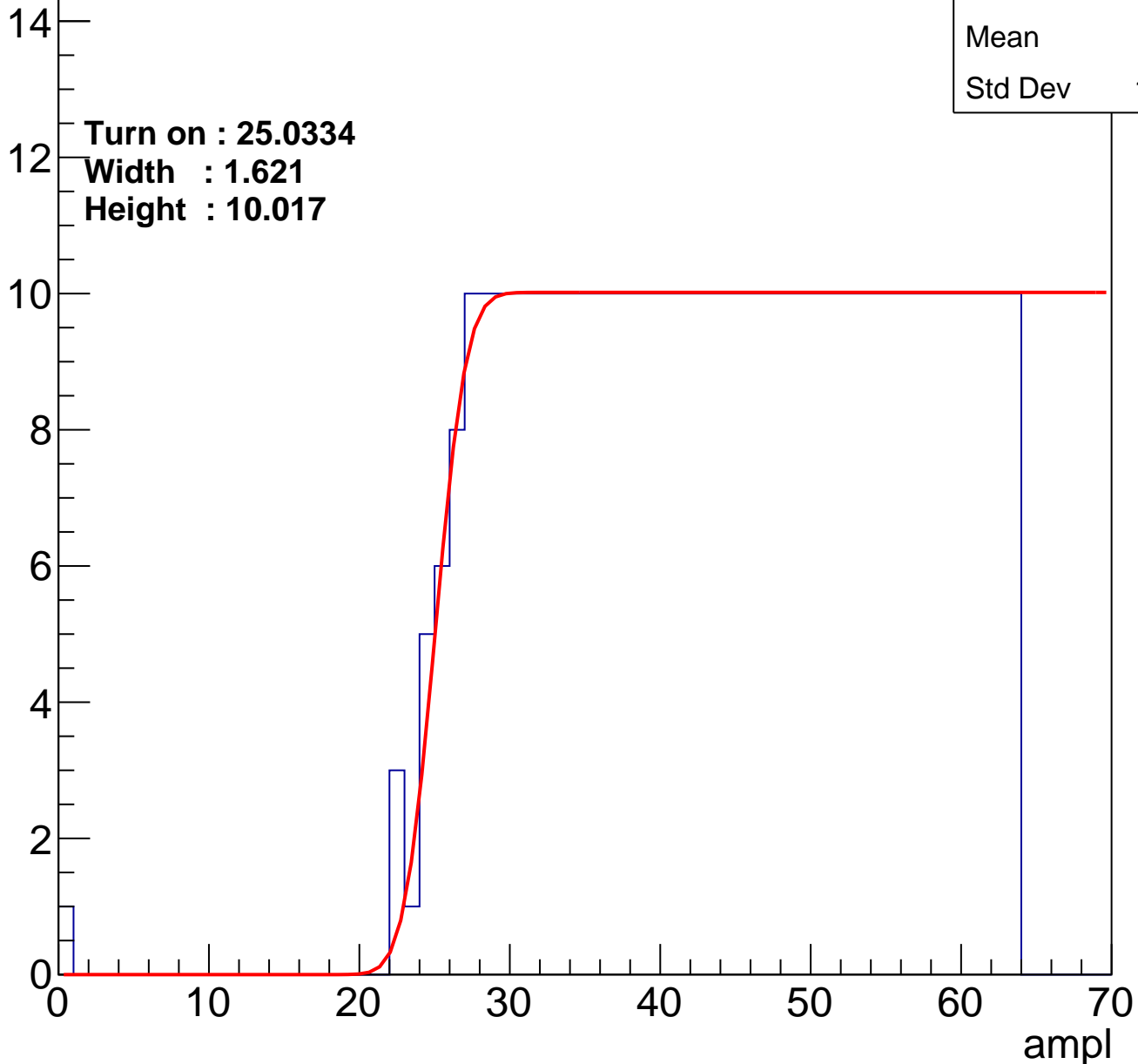
Entries	394
Mean	43.7
Std Dev	11.61

Turn on : 25.0334

Width : 1.621

Height : 10.017

Entry



B1L002S, U20-ch47

calib_packv5_042523_0143.root, FC#10, port B3

Entries	383
Mean	44.22
Std Dev	11.35

Turn on : 24.9962

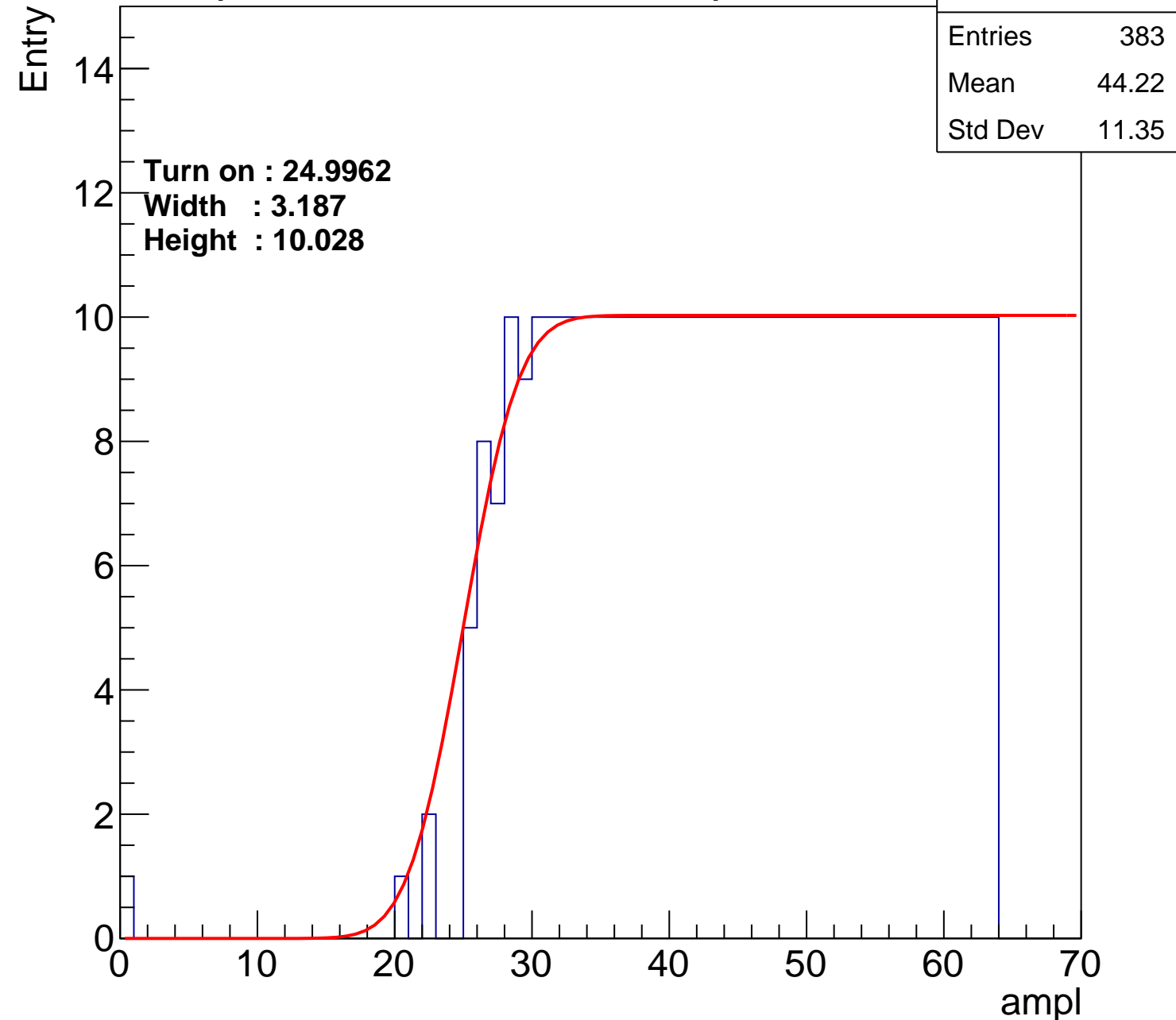
Width : 3.187

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch48

calib_packv5_042523_0143.root, FC#10, port B3

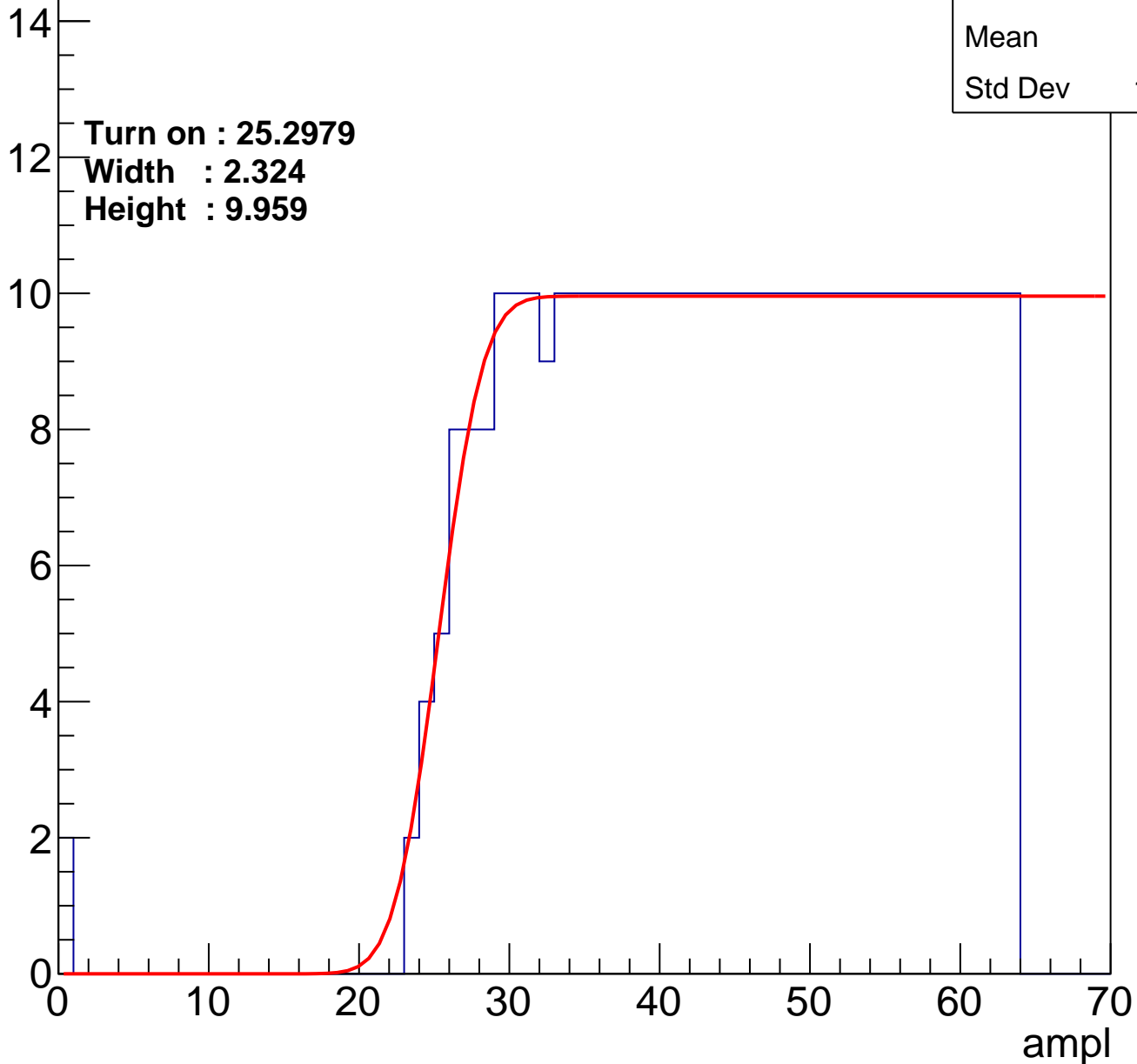
Entries	386
Mean	44
Std Dev	11.61

Turn on : 25.2979

Width : 2.324

Height : 9.959

Entry



B1L002S, U20-ch49

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.08
Std Dev	11.6

Turn on : 26.5332

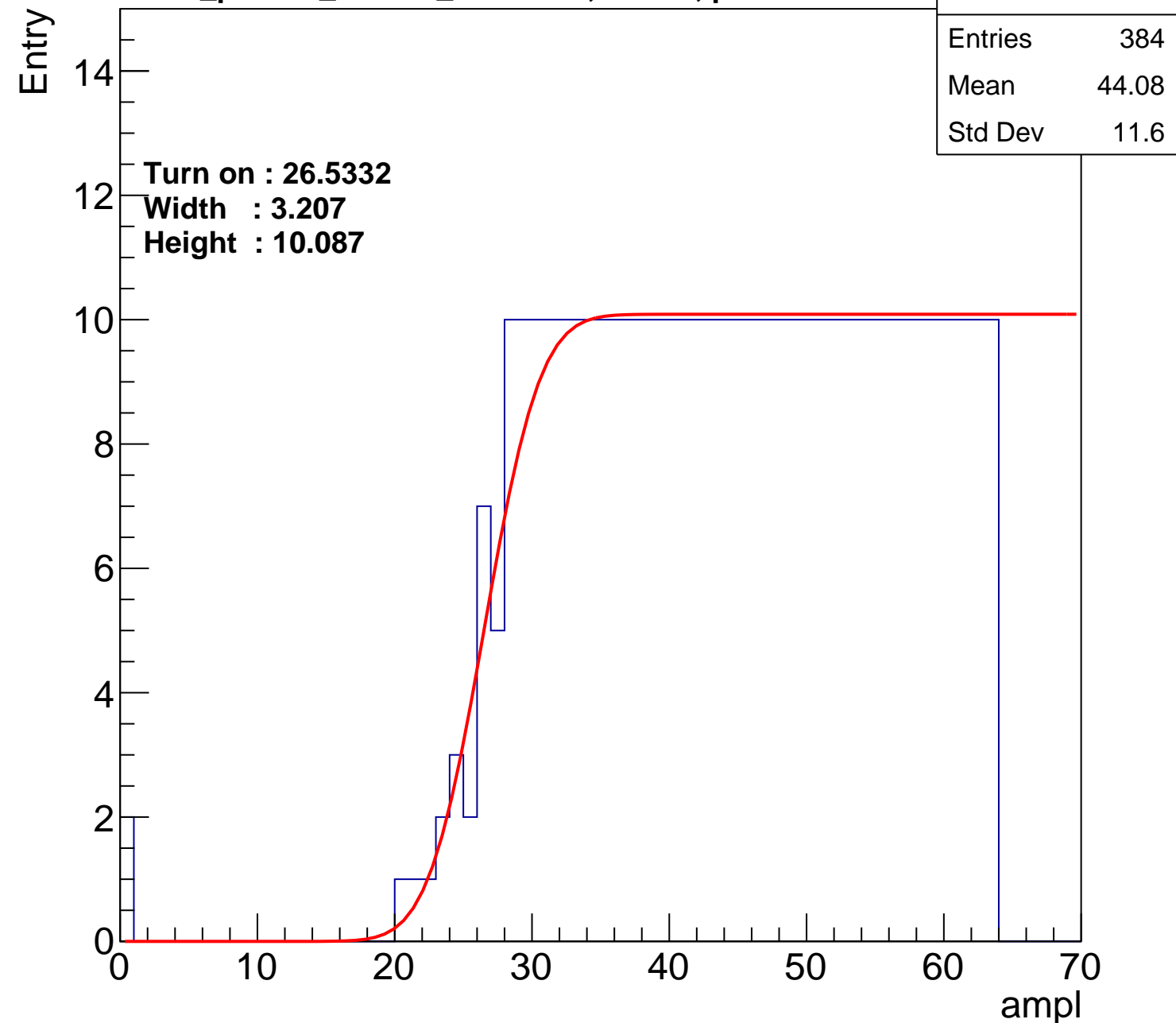
Width : 3.207

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch50

calib_packv5_042523_0143.root, FC#10, port B3

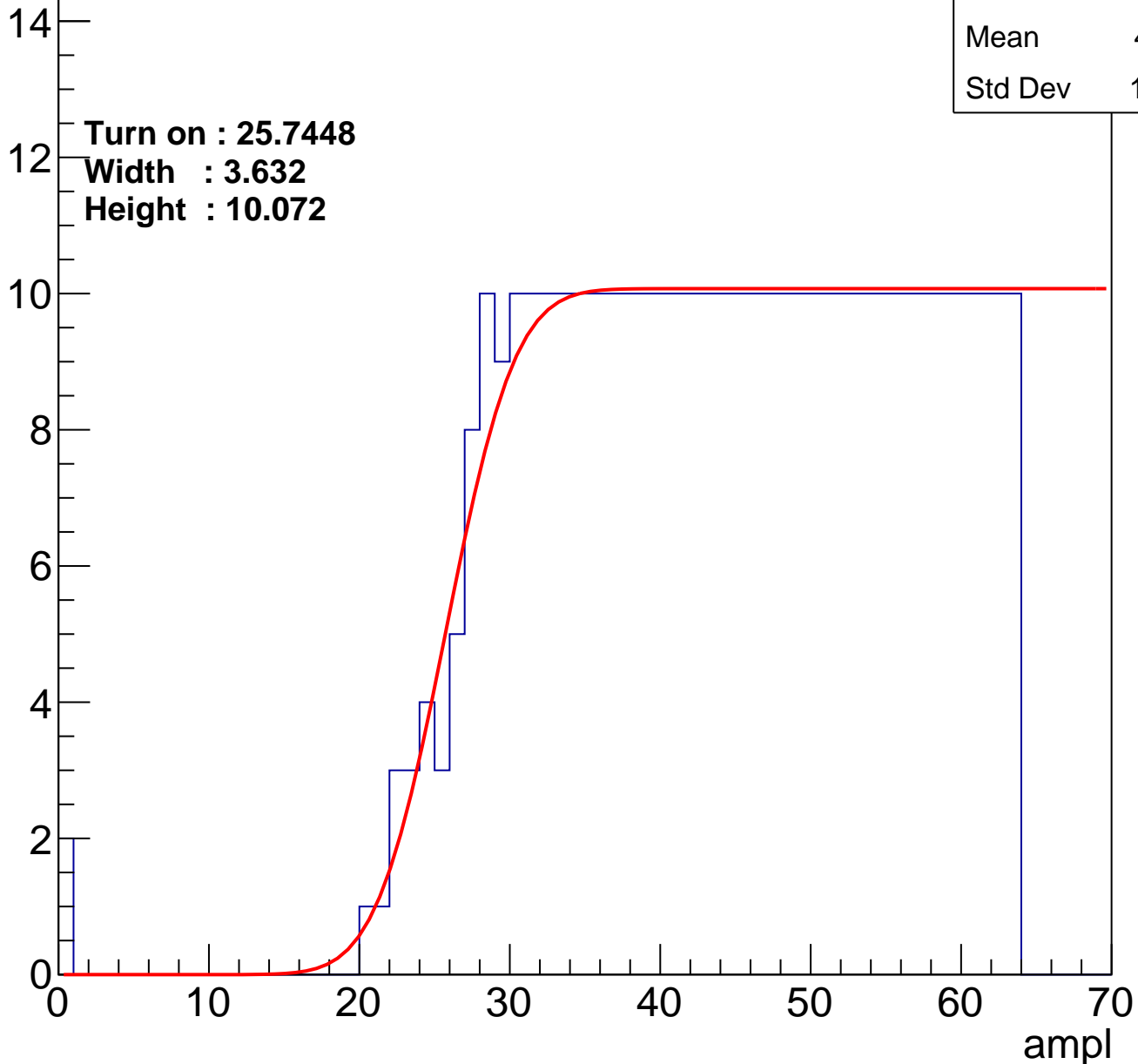
Entries	389
Mean	43.81
Std Dev	11.76

Turn on : 25.7448

Width : 3.632

Height : 10.072

Entry



B1L002S, U20-ch51

calib_packv5_042523_0143.root, FC#10, port B3

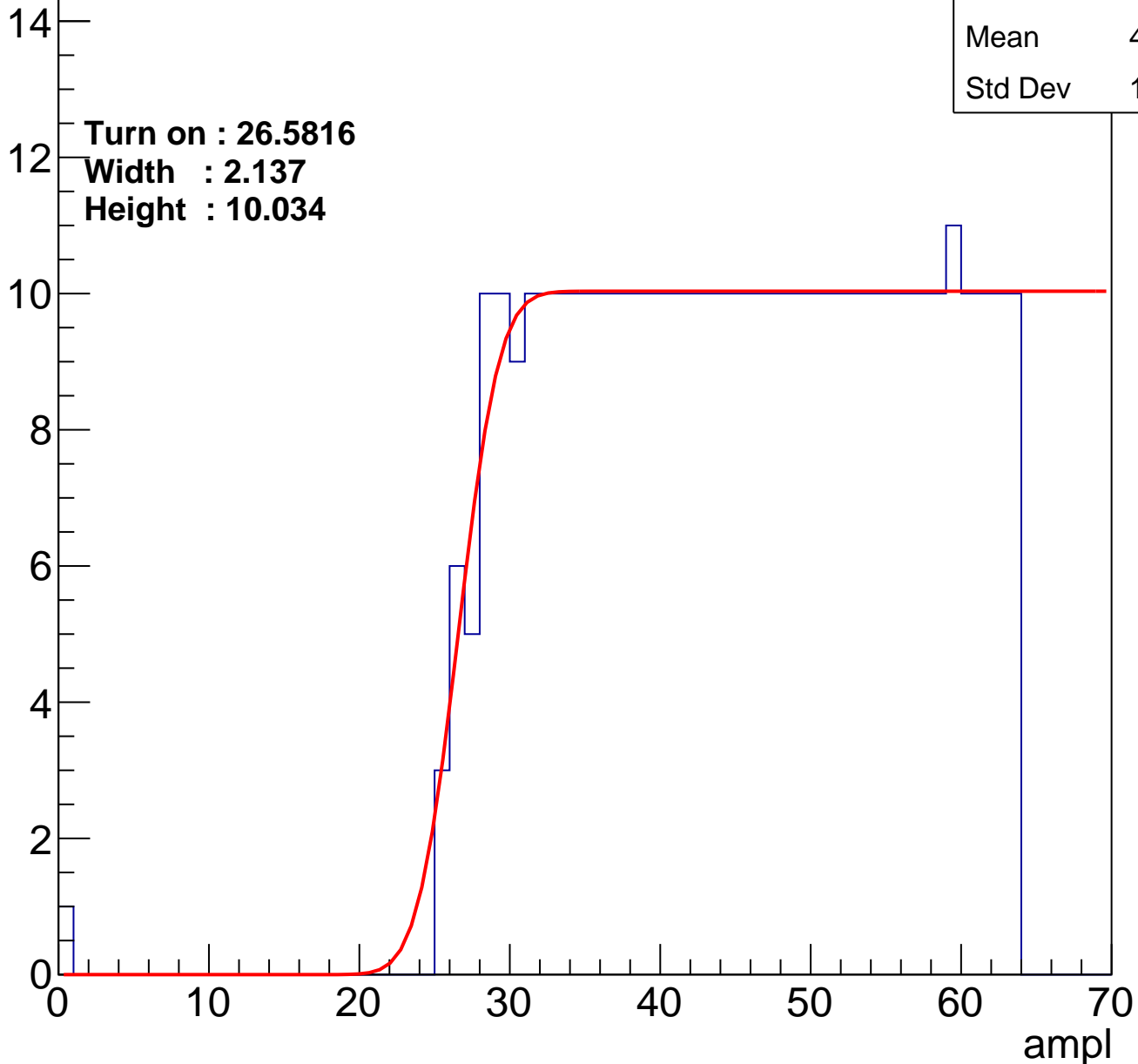
Entries	375
Mean	44.73
Std Dev	11.06

Turn on : 26.5816

Width : 2.137

Height : 10.034

Entry



B1L002S, U20-ch52

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.52
Std Dev	11.73

Turn on : 24.4685

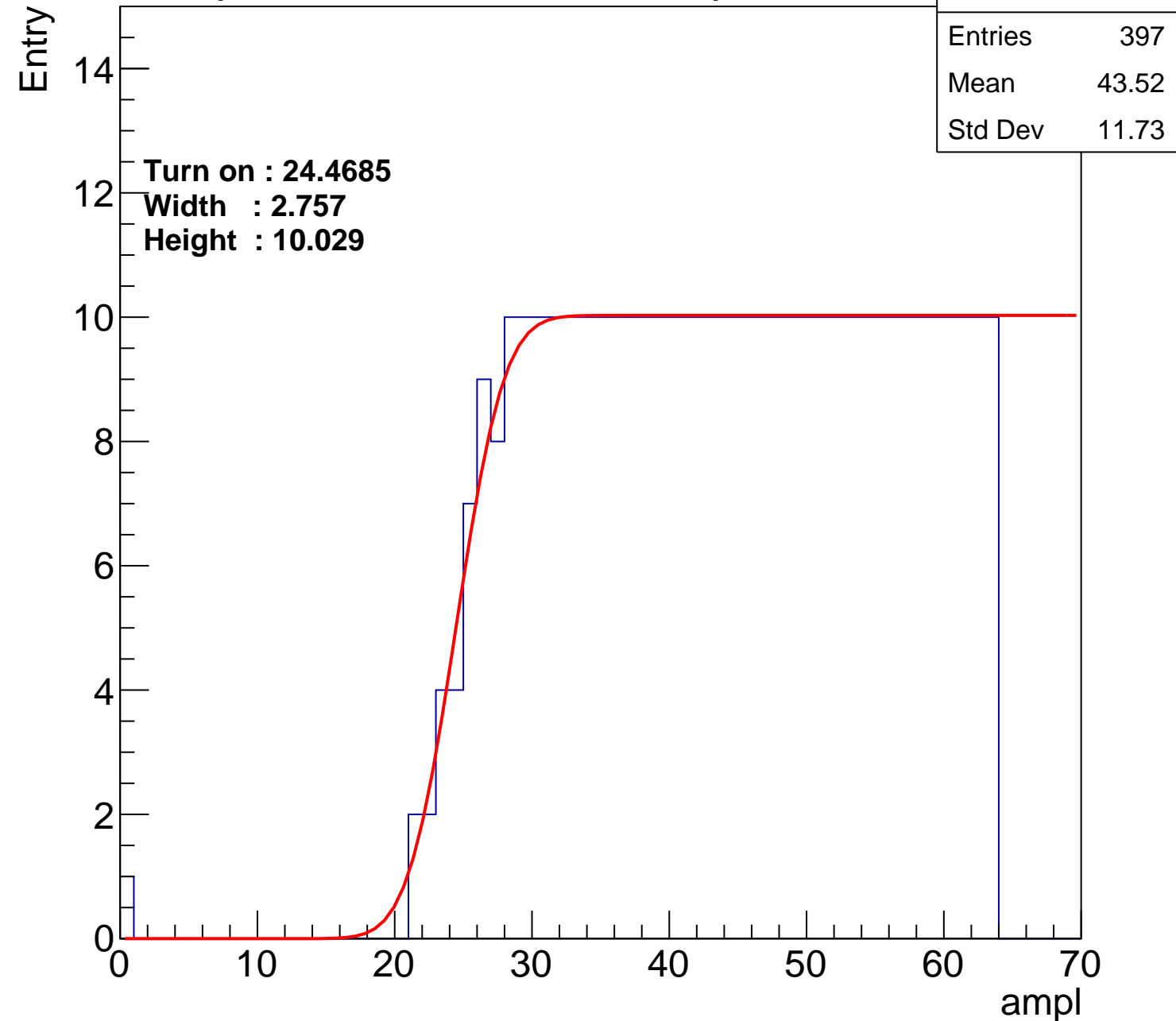
Width : 2.757

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch53

calib_packv5_042523_0143.root, FC#10, port B3

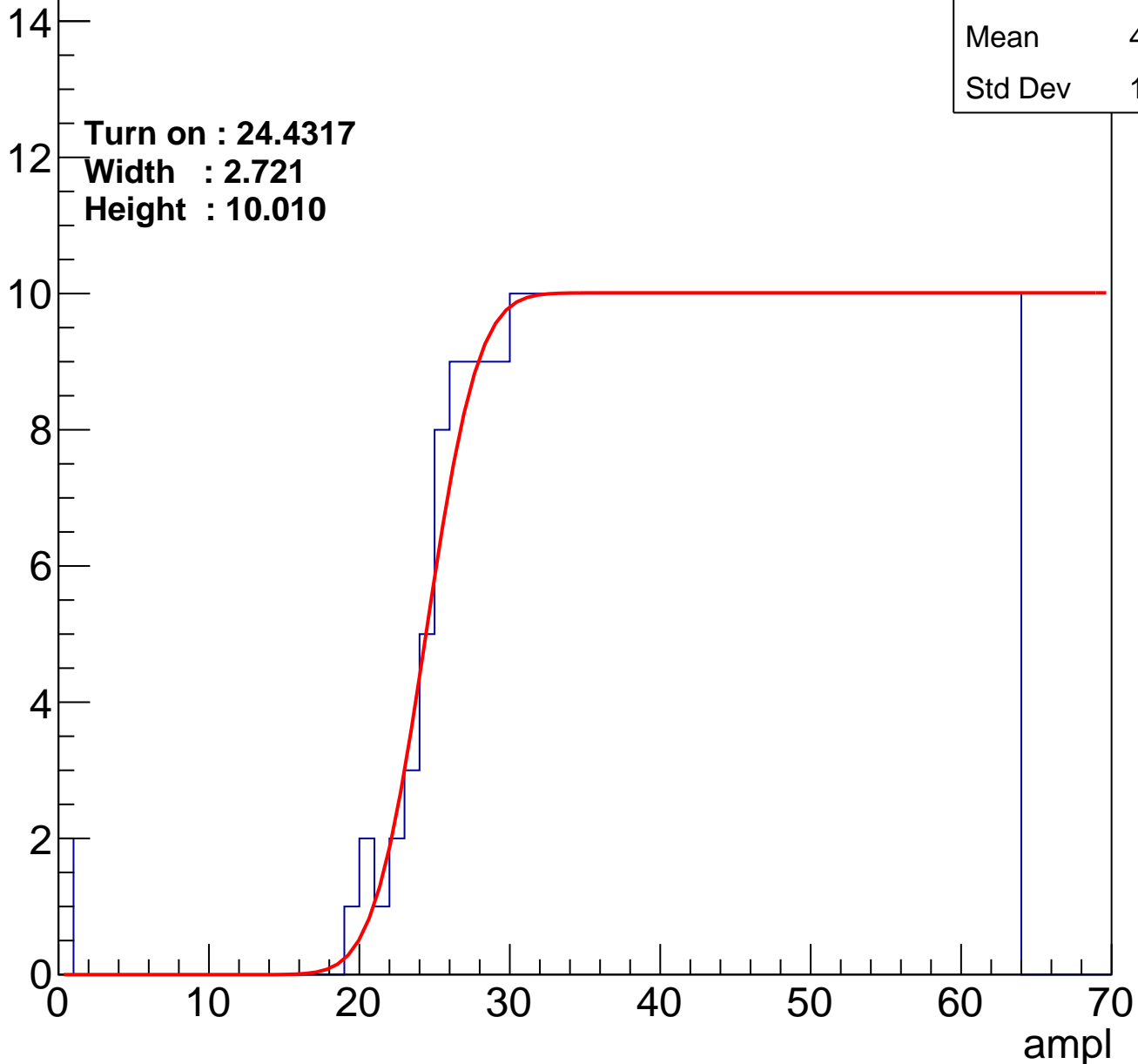
Entries	400
Mean	43.28
Std Dev	12.03

Turn on : 24.4317

Width : 2.721

Height : 10.010

Entry



B1L002S, U20-ch54

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.28
Std Dev	12.04

Turn on : 24.7927

Width : 2.907

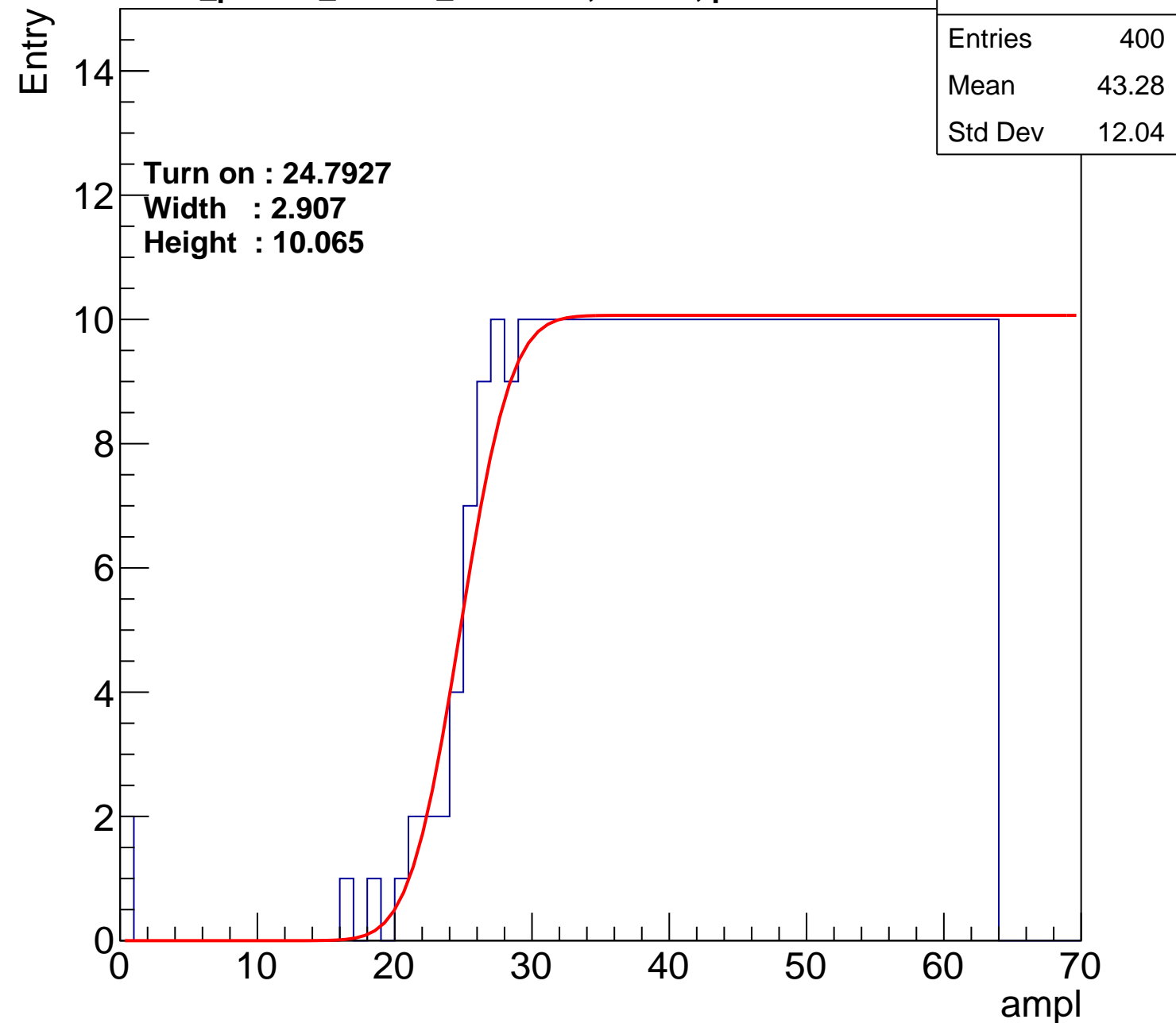
Height : 10.065

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch55

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.13
Std Dev	11.88

Turn on : 26.8553

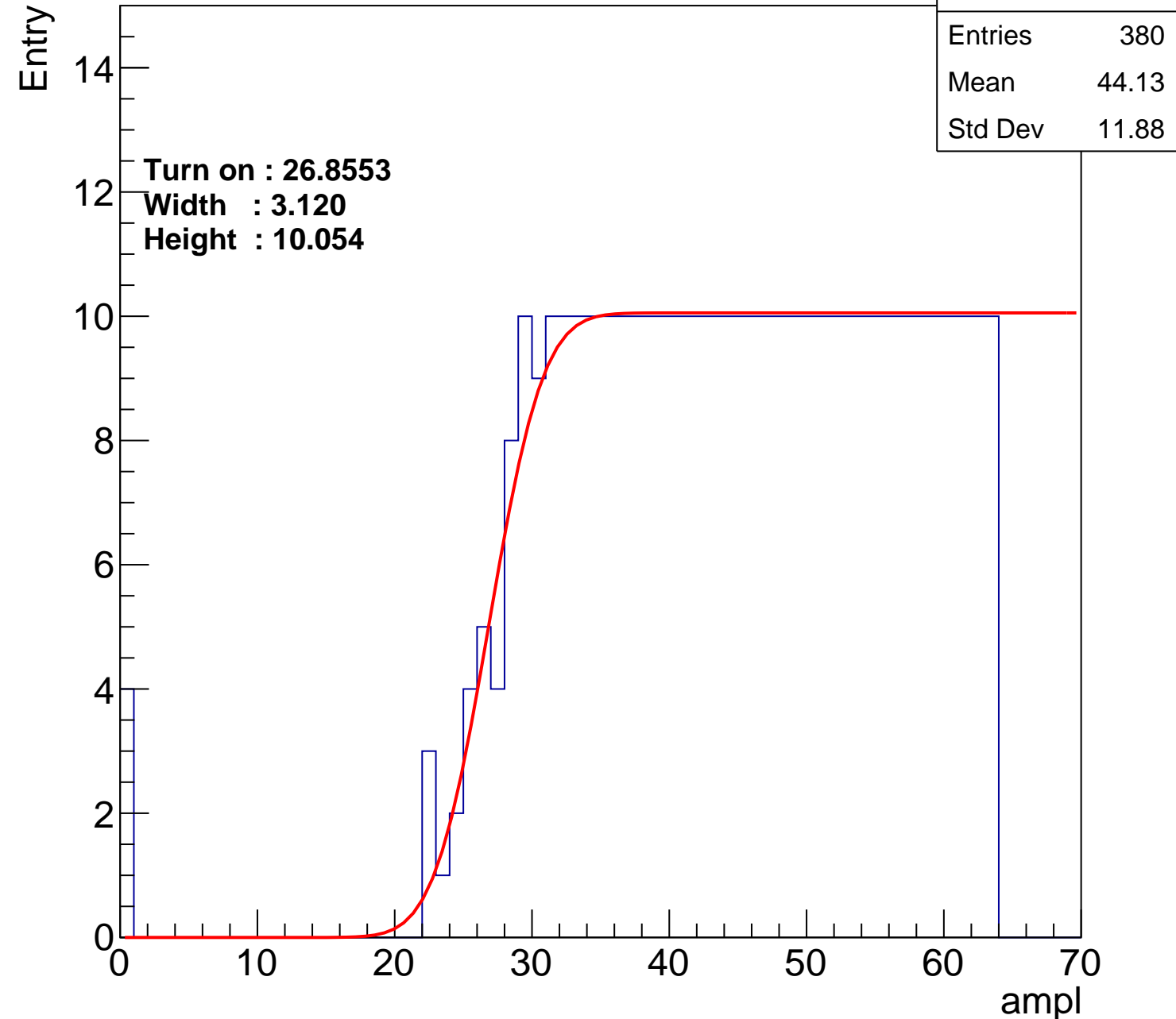
Width : 3.120

Height : 10.054

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch56

calib_packv5_042523_0143.root, FC#10, port B3

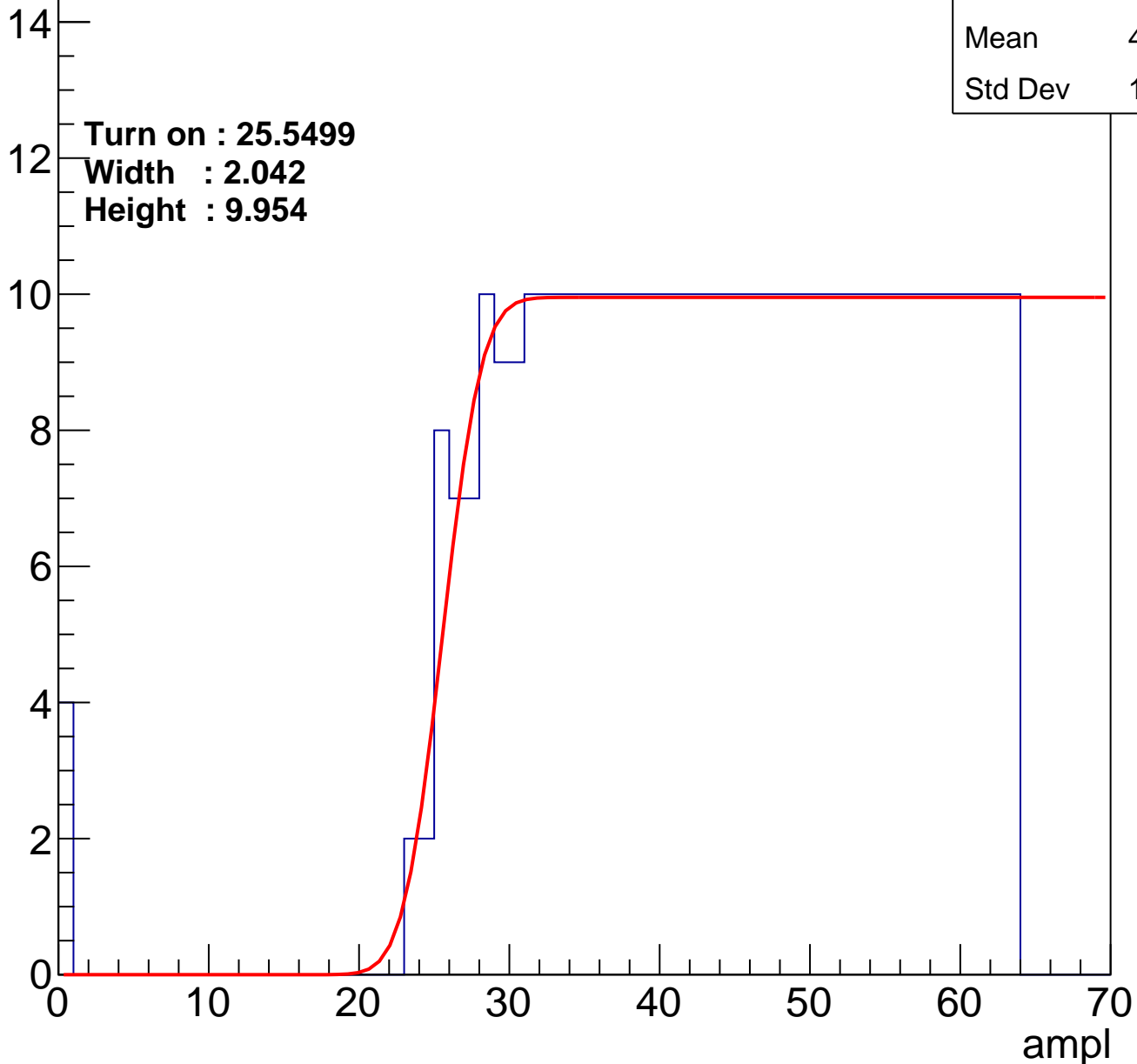
Entries	388
Mean	43.78
Std Dev	11.99

Turn on : 25.5499

Width : 2.042

Height : 9.954

Entry



B1L002S, U20-ch57

calib_packv5_042523_0143.root, FC#10, port B3

Entries	414
Mean	42.55
Std Dev	12.5

Turn on : 22.7976

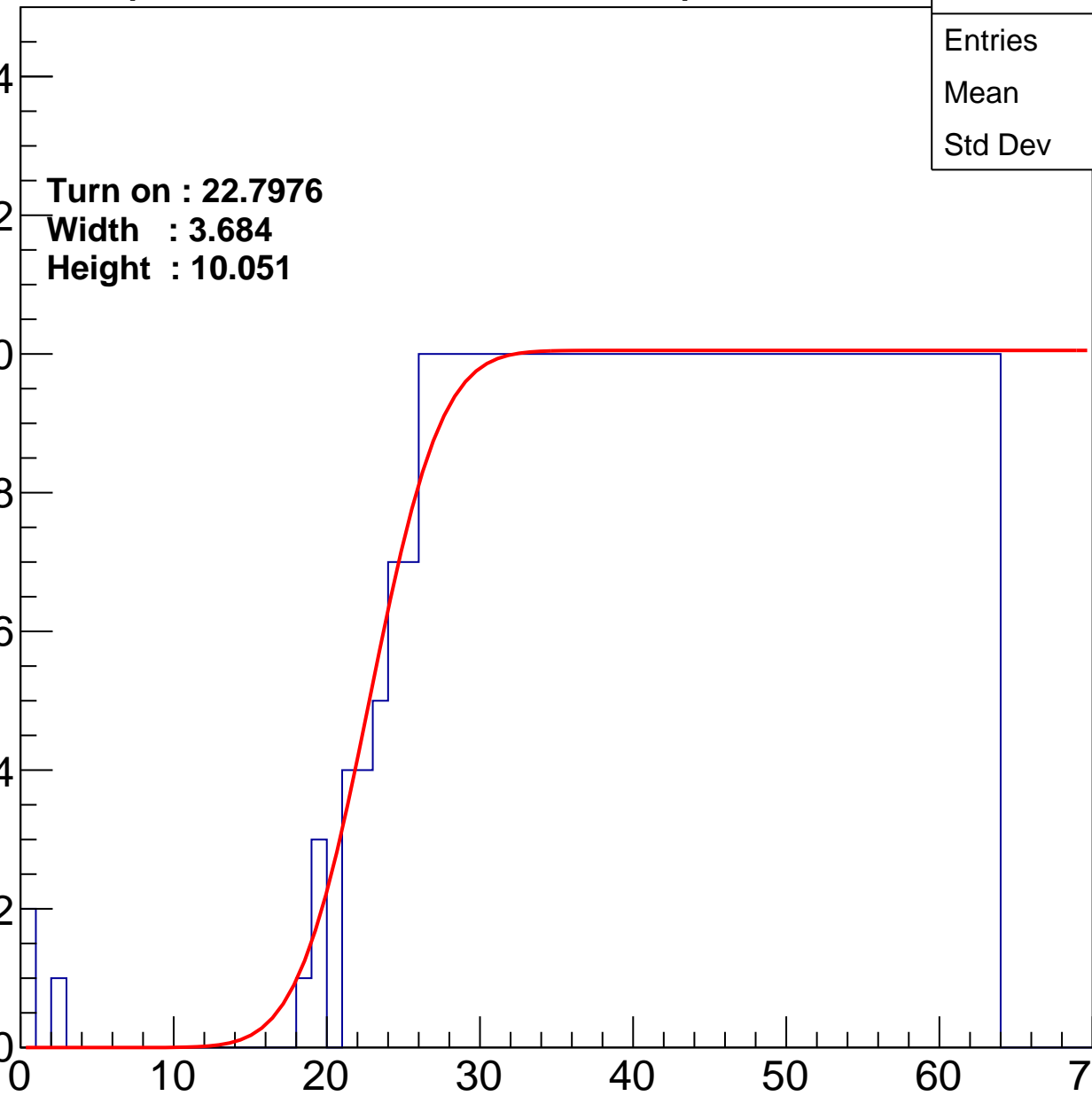
Width : 3.684

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch58

calib_packv5_042523_0143.root, FC#10, port B3

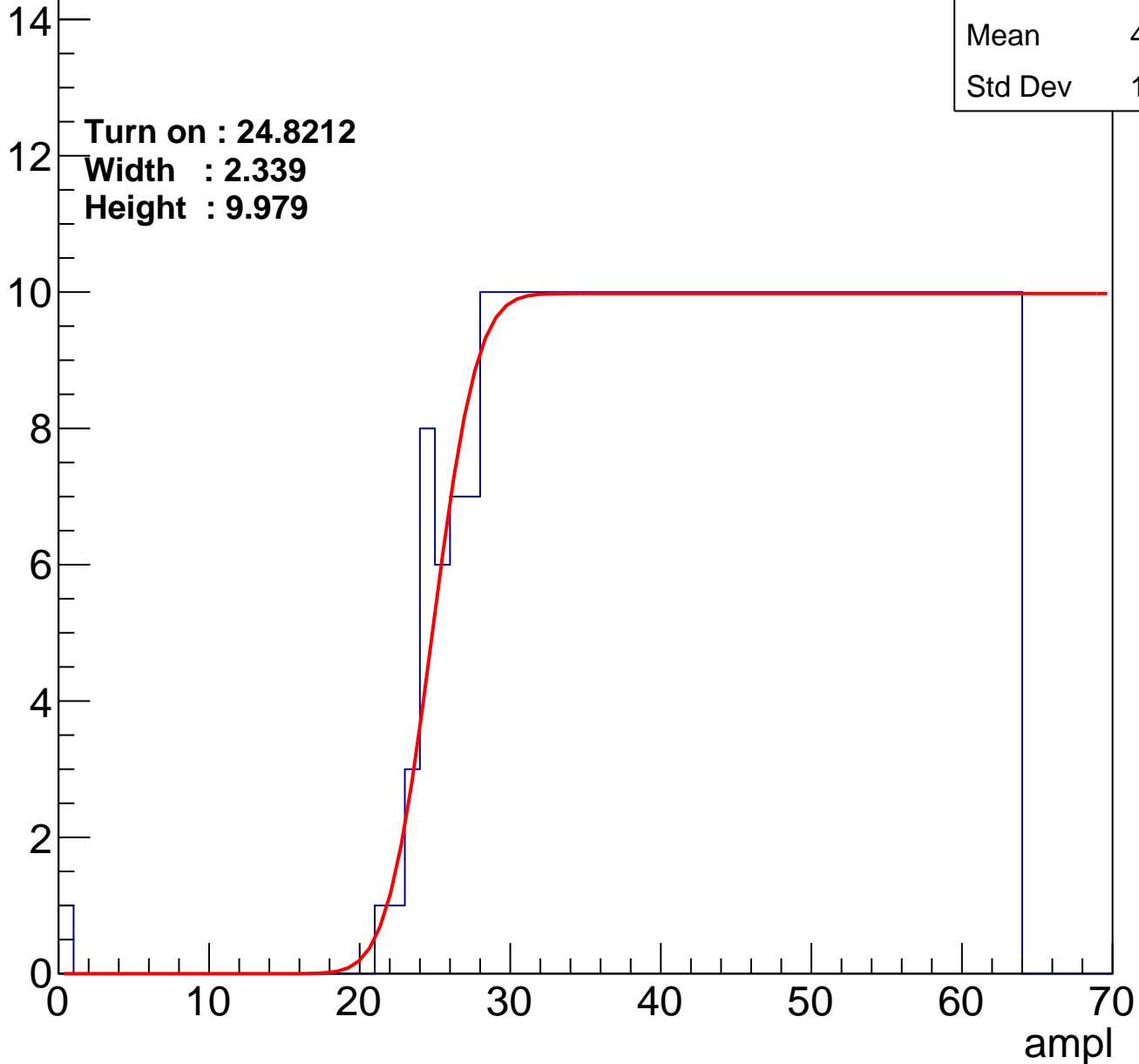
Entries	394
Mean	43.67
Std Dev	11.66

Turn on : 24.8212

Width : 2.339

Height : 9.979

Entry



B1L002S, U20-ch59

calib_packv5_042523_0143.root, FC#10, port B3

Entries	386
Mean	43.94
Std Dev	11.78

Turn on : 25.8008

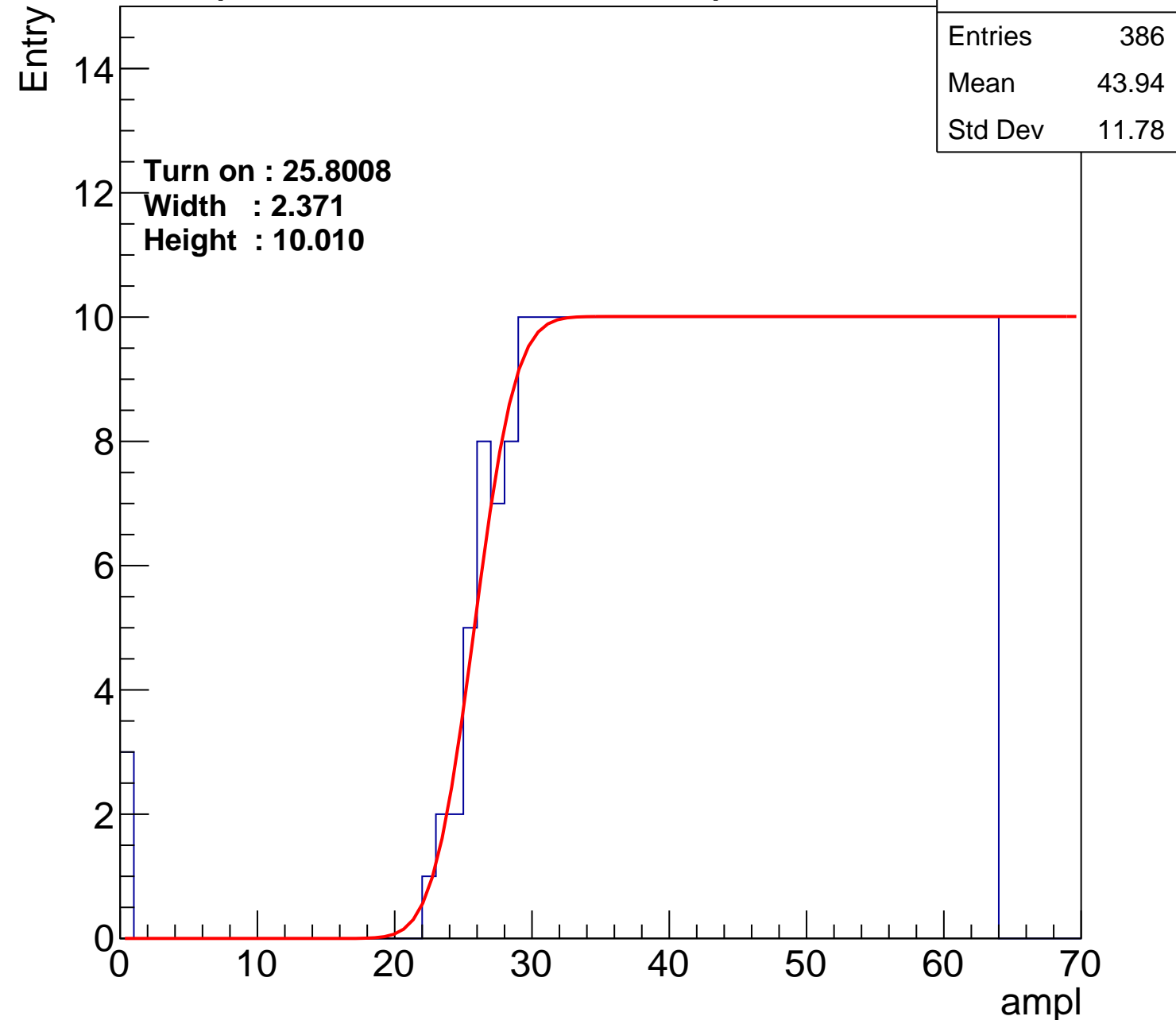
Width : 2.371

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch60

calib_packv5_042523_0143.root, FC#10, port B3

Entries	388
Mean	43.69
Std Dev	12.21

Turn on : 25.8988

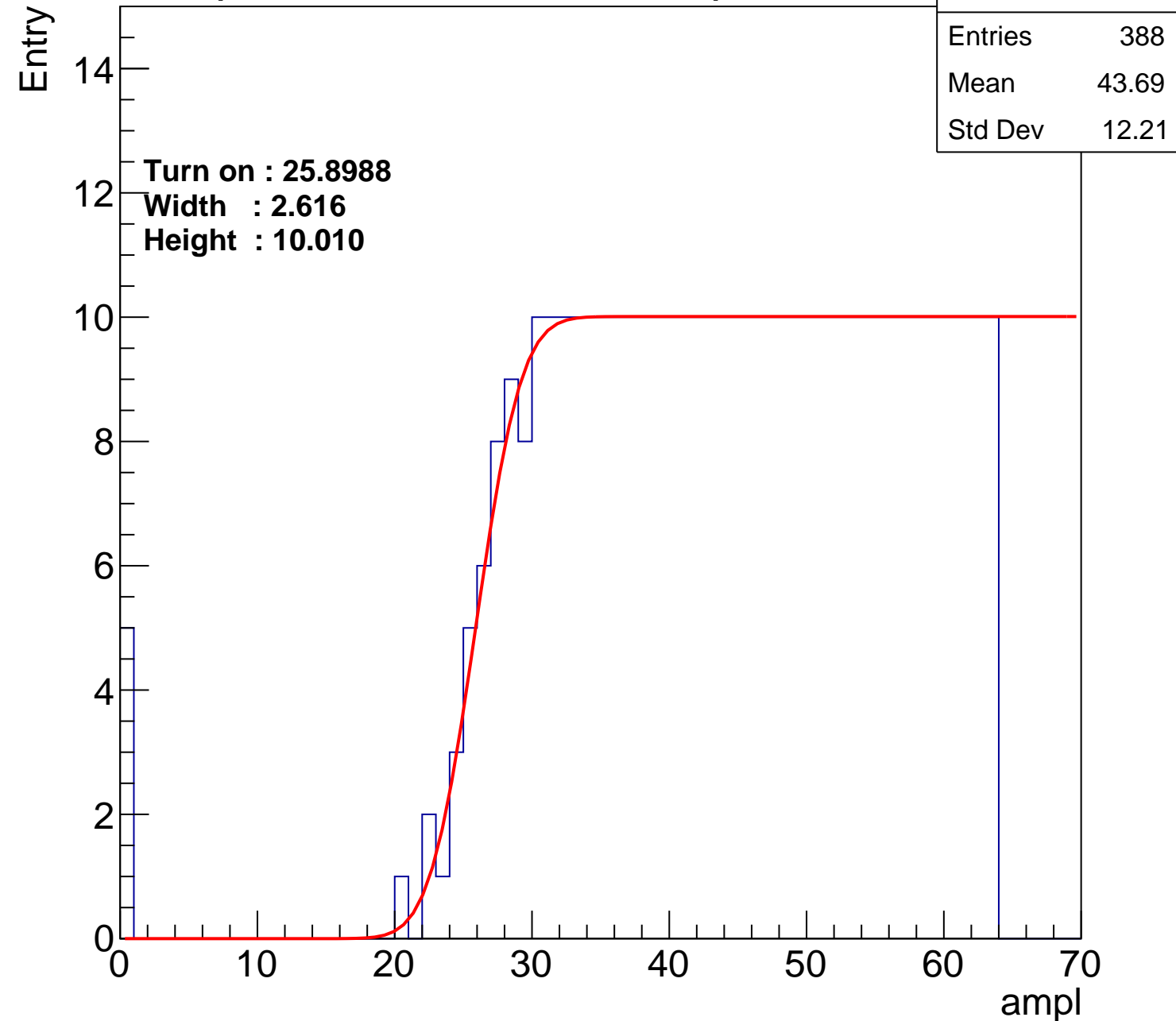
Width : 2.616

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch61

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.52
Std Dev	12.05

Turn on : 25.3768

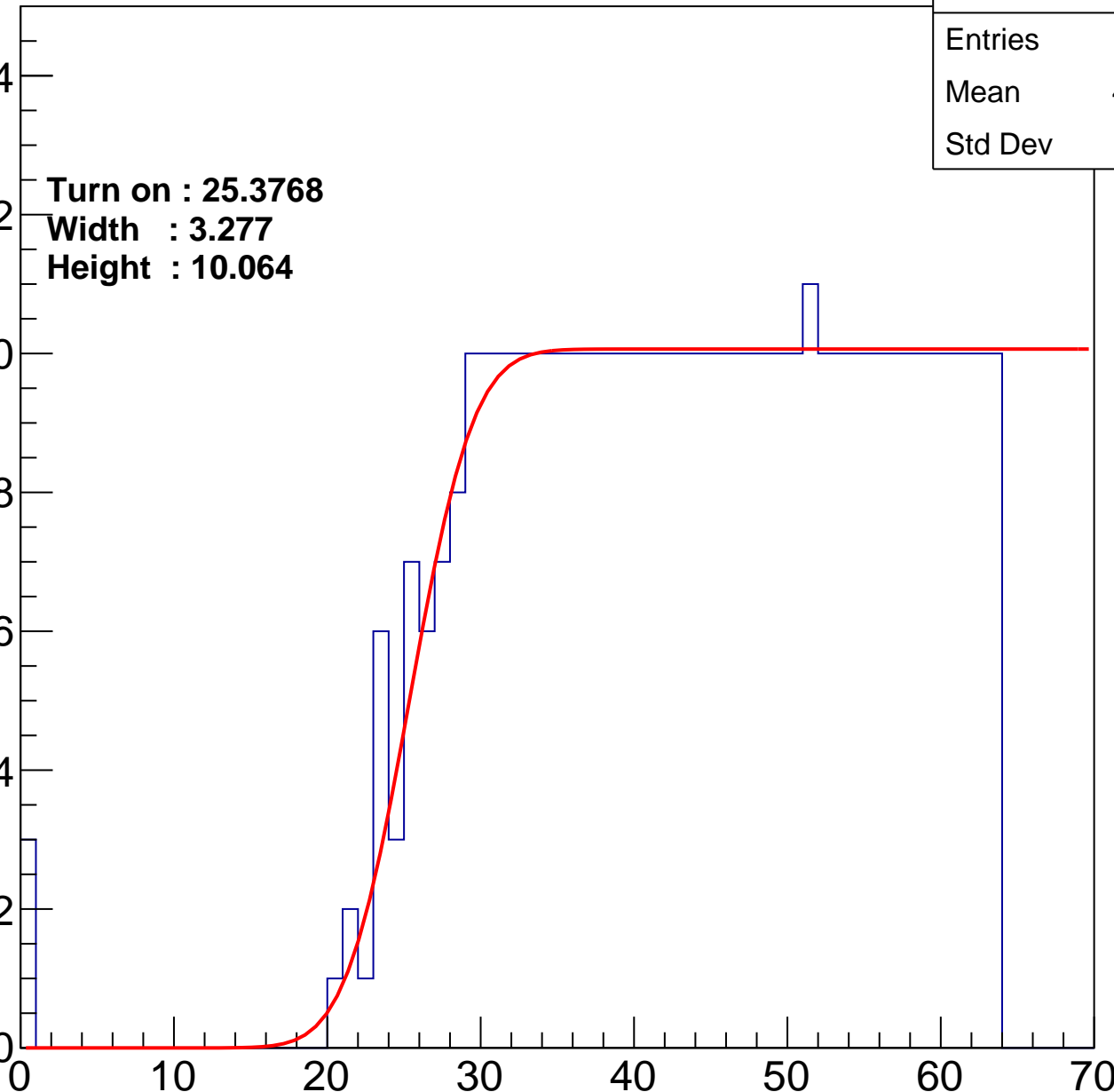
Width : 3.277

Height : 10.064

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch62

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.36
Std Dev	11.29

Turn on : 26.6147

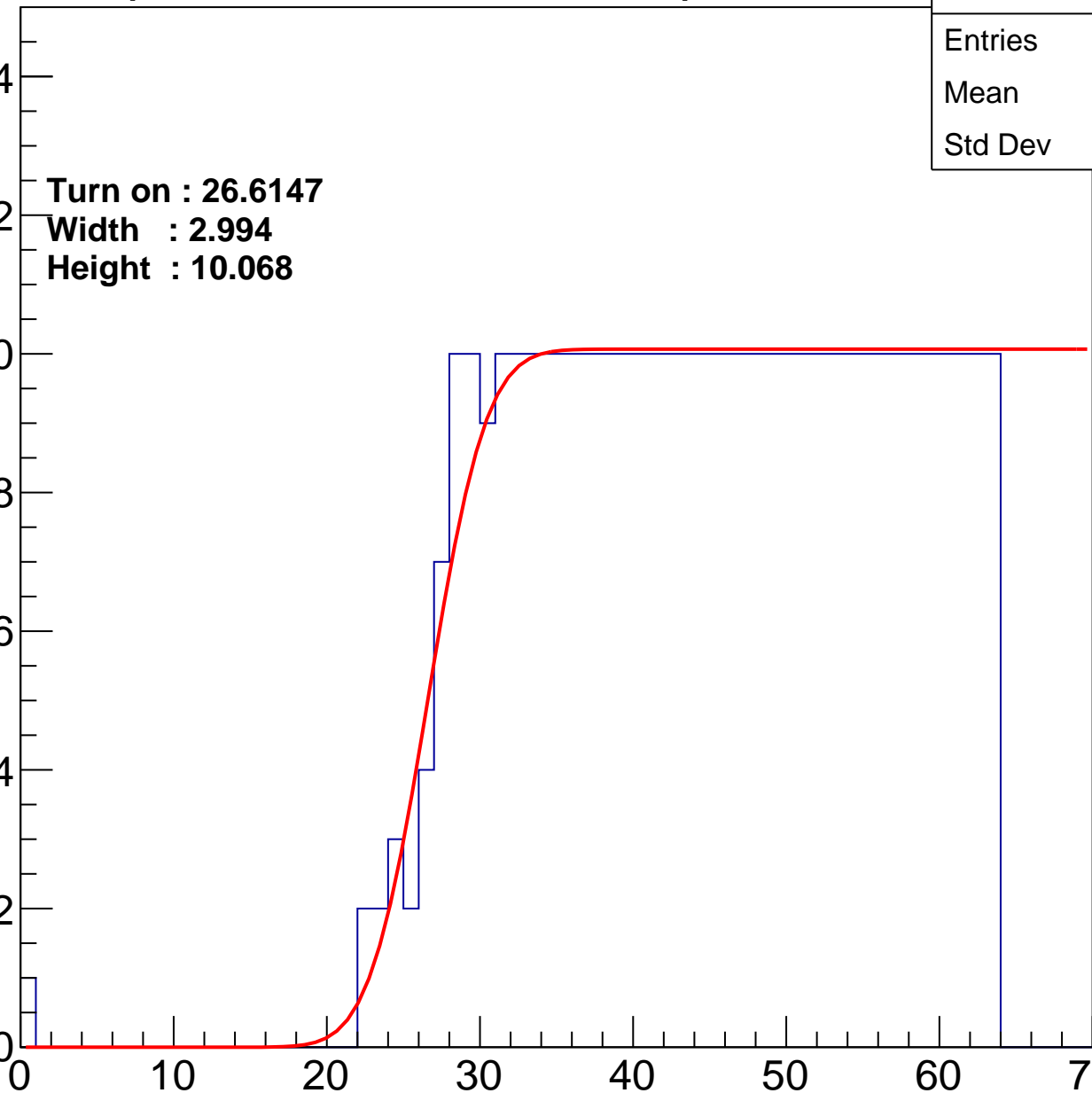
Width : 2.994

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch63

calib_packv5_042523_0143.root, FC#10, port B3

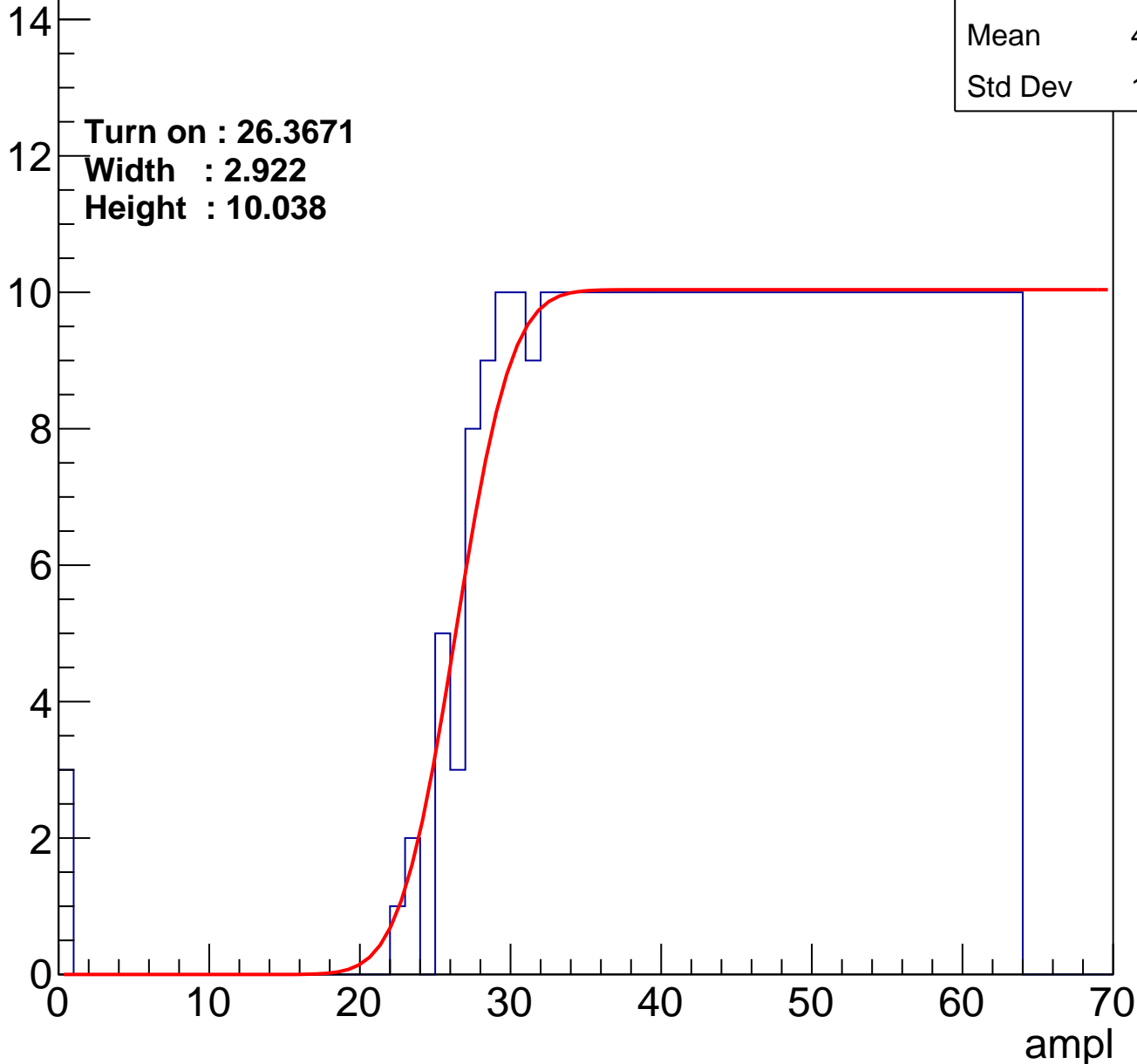
Entries	380
Mean	44.23
Std Dev	11.64

Turn on : 26.3671

Width : 2.922

Height : 10.038

Entry



B1L002S, U20-ch64

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.08
Std Dev	12.19

Turn on : 24.0718

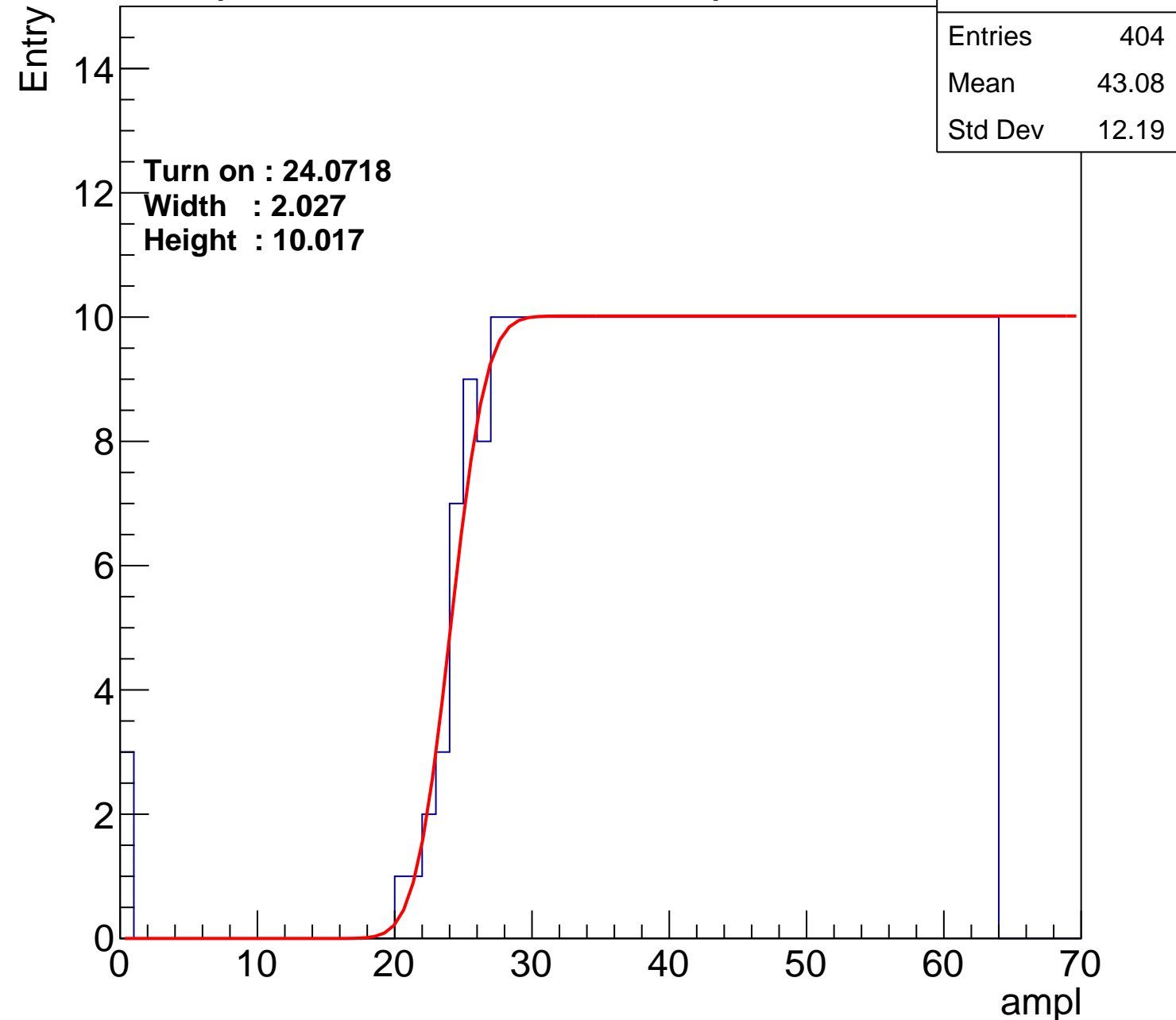
Width : 2.027

Height : 10.017

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch65

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.92
Std Dev	11.94

Turn on : 26.1048

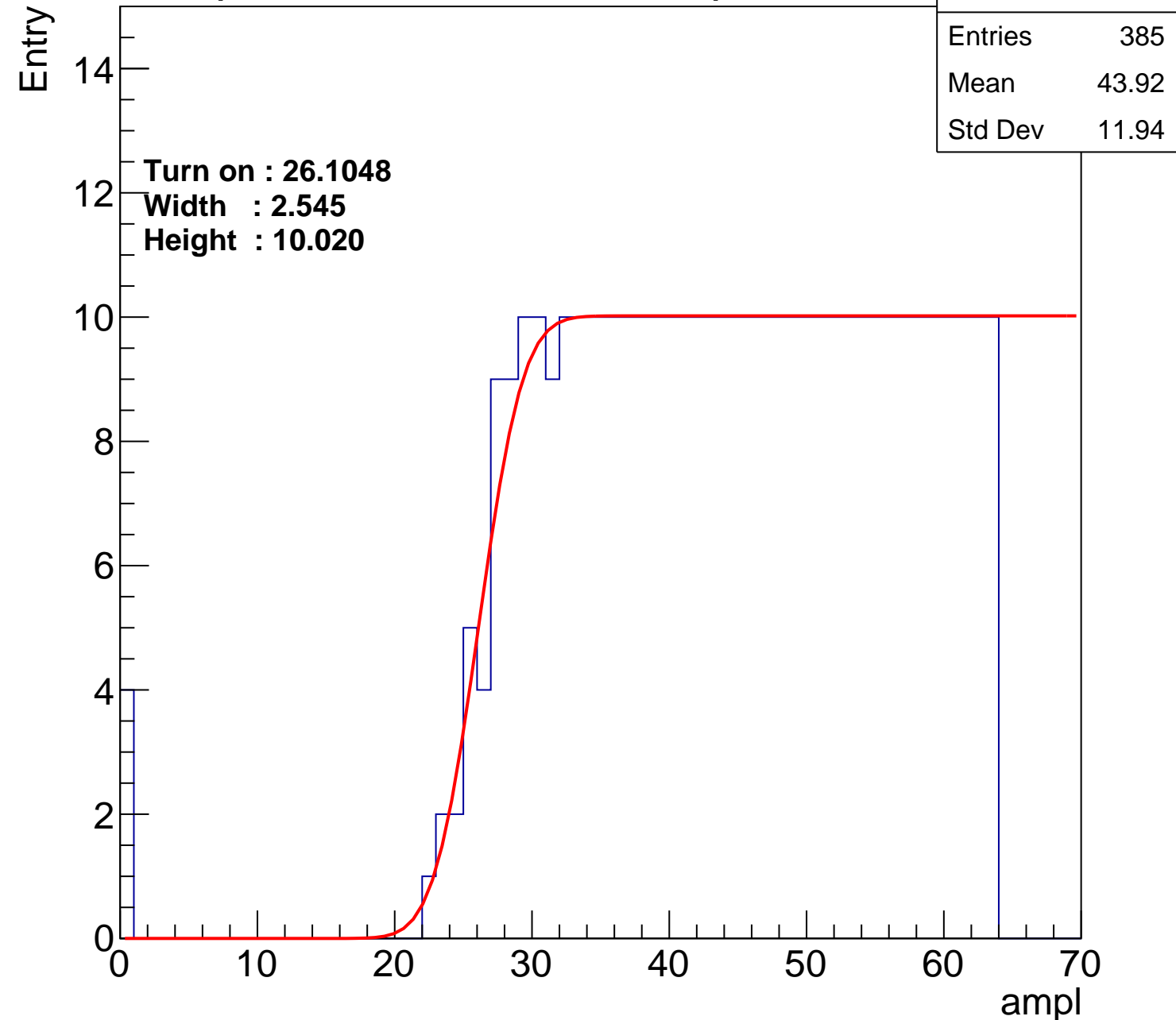
Width : 2.545

Height : 10.020

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch66

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.64
Std Dev	11.15

Turn on : 27.0885

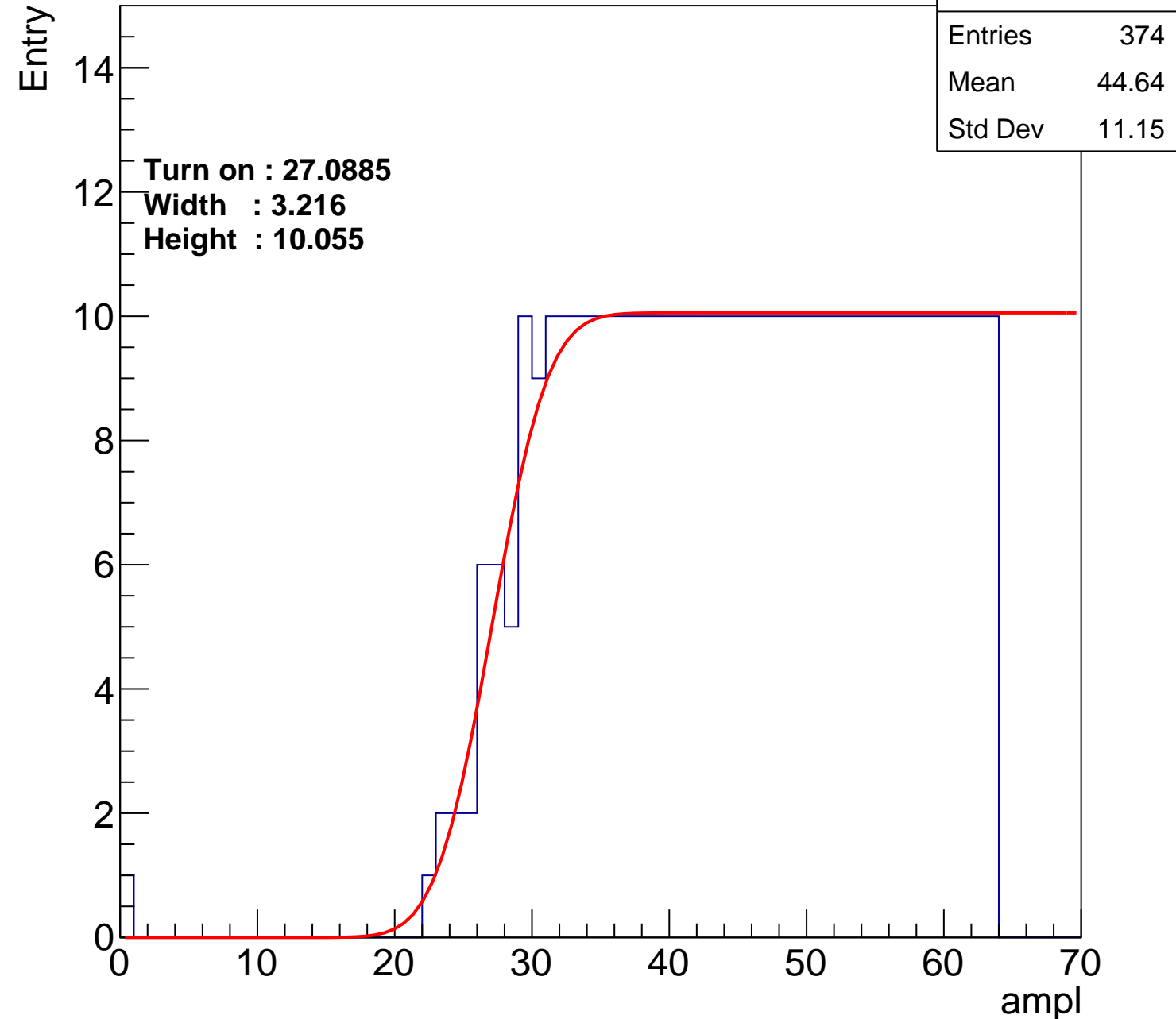
Width : 3.216

Height : 10.055

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch67

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.06
Std Dev	11.57

Turn on : 25.5587

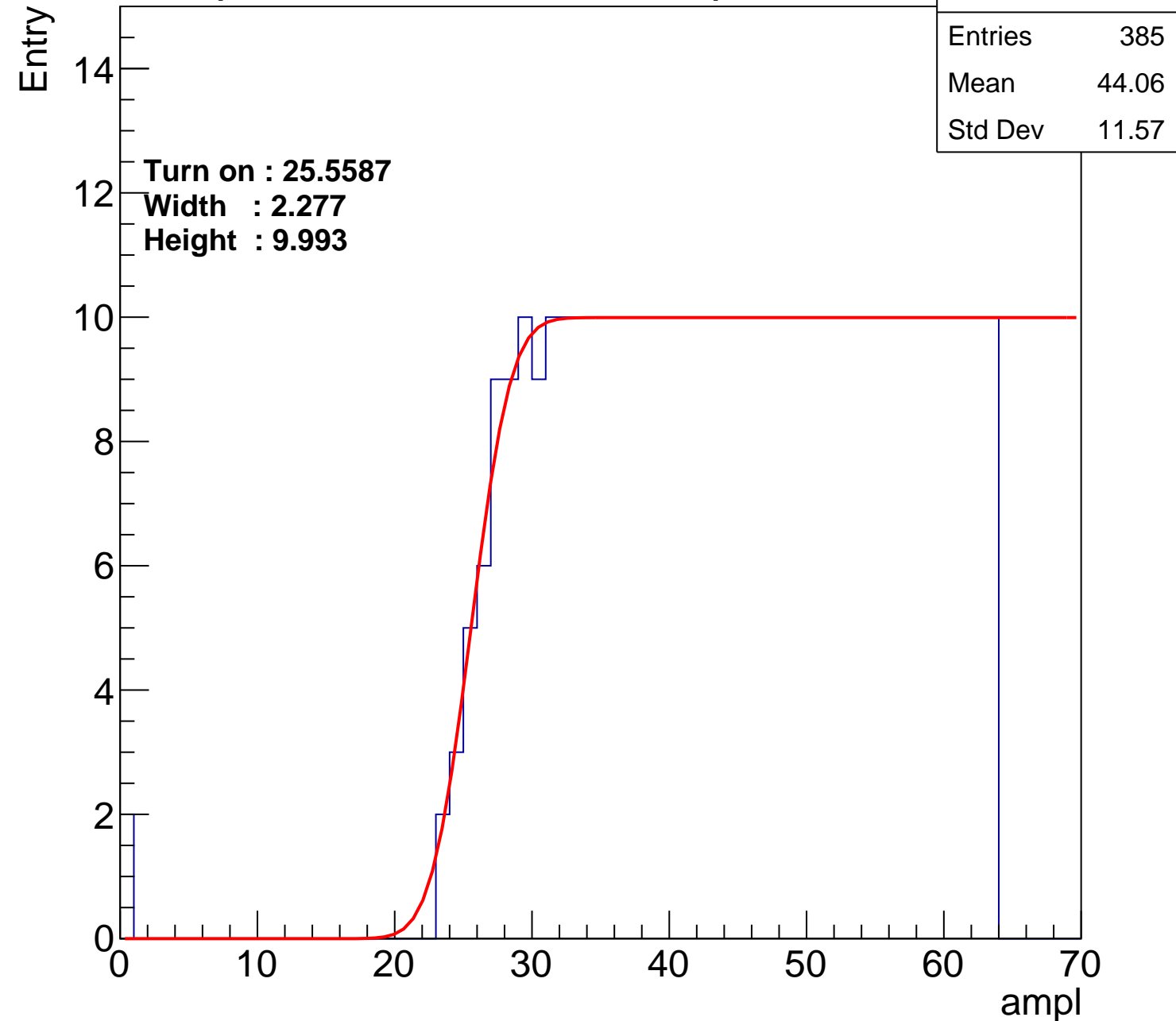
Width : 2.277

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch68

calib_packv5_042523_0143.root, FC#10, port B3

Entries	369
Mean	44.67
Std Dev	11.62

Turn on : 27.7453

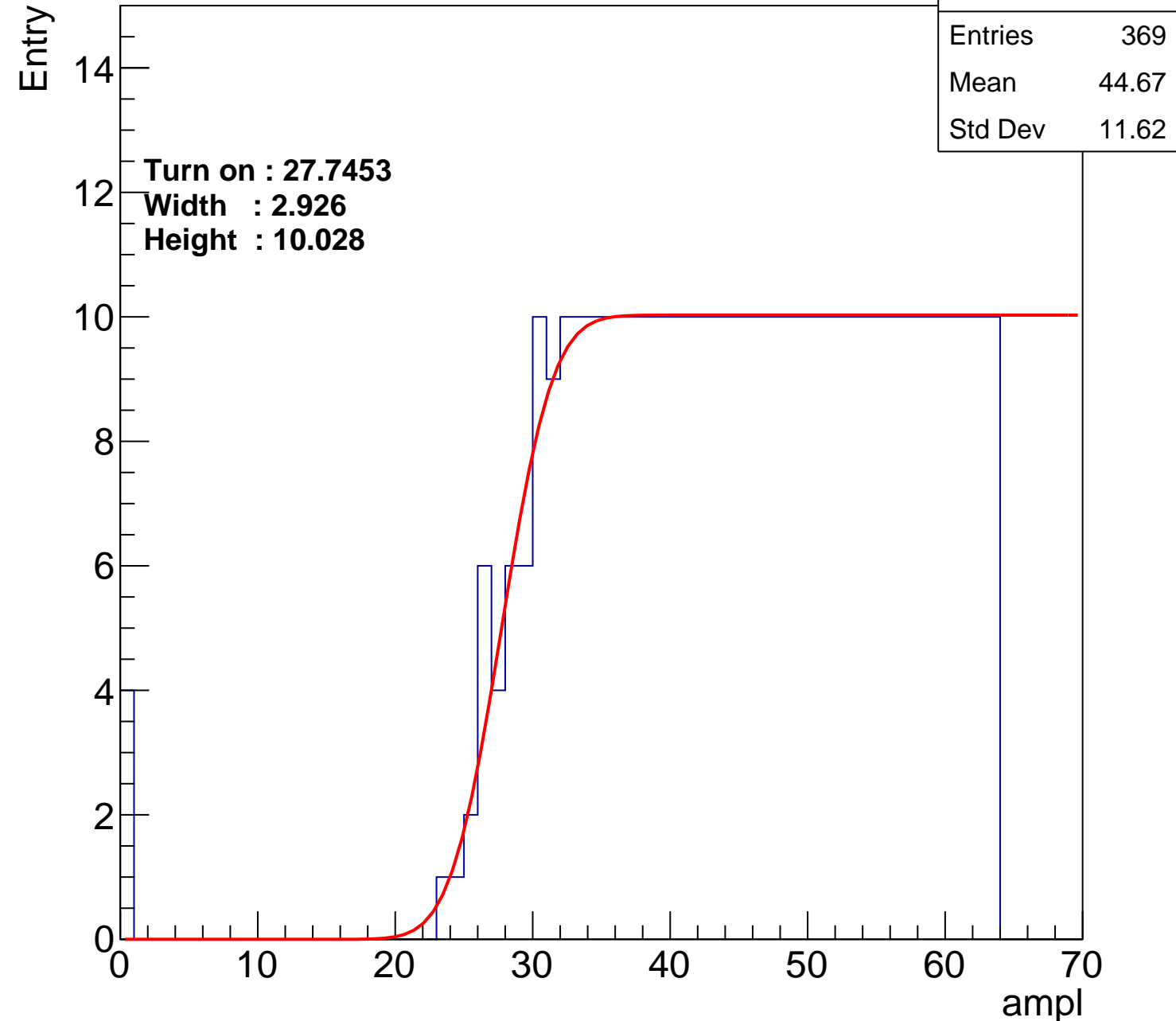
Width : 2.926

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch69

calib_packv5_042523_0143.root, FC#10, port B3

Entries	413
Mean	42.63
Std Dev	12.43

Turn on : 23.3555

Width : 1.978

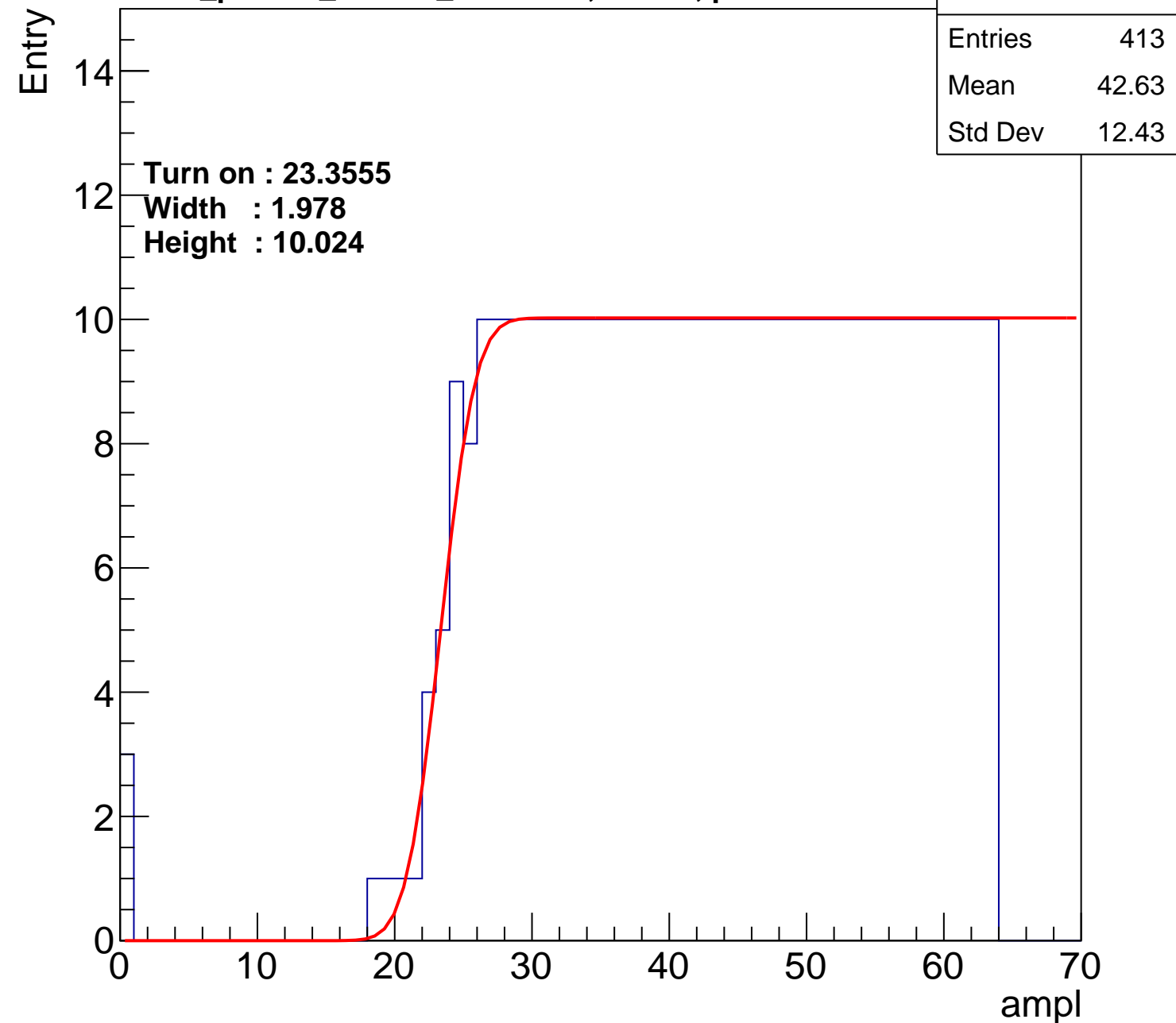
Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch70

calib_packv5_042523_0143.root, FC#10, port B3

Entries	375
Mean	44.59
Std Dev	11.17

Turn on : 27.0509

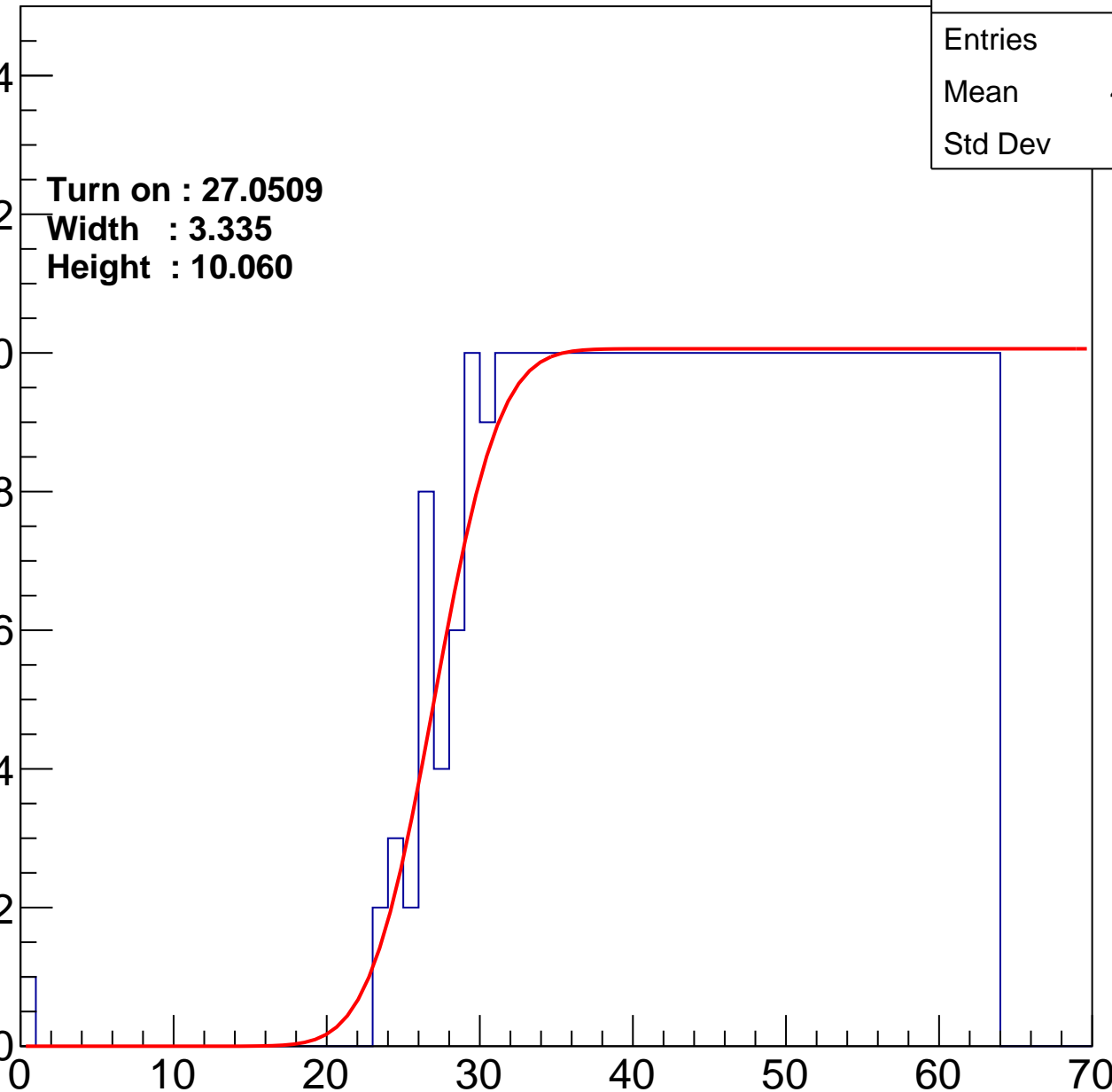
Width : 3.335

Height : 10.060

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch71

calib_packv5_042523_0143.root, FC#10, port B3

Entries	368
Mean	44.92
Std Dev	11.02

Turn on : 27.5104

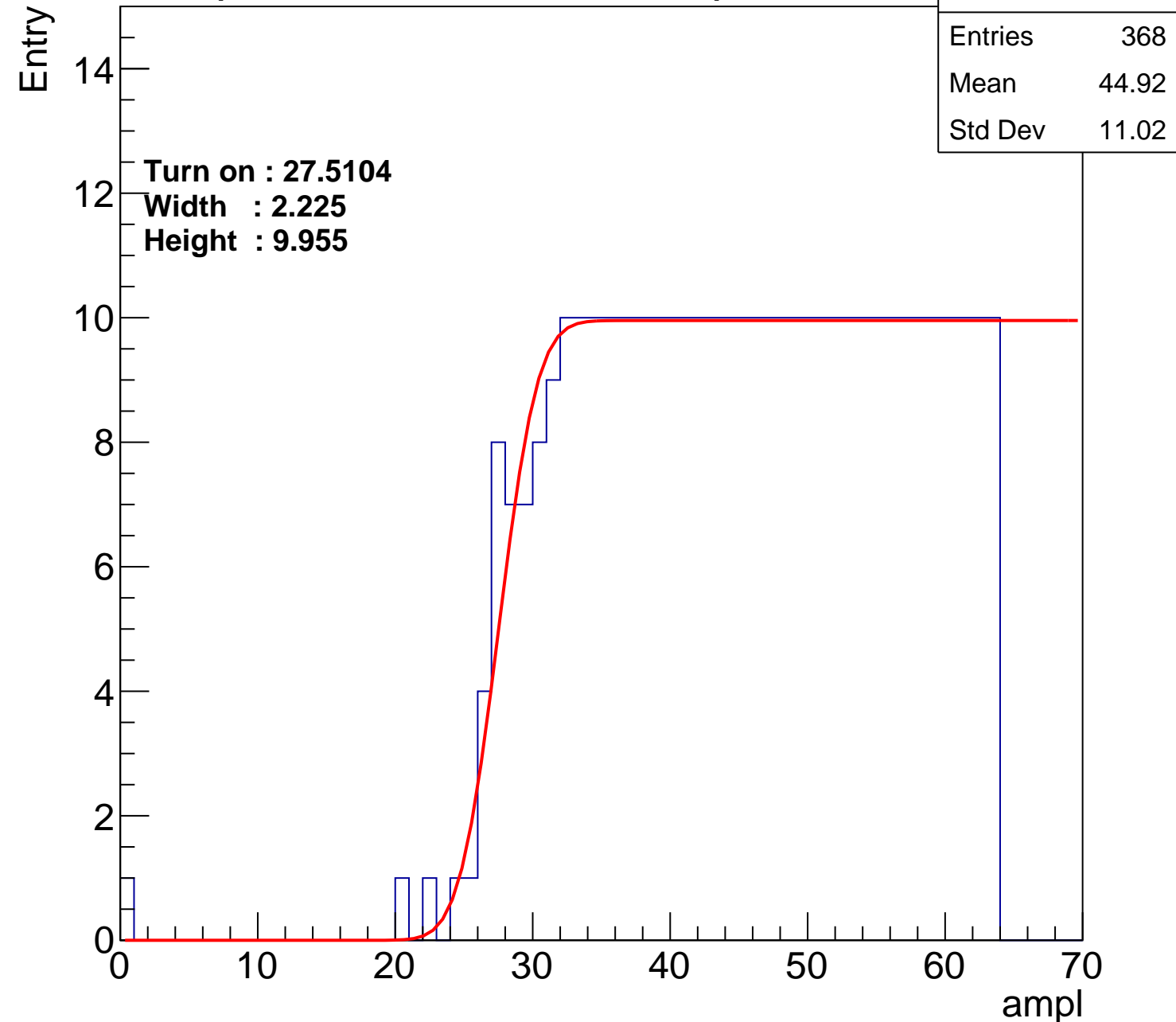
Width : 2.225

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch72

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.95
Std Dev	12.18

Turn on : 23.5438

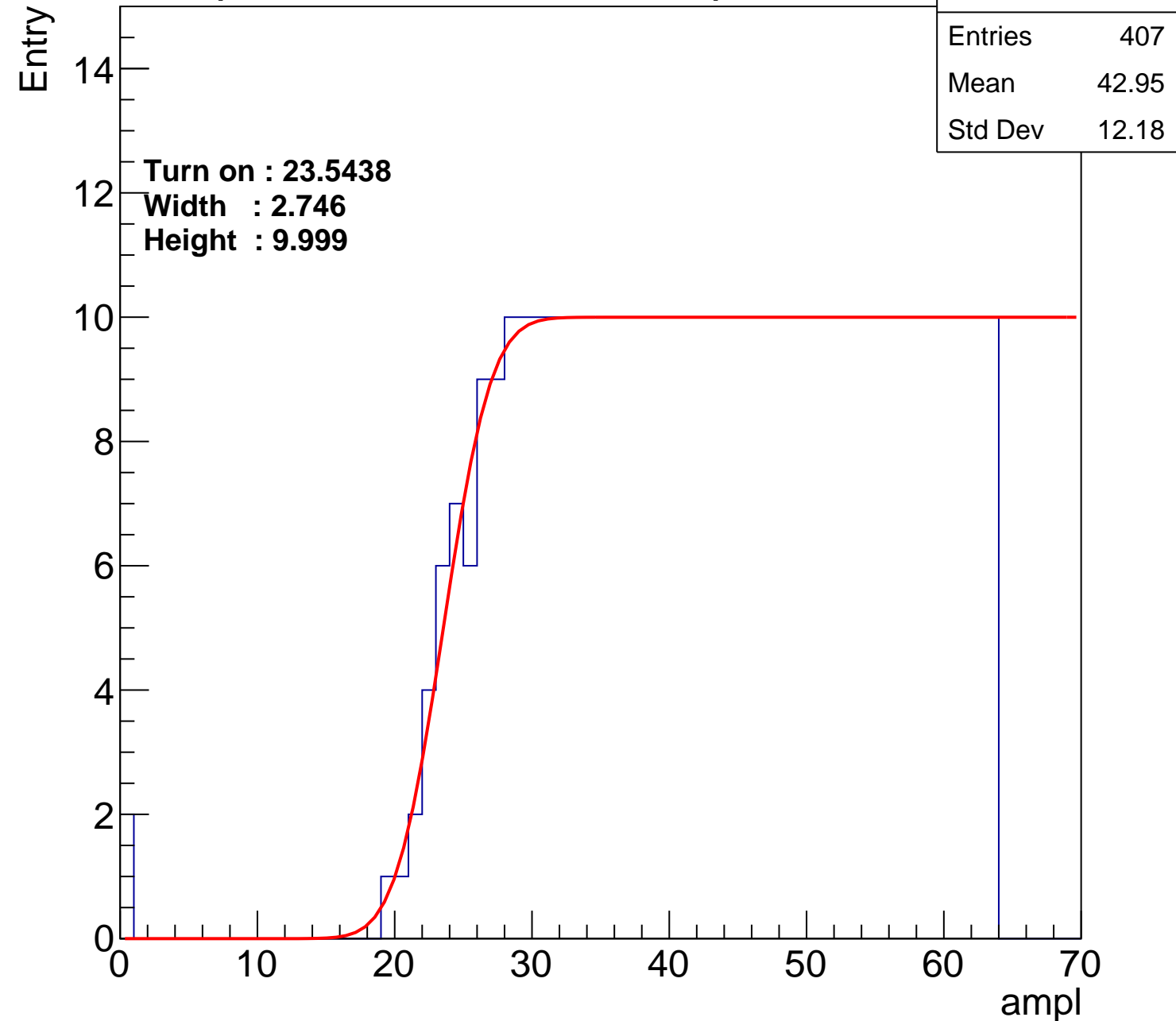
Width : 2.746

Height : 9.999

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch73

calib_packv5_042523_0143.root, FC#10, port B3

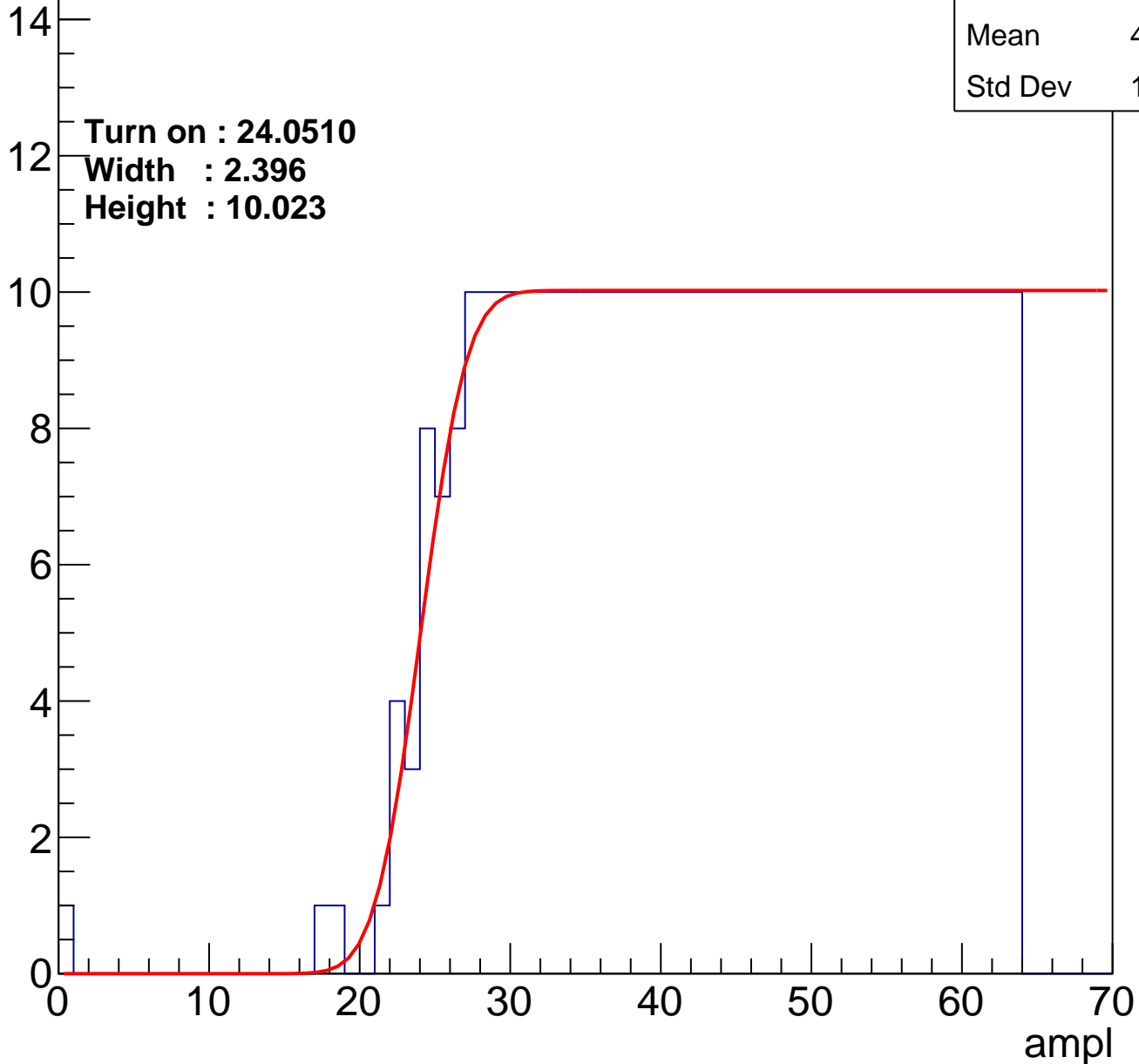
Entries	404
Mean	43.16
Std Dev	11.95

Turn on : 24.0510

Width : 2.396

Height : 10.023

Entry



B1L002S, U20-ch74

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.98
Std Dev	11.52

Turn on : 25.1805

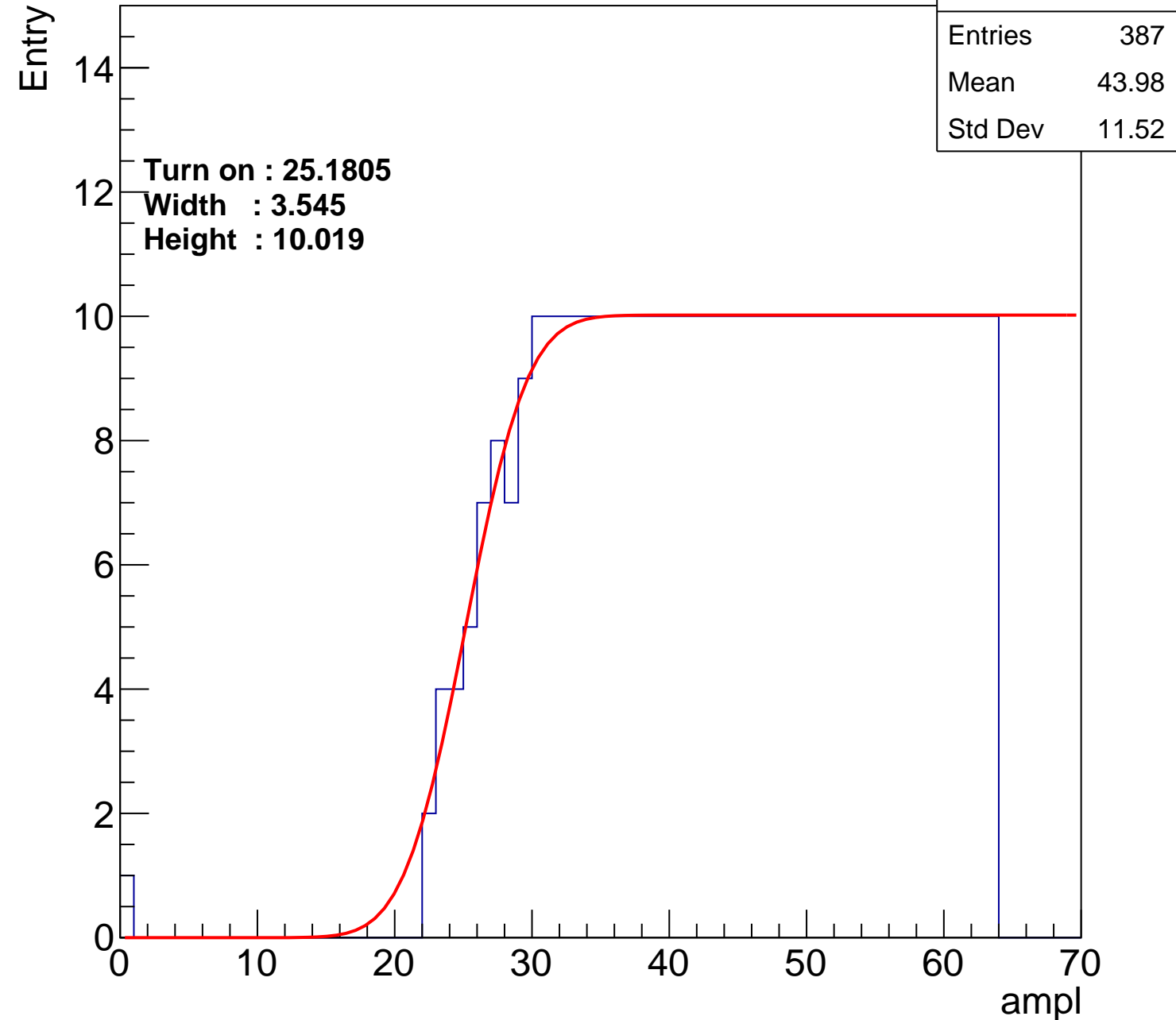
Width : 3.545

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch75

calib_packv5_042523_0143.root, FC#10, port B3

Entries	389
Mean	43.83
Std Dev	11.73

Turn on : 25.6271

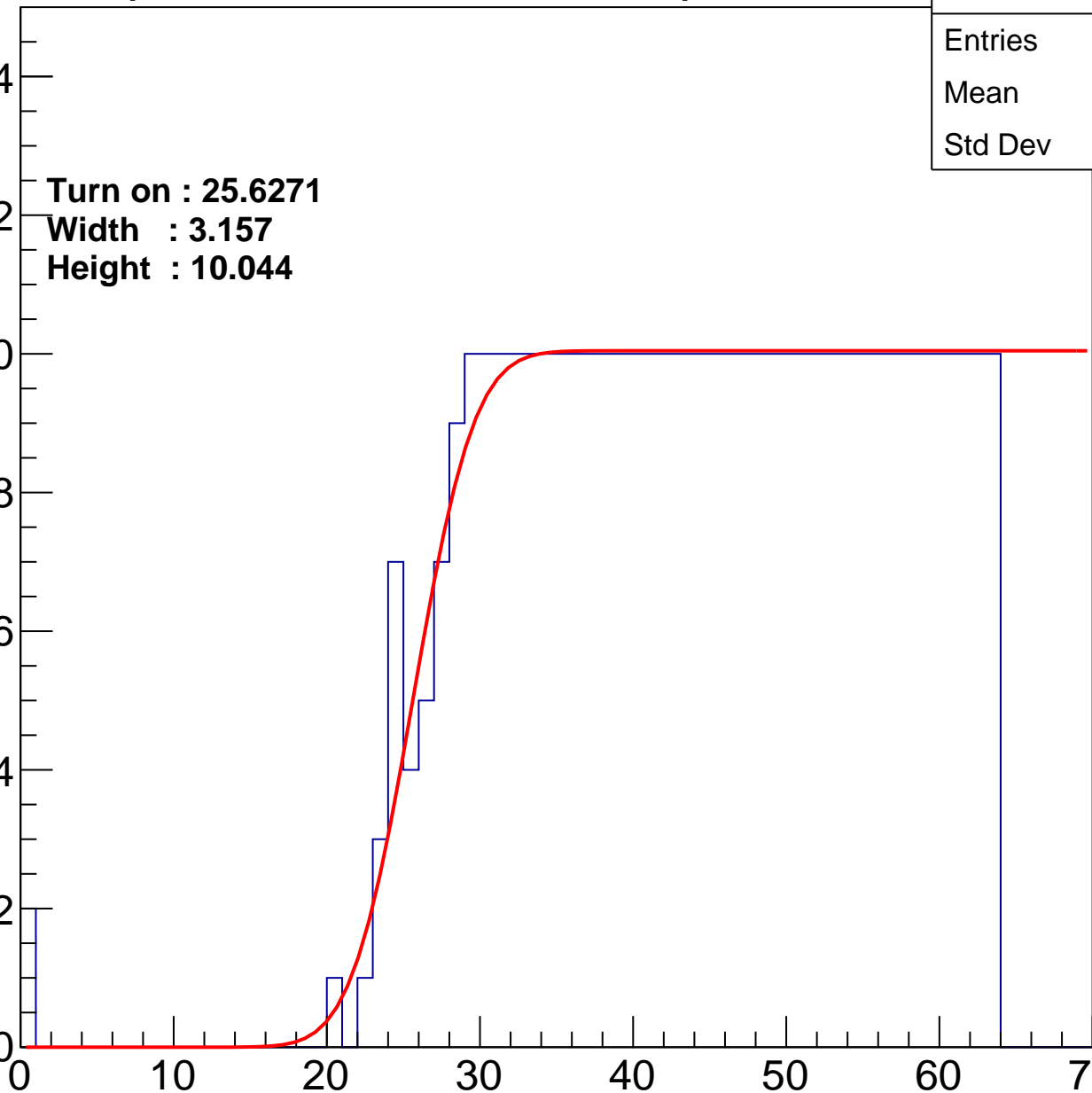
Width : 3.157

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch76

calib_packv5_042523_0143.root, FC#10, port B3

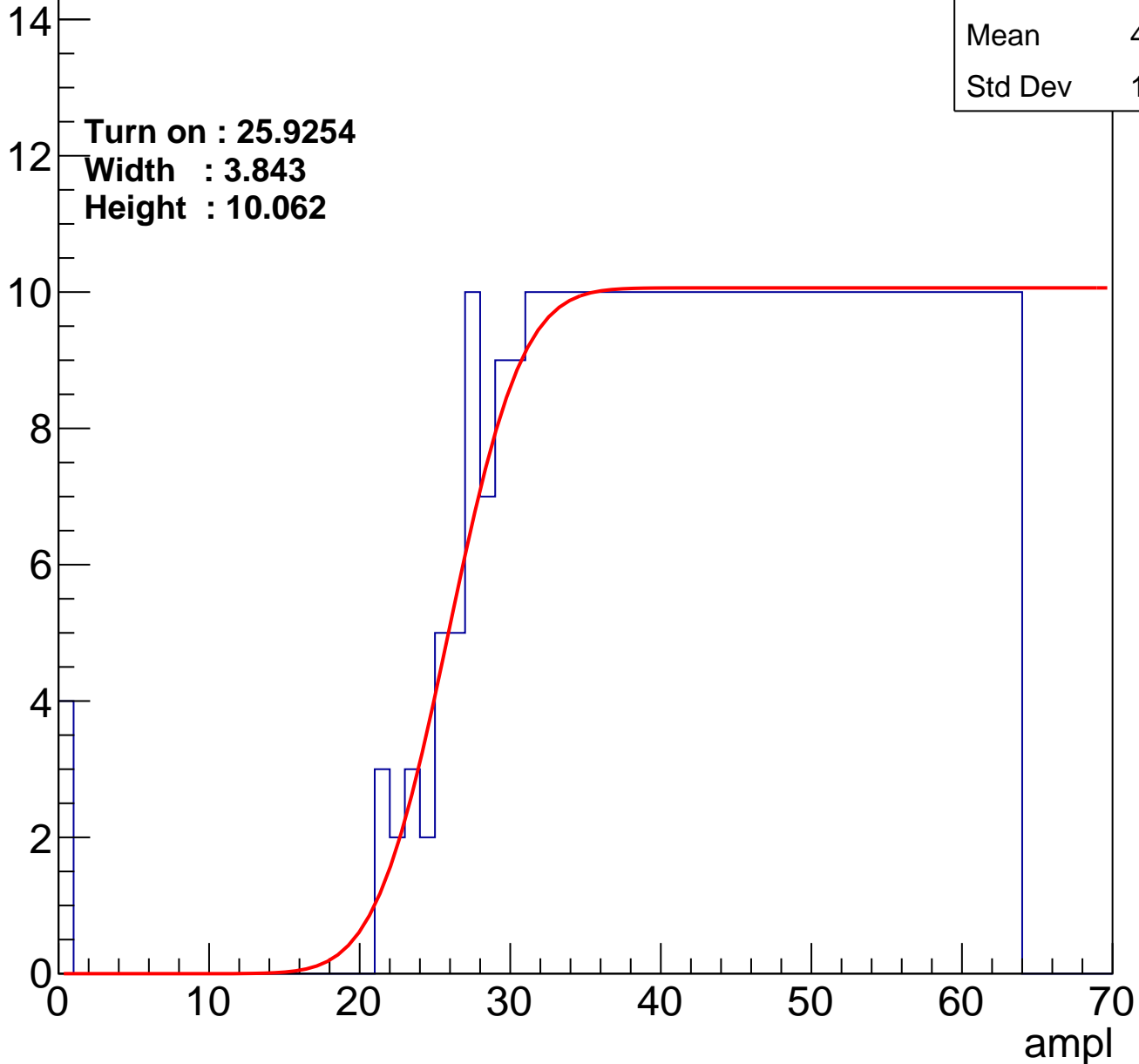
Entries	389
Mean	43.67
Std Dev	12.12

Turn on : 25.9254

Width : 3.843

Height : 10.062

Entry



B1L002S, U20-ch77

calib_packv5_042523_0143.root, FC#10, port B3

Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 26.3256

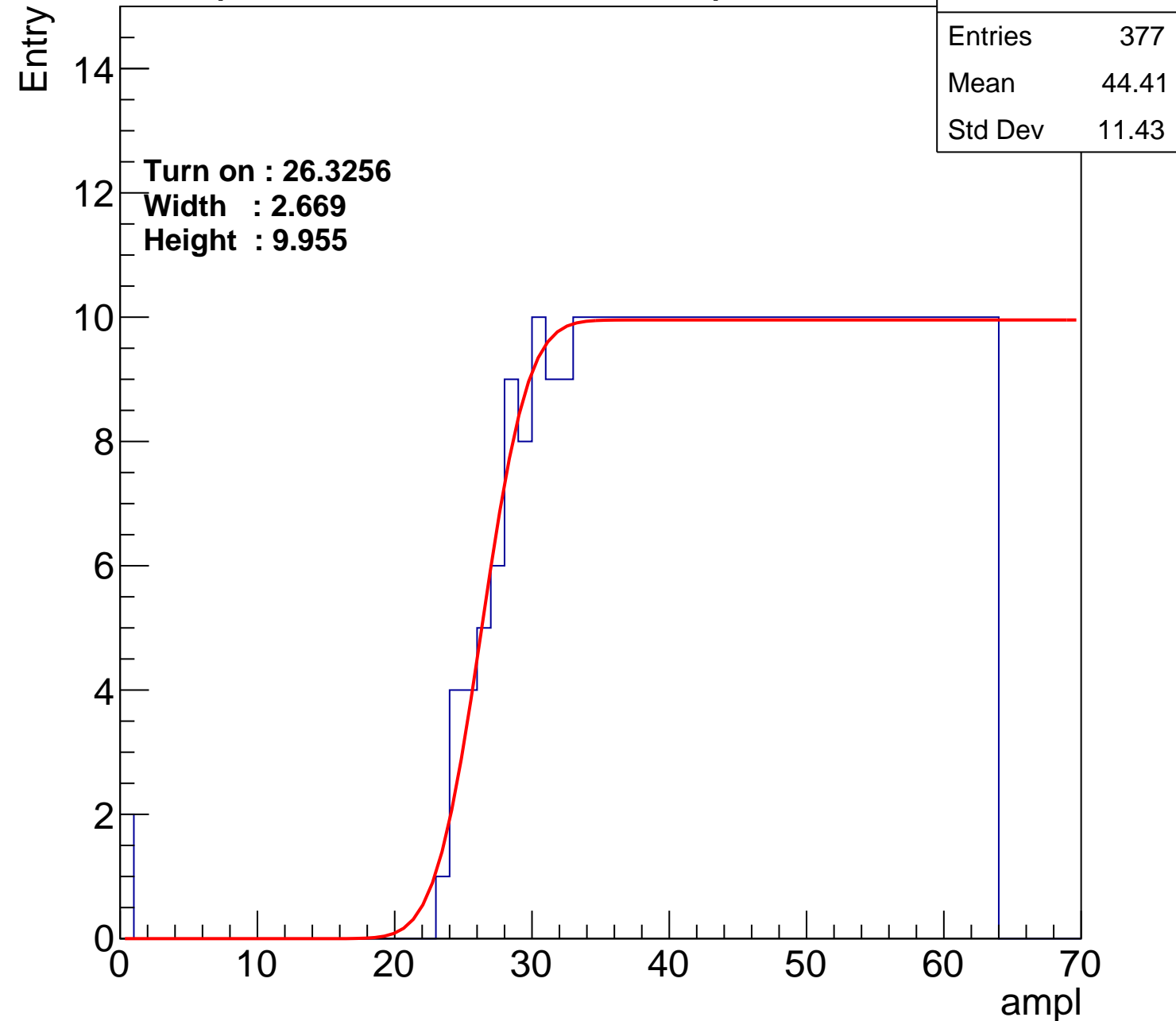
Width : 2.669

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch78

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.37
Std Dev	12.11

Turn on : 24.7450

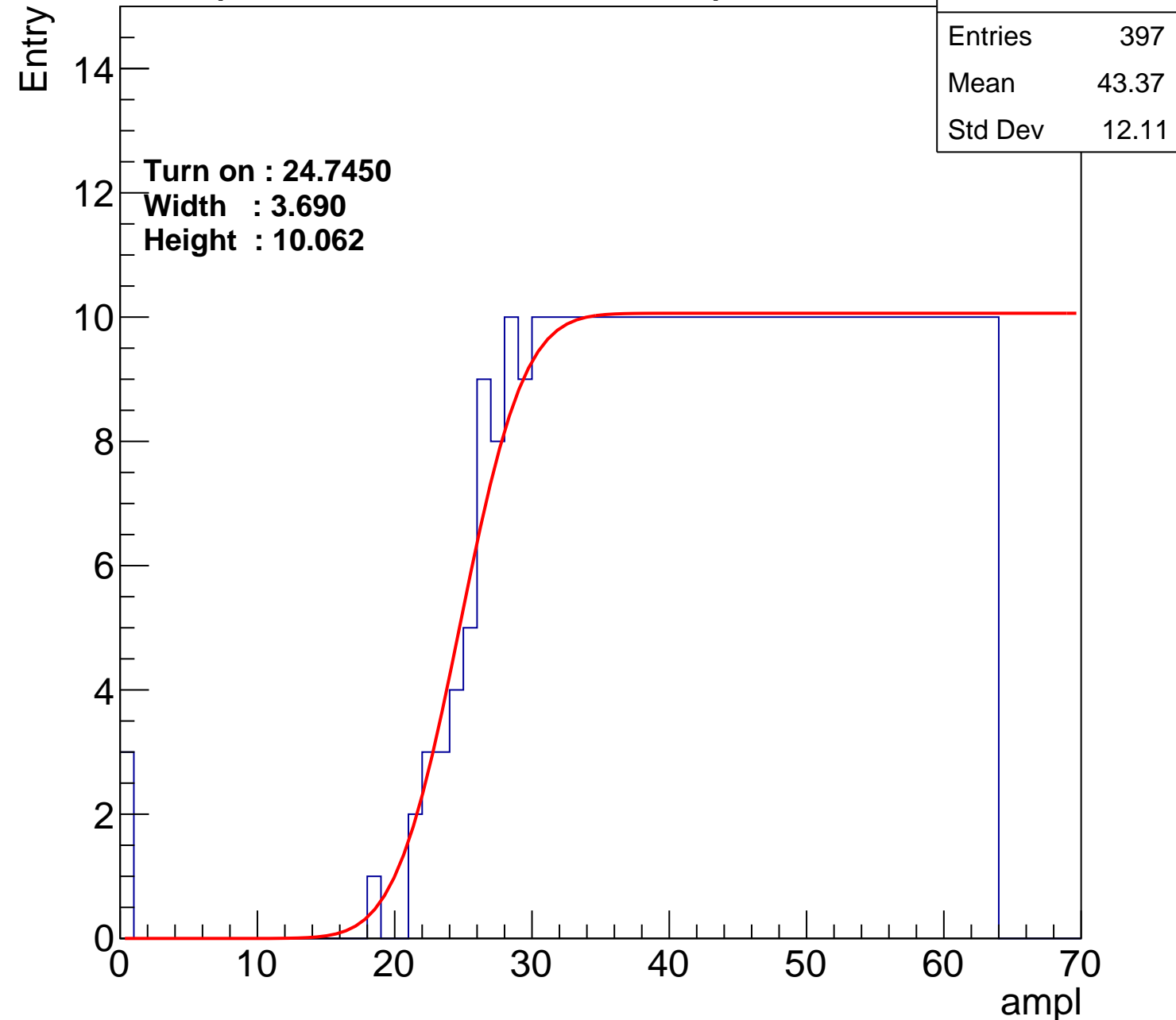
Width : 3.690

Height : 10.062

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch79

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.27
Std Dev	11.5

Turn on : 25.9619

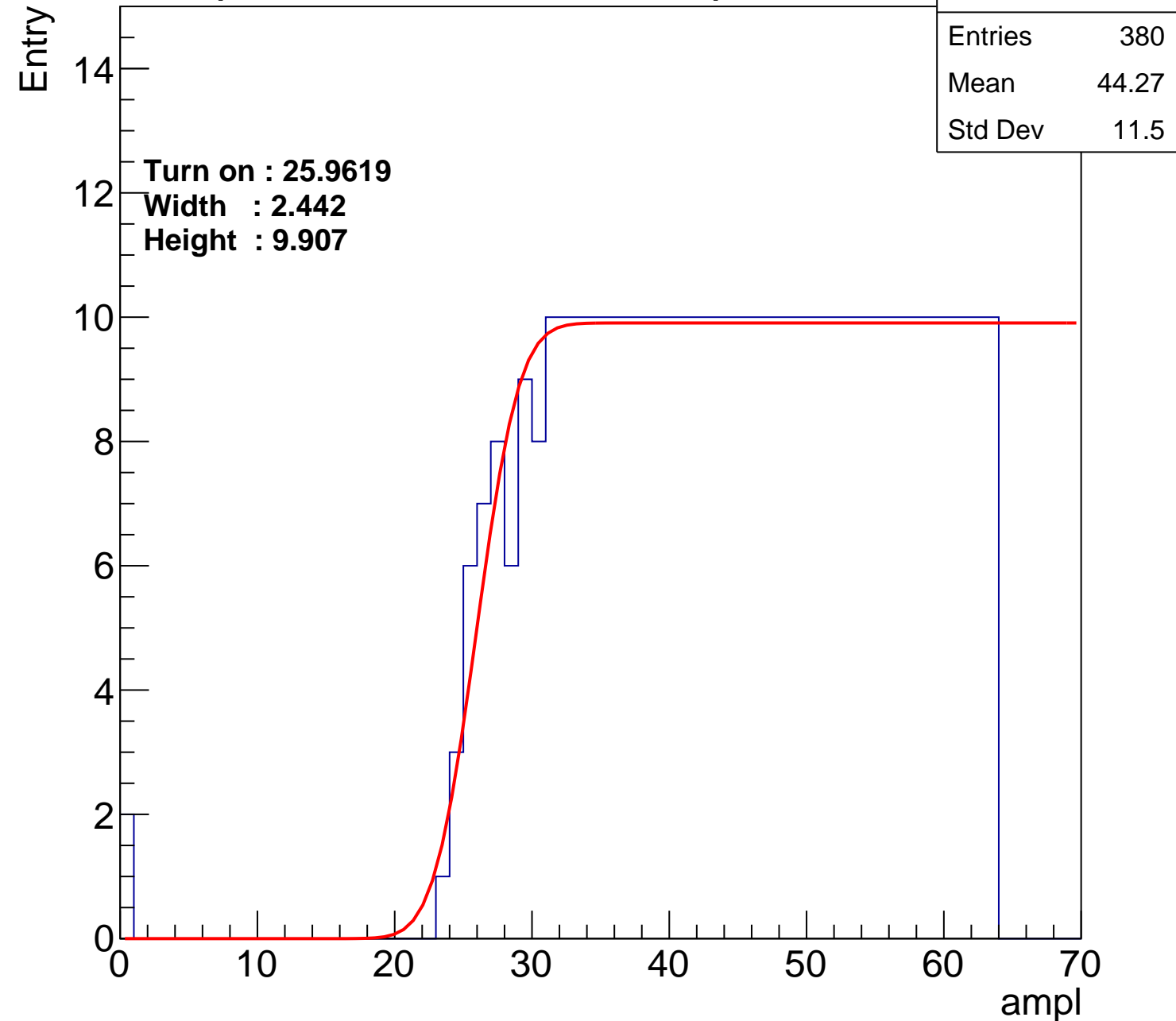
Width : 2.442

Height : 9.907

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch80

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.09
Std Dev	12.34

Turn on : 24.7025

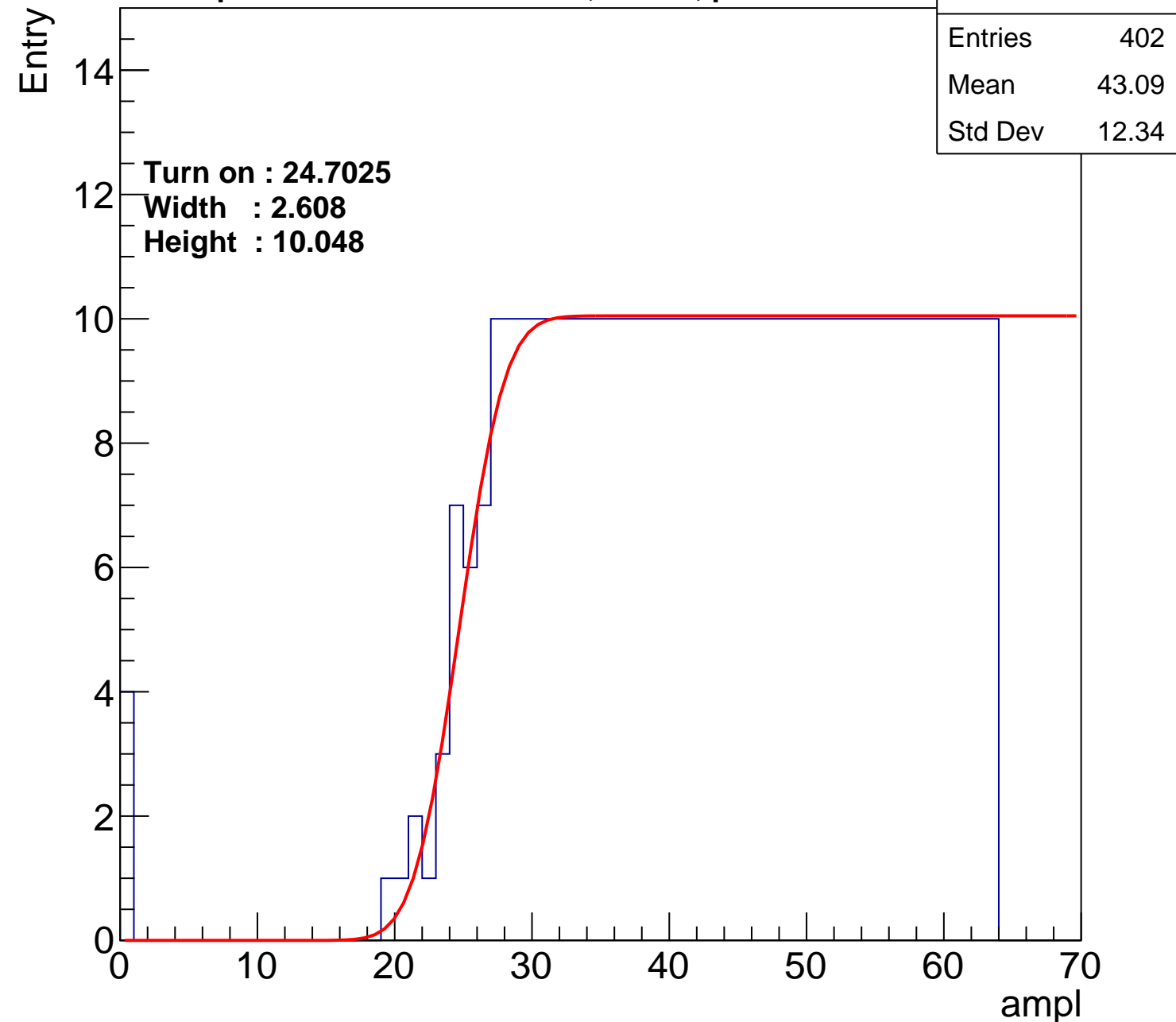
Width : 2.608

Height : 10.048

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch81

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.03
Std Dev	11.74

Turn on : 26.1491

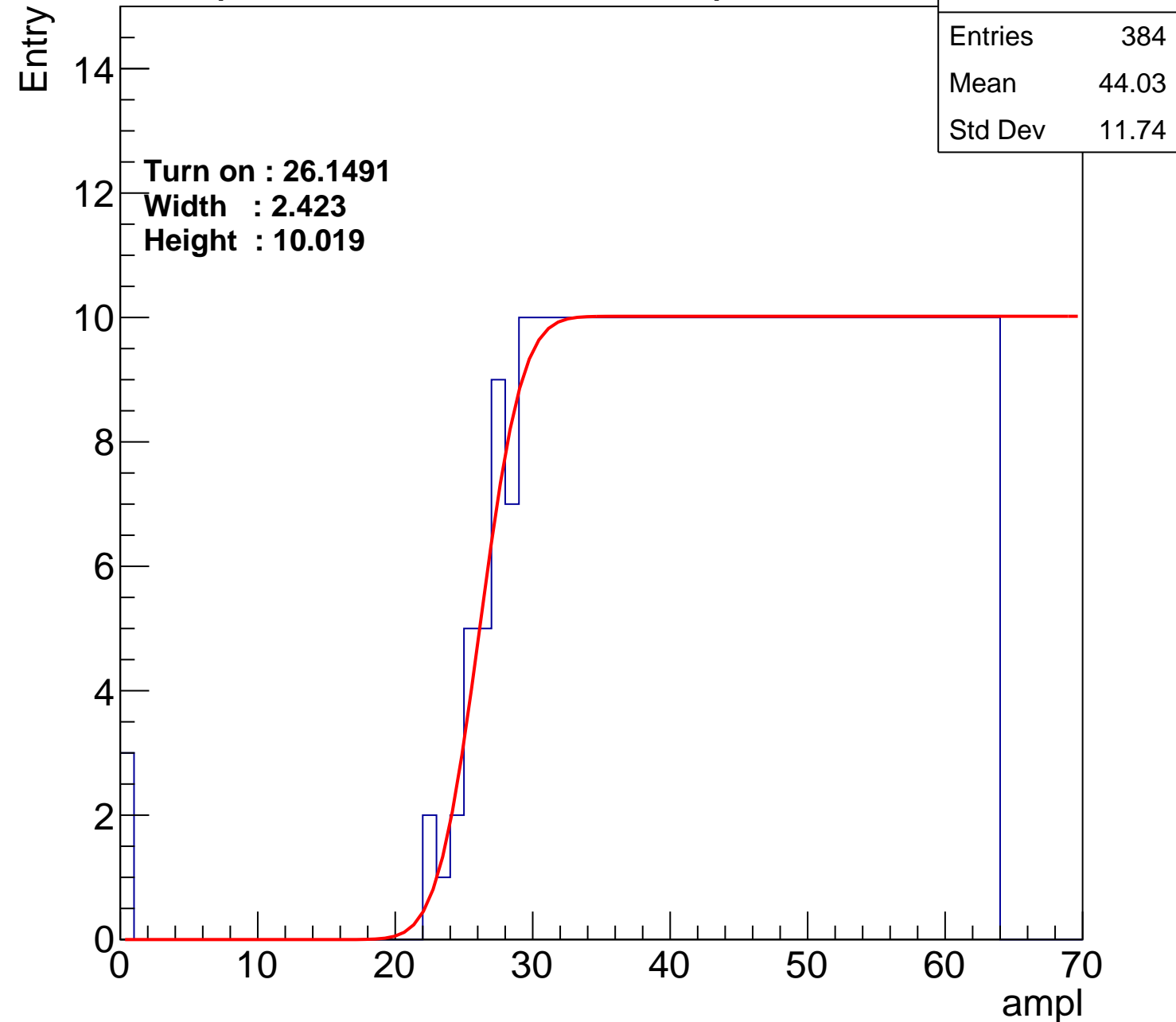
Width : 2.423

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch82

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.84
Std Dev	11.88

Turn on : 25.4611

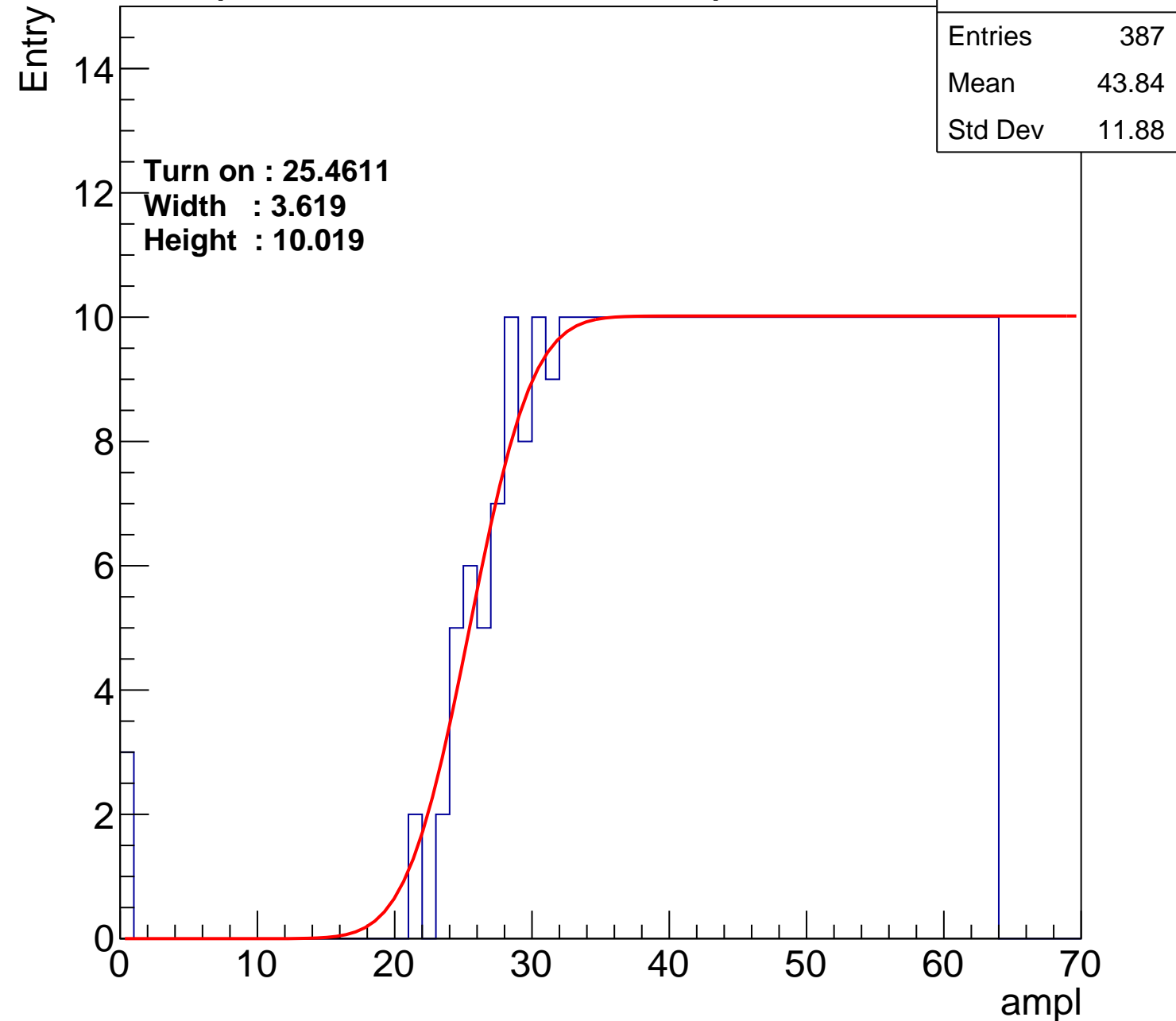
Width : 3.619

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch83

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.81
Std Dev	11.7

Turn on : 25.1550

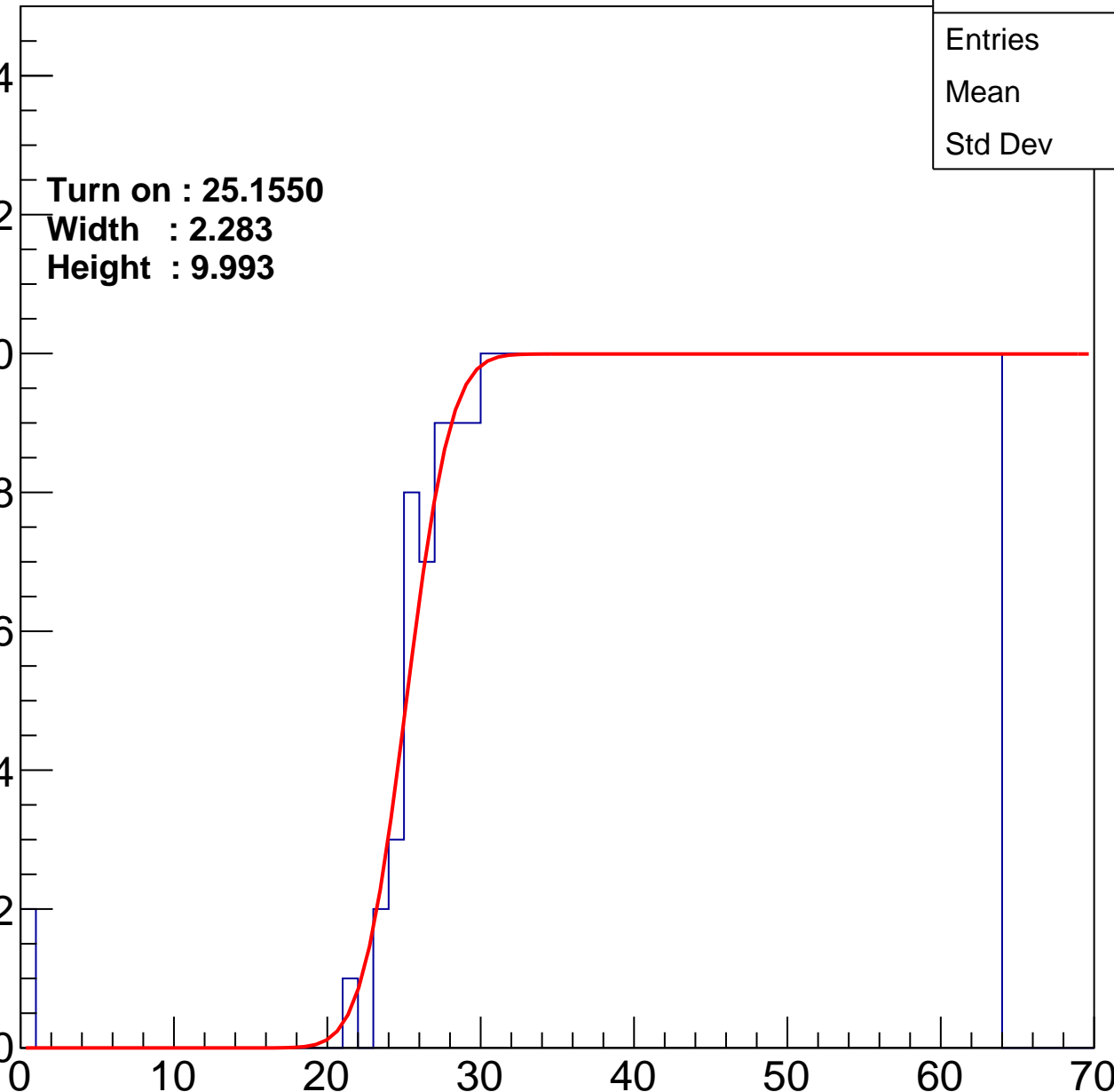
Width : 2.283

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch84

calib_packv5_042523_0143.root, FC#10, port B3

Entries	387
Mean	43.91
Std Dev	11.78

Turn on : 25.6728

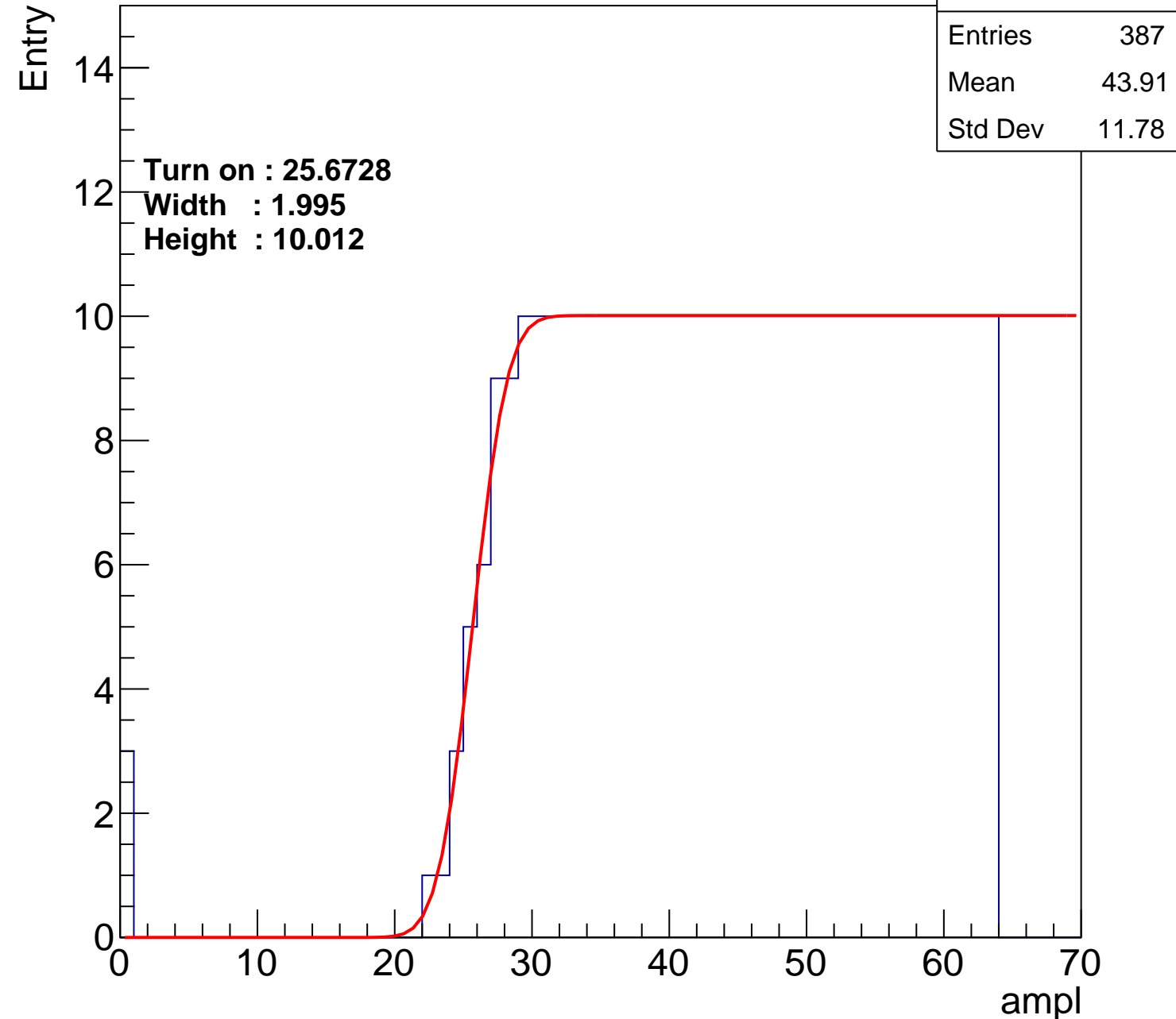
Width : 1.995

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch85

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.62
Std Dev	11.31

Turn on : 27.0968

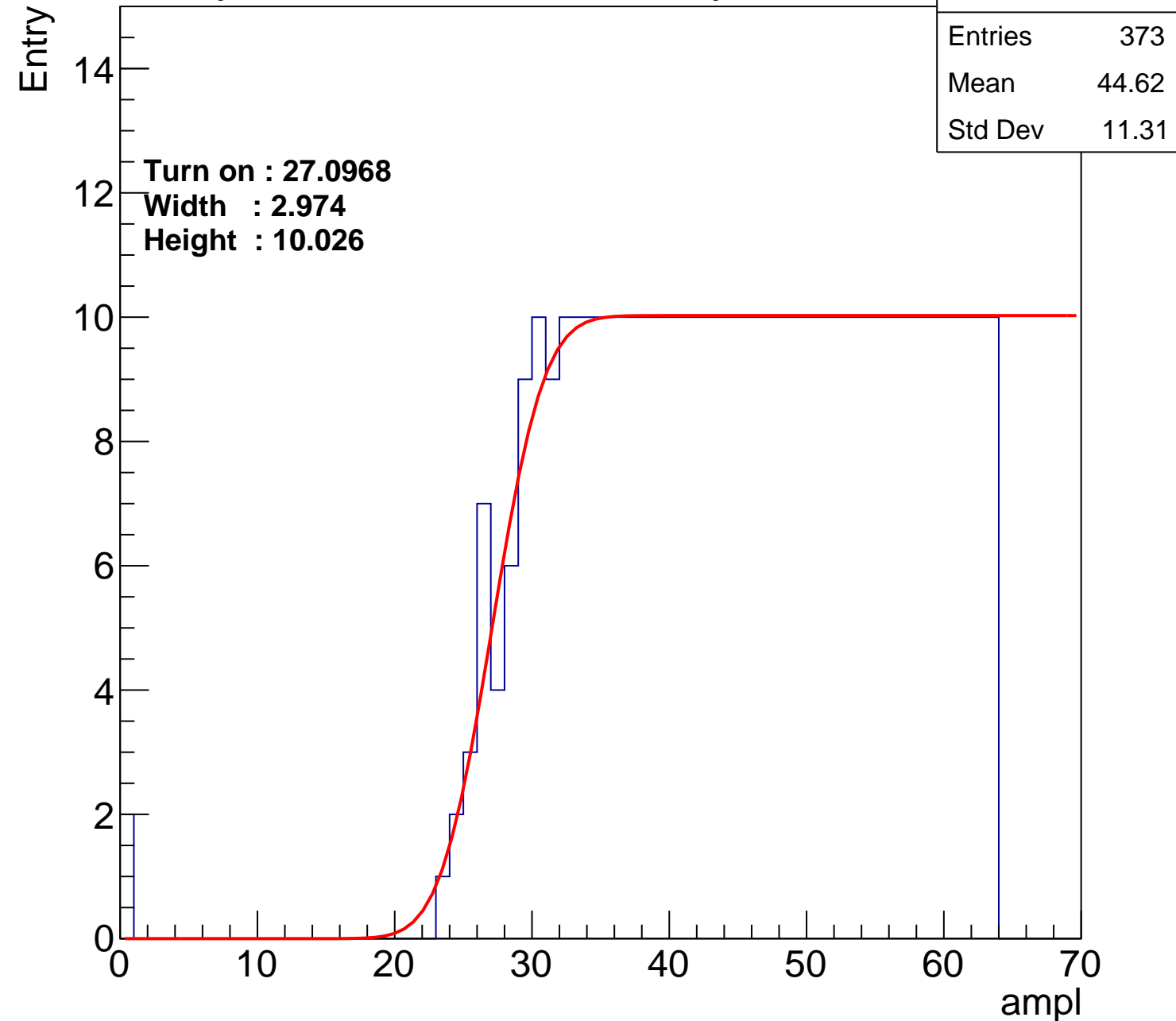
Width : 2.974

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch86

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.42
Std Dev	12.19

Turn on : 25.0049

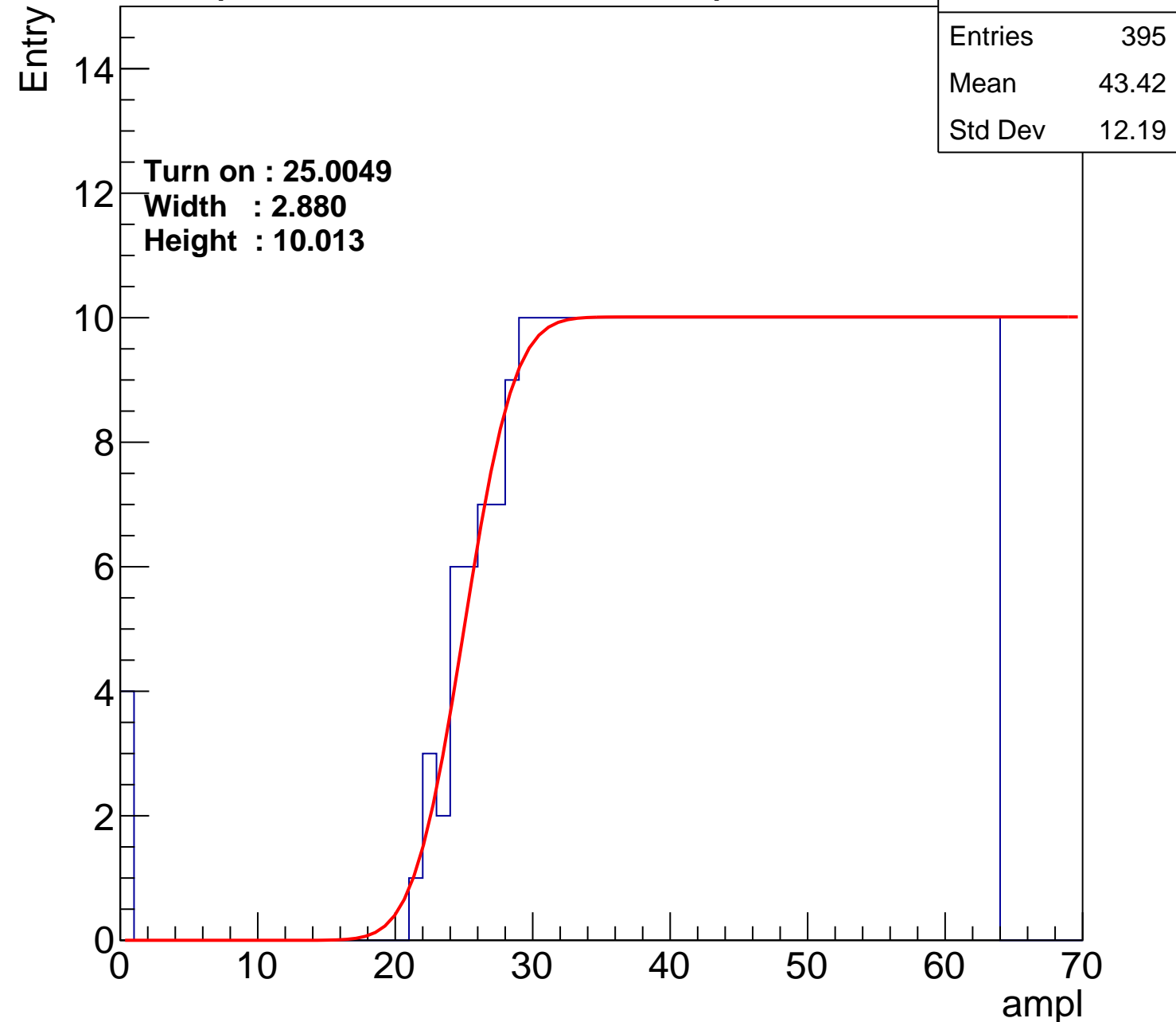
Width : 2.880

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch87

calib_packv5_042523_0143.root, FC#10, port B3

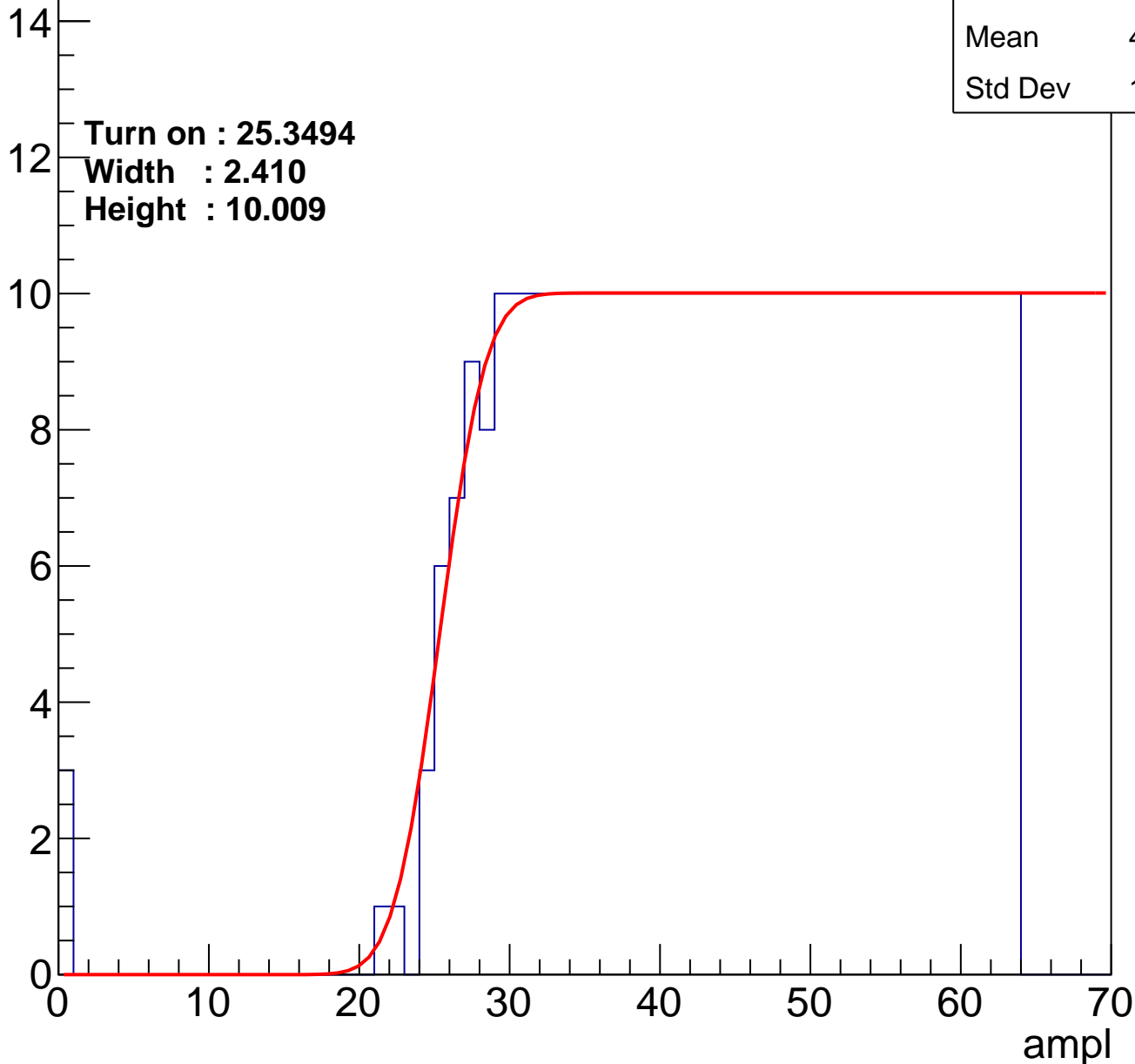
Entries	388
Mean	43.85
Std Dev	11.82

Turn on : 25.3494

Width : 2.410

Height : 10.009

Entry



B1L002S, U20-ch88

calib_packv5_042523_0143.root, FC#10, port B3

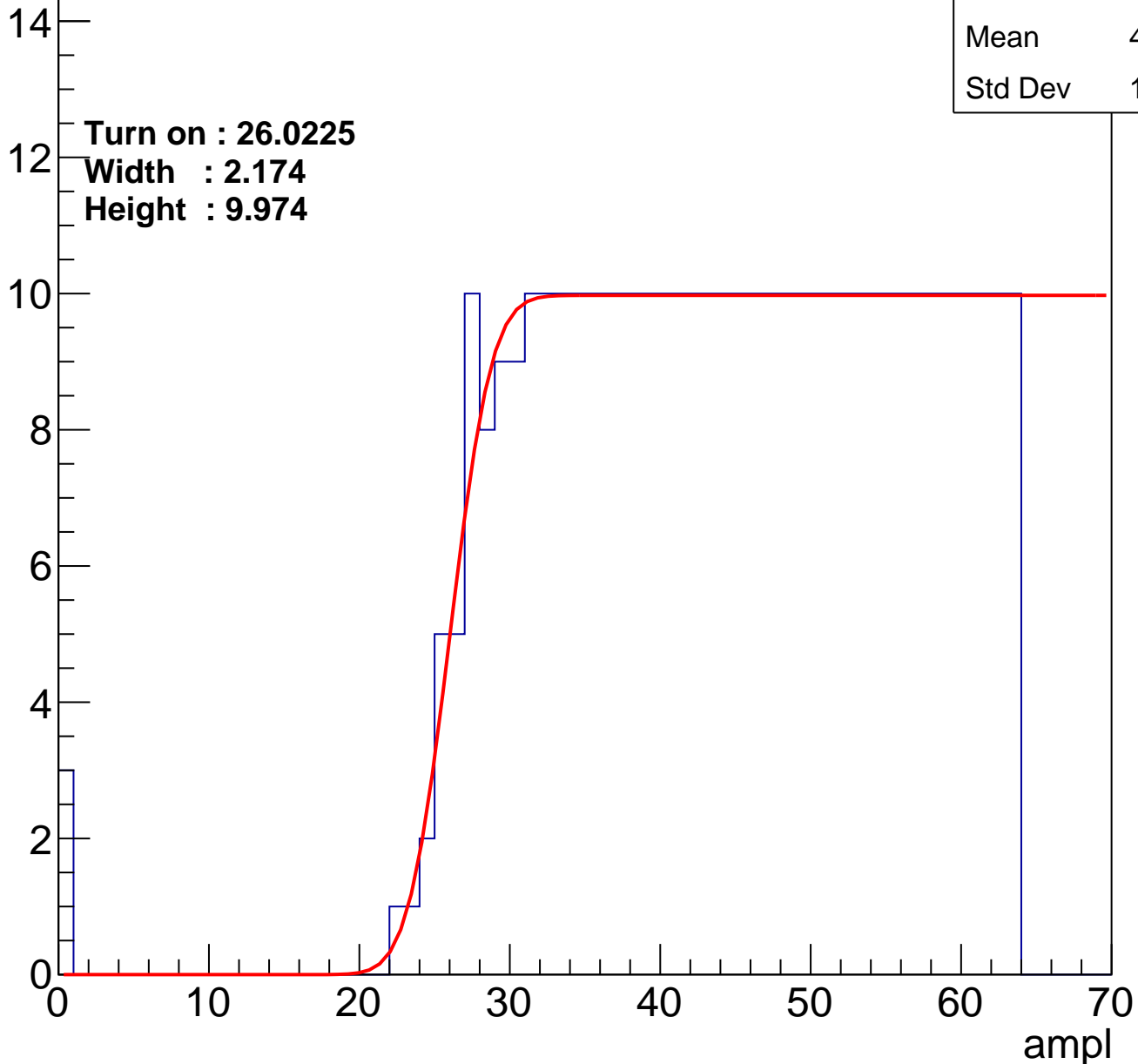
Entries	383
Mean	44.08
Std Dev	11.72

Turn on : 26.0225

Width : 2.174

Height : 9.974

Entry



B1L002S, U20-ch89

calib_packv5_042523_0143.root, FC#10, port B3

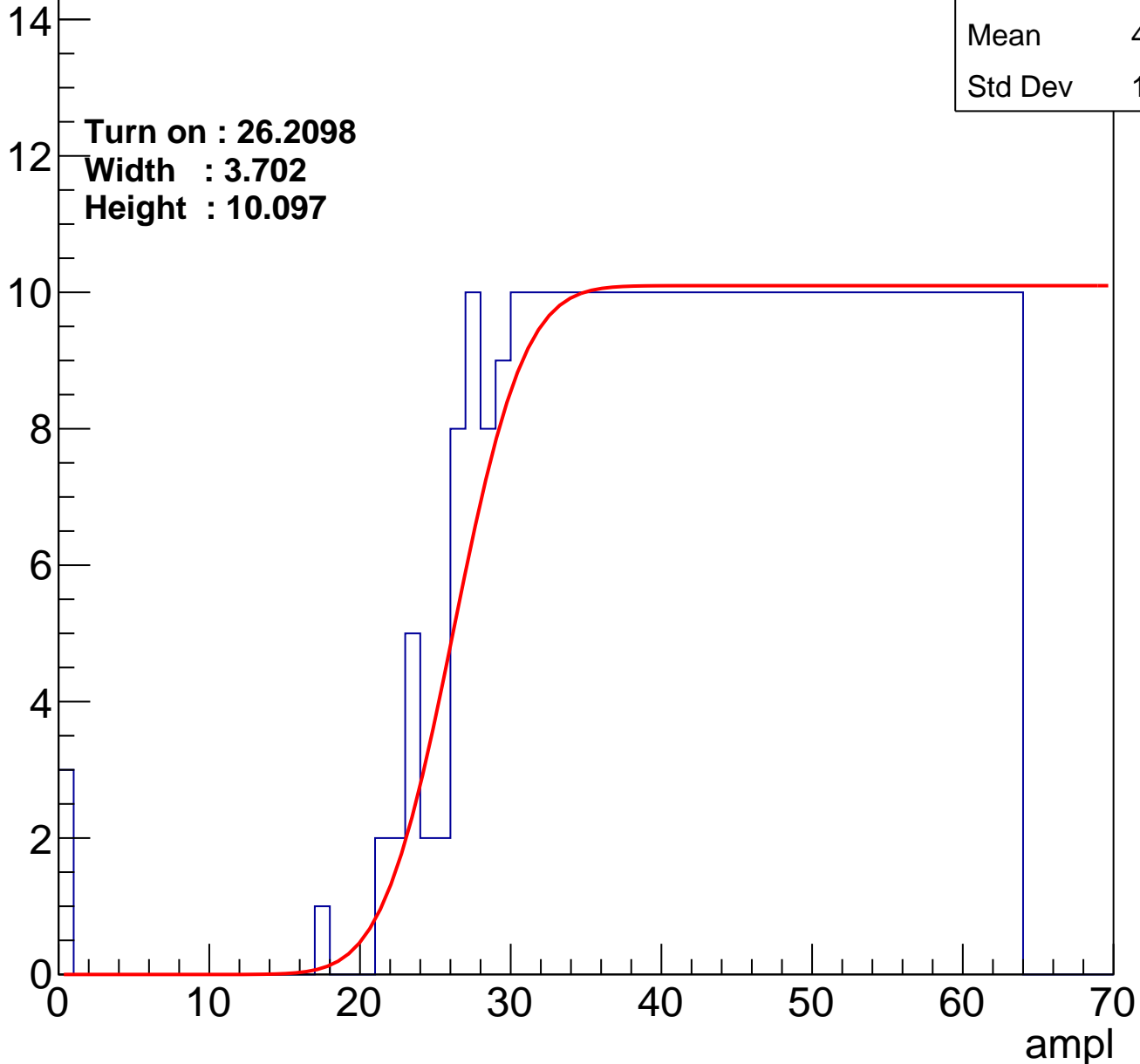
Entries	392
Mean	43.59
Std Dev	12.02

Turn on : 26.2098

Width : 3.702

Height : 10.097

Entry



B1L002S, U20-ch90

calib_packv5_042523_0143.root, FC#10, port B3

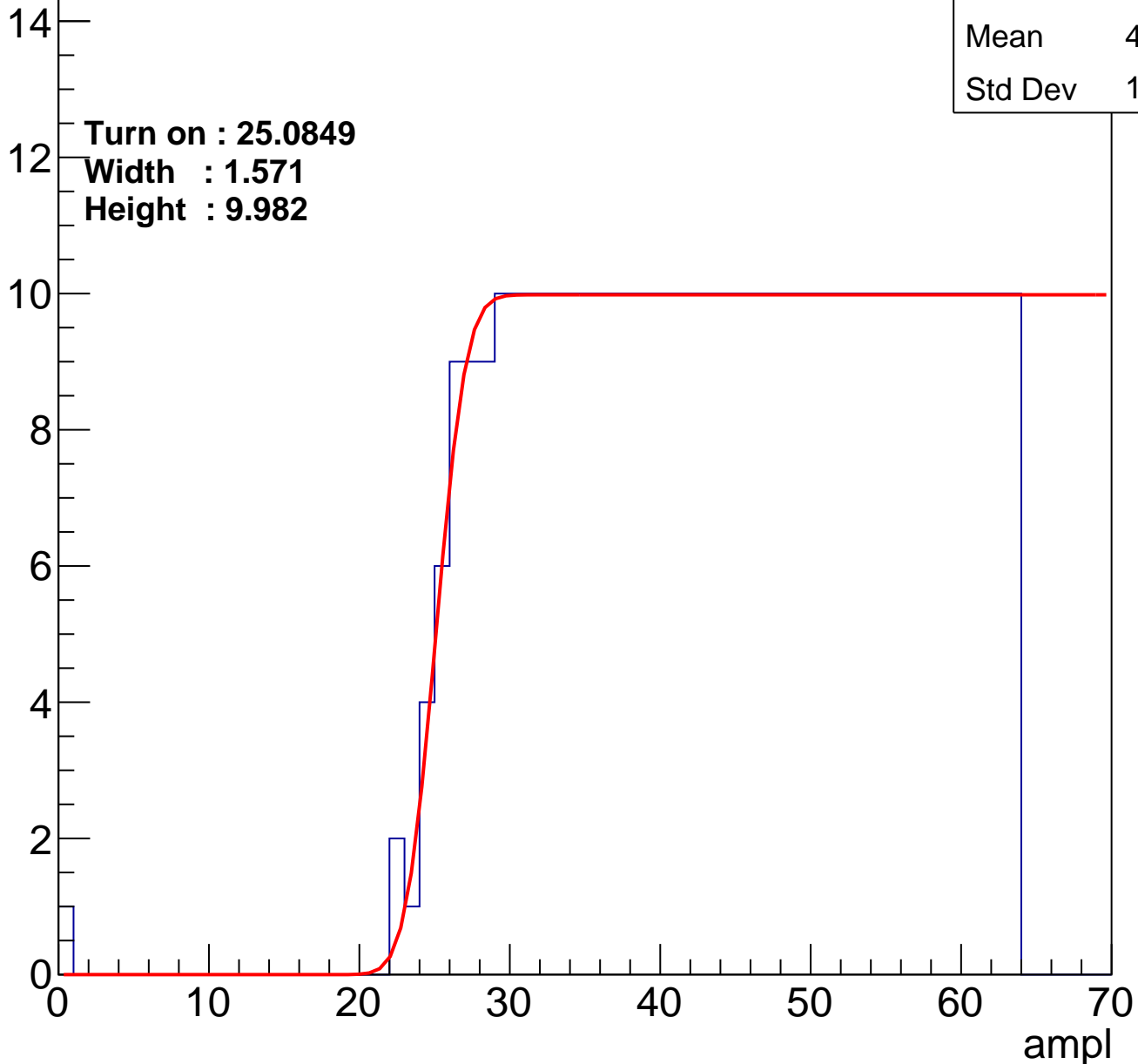
Entries	391
Mean	43.84
Std Dev	11.53

Turn on : 25.0849

Width : 1.571

Height : 9.982

Entry



B1L002S, U20-ch91

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.6
Std Dev	11.3

Turn on : 26.9862

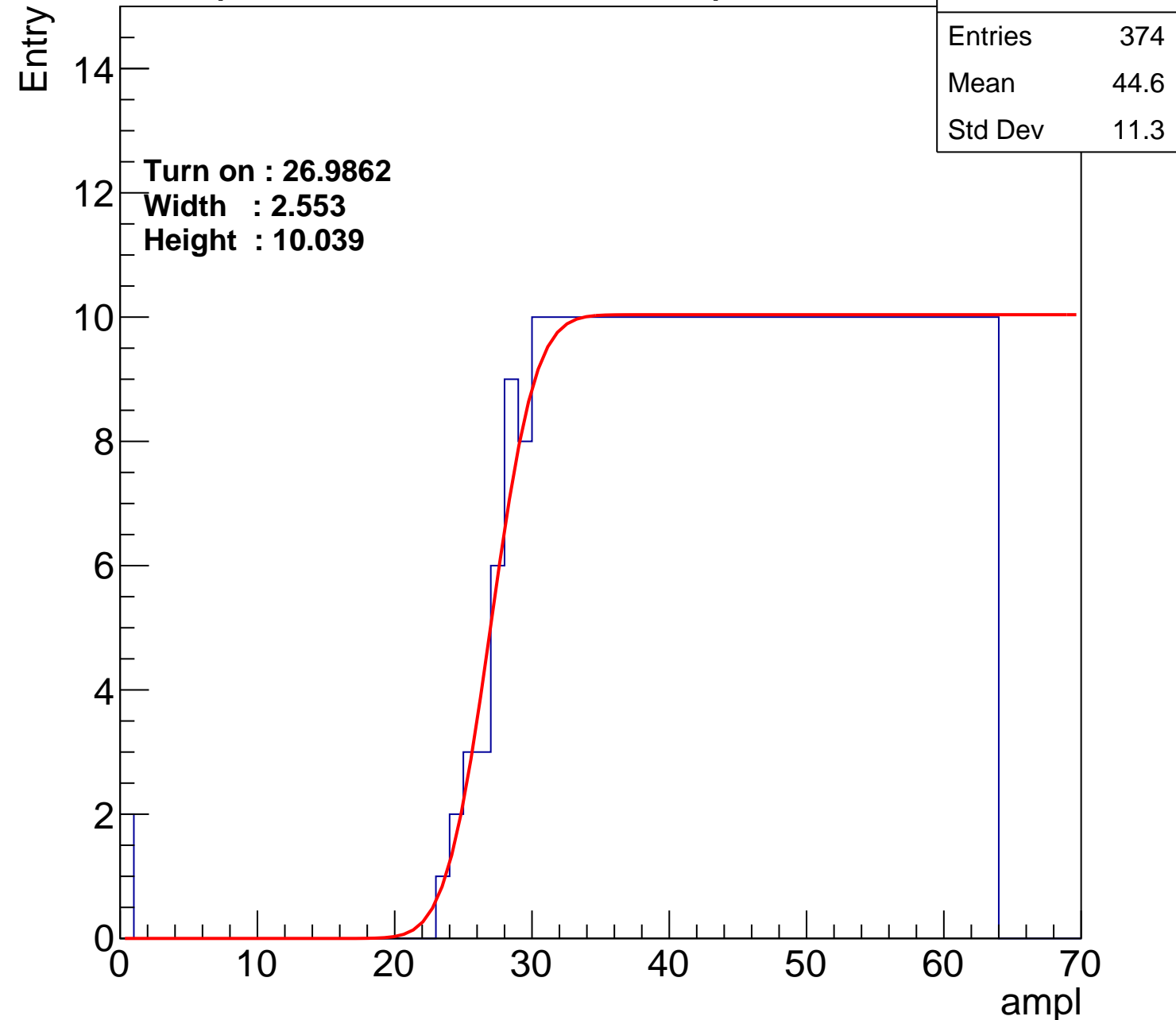
Width : 2.553

Height : 10.039

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch92

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.79
Std Dev	11.75

Turn on : 25.3718

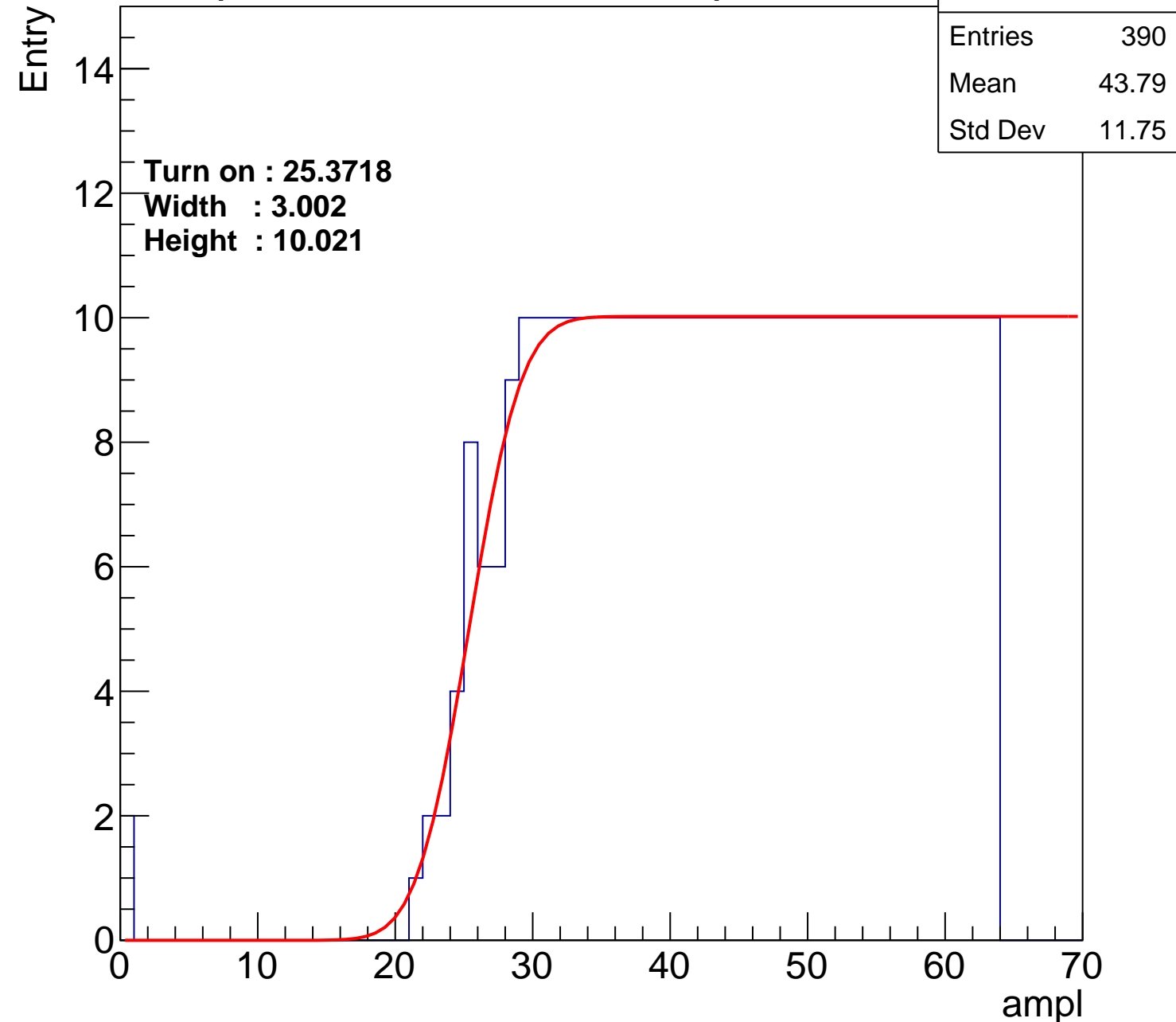
Width : 3.002

Height : 10.021

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch93

calib_packv5_042523_0143.root, FC#10, port B3

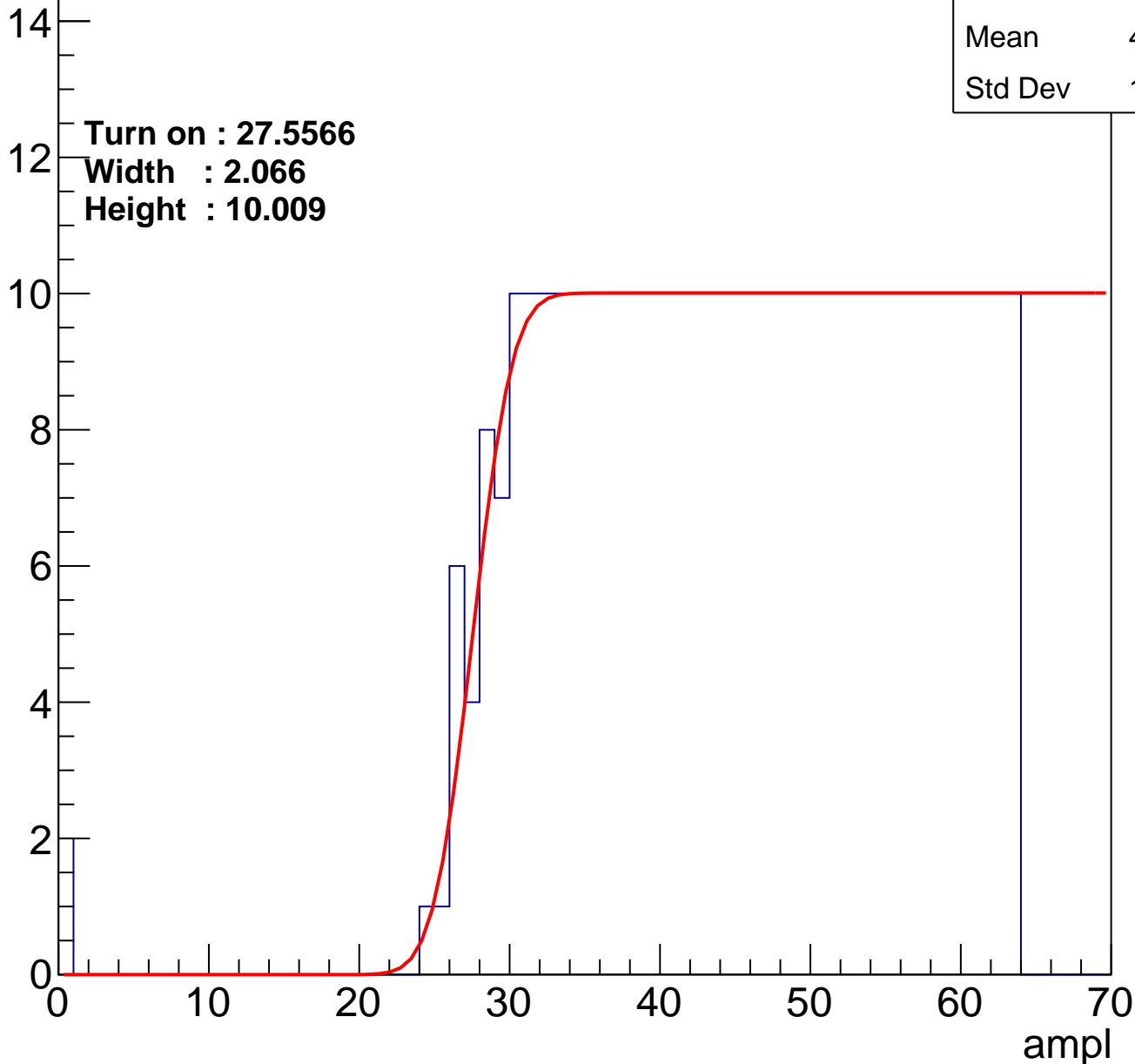
Entries	369
Mean	44.85
Std Dev	11.16

Turn on : 27.5566

Width : 2.066

Height : 10.009

Entry



B1L002S, U20-ch94

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.68
Std Dev	11.82

Turn on : 25.2859

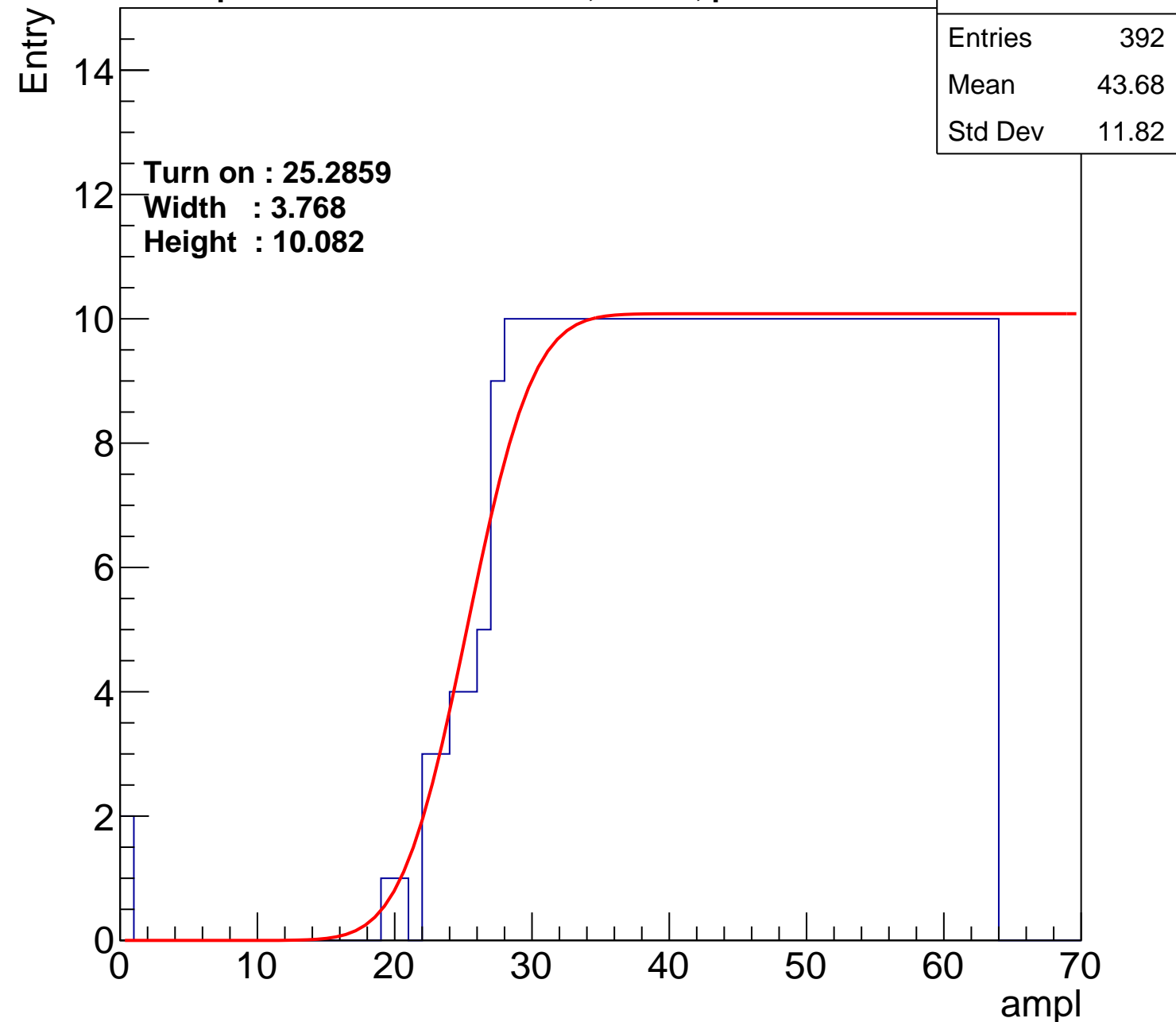
Width : 3.768

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch95

calib_packv5_042523_0143.root, FC#10, port B3

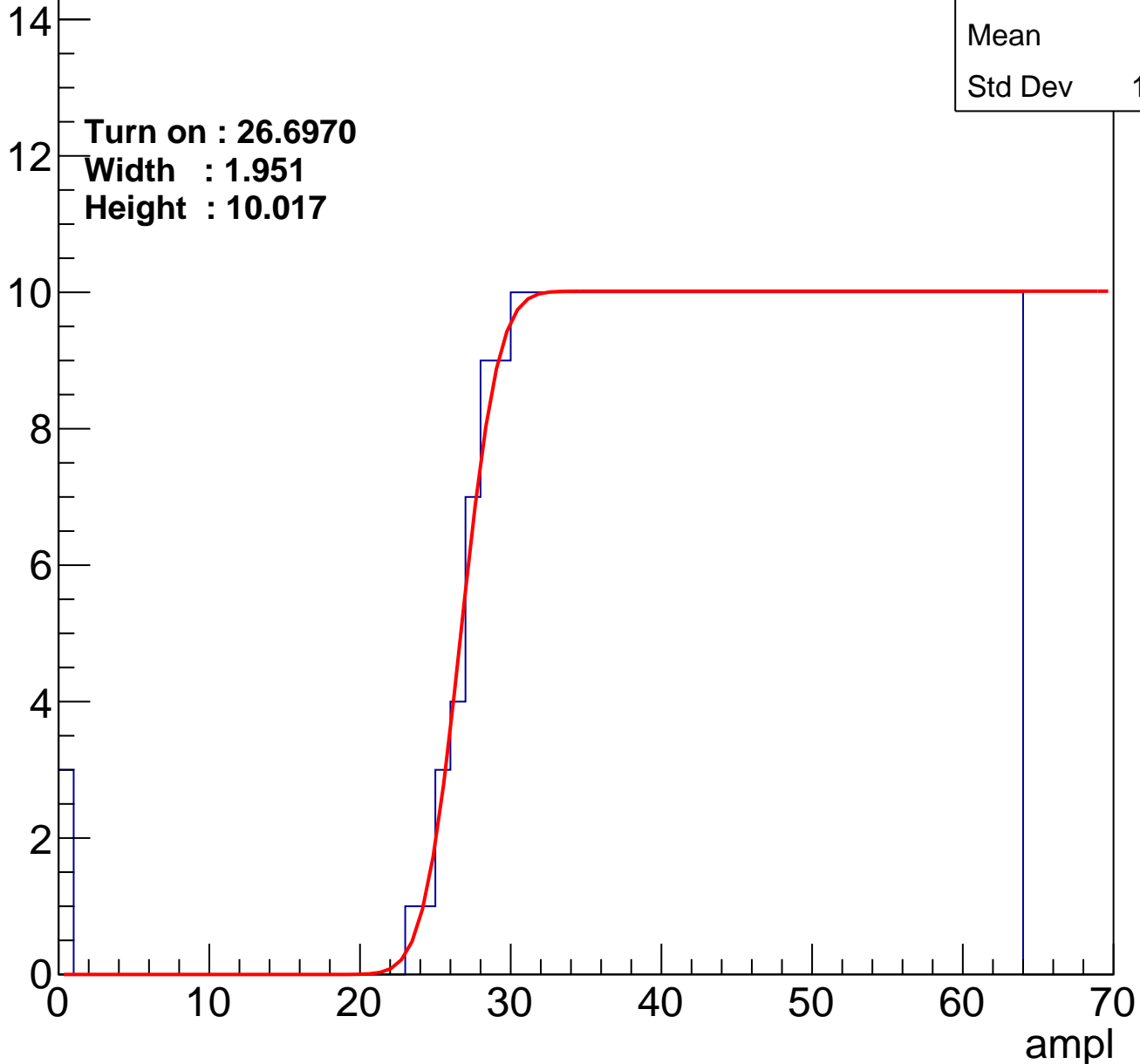
Entries	377
Mean	44.4
Std Dev	11.54

Turn on : 26.6970

Width : 1.951

Height : 10.017

Entry



B1L002S, U20-ch96

calib_packv5_042523_0143.root, FC#10, port B3

Entries	400
Mean	43.37
Std Dev	11.82

Turn on : 24.1085

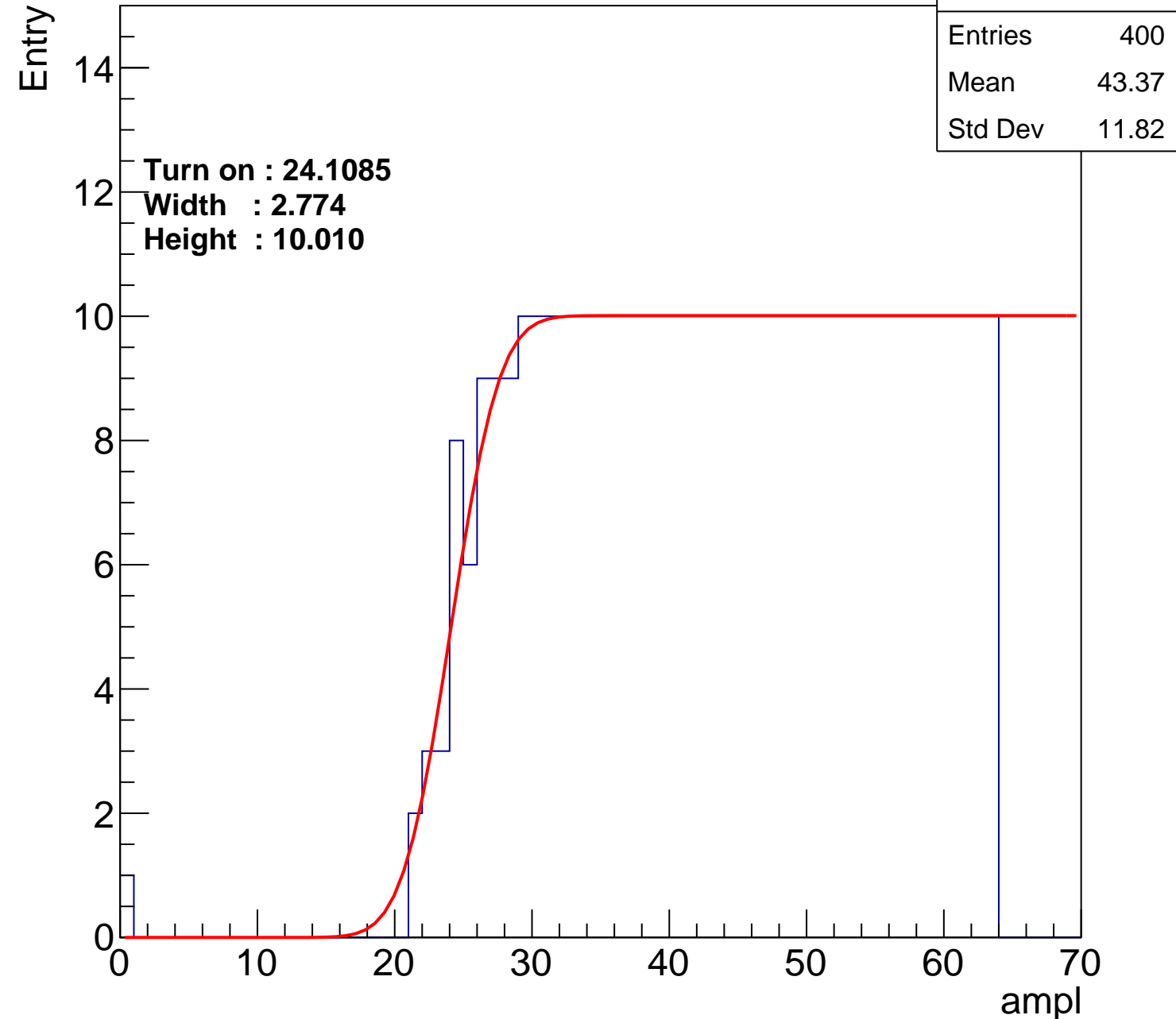
Width : 2.774

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch97

calib_packv5_042523_0143.root, FC#10, port B3

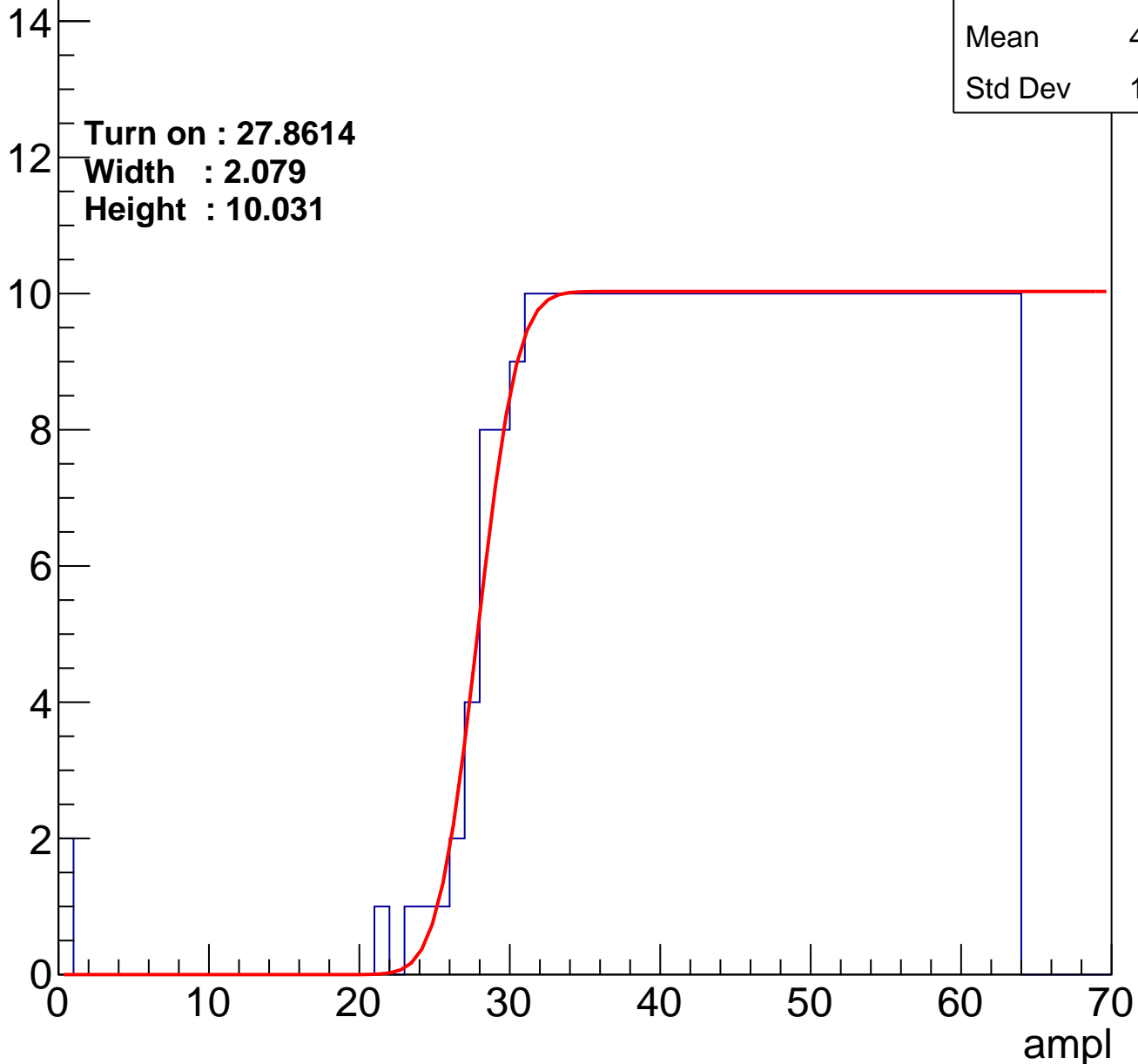
Entries	367
Mean	44.93
Std Dev	11.15

Turn on : 27.8614

Width : 2.079

Height : 10.031

Entry



B1L002S, U20-ch98

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.75
Std Dev	11.88

Turn on : 24.8212

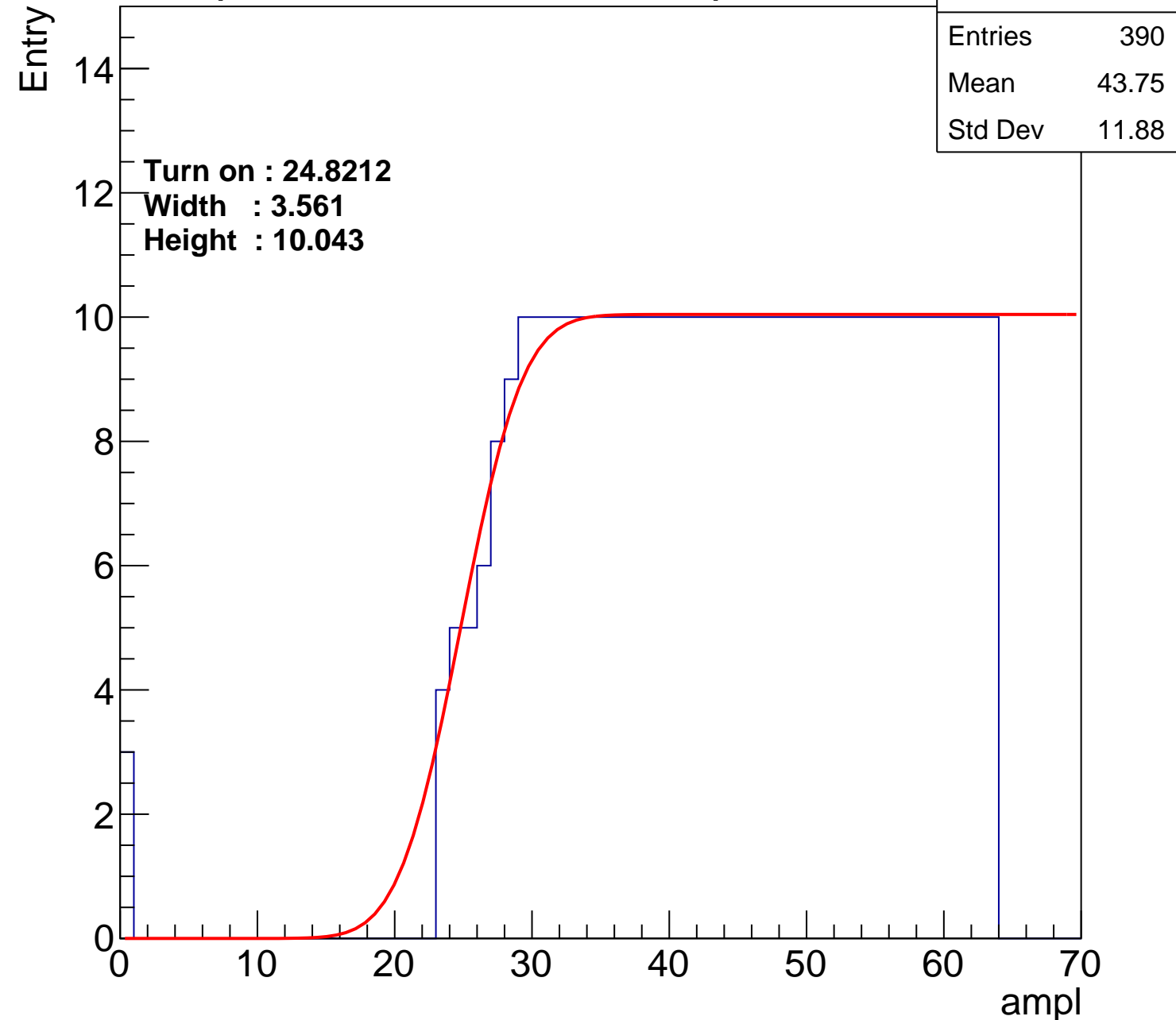
Width : 3.561

Height : 10.043

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch99

calib_packv5_042523_0143.root, FC#10, port B3

Entries	395
Mean	43.6
Std Dev	11.72

Turn on : 25.3235

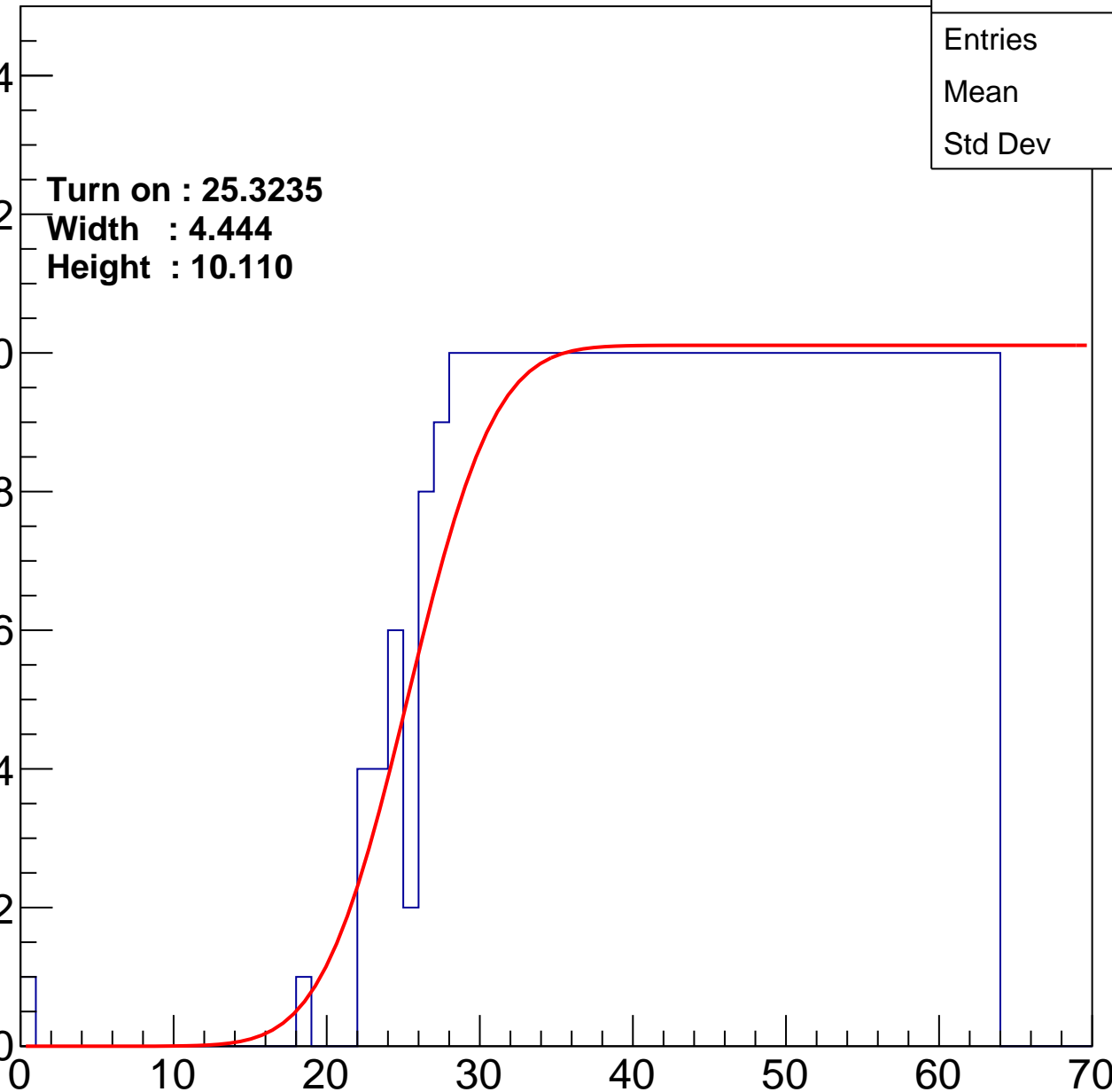
Width : 4.444

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch100

calib_packv5_042523_0143.root, FC#10, port B3

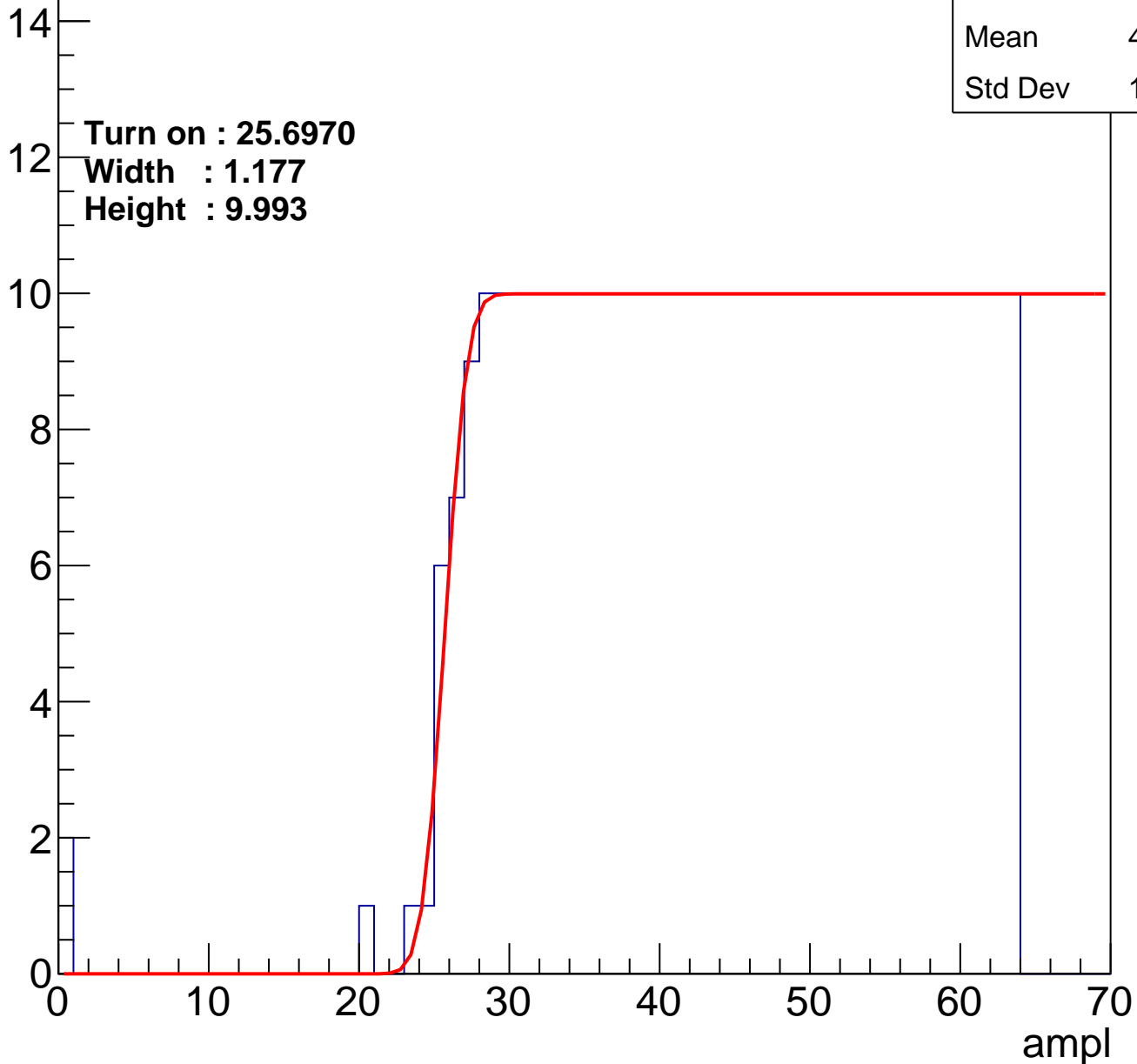
Entries	387
Mean	43.98
Std Dev	11.59

Turn on : 25.6970

Width : 1.177

Height : 9.993

Entry



B1L002S, U20-ch101

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	43.99
Std Dev	11.69

Turn on : 26.5232

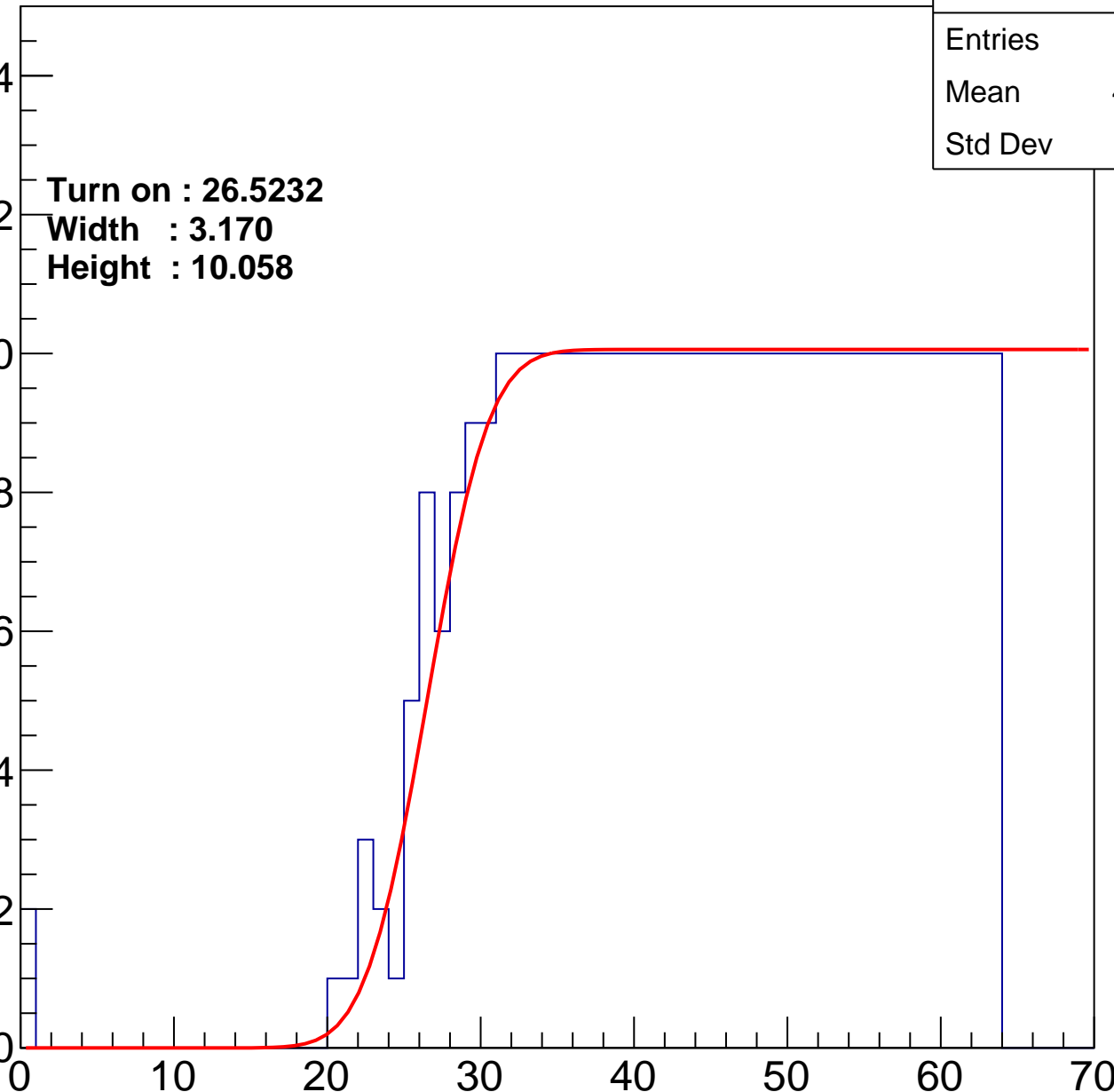
Width : 3.170

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch102

calib_packv5_042523_0143.root, FC#10, port B3

Entries	379
Mean	44.36
Std Dev	11.41

Turn on : 26.3913

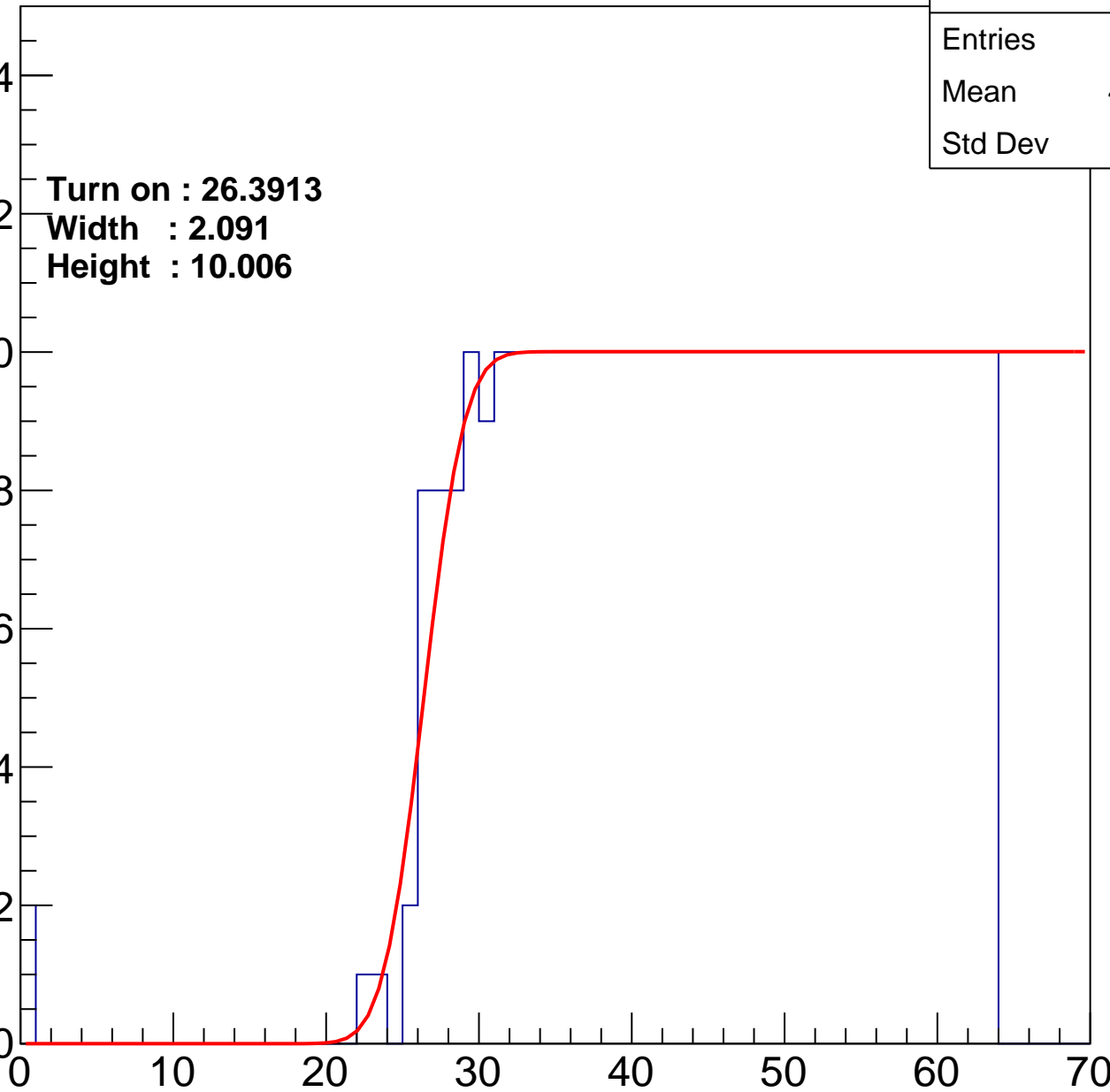
Width : 2.091

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch103

calib_packv5_042523_0143.root, FC#10, port B3

Entries	372
Mean	44.71
Std Dev	11.24

Turn on : 27.1137

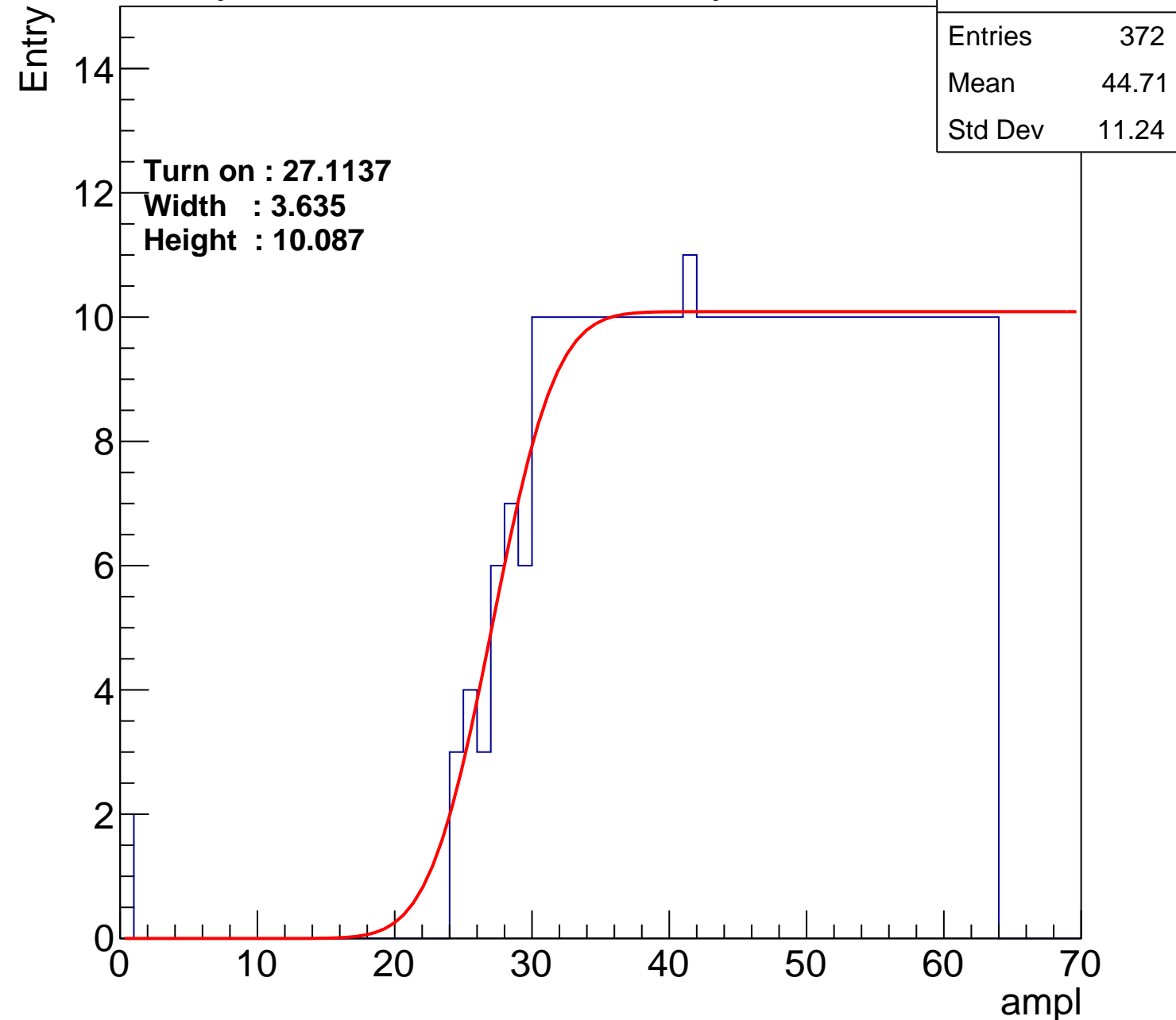
Width : 3.635

Height : 10.087

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch104

calib_packv5_042523_0143.root, FC#10, port B3

Entries	407
Mean	42.97
Std Dev	12.15

Turn on : 24.2441

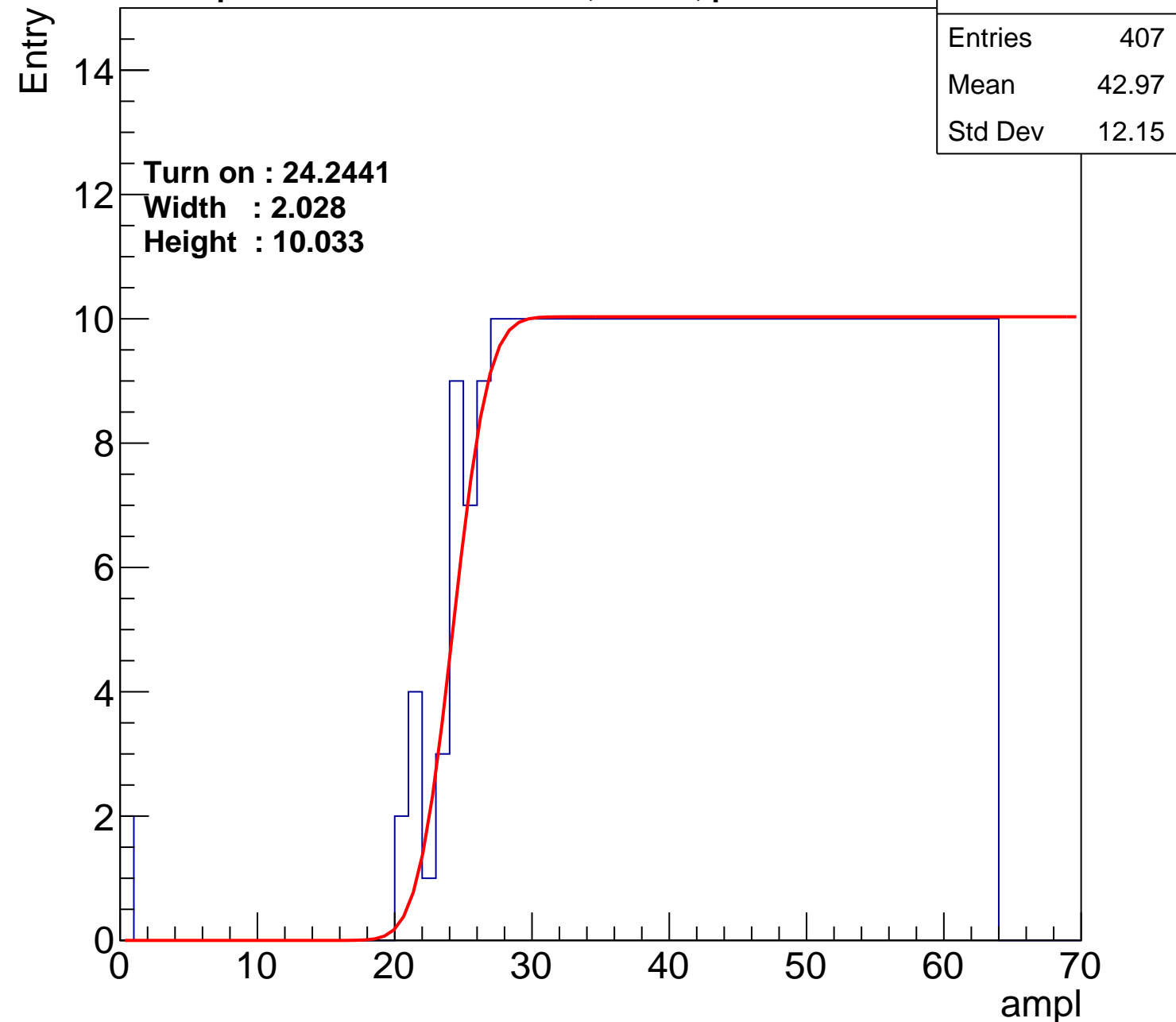
Width : 2.028

Height : 10.033

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch105

calib_packv5_042523_0143.root, FC#10, port B3

Entries	402
Mean	43.26
Std Dev	11.9

Turn on : 23.8742

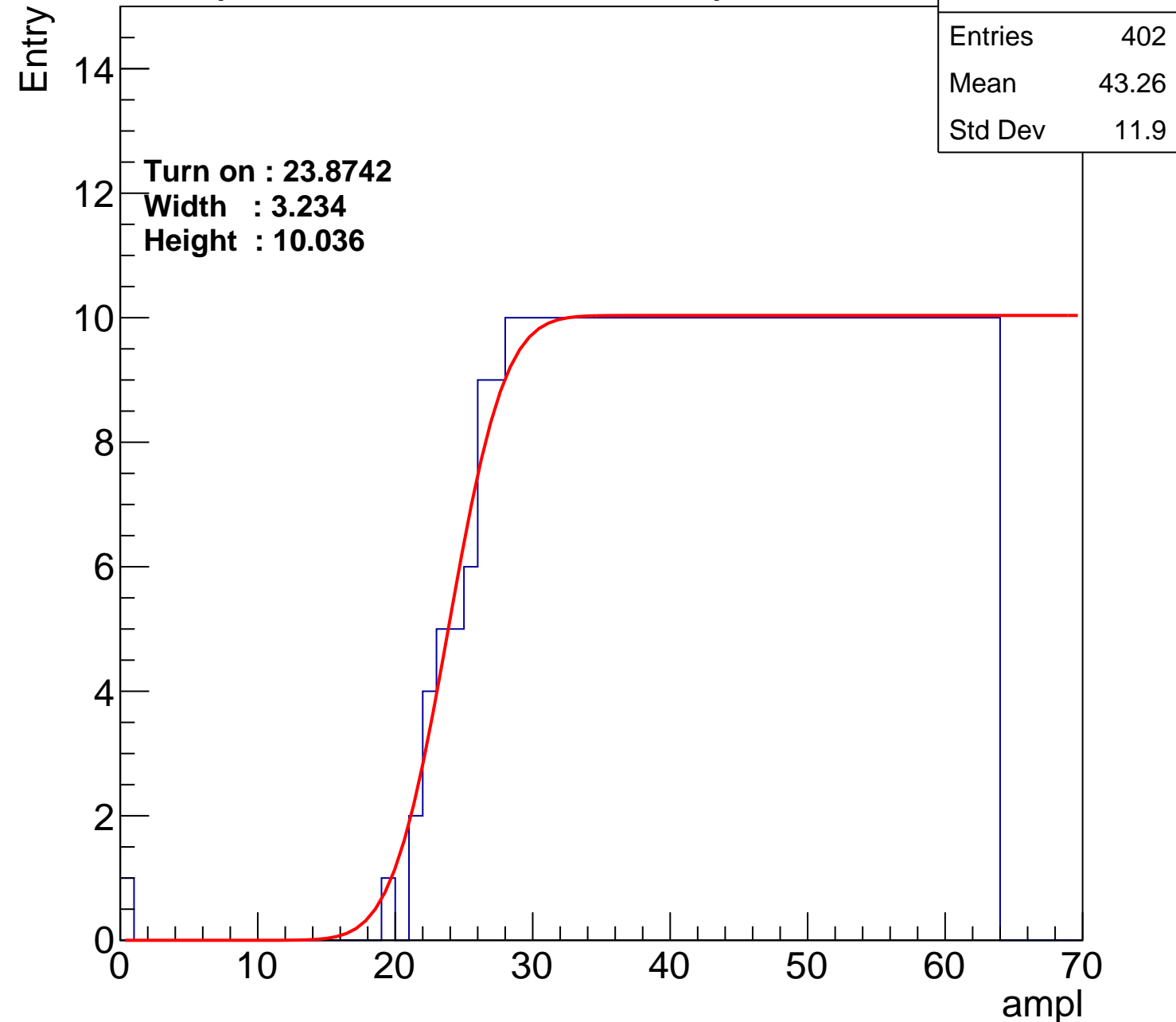
Width : 3.234

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch106

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.79
Std Dev	11.63

Turn on : 25.0359

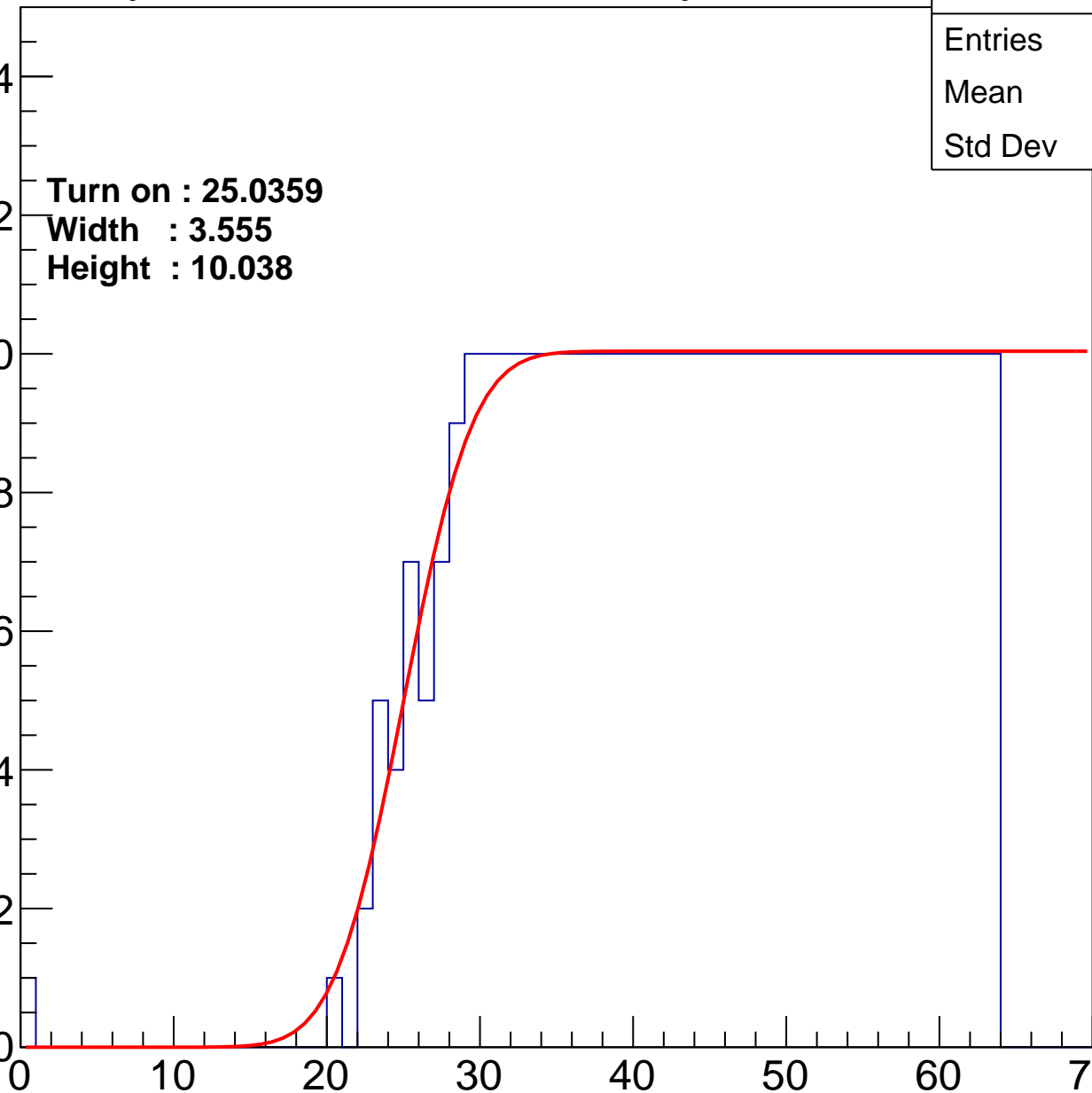
Width : 3.555

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch107

calib_packv5_042523_0143.root, FC#10, port B3

Entries	381
Mean	44.3
Std Dev	11.32

Turn on : 26.3060

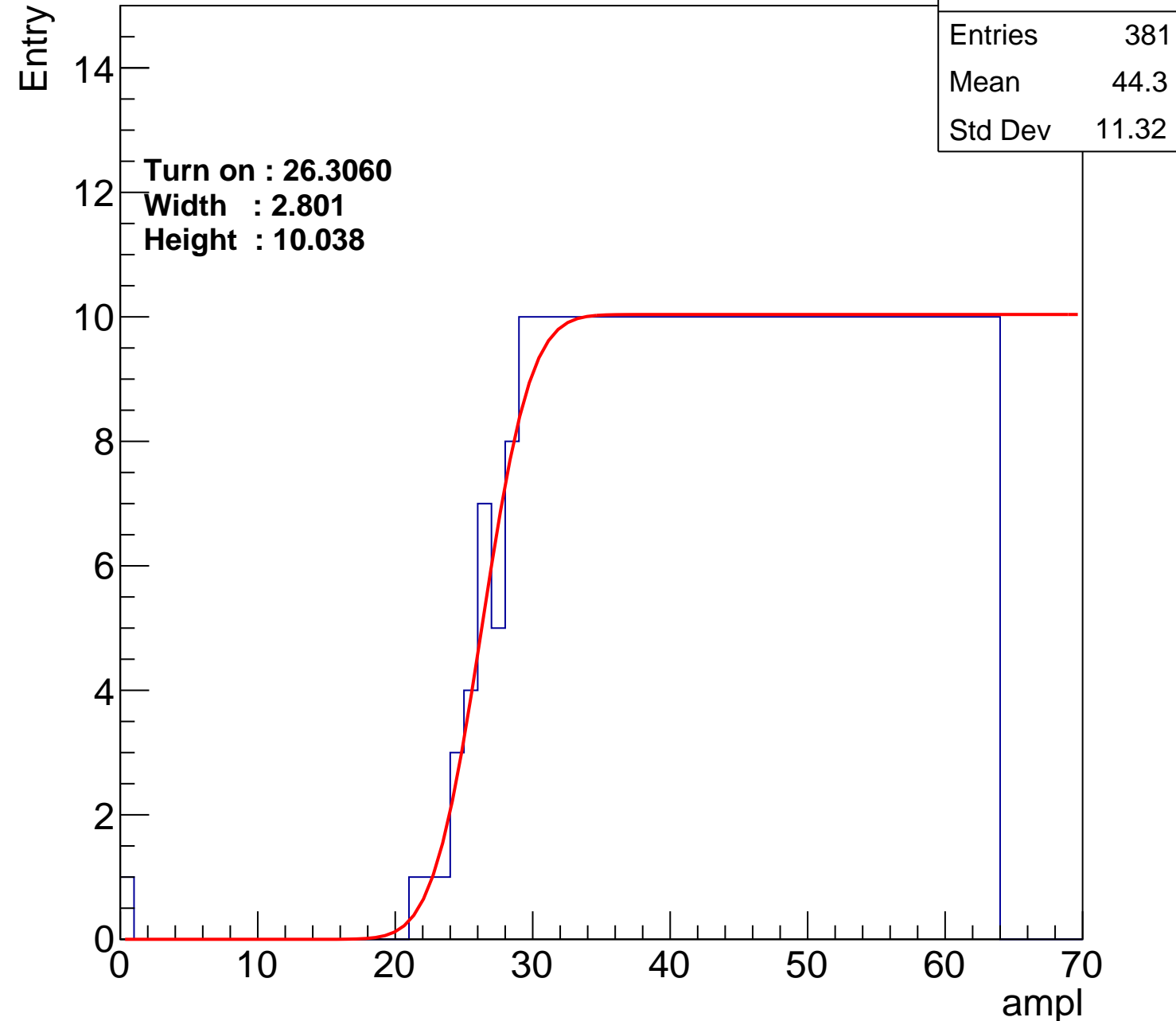
Width : 2.801

Height : 10.038

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch108

calib_packv5_042523_0143.root, FC#10, port B3

Entries	403
Mean	43.21
Std Dev	11.92

Turn on : 23.6864

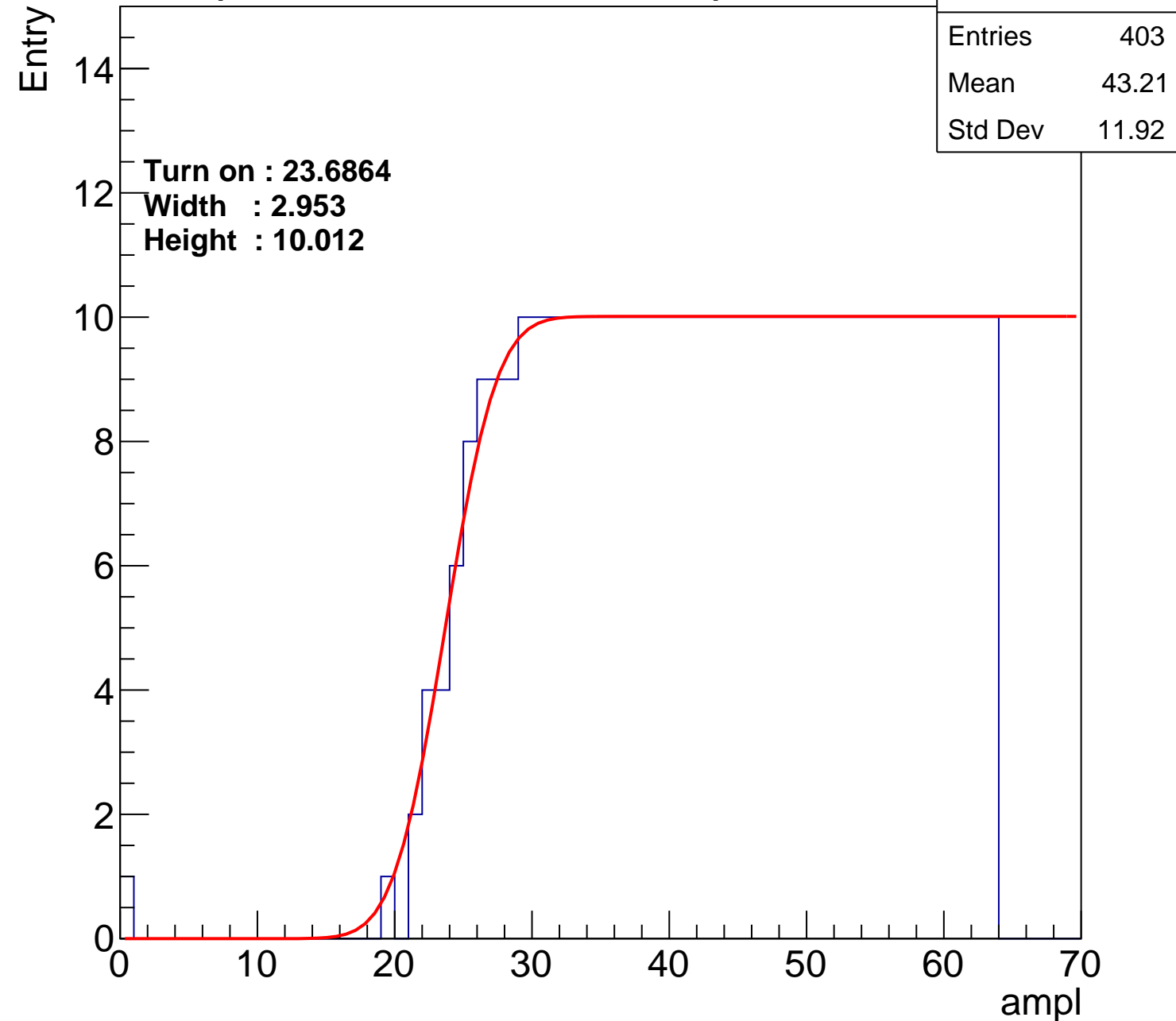
Width : 2.953

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch109

calib_packv5_042523_0143.root, FC#10, port B3

Entries	373
Mean	44.69
Std Dev	11.12

Turn on : 27.1554

Width : 2.974

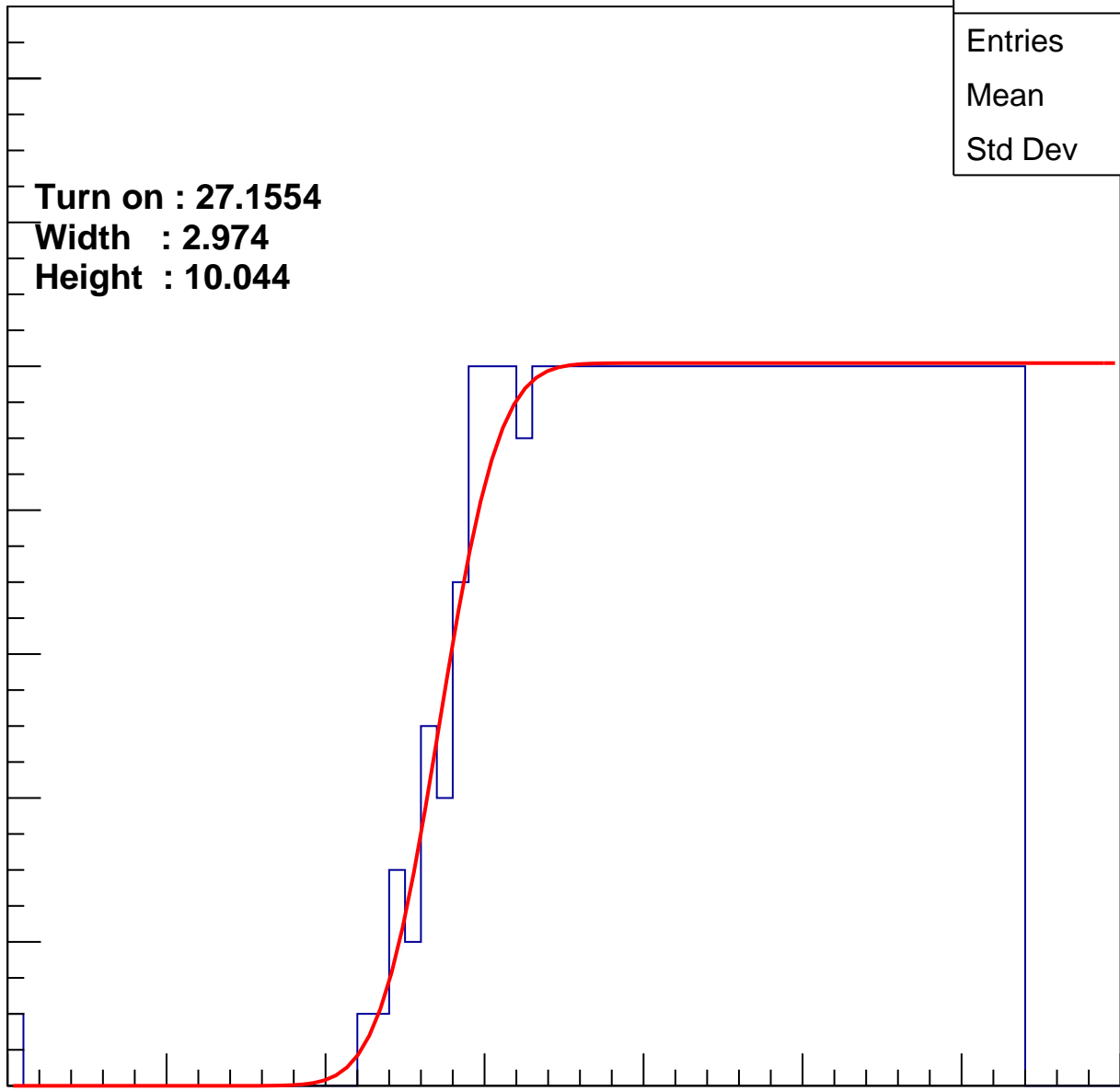
Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch110

calib_packv5_042523_0143.root, FC#10, port B3

Entries	393
Mean	43.68
Std Dev	11.69

Turn on : 24.7967

Width : 2.659

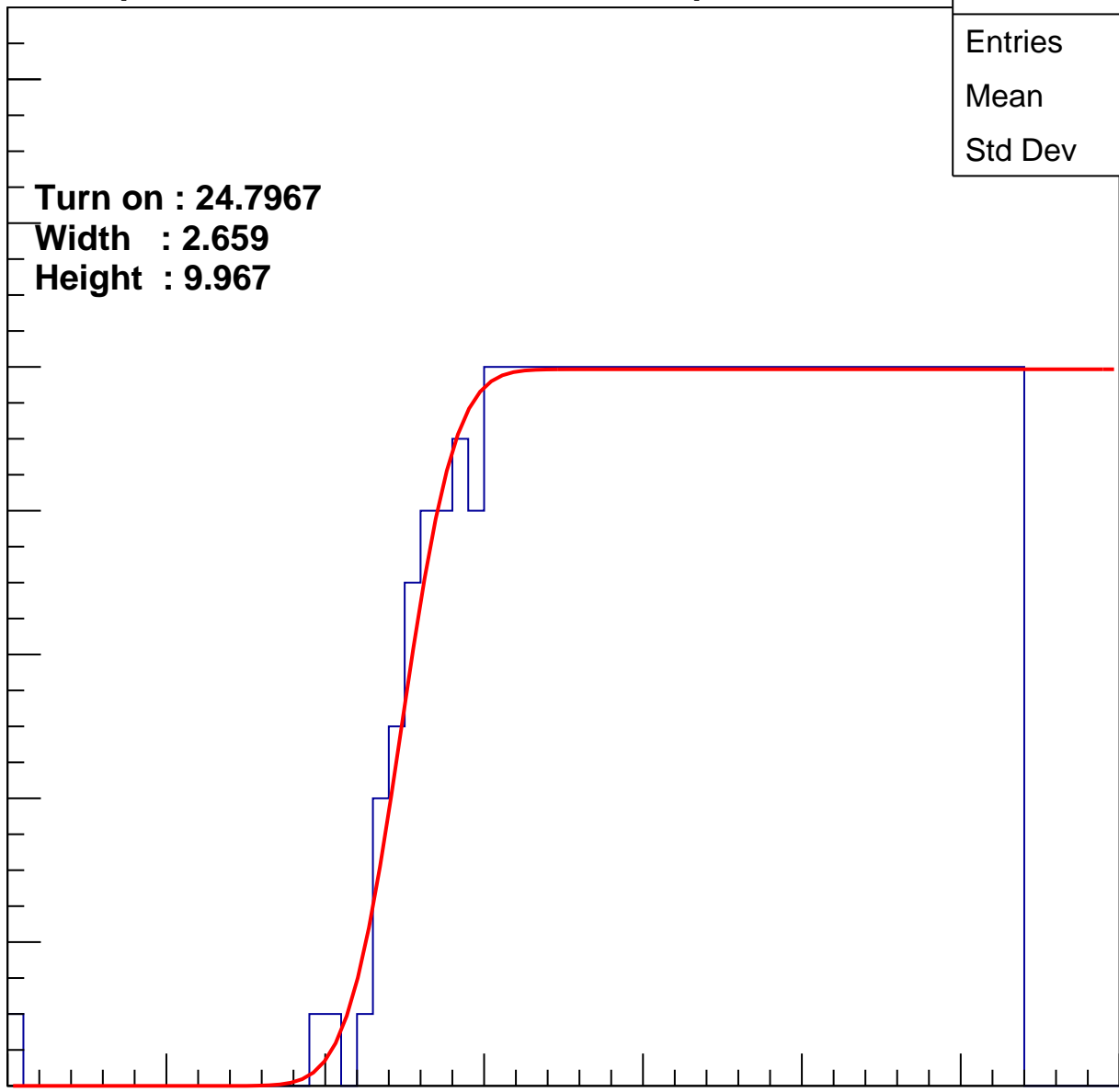
Height : 9.967

Entry

14
12
10
8
6
4
2
0

ampl

0 10 20 30 40 50 60 70



B1L002S, U20-ch111

calib_packv5_042523_0143.root, FC#10, port B3

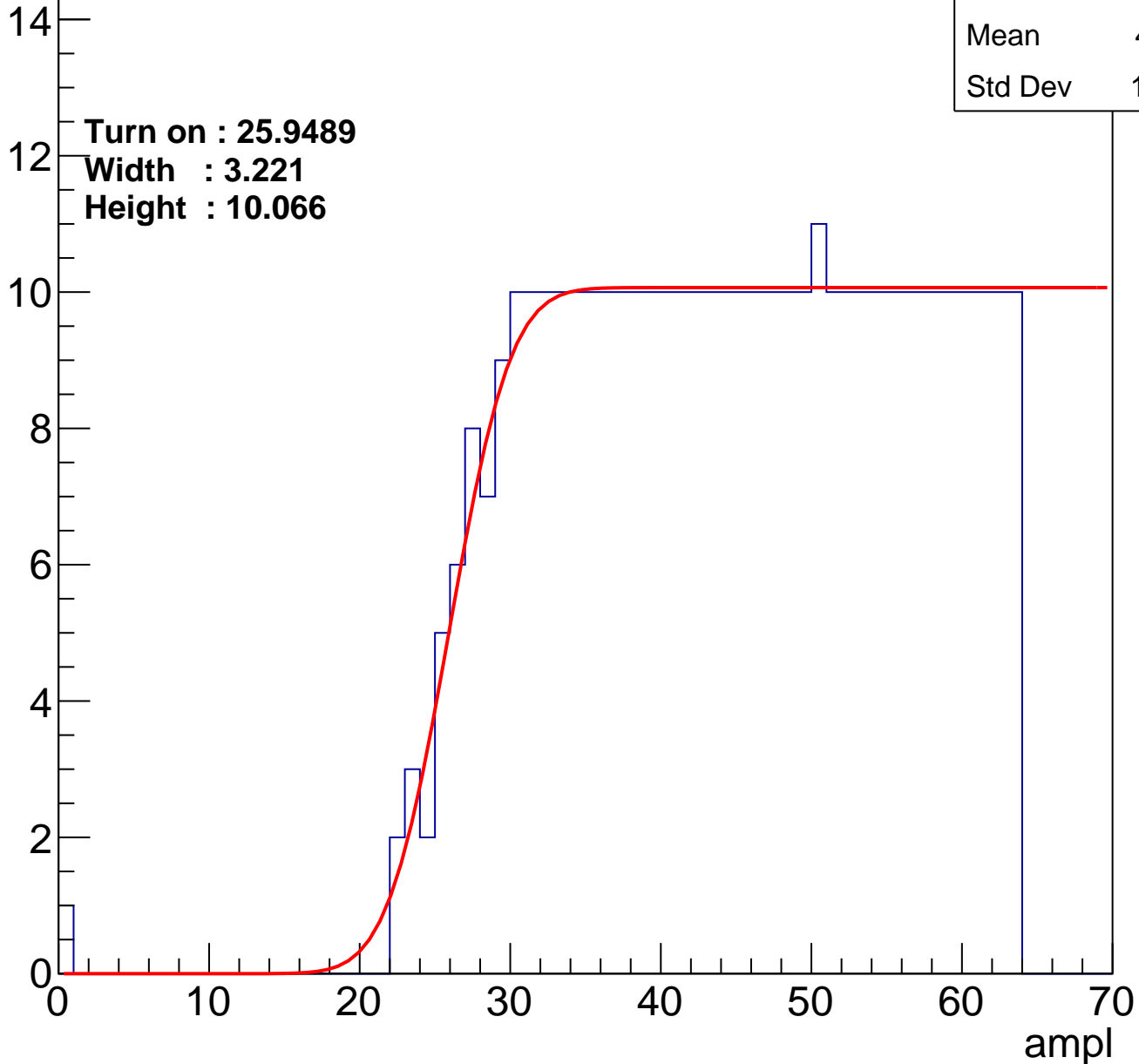
Entries	384
Mean	44.21
Std Dev	11.39

Turn on : 25.9489

Width : 3.221

Height : 10.066

Entry



B1L002S, U20-ch112

calib_packv5_042523_0143.root, FC#10, port B3

Entries	374
Mean	44.69
Std Dev	11.06

Turn on : 26.6630

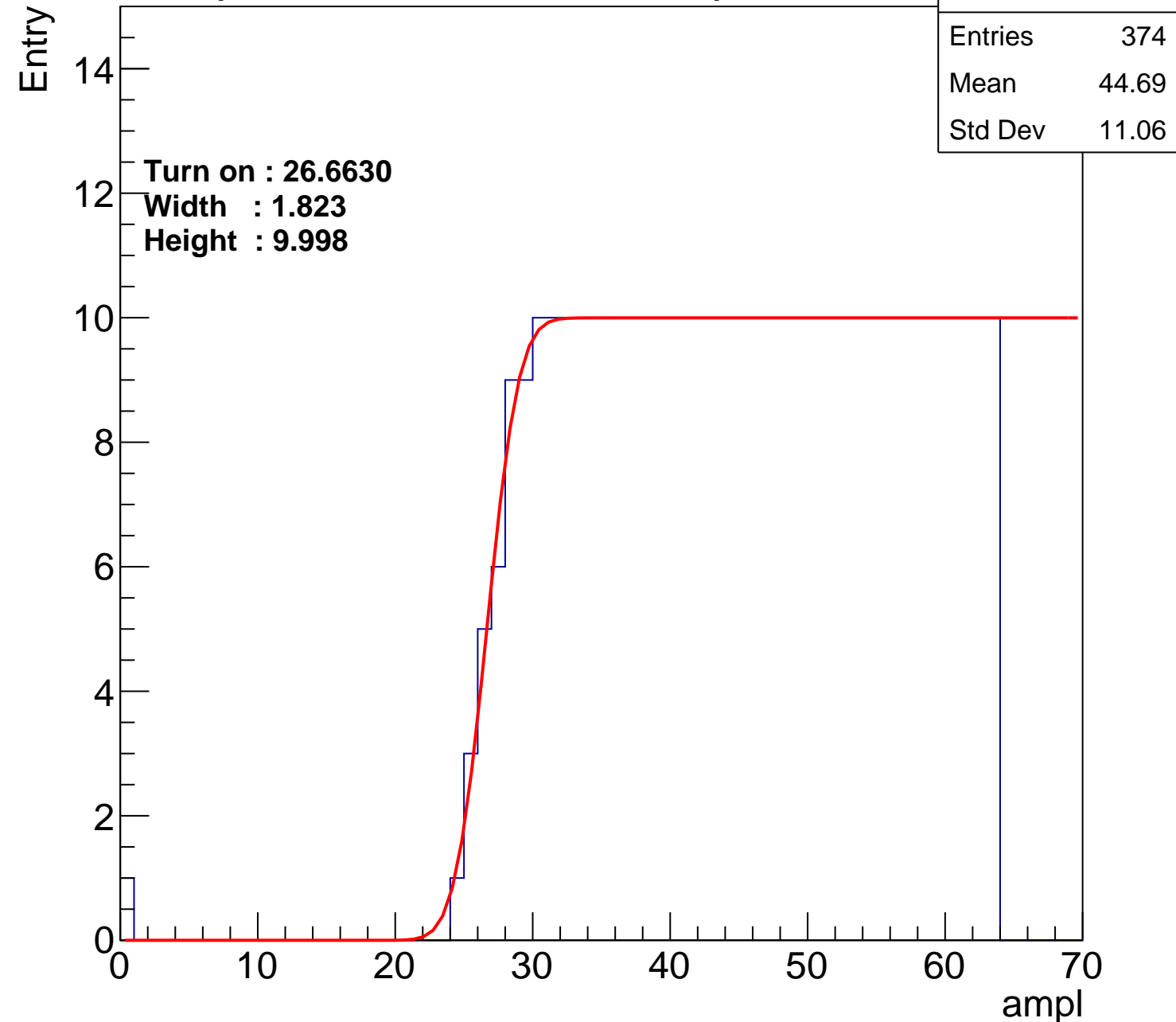
Width : 1.823

Height : 9.998

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch113

calib_packv5_042523_0143.root, FC#10, port B3

Entries	392
Mean	43.66
Std Dev	11.84

Turn on : 25.2463

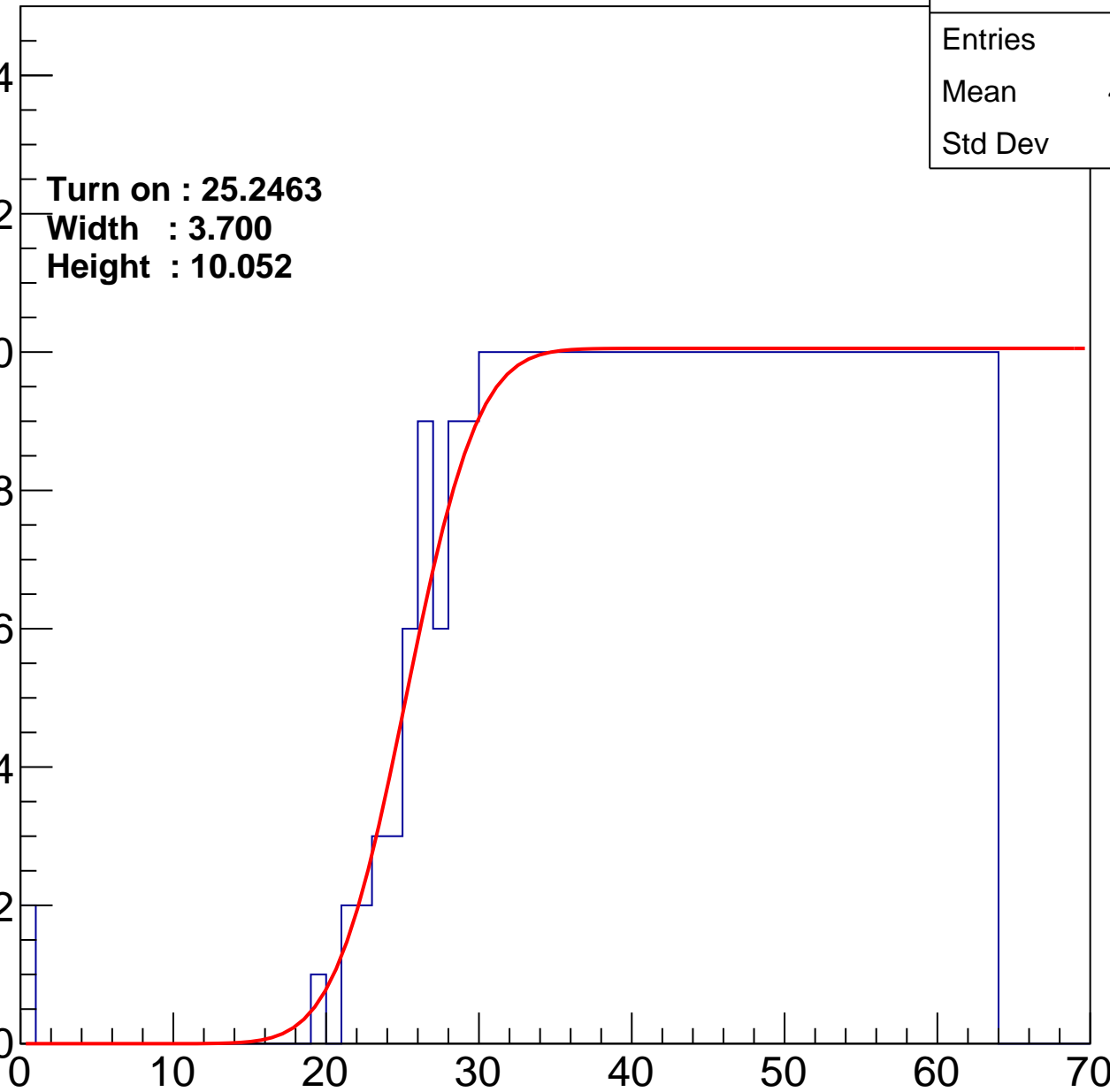
Width : 3.700

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch114

calib_packv5_042523_0143.root, FC#10, port B3

Entries	401
Mean	43.3
Std Dev	11.89

Turn on : 24.1357

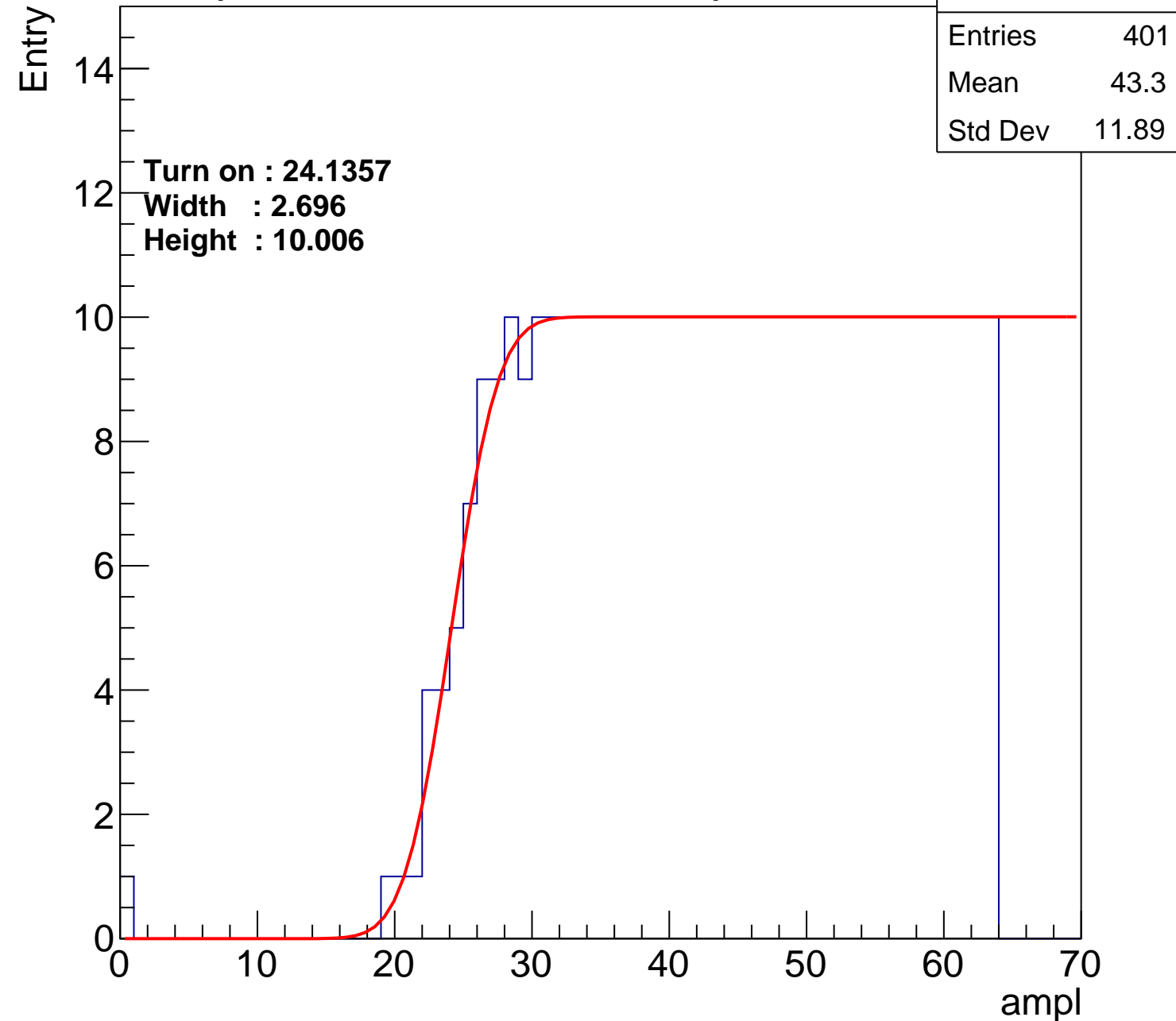
Width : 2.696

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch115

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.67
Std Dev	11.65

Turn on : 24.7477

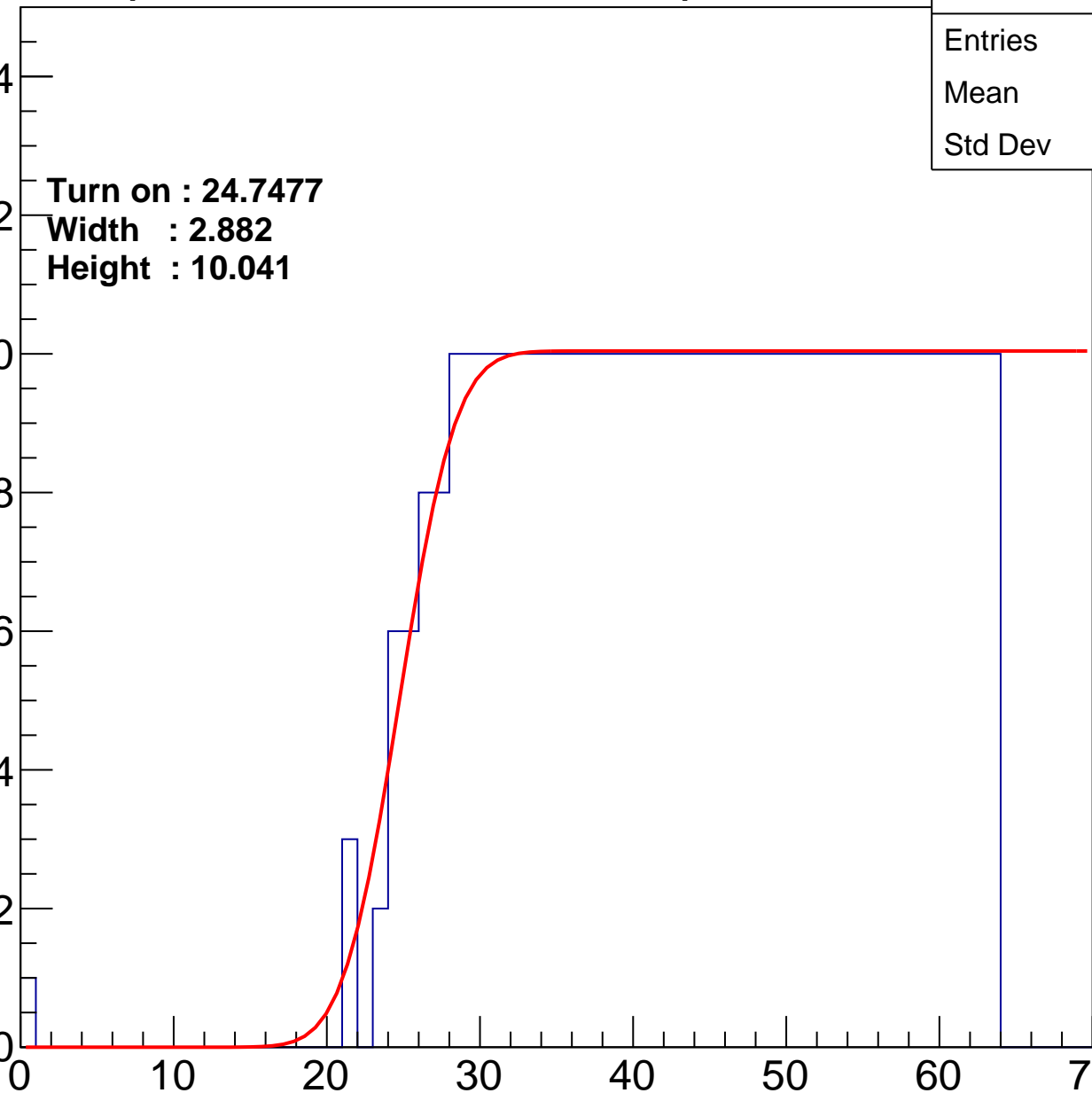
Width : 2.882

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch116

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.45
Std Dev	12.2

Turn on : 25.2210

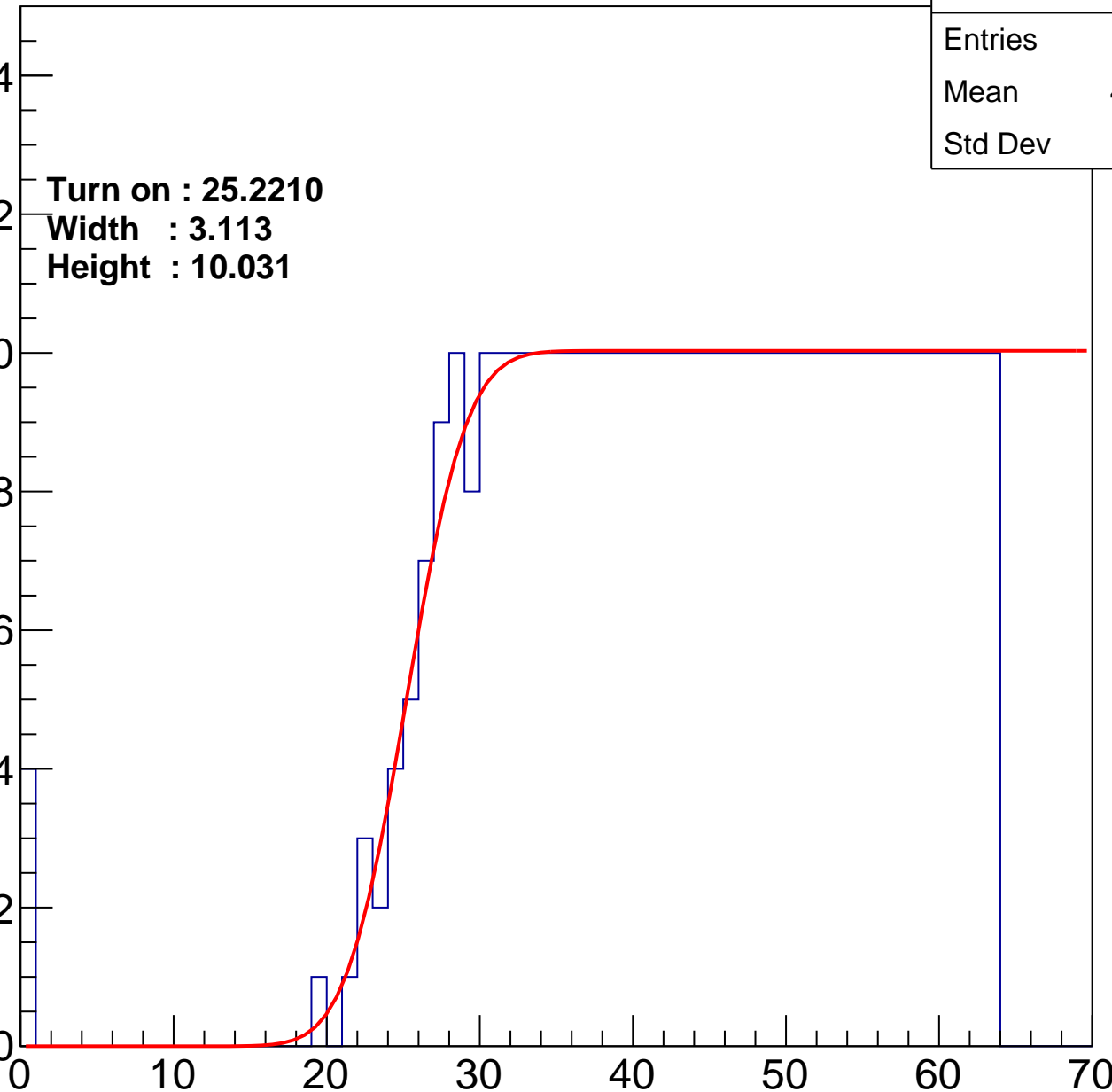
Width : 3.113

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch117

calib_packv5_042523_0143.root, FC#10, port B3

Entries	399
Mean	43.34
Std Dev	11.99

Turn on : 23.9888

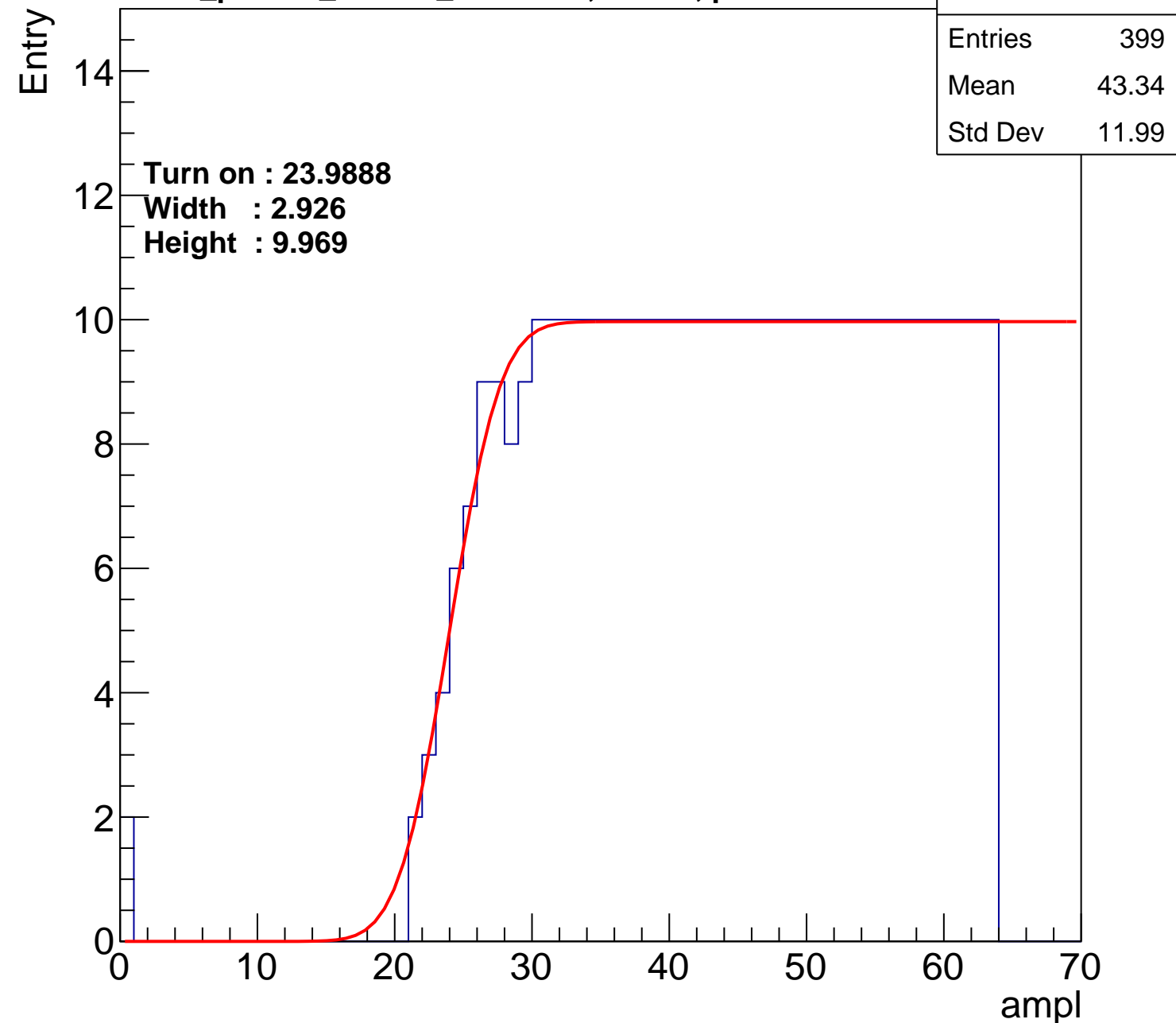
Width : 2.926

Height : 9.969

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch118

calib_packv5_042523_0143.root, FC#10, port B3

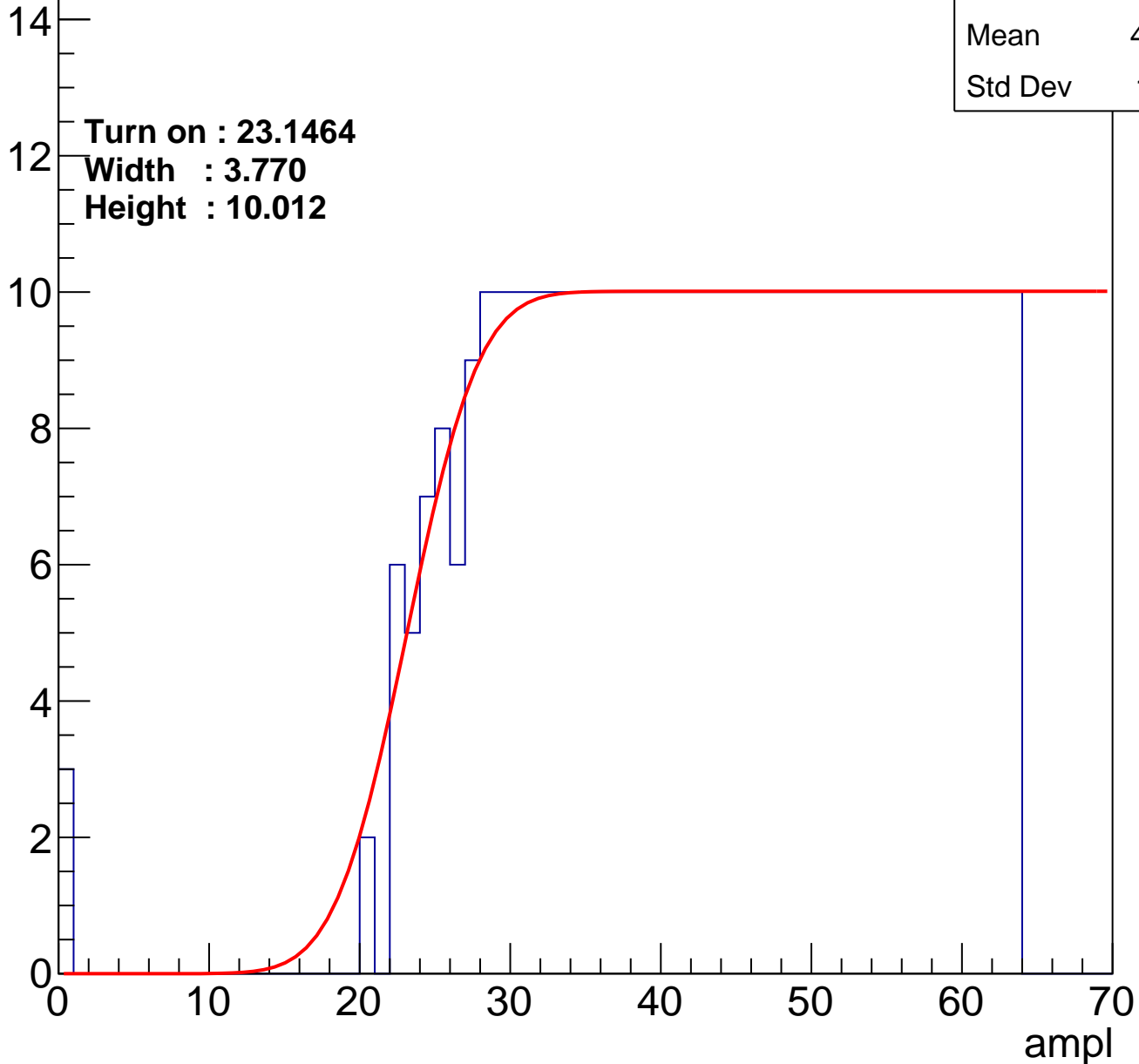
Entries	406
Mean	42.94
Std Dev	12.31

Turn on : 23.1464

Width : 3.770

Height : 10.012

Entry



B1L002S, U20-ch119

calib_packv5_042523_0143.root, FC#10, port B3

Entries	380
Mean	44.22
Std Dev	11.67

Turn on : 26.8506

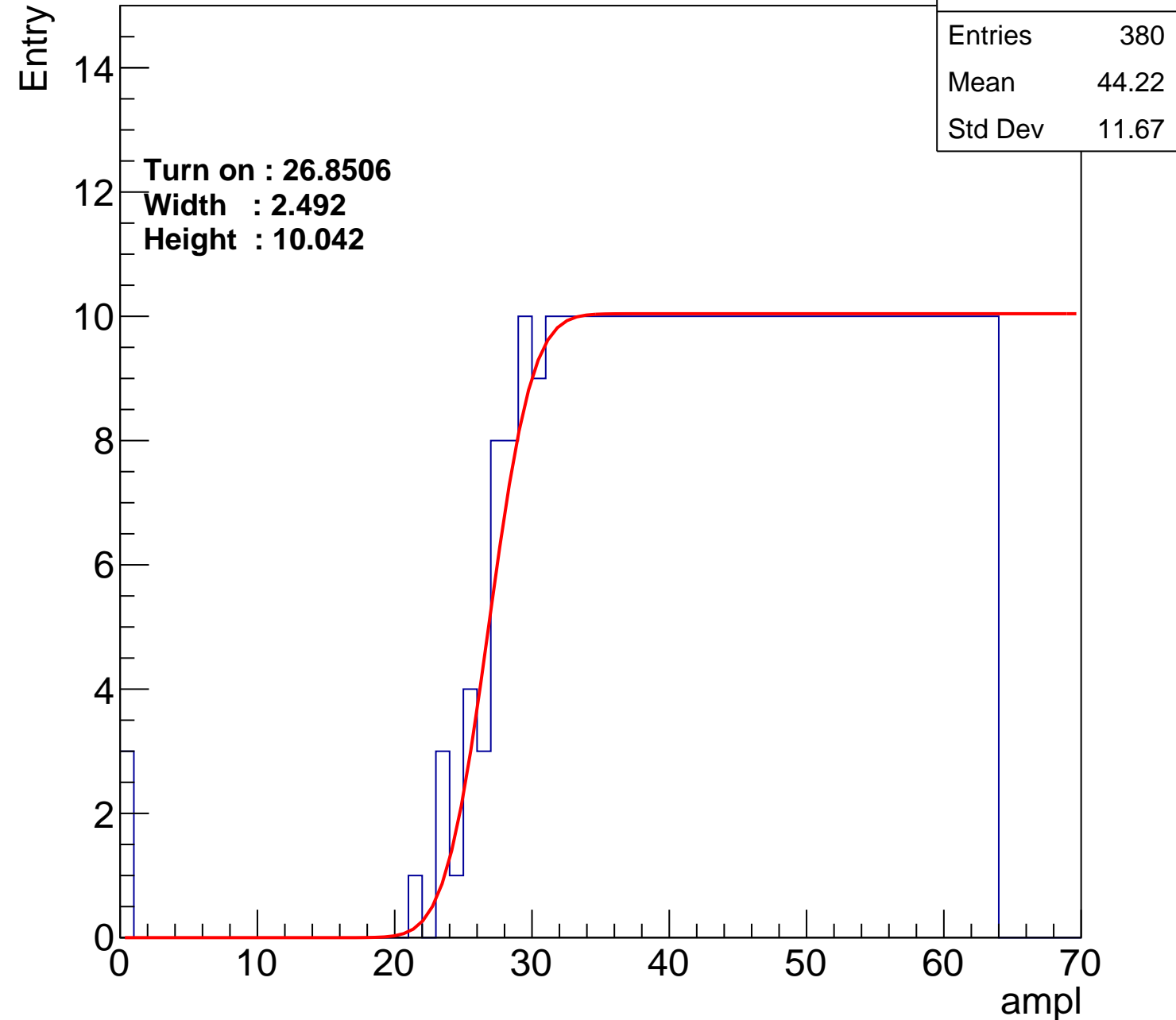
Width : 2.492

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch120

calib_packv5_042523_0143.root, FC#10, port B3

Entries	385
Mean	44.09
Std Dev	11.46

Turn on : 25.8999

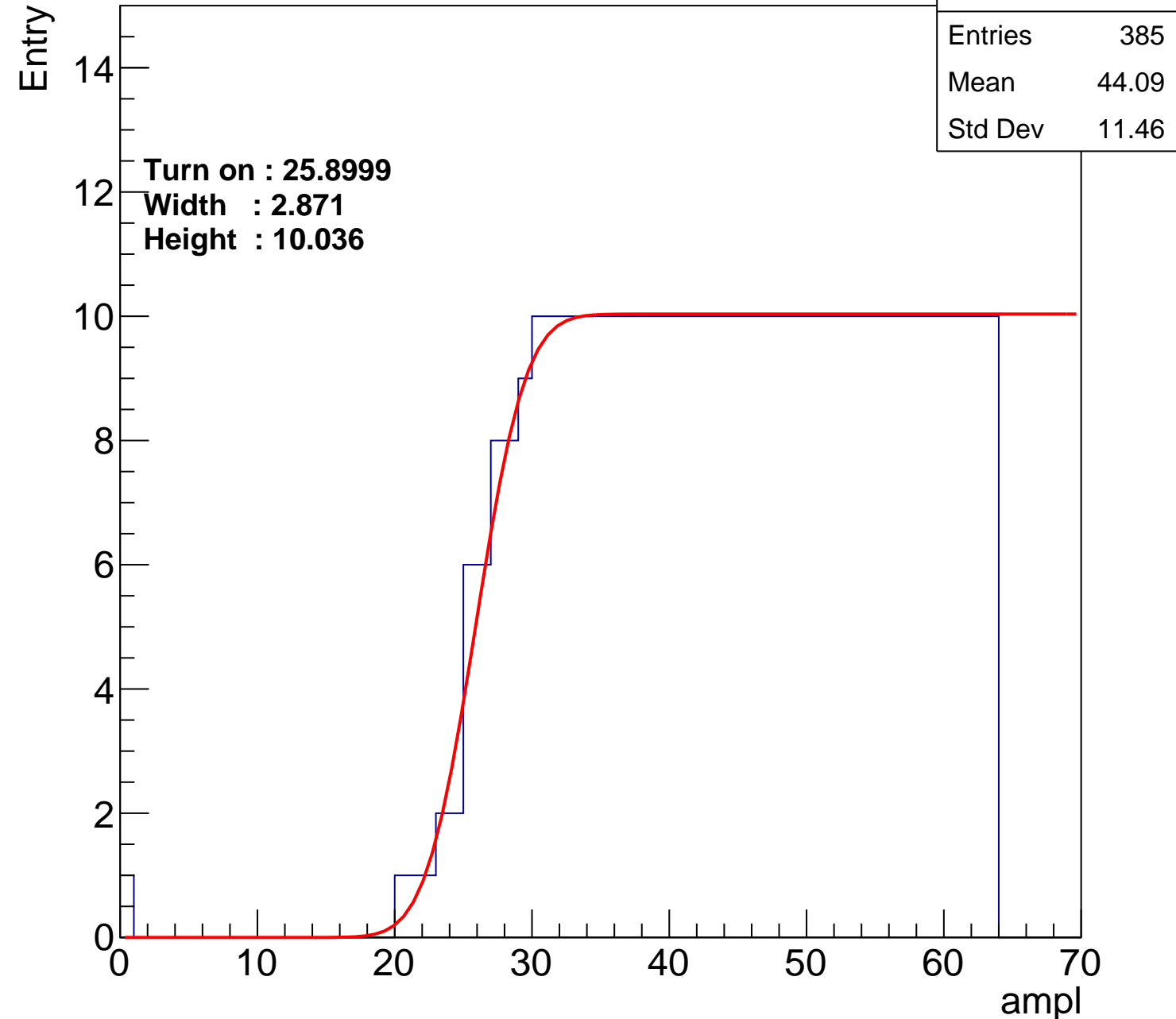
Width : 2.871

Height : 10.036

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch121

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.26
Std Dev	11.36

Turn on : 25.5694

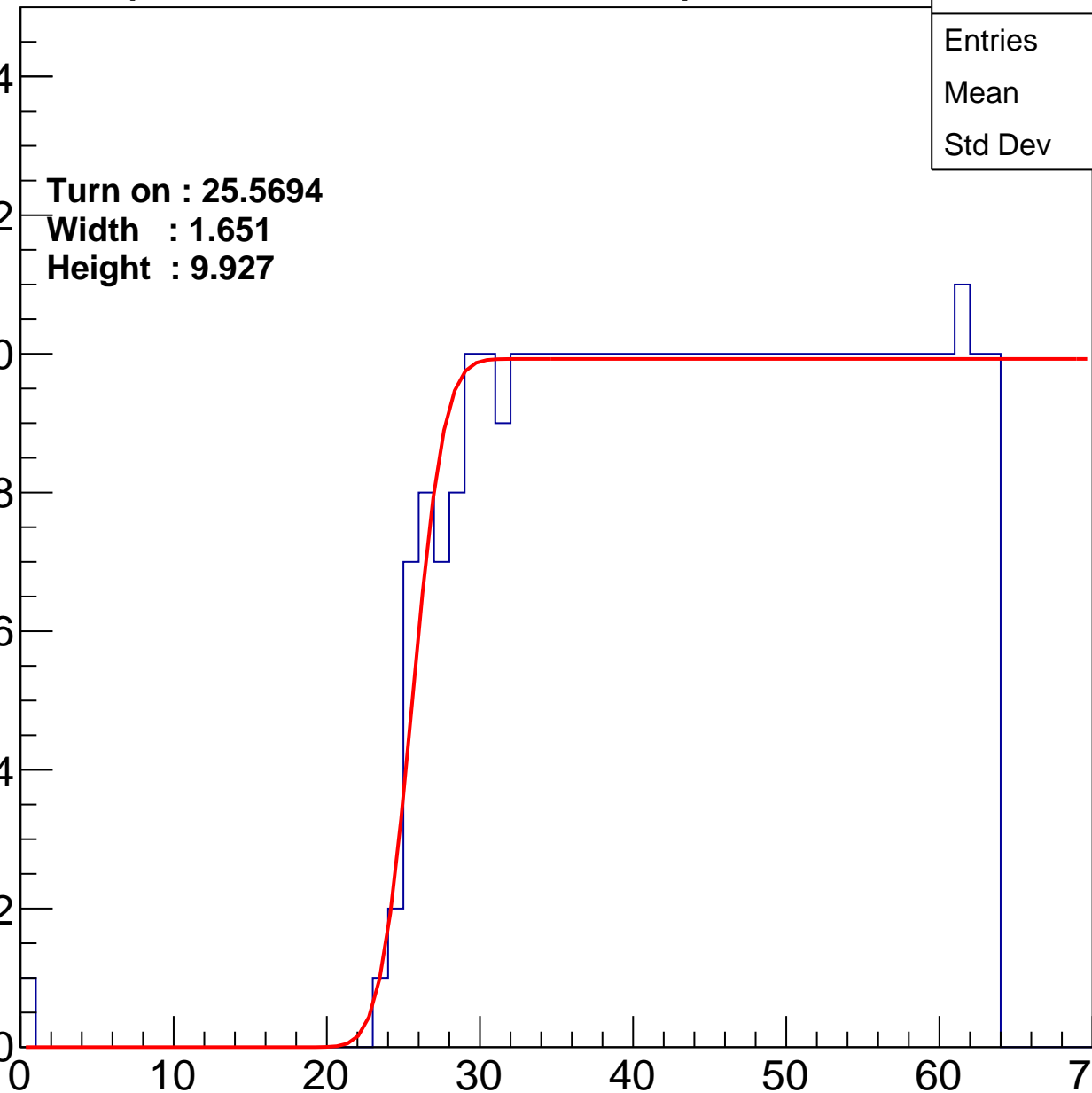
Width : 1.651

Height : 9.927

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch122

calib_packv5_042523_0143.root, FC#10, port B3

Entries	404
Mean	43.05
Std Dev	12.25

Turn on : 24.3319

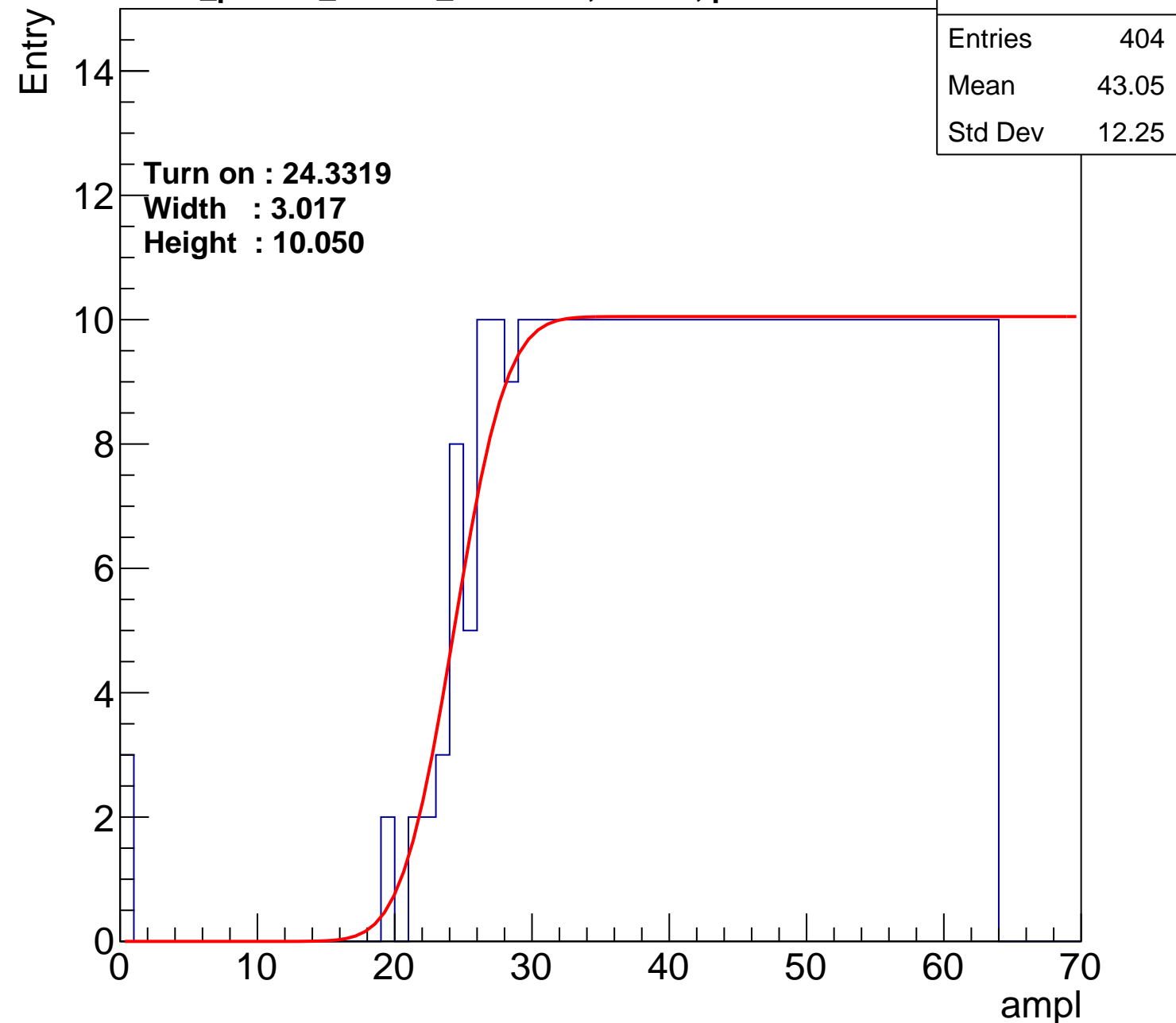
Width : 3.017

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch123

calib_packv5_042523_0143.root, FC#10, port B3

Entries	384
Mean	44.16
Std Dev	11.39

Turn on : 25.8224

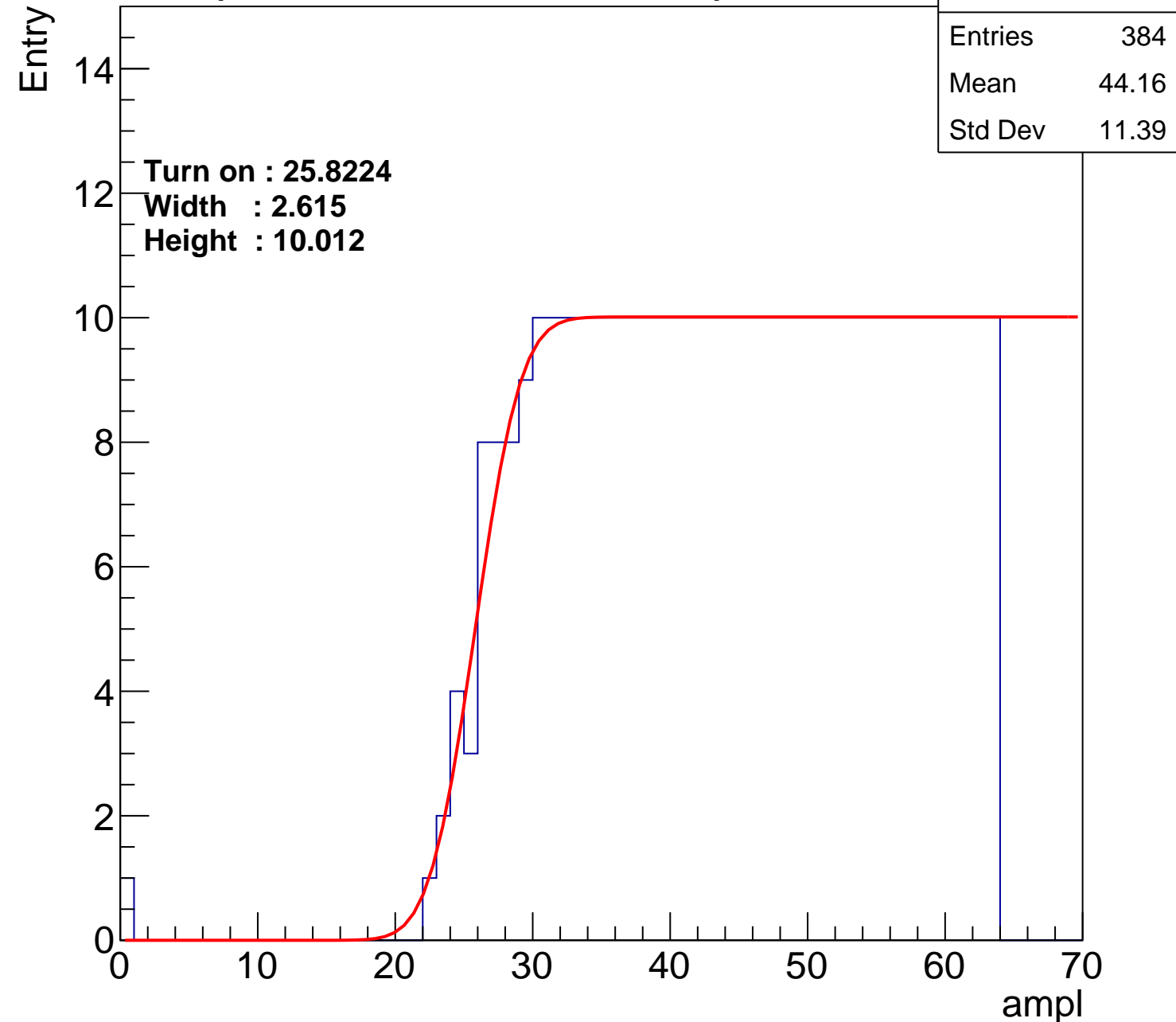
Width : 2.615

Height : 10.012

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch124

calib_packv5_042523_0143.root, FC#10, port B3

Entries	397
Mean	43.39
Std Dev	12.07

Turn on : 24.7506

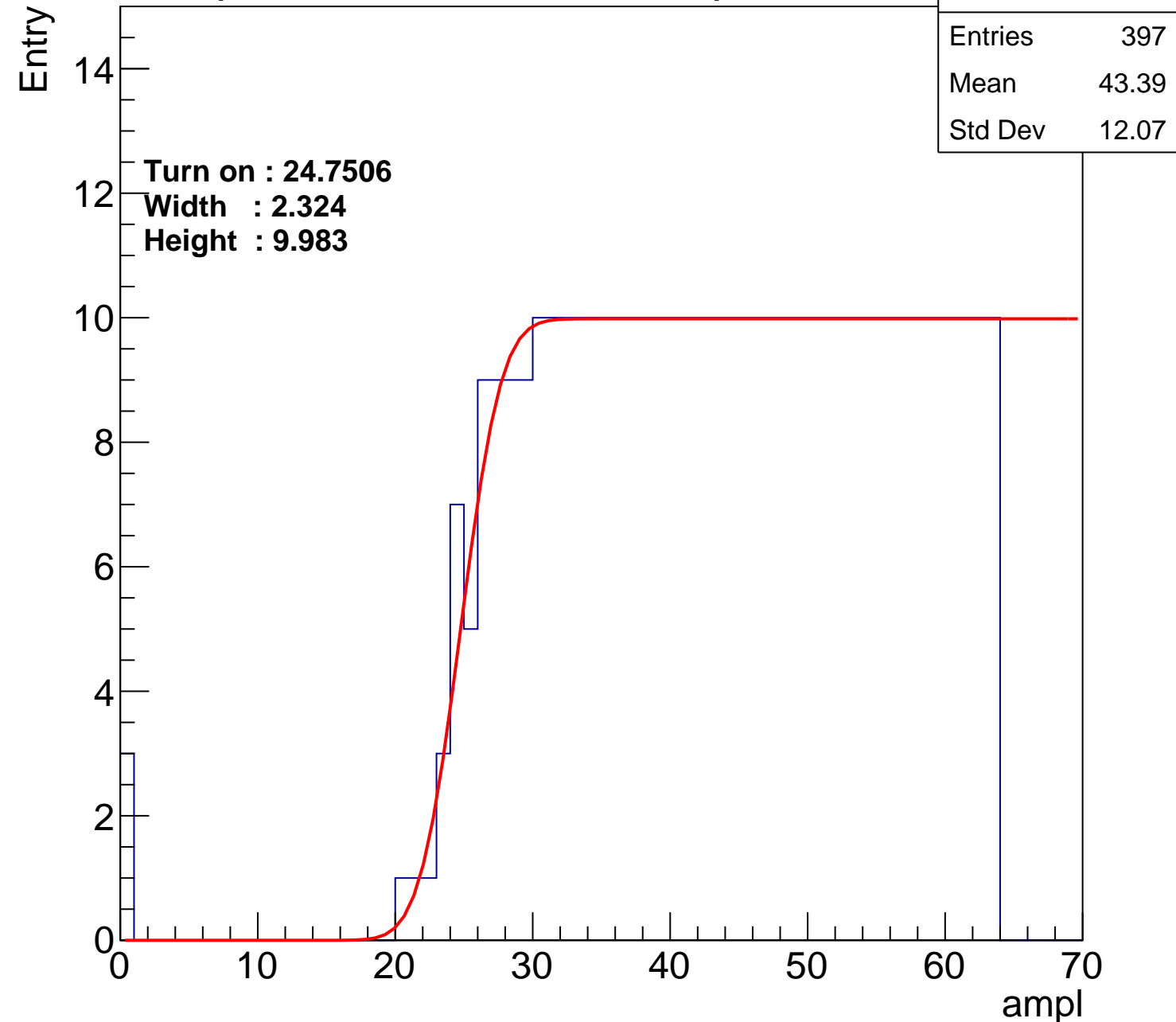
Width : 2.324

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch125

calib_packv5_042523_0143.root, FC#10, port B3

Entries	391
Mean	43.64
Std Dev	12.06

Turn on : 25.2847

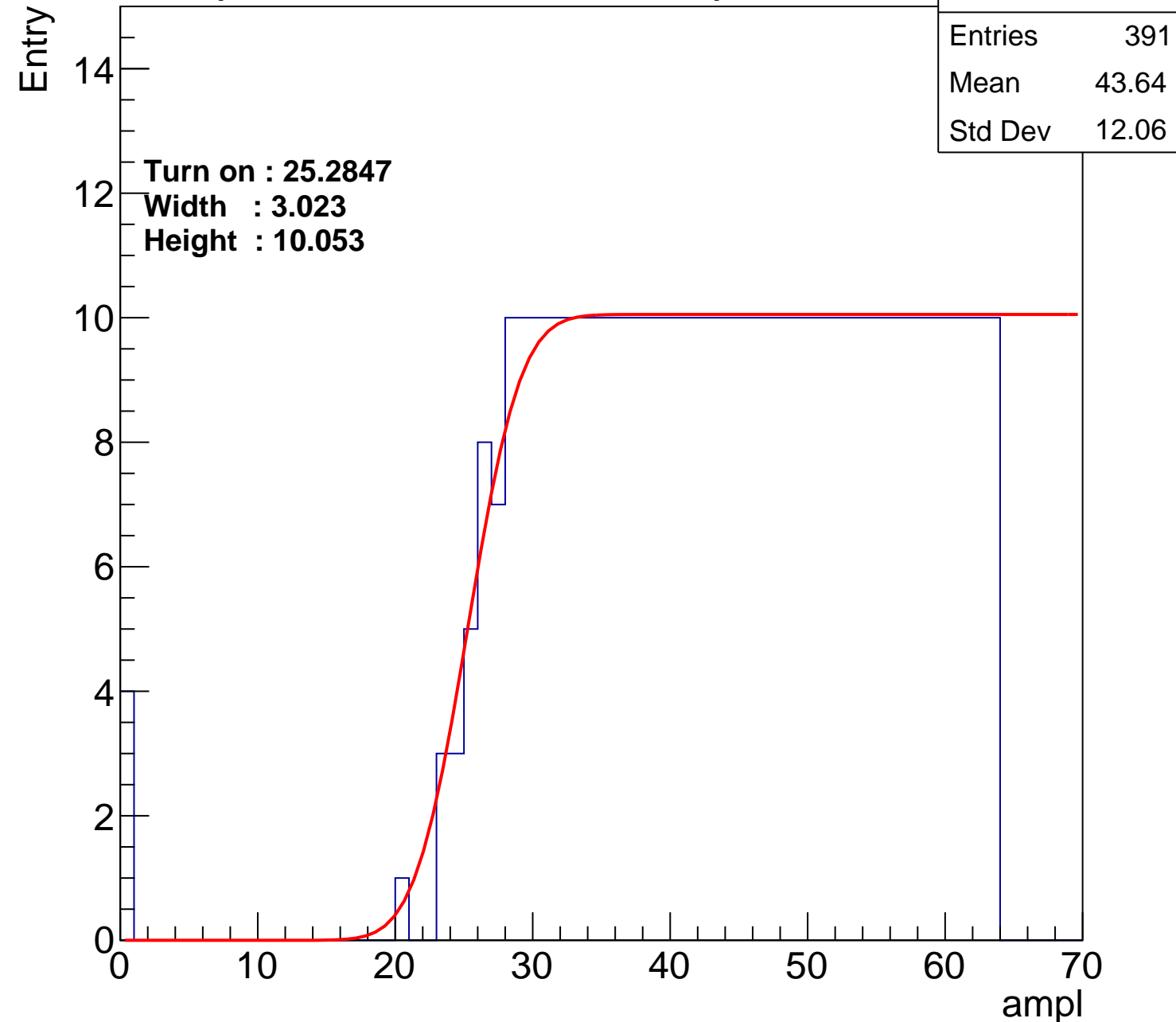
Width : 3.023

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch126

calib_packv5_042523_0143.root, FC#10, port B3

Entries	390
Mean	43.8
Std Dev	11.73

Turn on : 25.2804

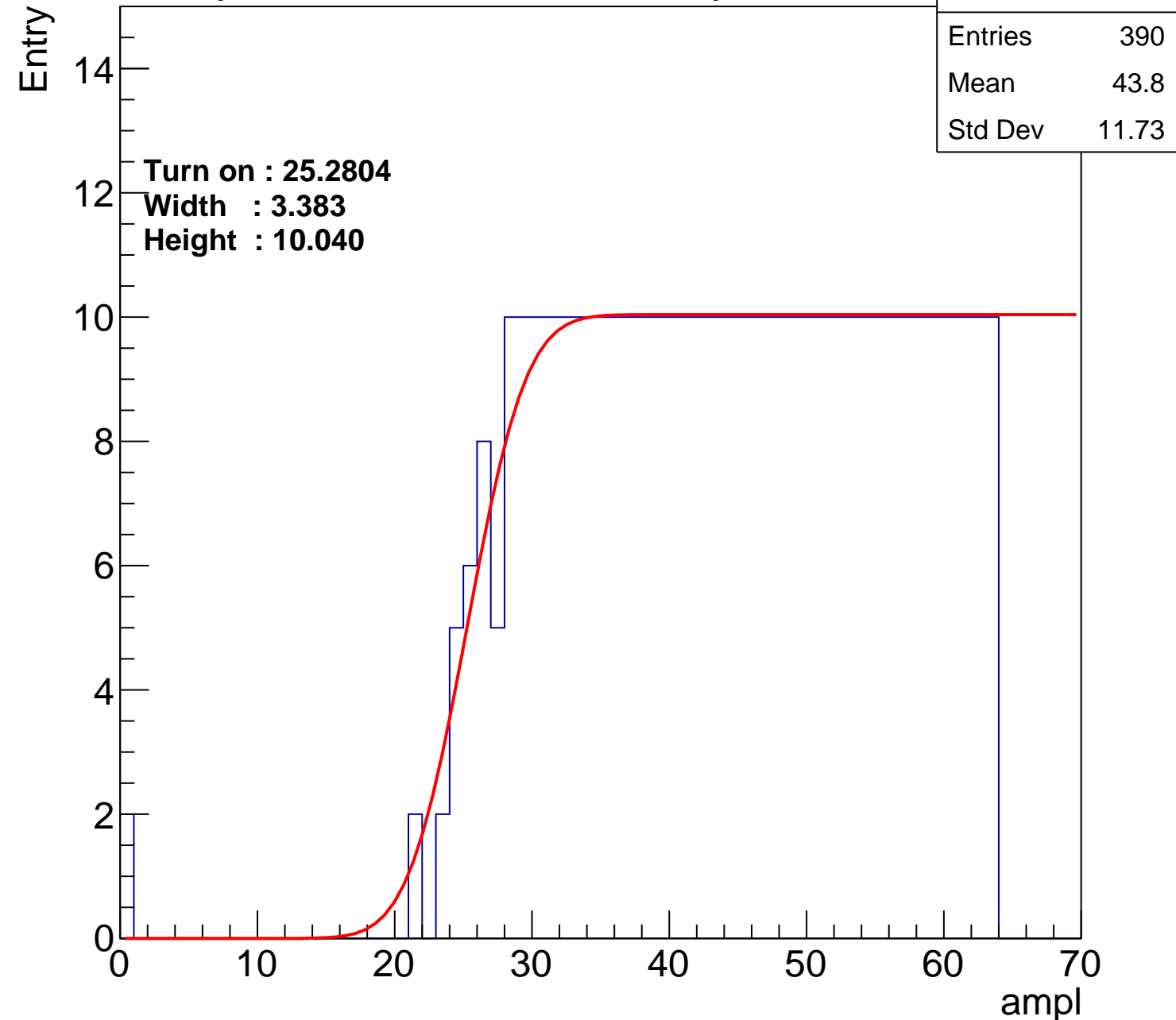
Width : 3.383

Height : 10.040

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.64
Std Dev	11.71

Turn on : 24.1944

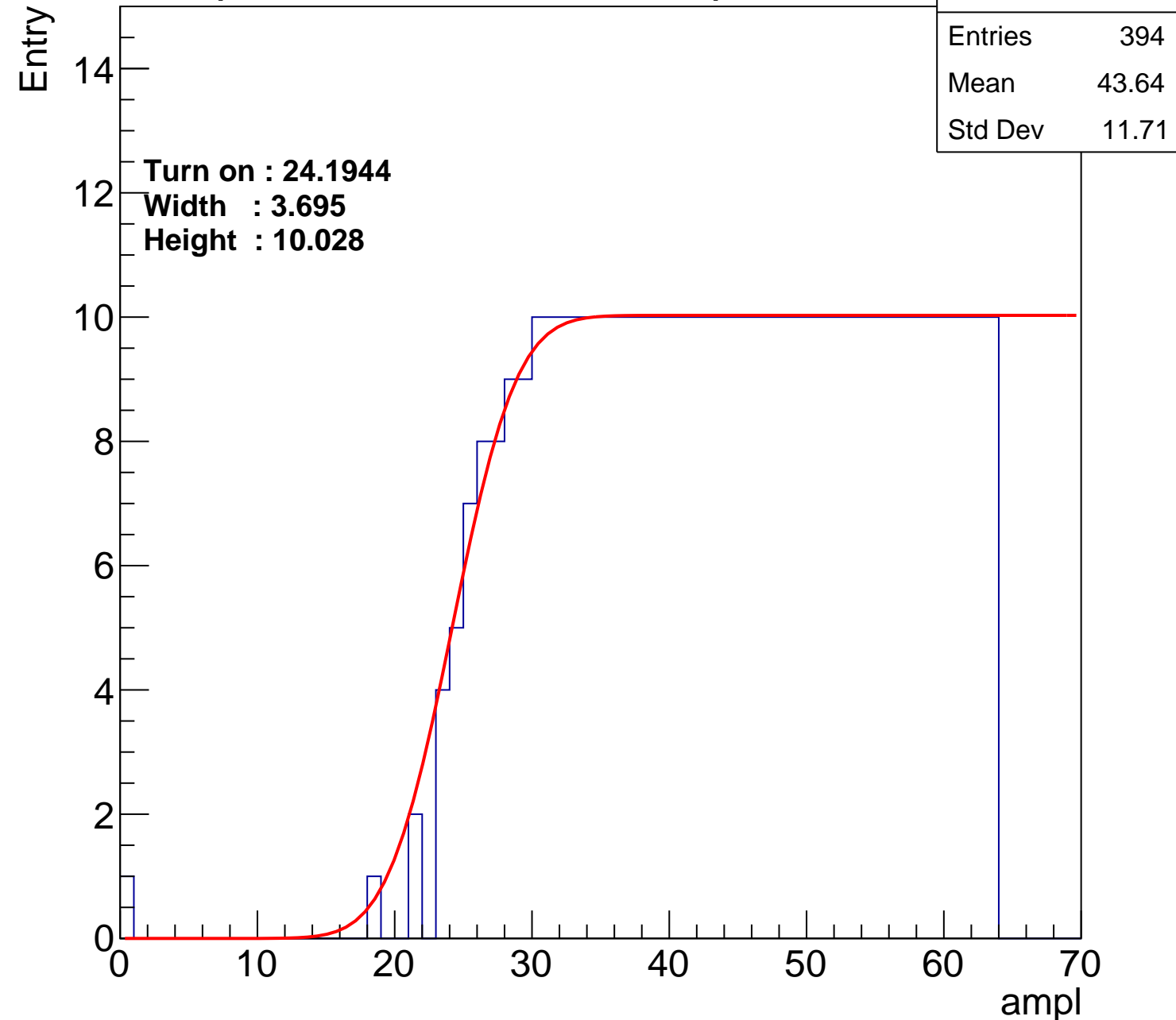
Width : 3.695

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B1L002S, U20-ch127

calib_packv5_042523_0143.root, FC#10, port B3

Entries	394
Mean	43.64
Std Dev	11.71

Turn on : 24.1944

Width : 3.695

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl

