

B0L002S, U9-ch0

calib_packv5_042523_0143.root, FC#8, port C1

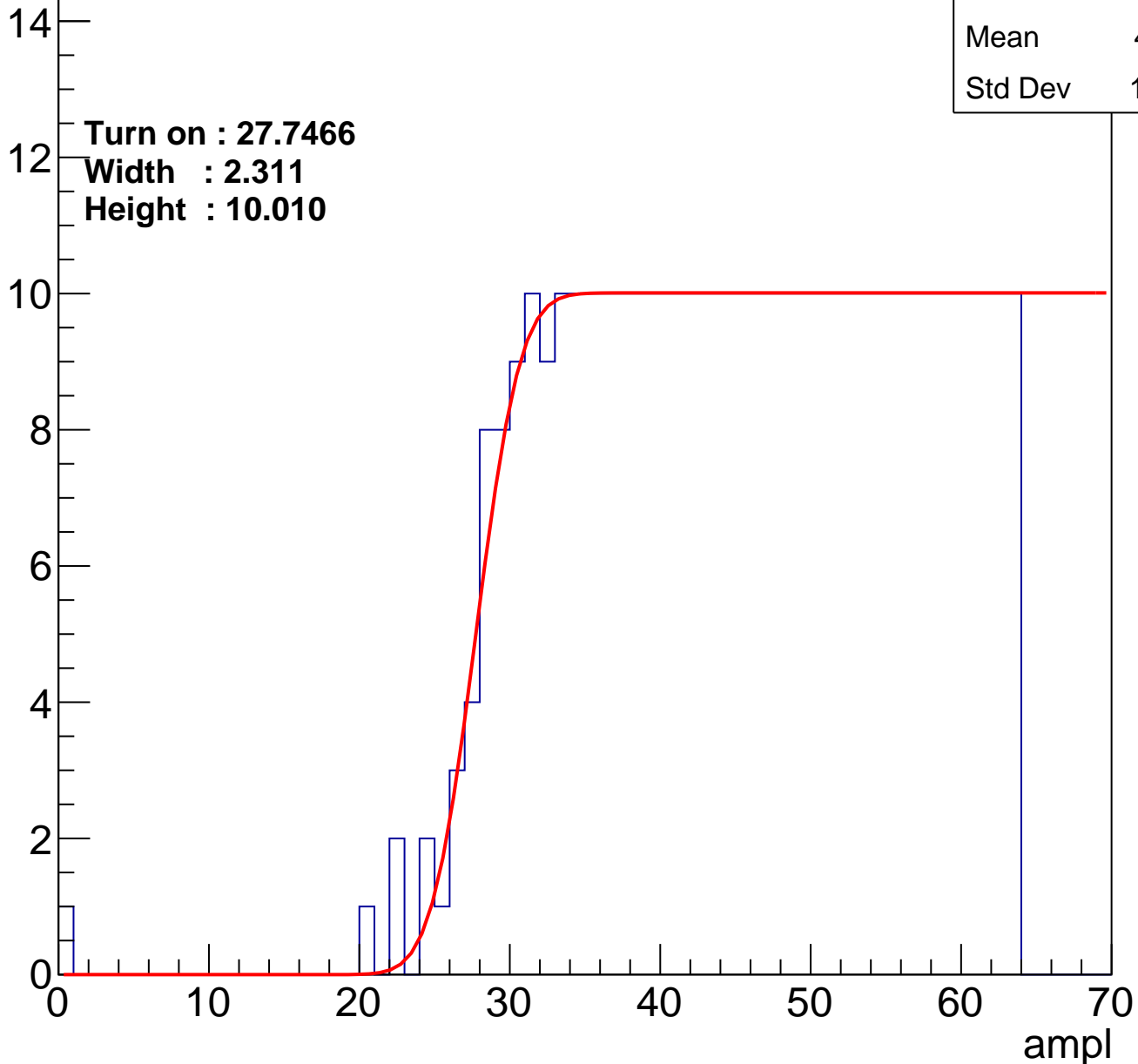
Entries	368
Mean	44.91
Std Dev	11.04

Turn on : 27.7466

Width : 2.311

Height : 10.010

Entry



B0L002S, U9-ch1

calib_packv5_042523_0143.root, FC#8, port C1

Entries	355
Mean	45.55
Std Dev	10.79

Turn on : 28.5988

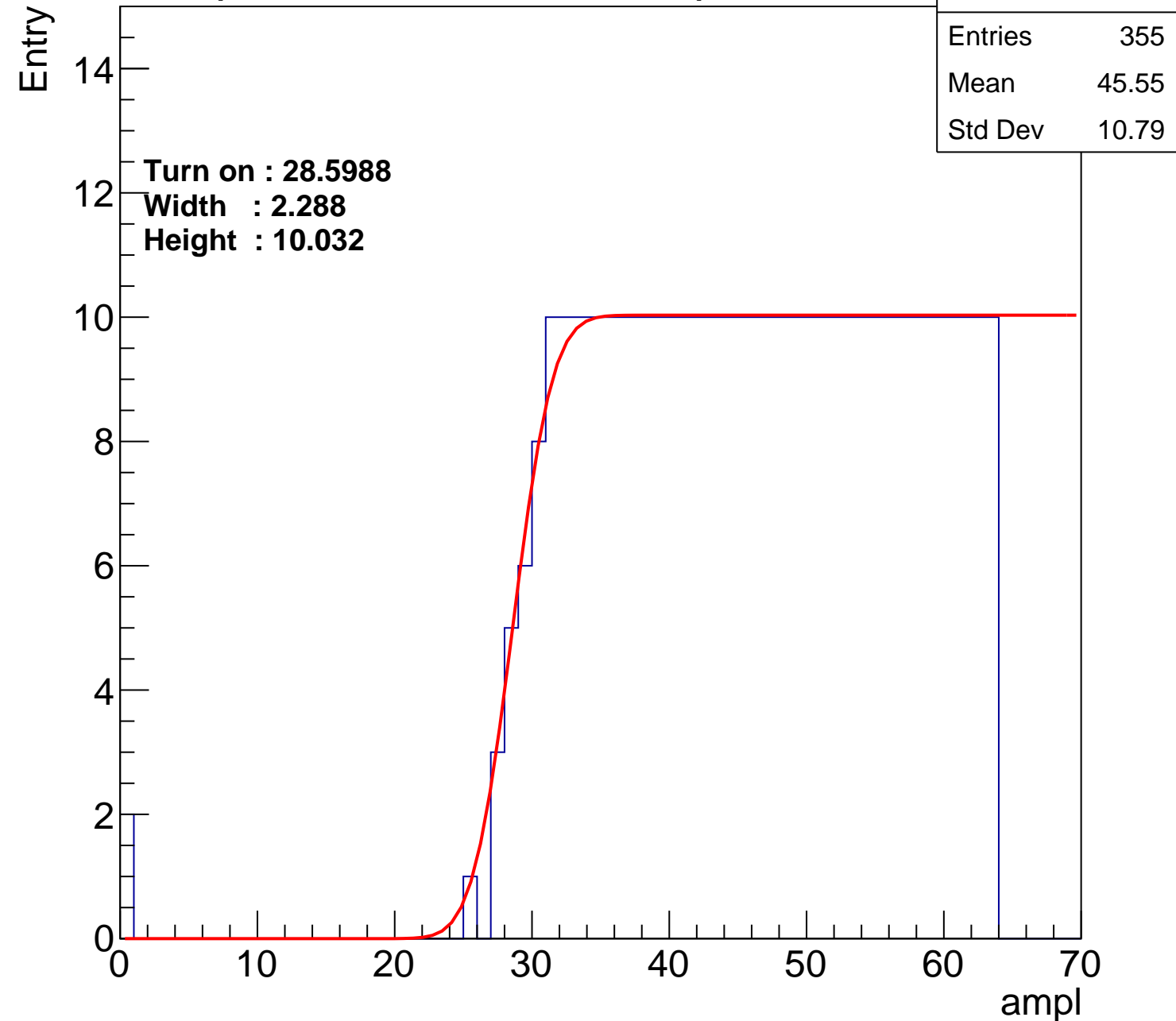
Width : 2.288

Height : 10.032

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch2

calib_packv5_042523_0143.root, FC#8, port C1

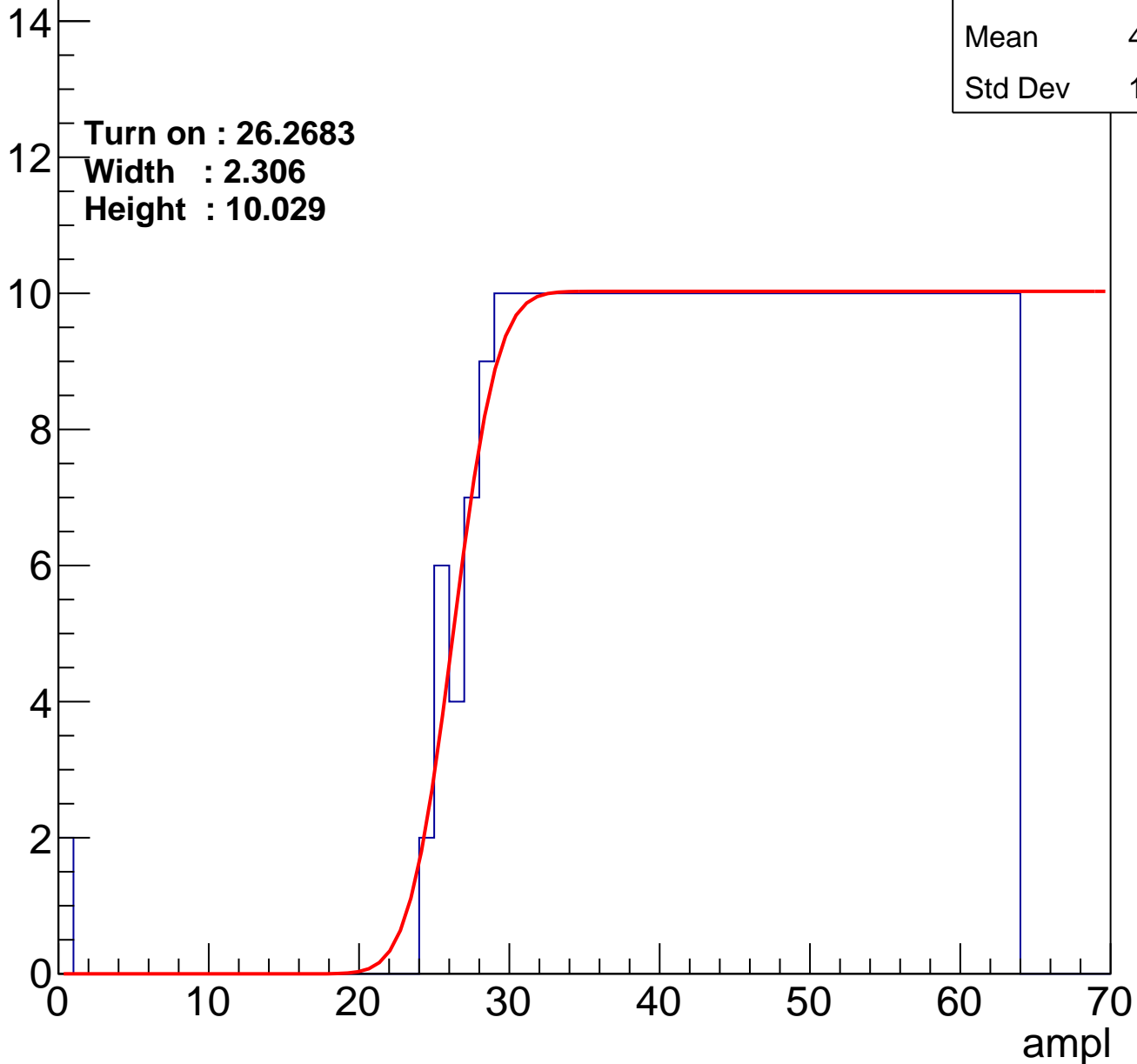
Entries	380
Mean	44.32
Std Dev	11.42

Turn on : 26.2683

Width : 2.306

Height : 10.029

Entry



B0L002S, U9-ch3

calib_packv5_042523_0143.root, FC#8, port C1

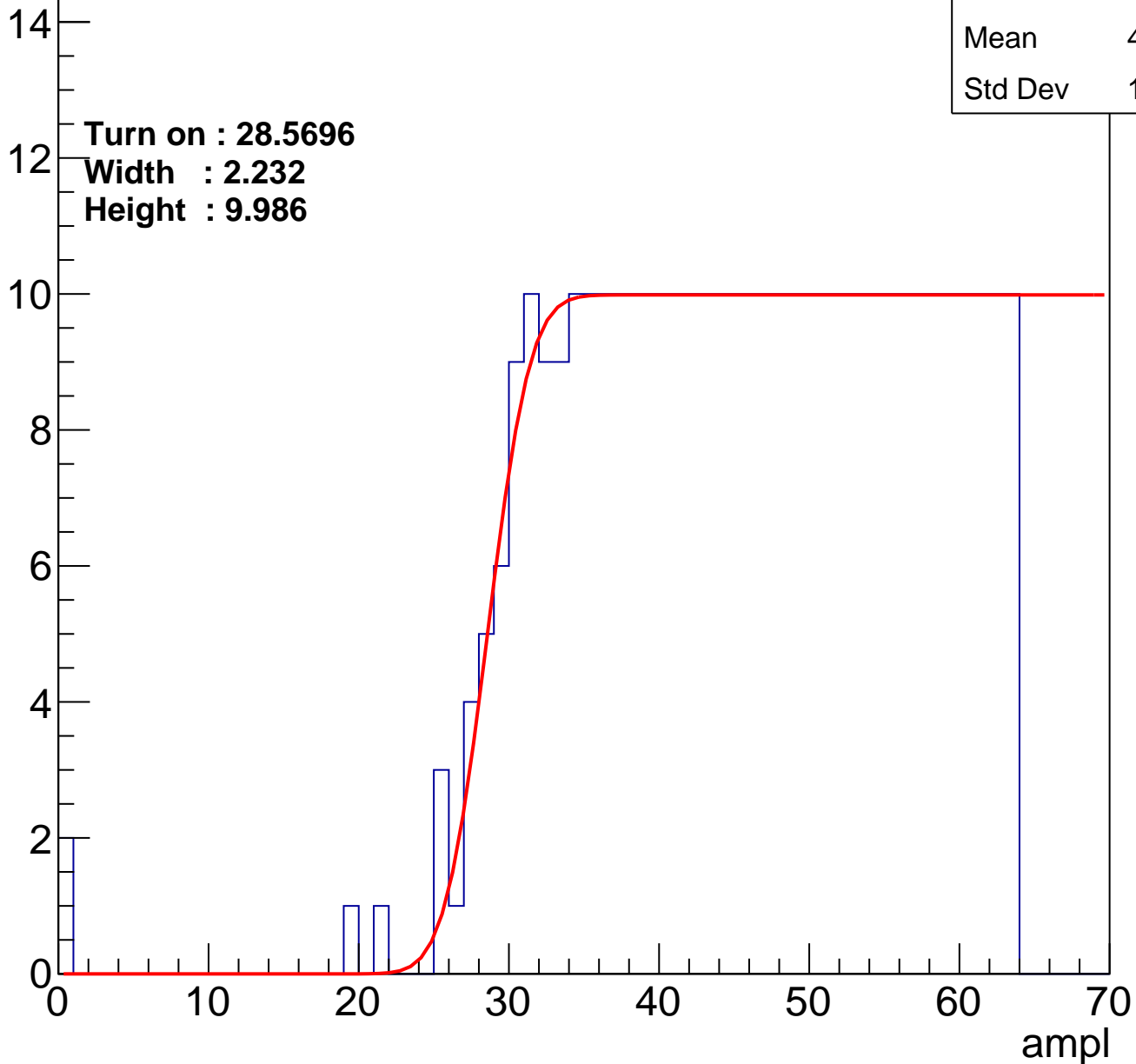
Entry

Entries	360
Mean	45.22
Std Dev	11.06

Turn on : 28.5696

Width : 2.232

Height : 9.986



B0L002S, U9-ch4

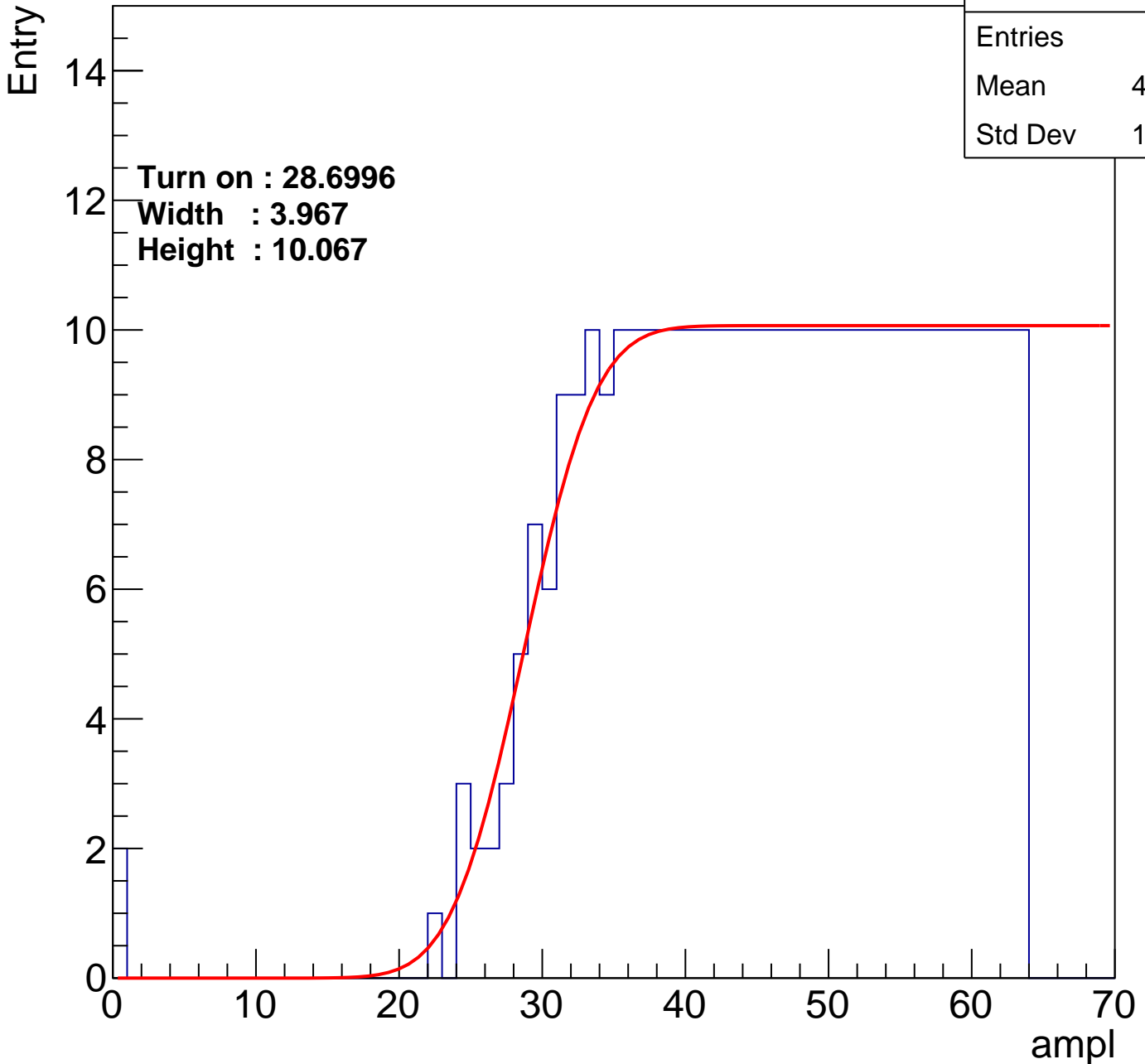
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.29
Std Dev	11.05

Turn on : 28.6996

Width : 3.967

Height : 10.067



B0L002S, U9-ch5

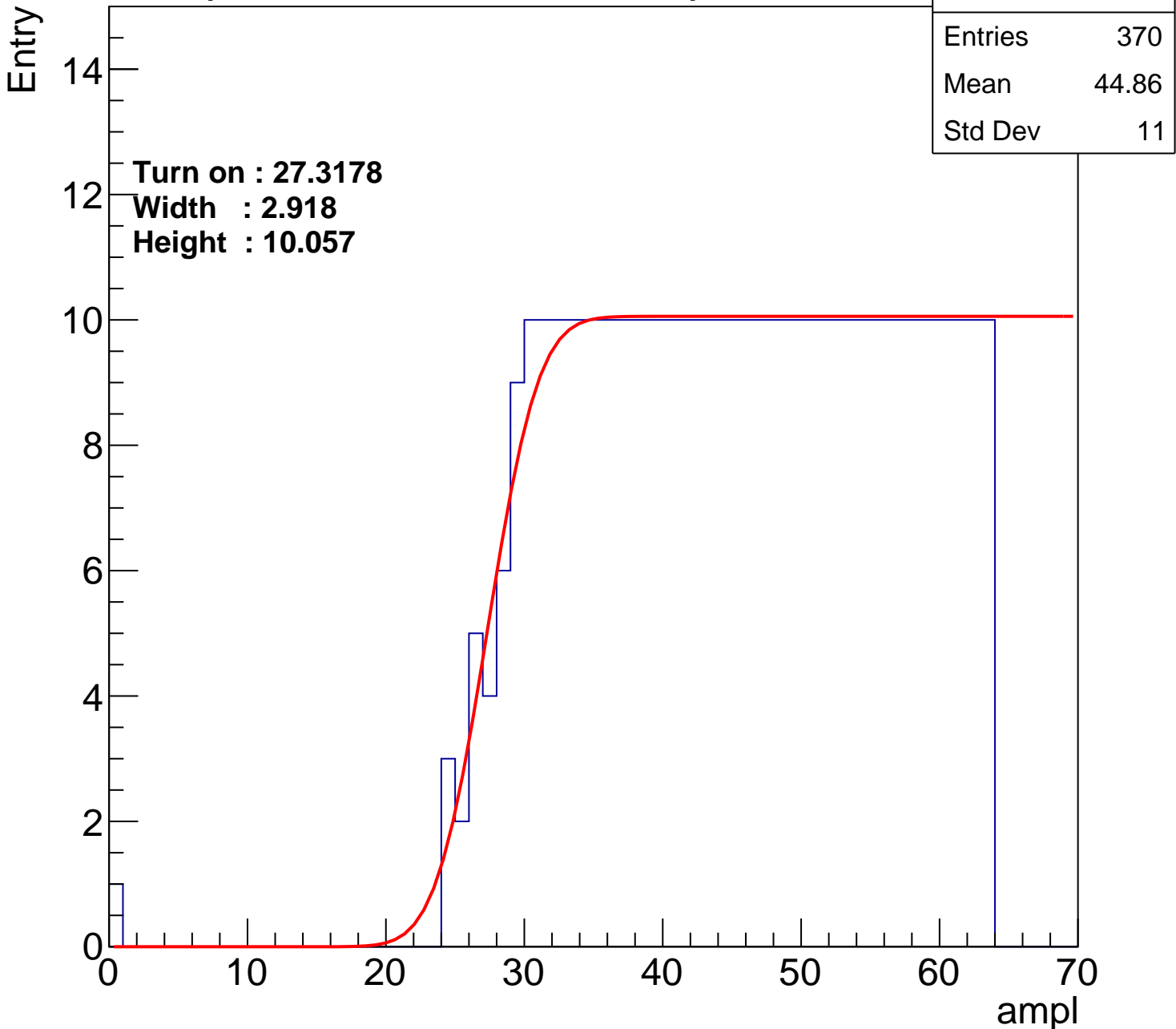
calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.86
Std Dev	11

Turn on : 27.3178

Width : 2.918

Height : 10.057



B0L002S, U9-ch6

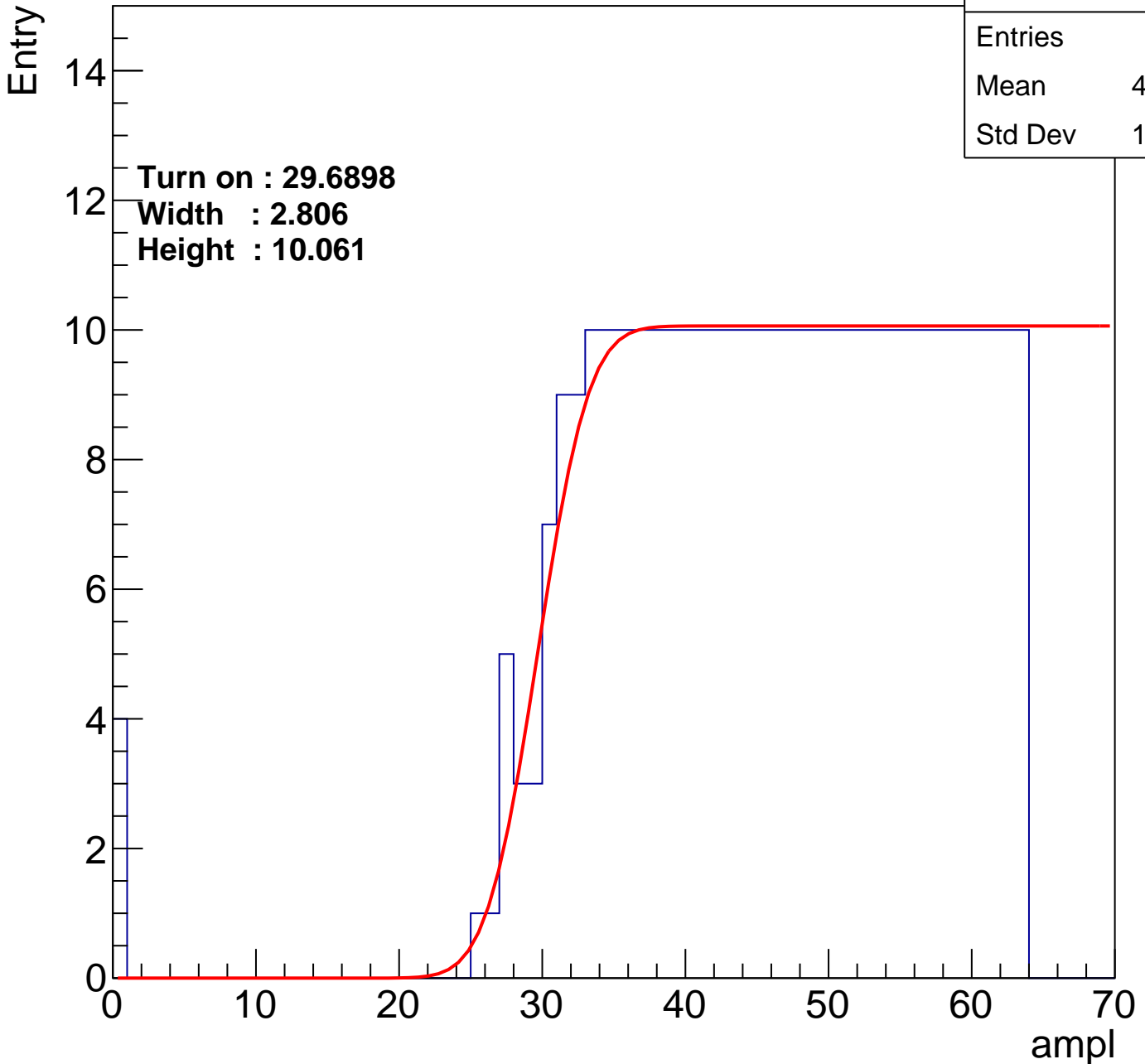
calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.49
Std Dev	11.24

Turn on : 29.6898

Width : 2.806

Height : 10.061



B0L002S, U9-ch7

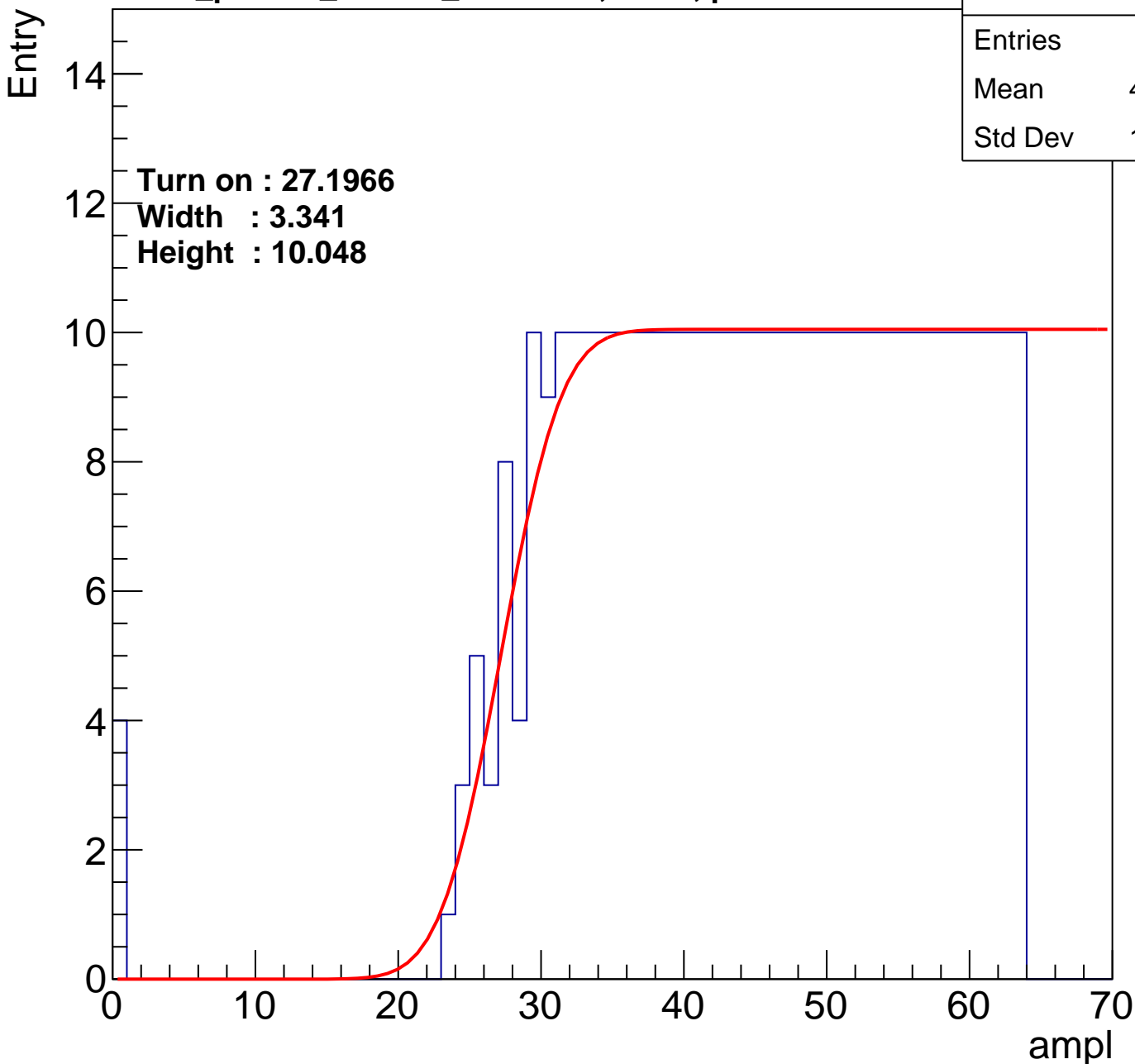
calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.29
Std Dev	11.79

Turn on : 27.1966

Width : 3.341

Height : 10.048



B0L002S, U9-ch8

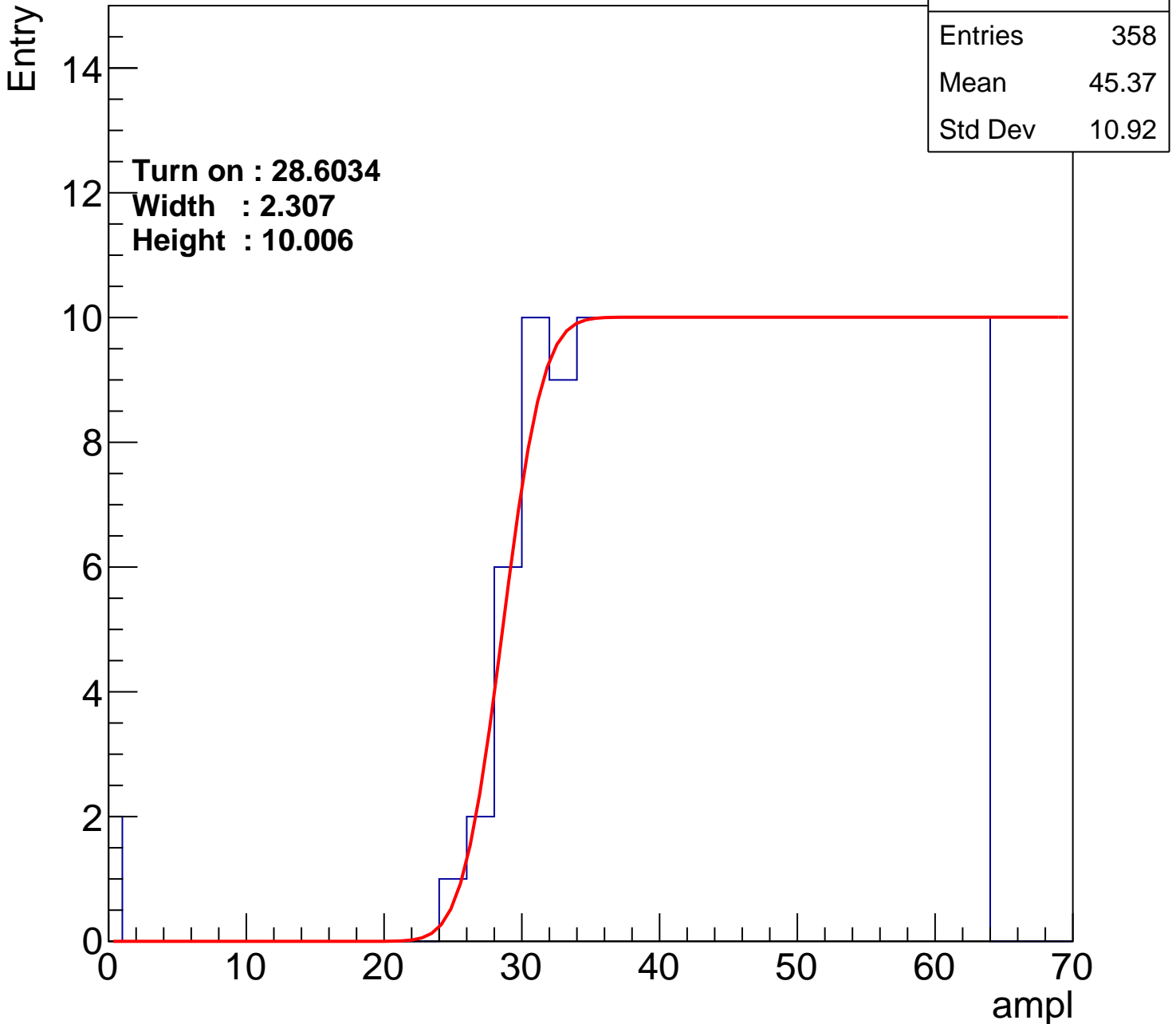
calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.37
Std Dev	10.92

Turn on : 28.6034

Width : 2.307

Height : 10.006



B0L002S, U9-ch9

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.68
Std Dev	11.78

Turn on : 27.7747

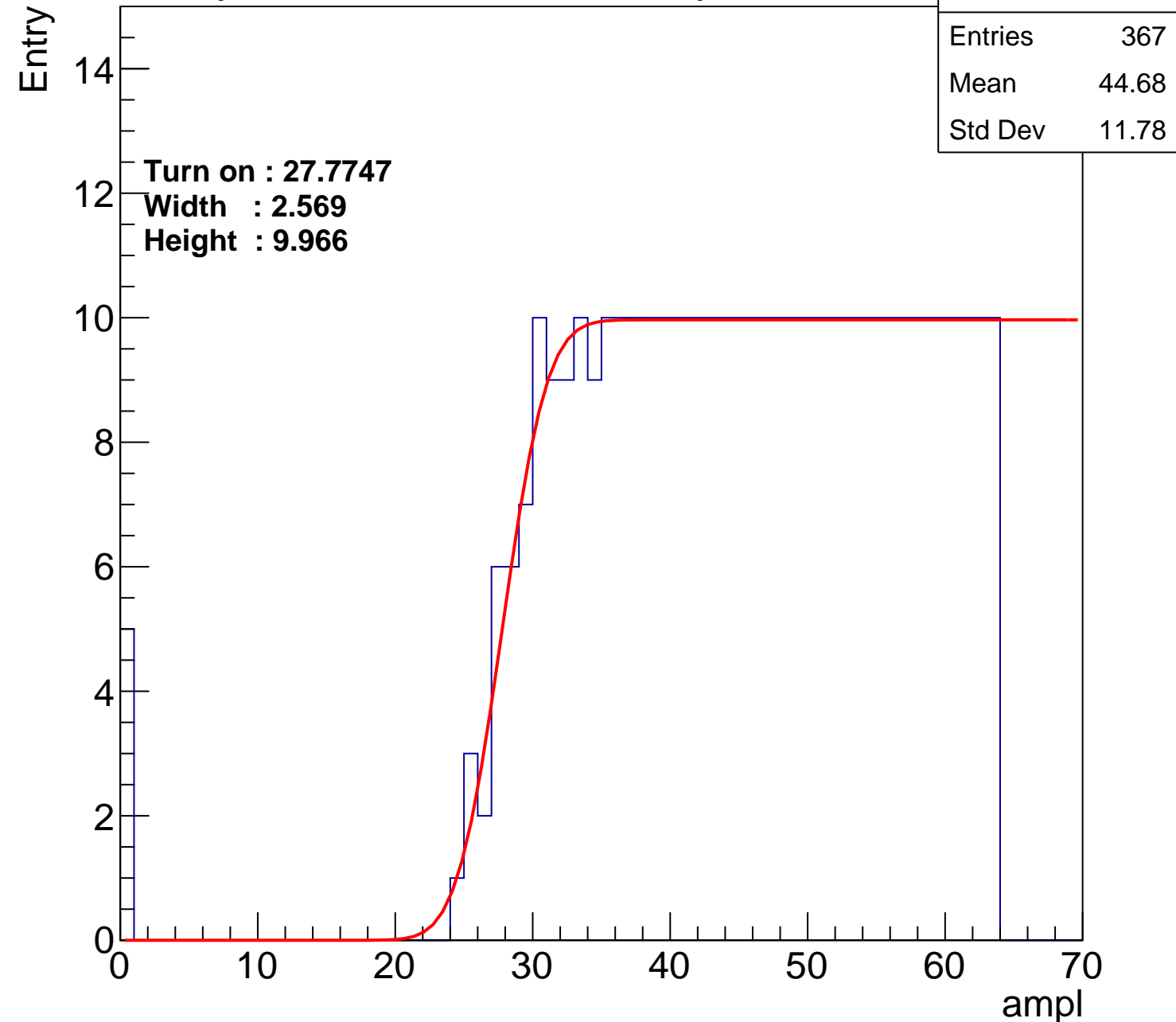
Width : 2.569

Height : 9.966

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch10

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.65
Std Dev	11.69

Turn on : 28.3257

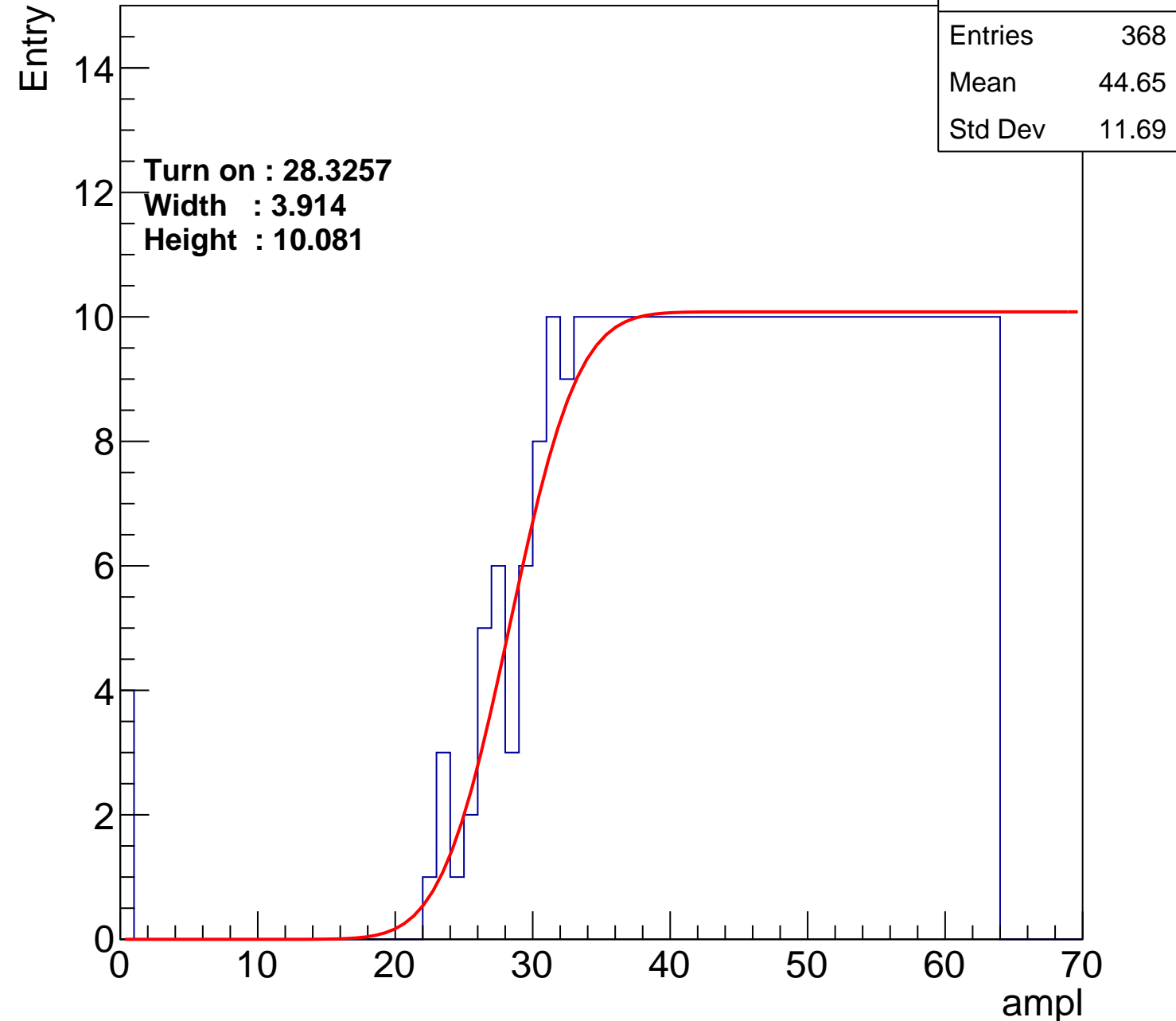
Width : 3.914

Height : 10.081

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch11

calib_packv5_042523_0143.root, FC#8, port C1

Entries	384
Mean	44.01
Std Dev	11.77

Turn on : 25.8337

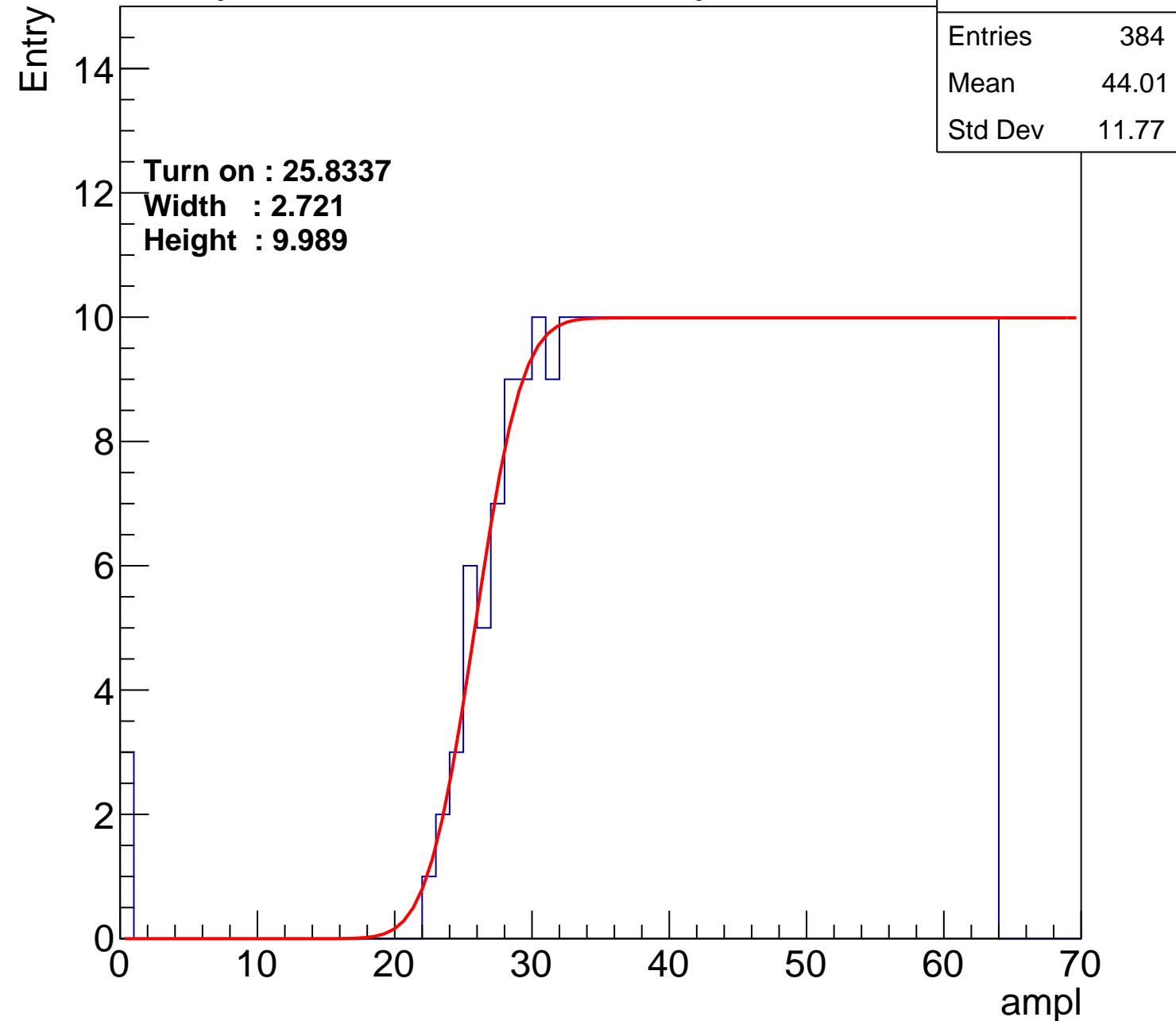
Width : 2.721

Height : 9.989

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch12

calib_packv5_042523_0143.root, FC#8, port C1

Entries	363
Mean	45.17
Std Dev	10.87

Turn on : 28.3561

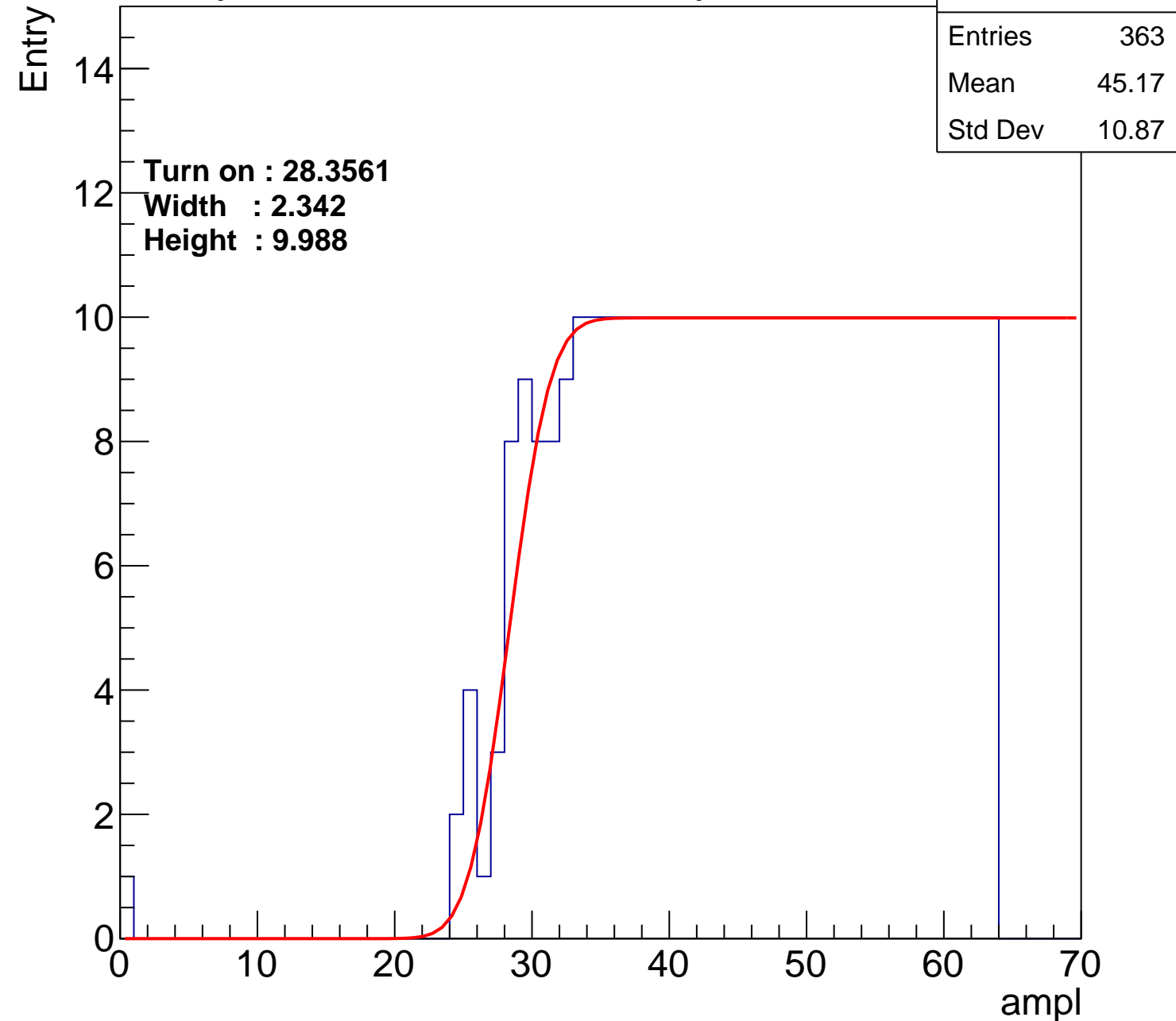
Width : 2.342

Height : 9.988

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch13

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.41
Std Dev	10.91

Turn on : 28.9858

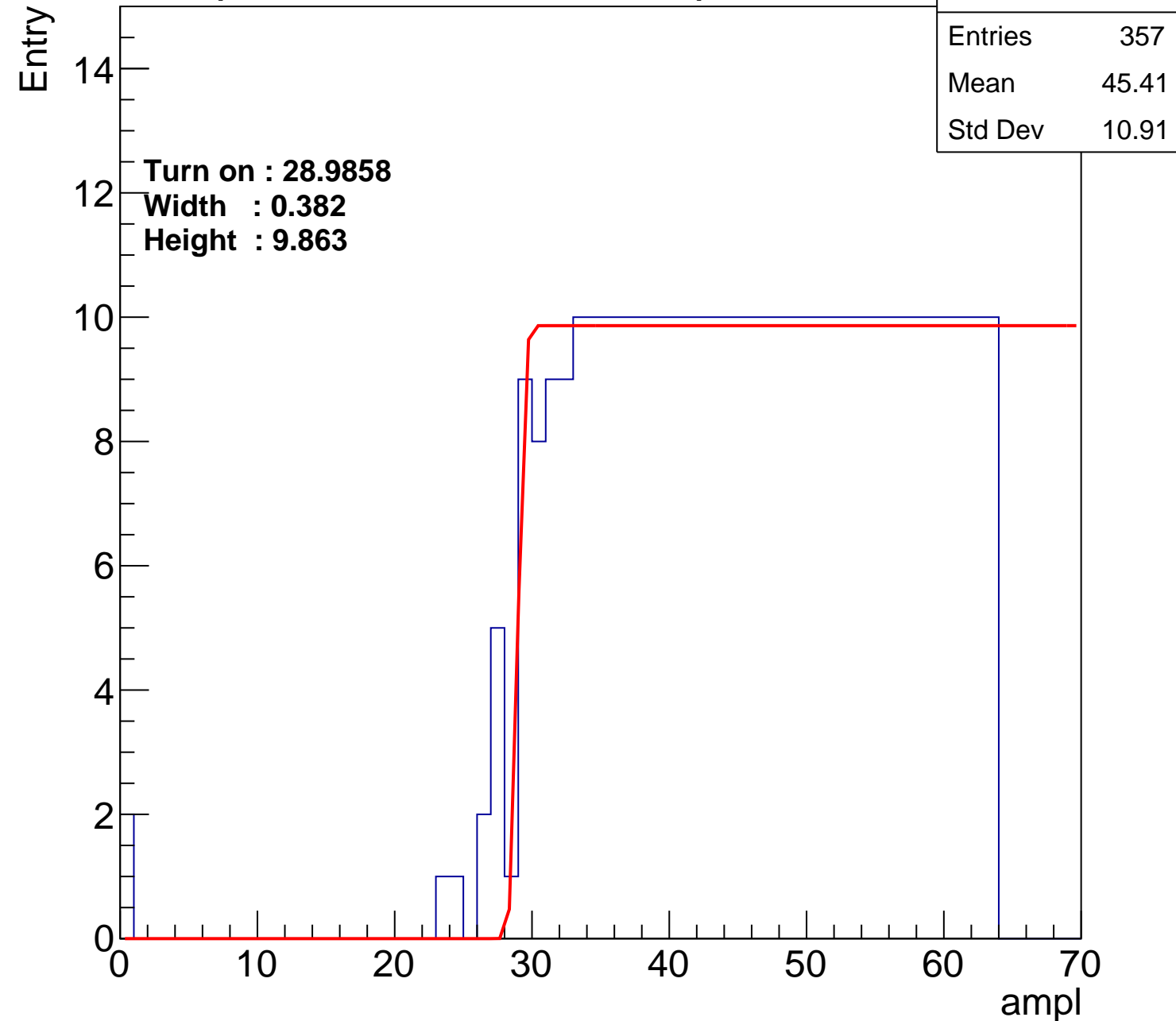
Width : 0.382

Height : 9.863

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch14

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.11
Std Dev	10.91

Turn on : 27.8900

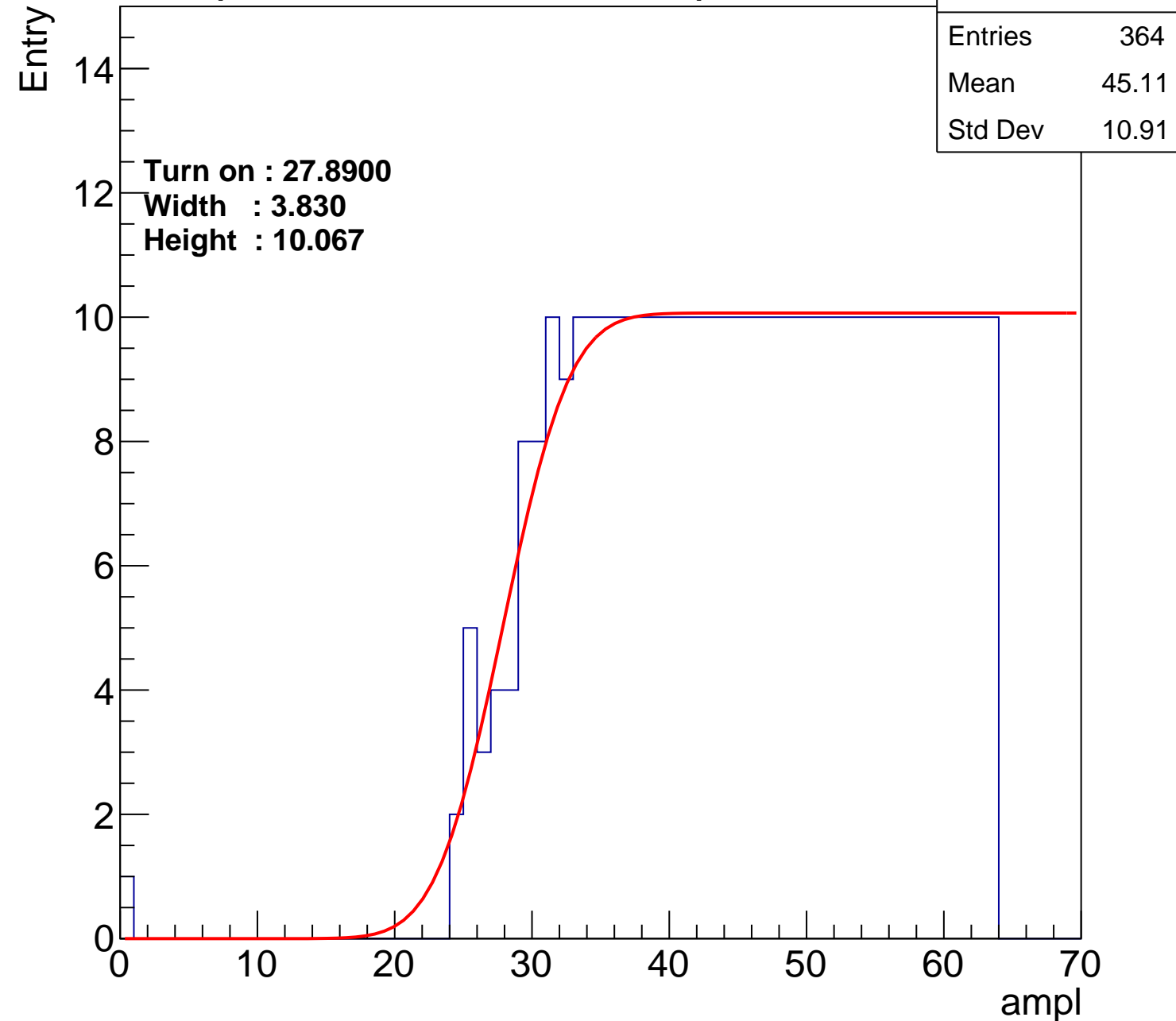
Width : 3.830

Height : 10.067

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch15

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.83
Std Dev	11.19

Turn on : 27.3115

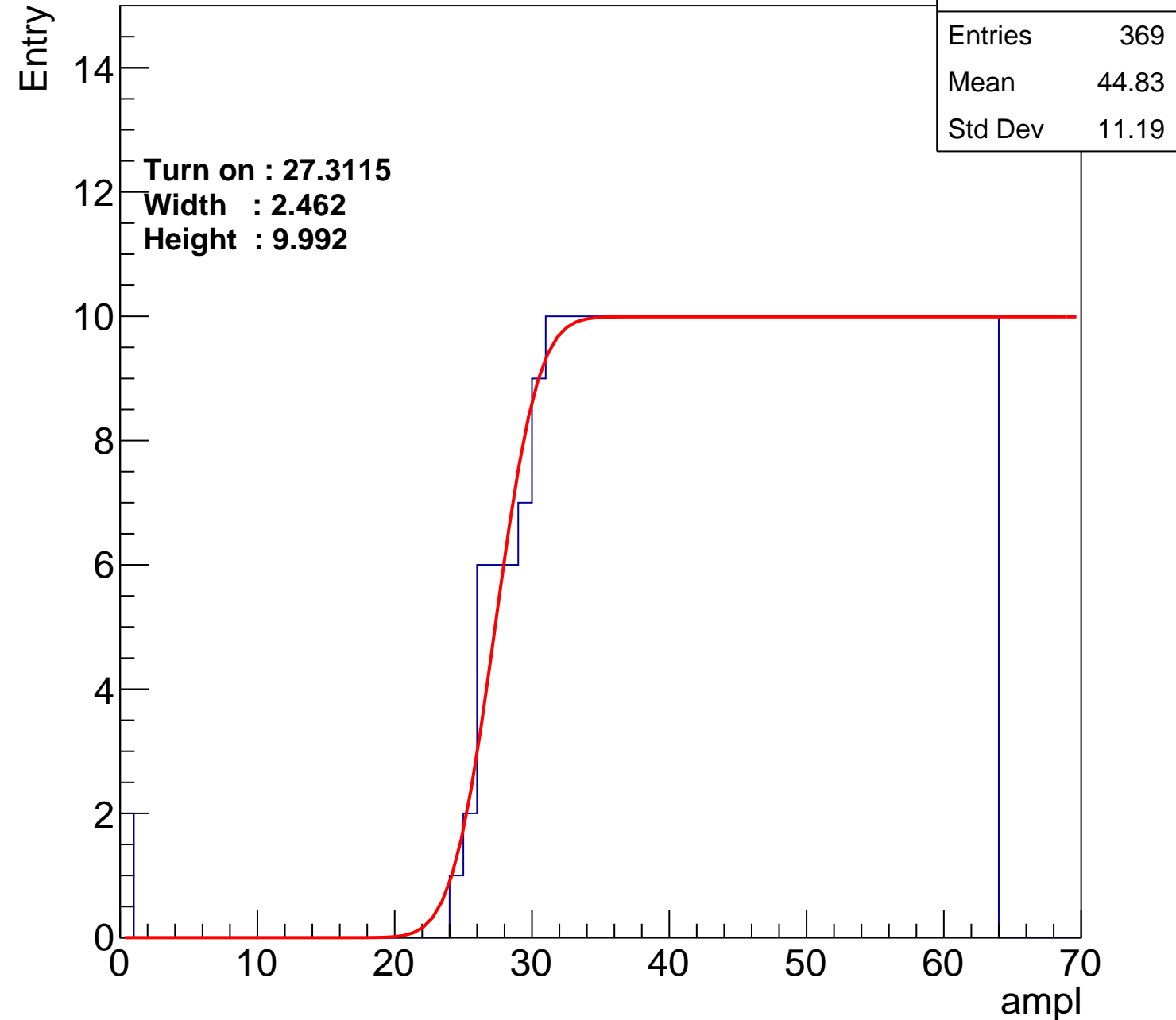
Width : 2.462

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch16

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	45.01
Std Dev	10.91

Turn on : 27.5093

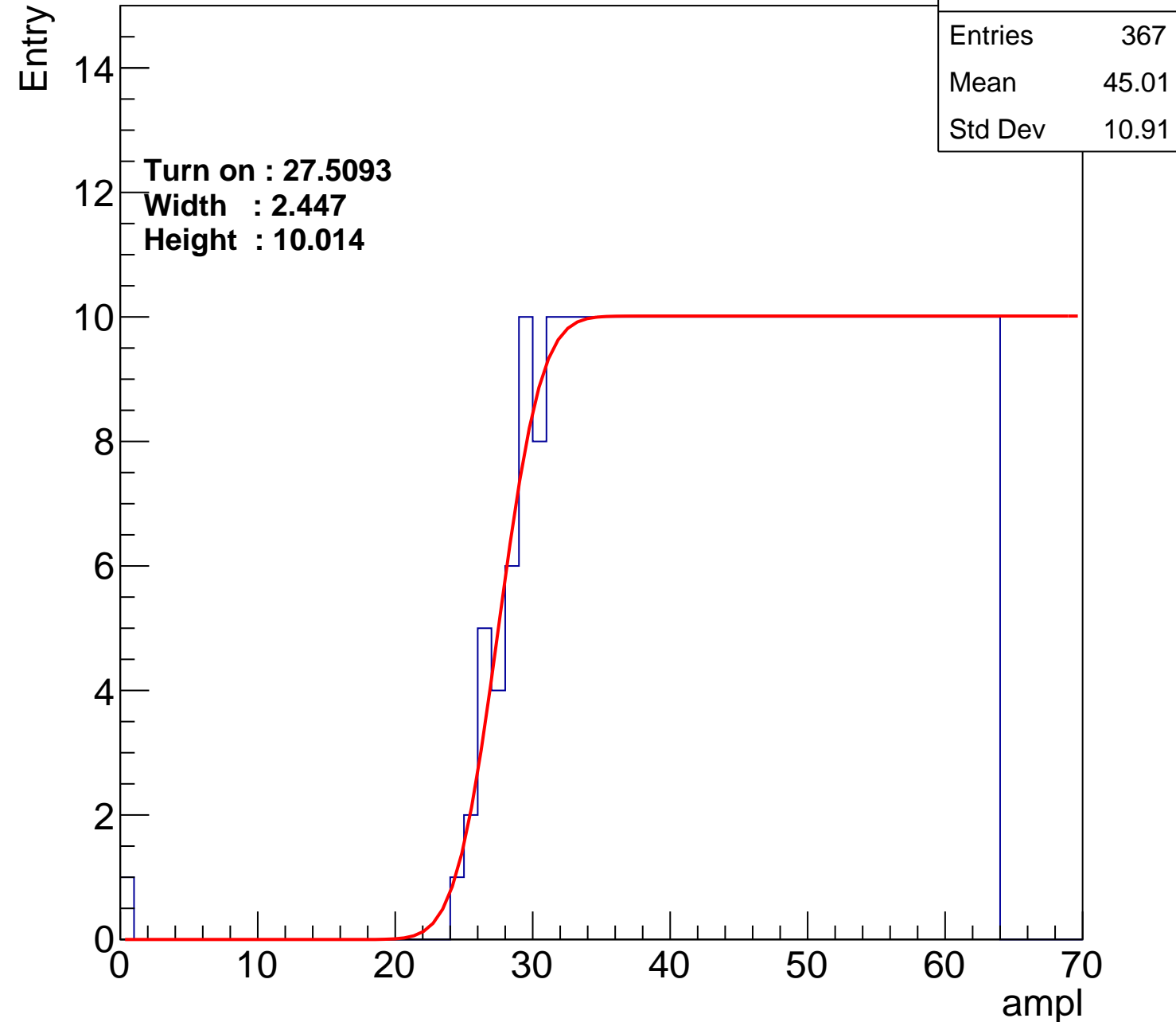
Width : 2.447

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch17

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.21
Std Dev	11

Turn on : 28.1259

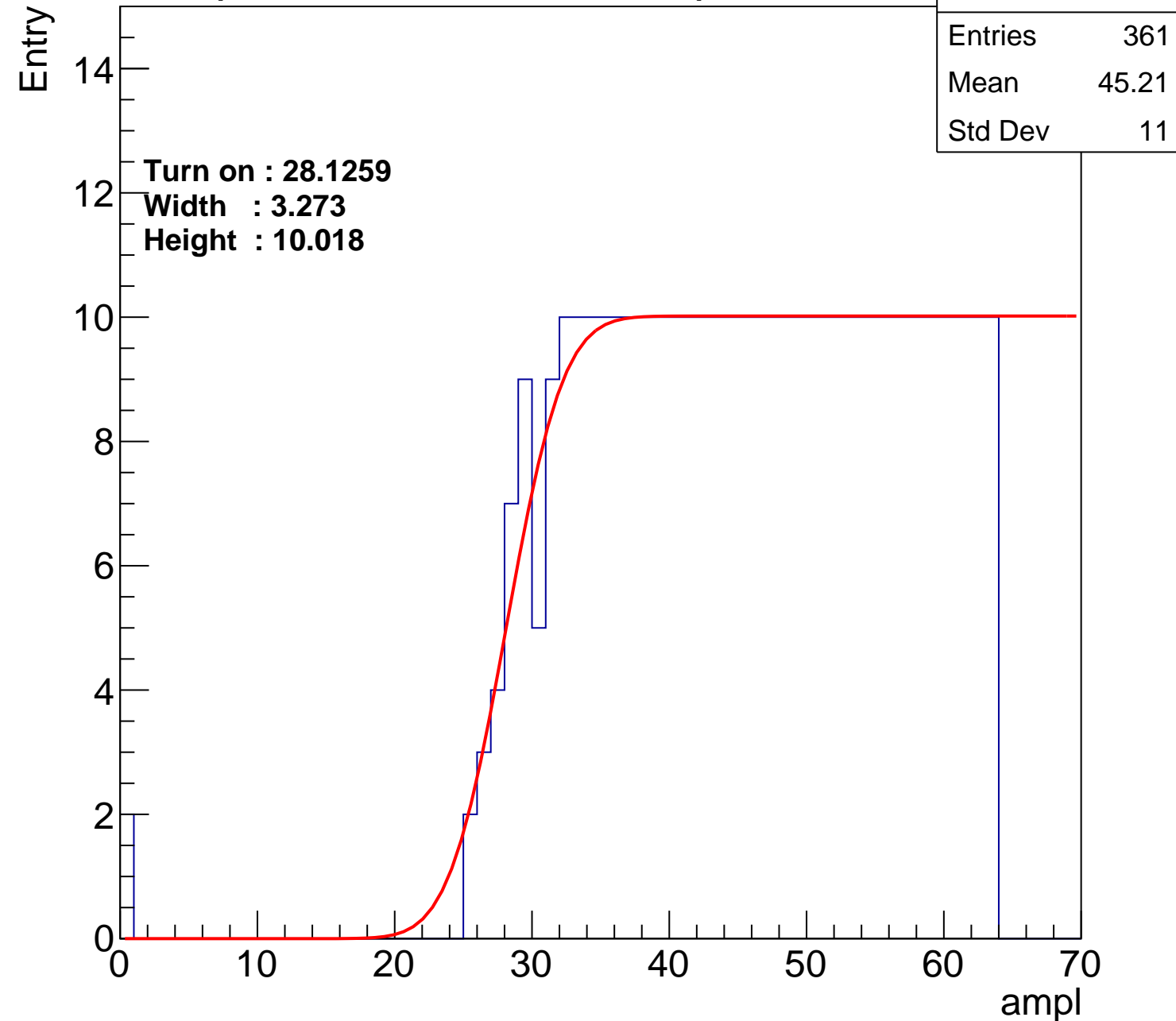
Width : 3.273

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch18

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.37
Std Dev	11.57

Turn on : 26.7895

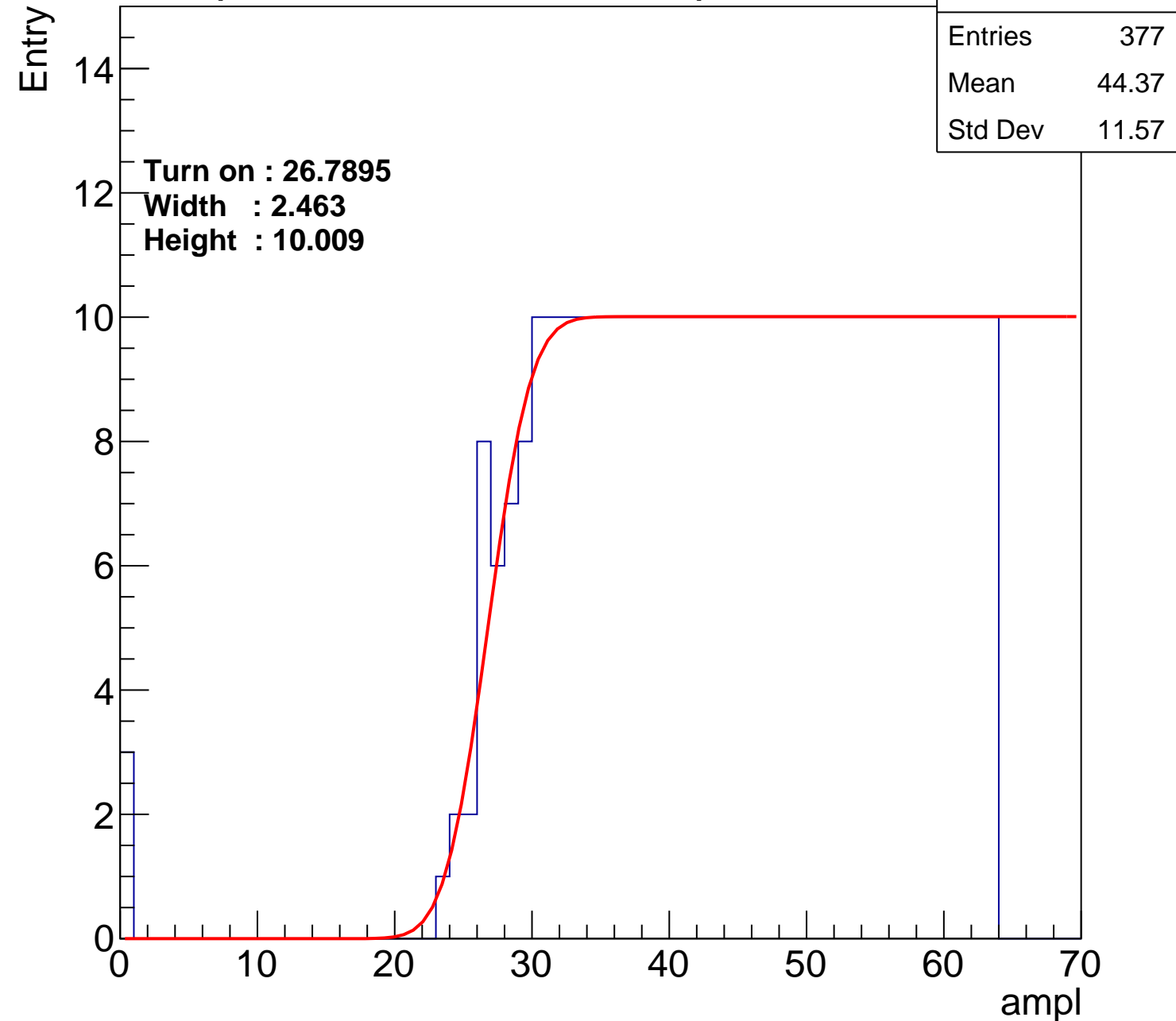
Width : 2.463

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch19

calib_packv5_042523_0143.root, FC#8, port C1

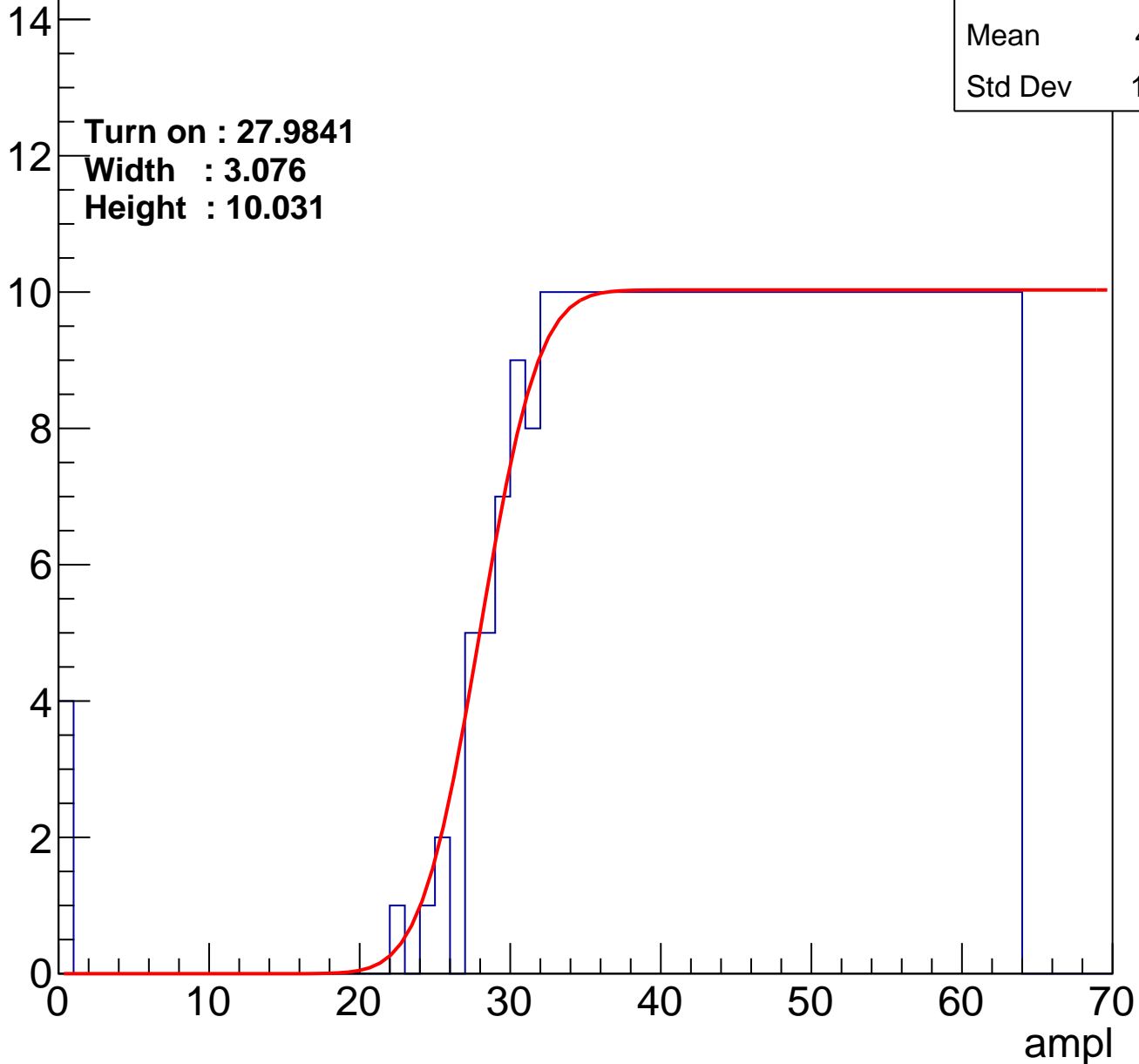
Entries	362
Mean	45.01
Std Dev	11.47

Turn on : 27.9841

Width : 3.076

Height : 10.031

Entry



B0L002S, U9-ch20

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.47
Std Dev	10.64

Turn on : 28.2062

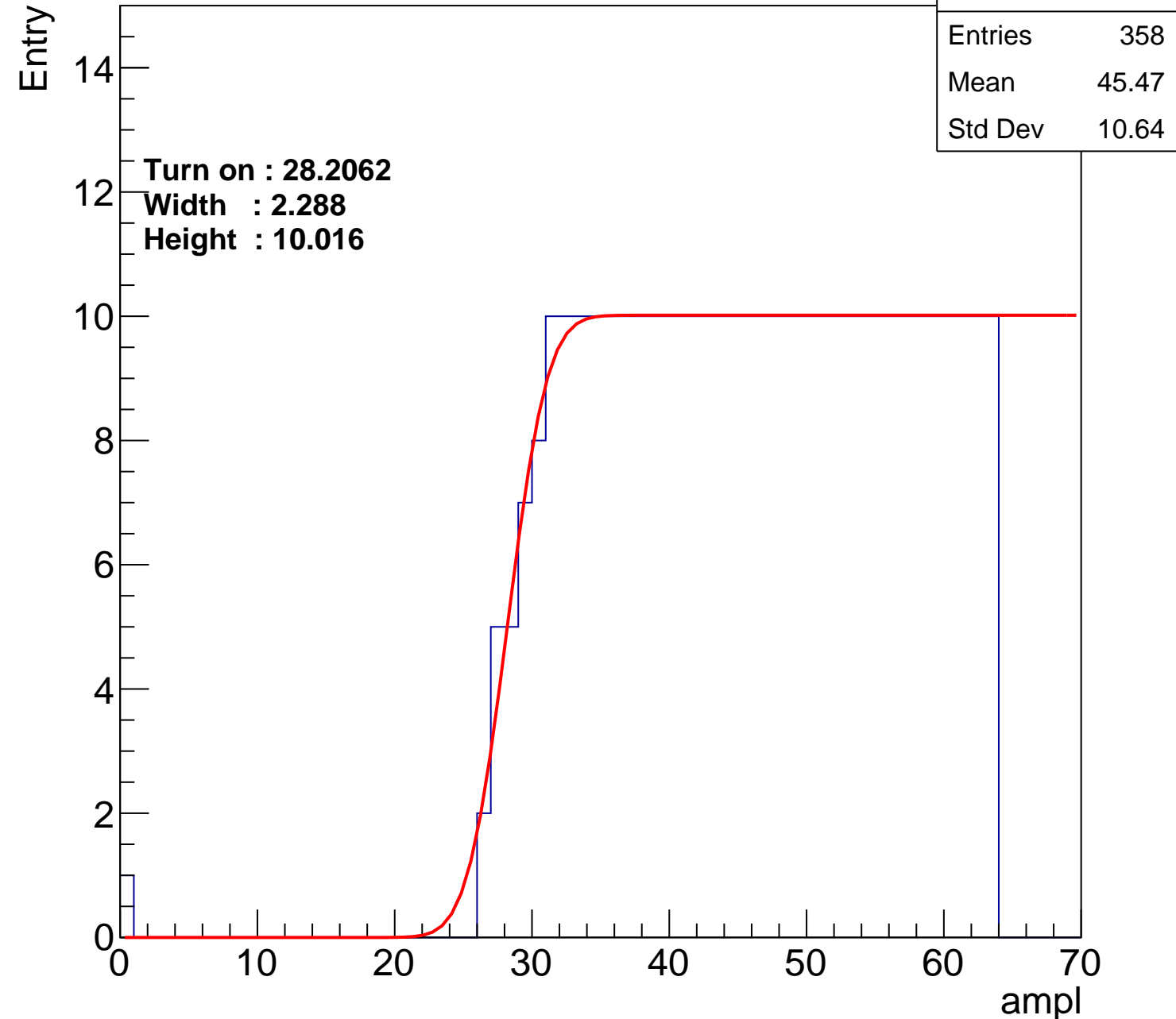
Width : 2.288

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch21

calib_packv5_042523_0143.root, FC#8, port C1

Entries	377
Mean	44.3
Std Dev	11.76

Turn on : 26.9207

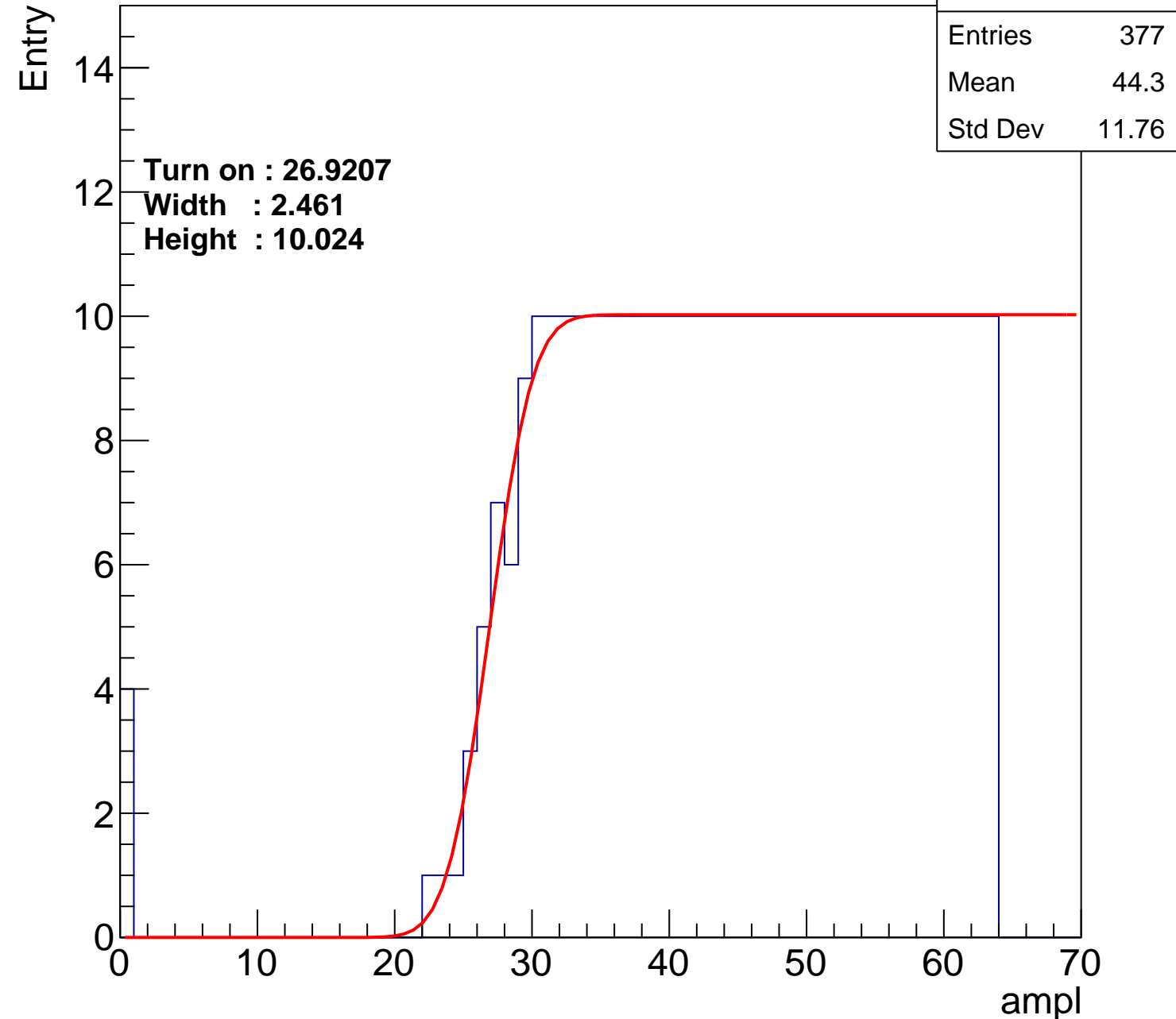
Width : 2.461

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch22

calib_packv5_042523_0143.root, FC#8, port C1

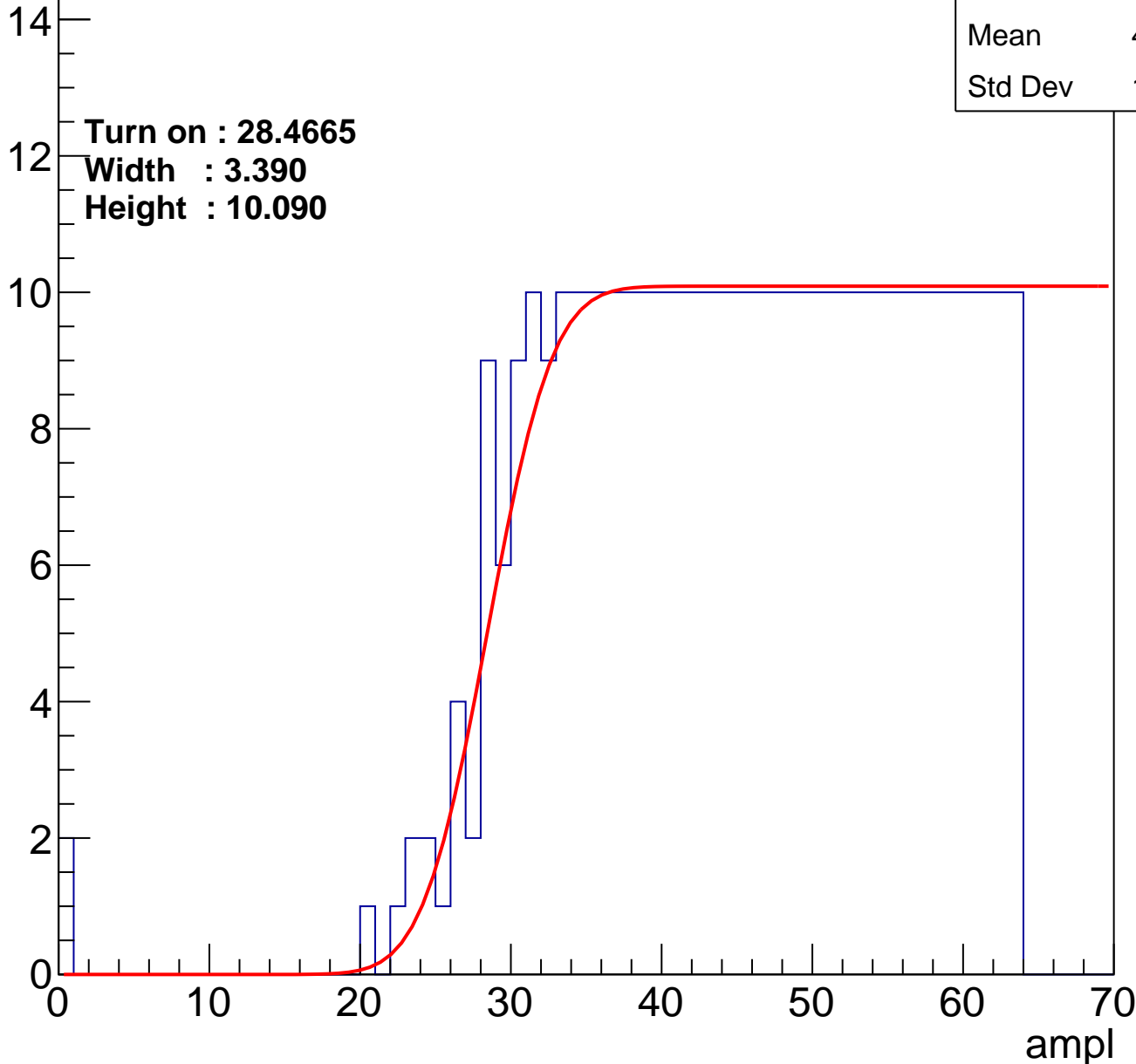
Entries	368
Mean	44.82
Std Dev	11.27

Turn on : 28.4665

Width : 3.390

Height : 10.090

Entry



B0L002S, U9-ch23

calib_packv5_042523_0143.root, FC#8, port C1

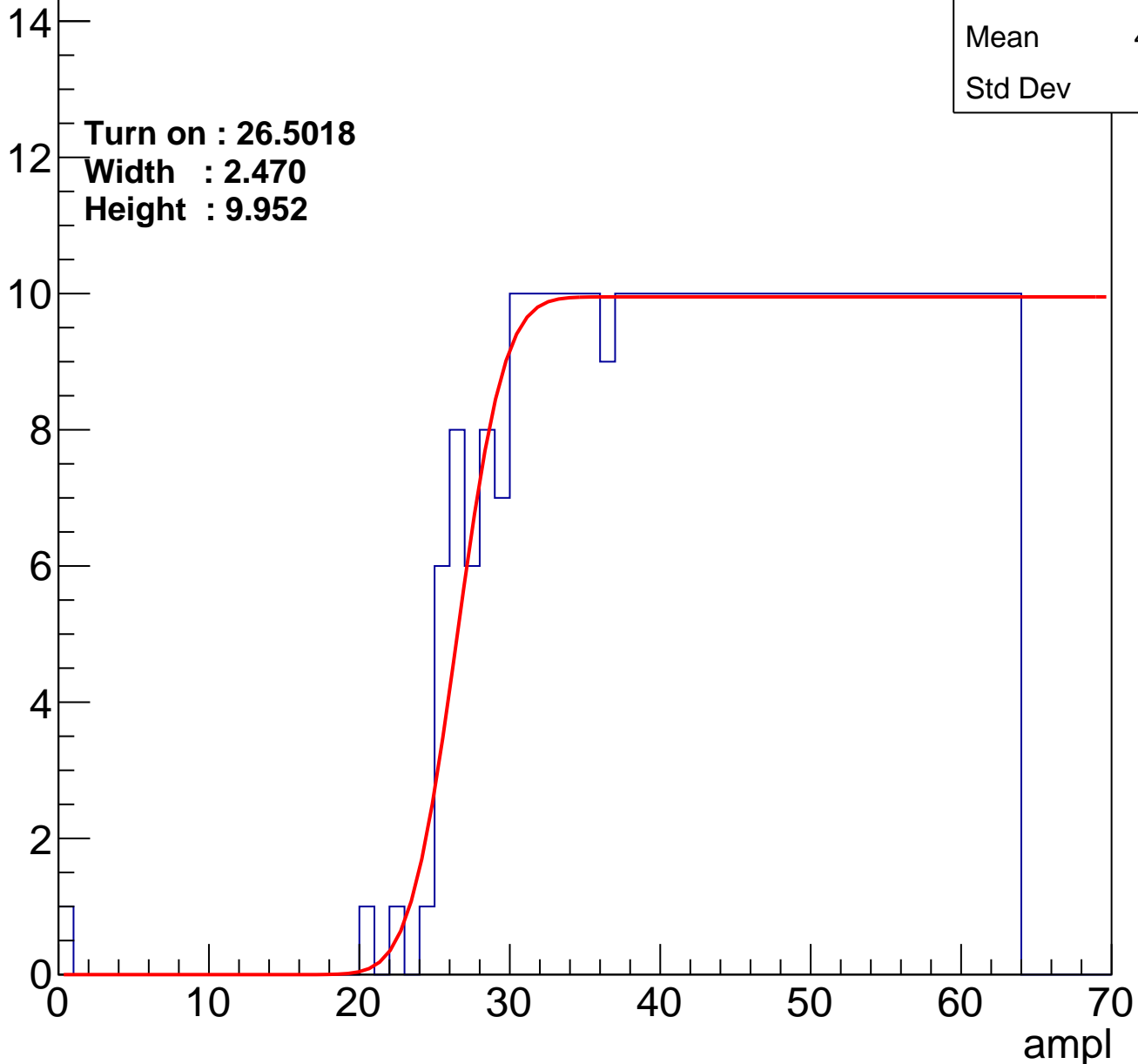
Entries	378
Mean	44.41
Std Dev	11.3

Turn on : 26.5018

Width : 2.470

Height : 9.952

Entry



B0L002S, U9-ch24

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.54
Std Dev	10.65

Turn on : 29.0376

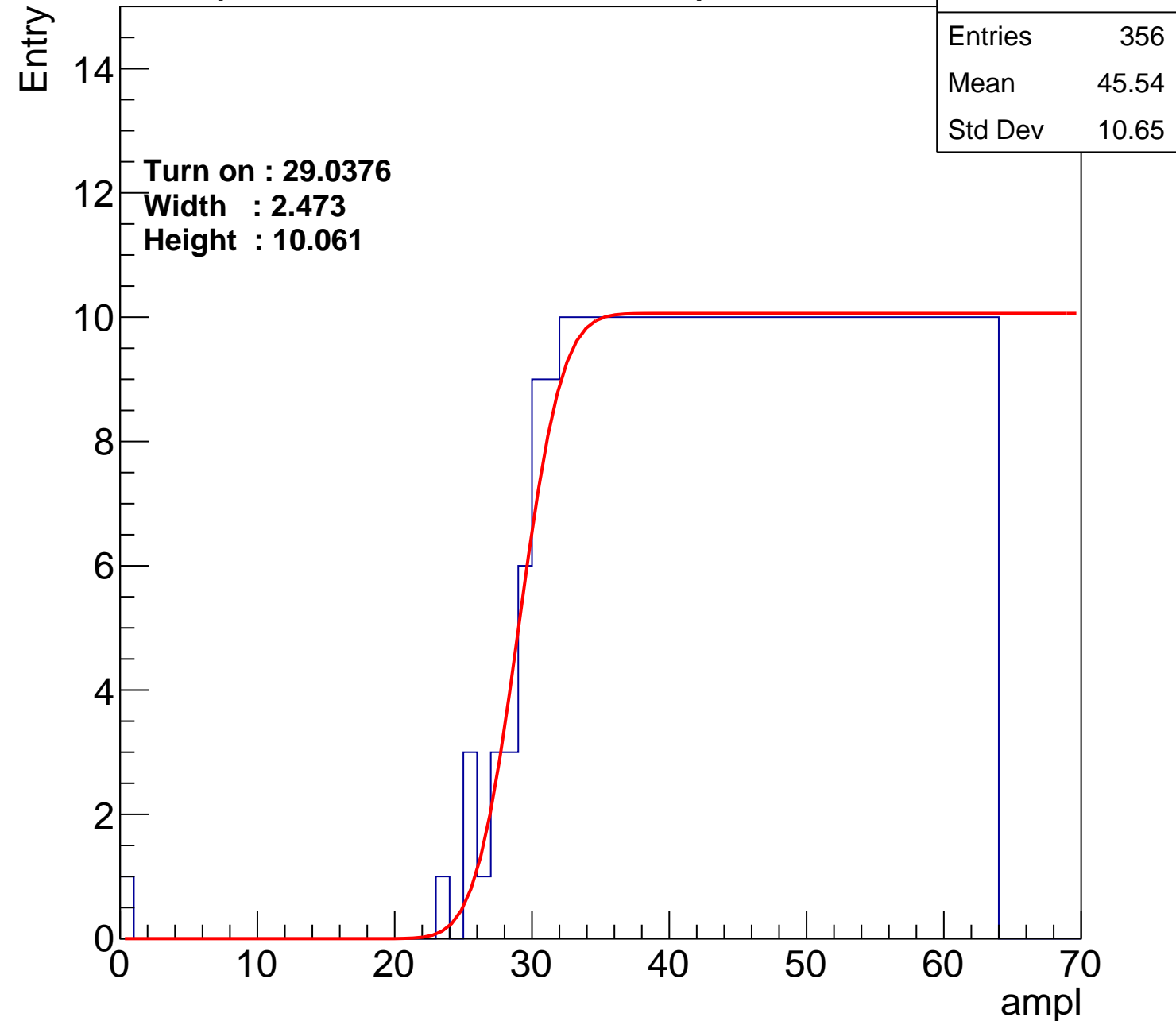
Width : 2.473

Height : 10.061

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch25

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.67
Std Dev	10.59

Turn on : 28.8480

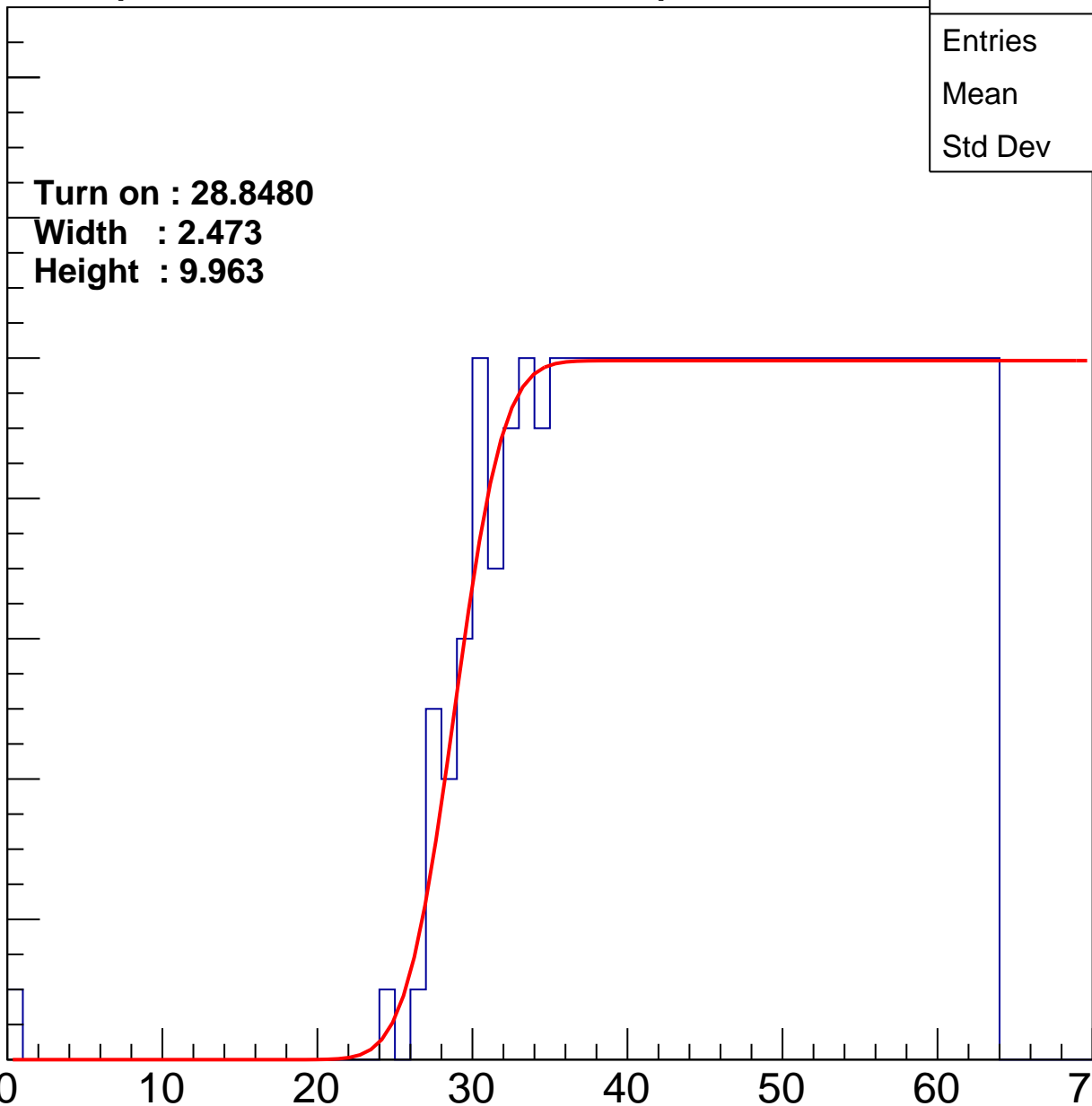
Width : 2.473

Height : 9.963

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch26

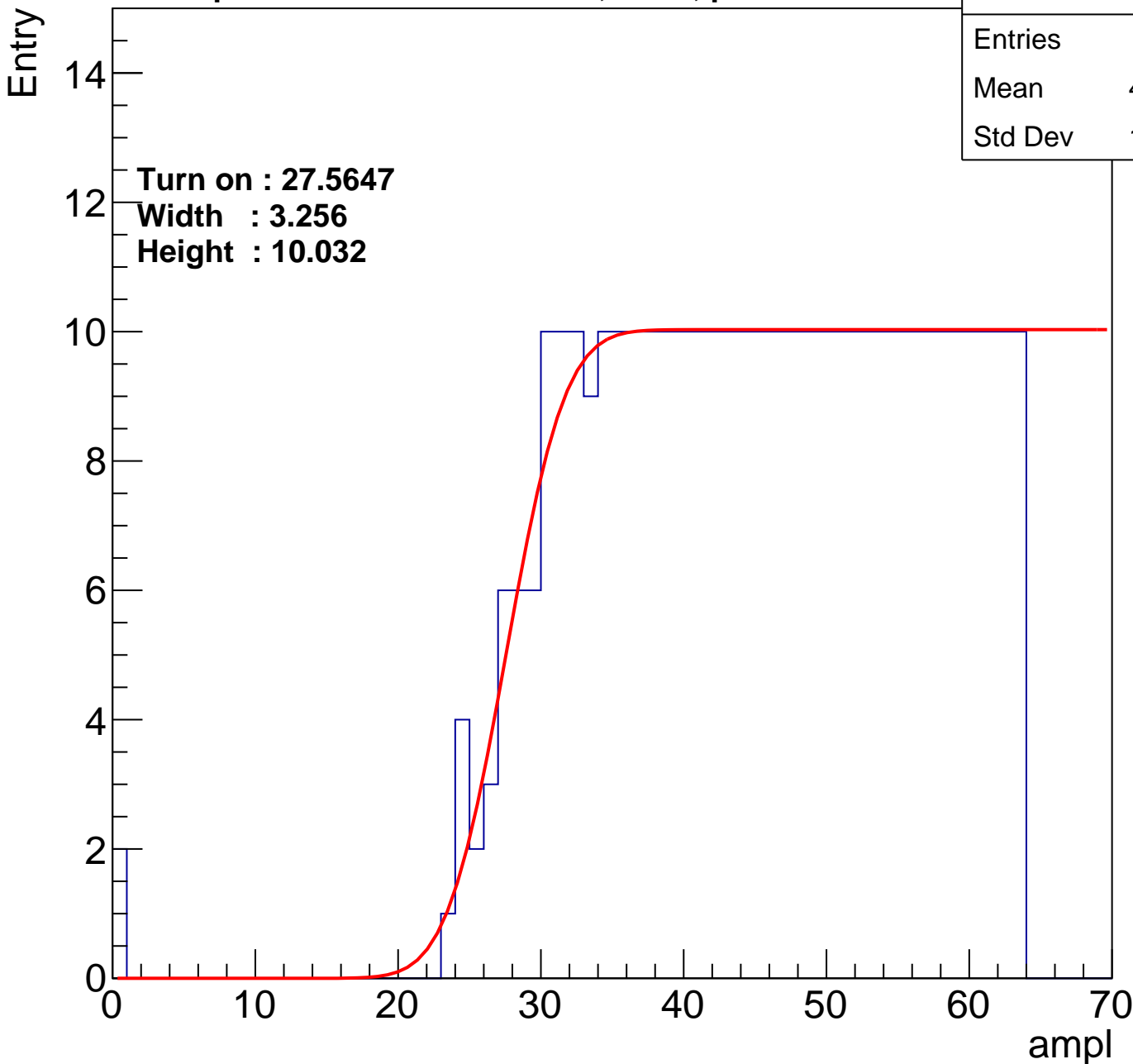
calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.79
Std Dev	11.25

Turn on : 27.5647

Width : 3.256

Height : 10.032



B0L002S, U9-ch27

calib_packv5_042523_0143.root, FC#8, port C1

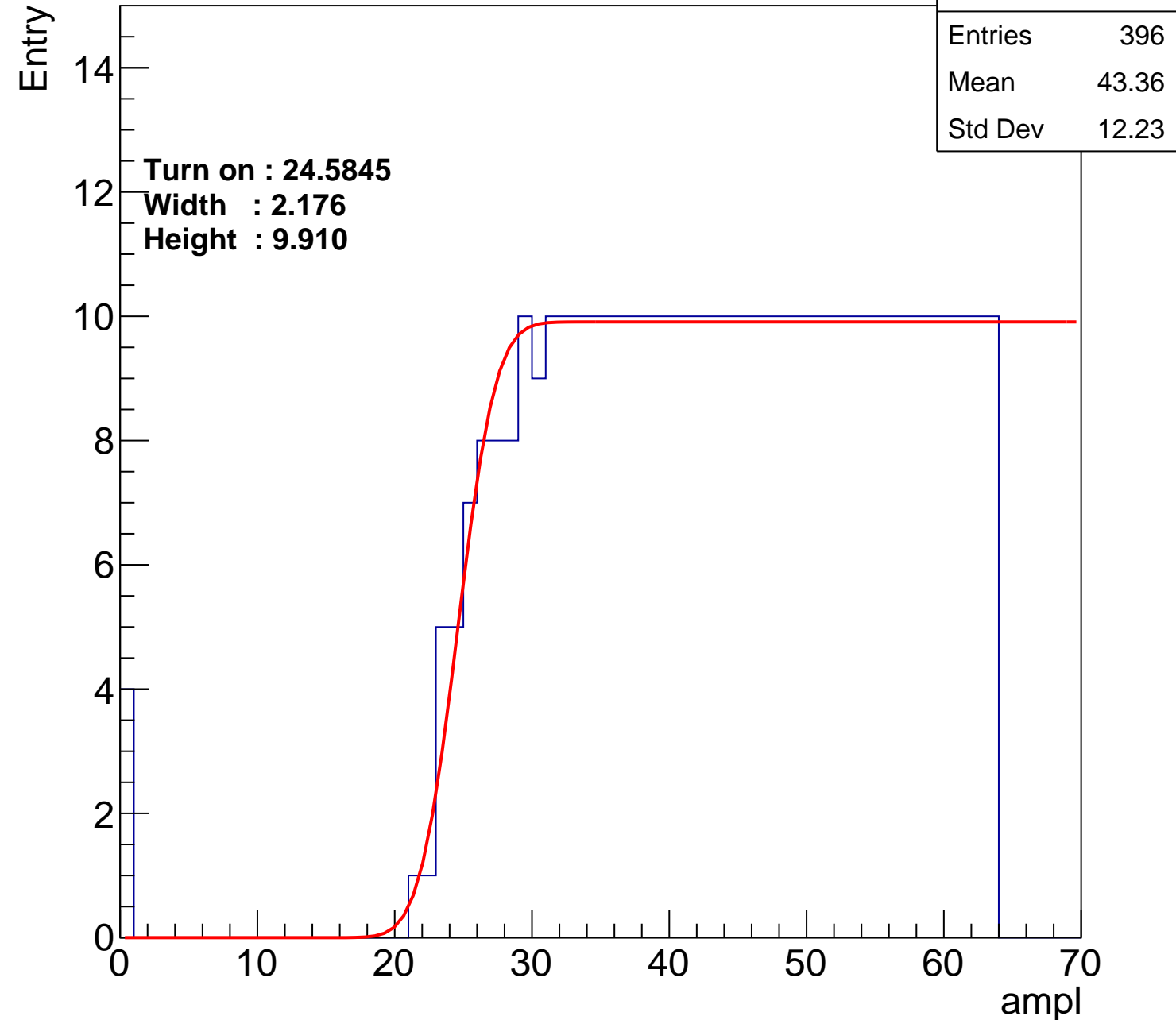
Entry

14
12
10
8
6
4
2
0

Turn on : 24.5845
Width : 2.176
Height : 9.910

Entries	396
Mean	43.36
Std Dev	12.23

ampl



B0L002S, U9-ch28

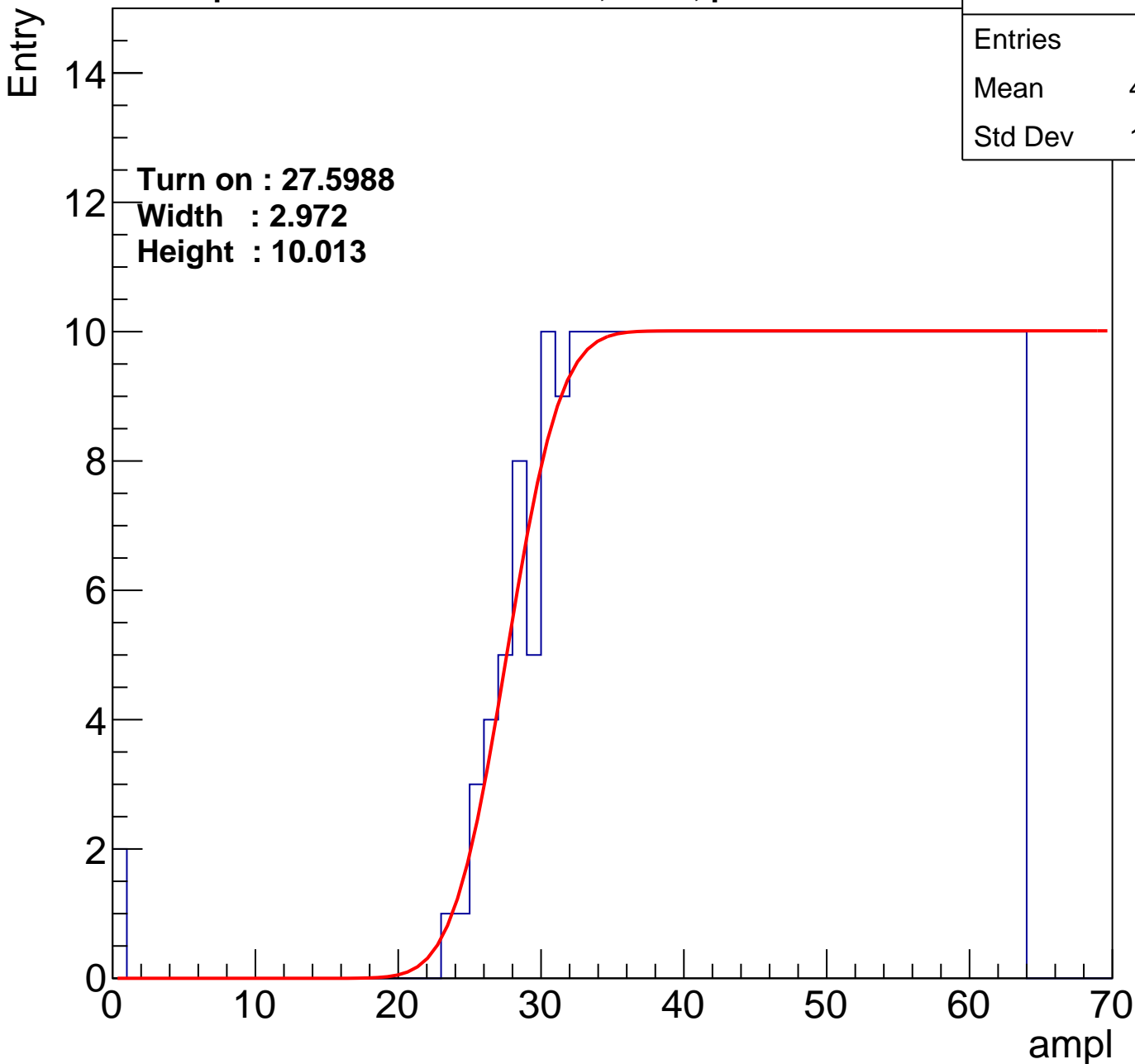
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.5988

Width : 2.972

Height : 10.013



B0L002S, U9-ch29

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.55
Std Dev	11.38

Turn on : 26.9284

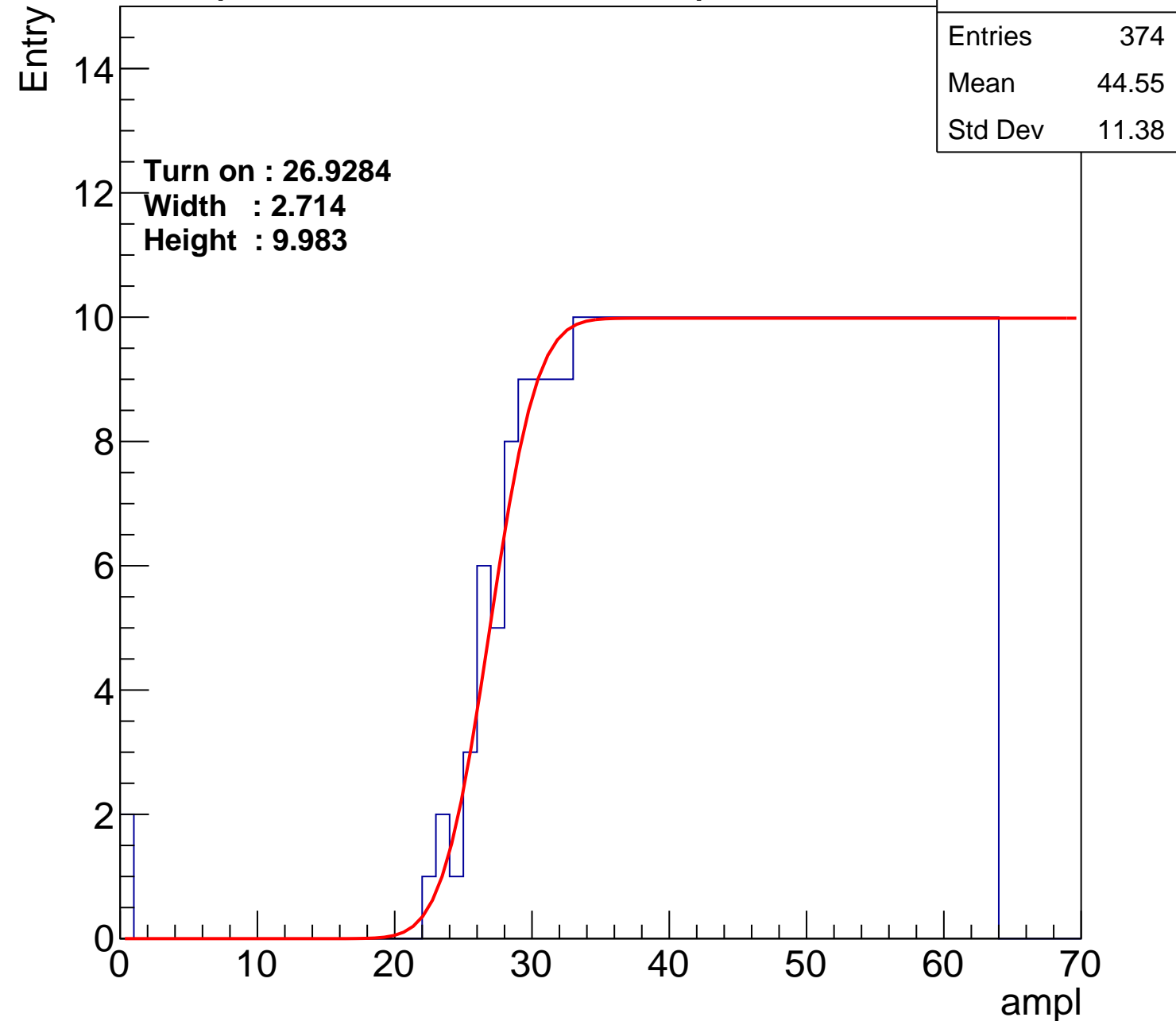
Width : 2.714

Height : 9.983

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch30

calib_packv5_042523_0143.root, FC#8, port C1

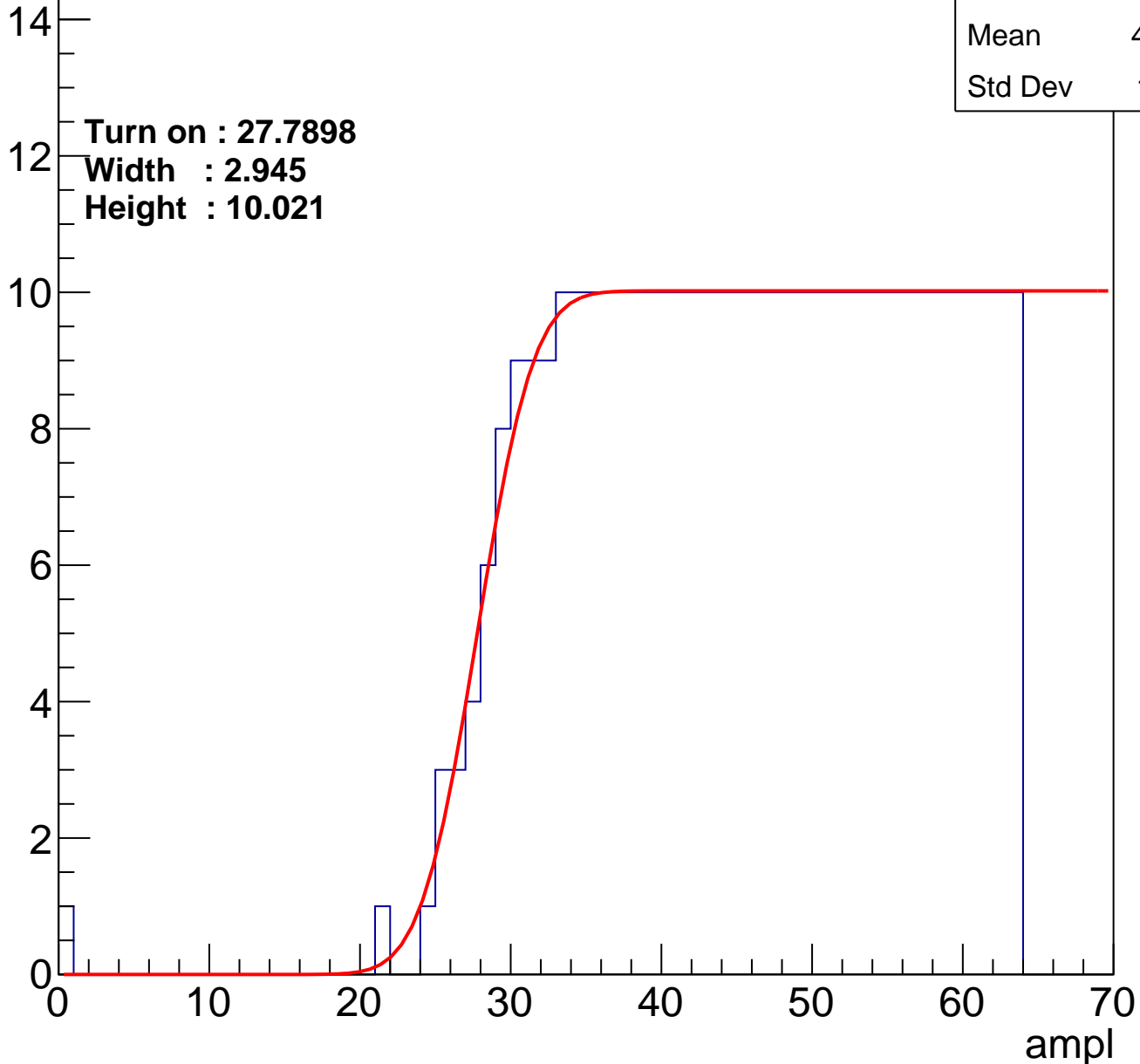
Entries	364
Mean	45.12
Std Dev	10.91

Turn on : 27.7898

Width : 2.945

Height : 10.021

Entry



B0L002S, U9-ch31

calib_packv5_042523_0143.root, FC#8, port C1

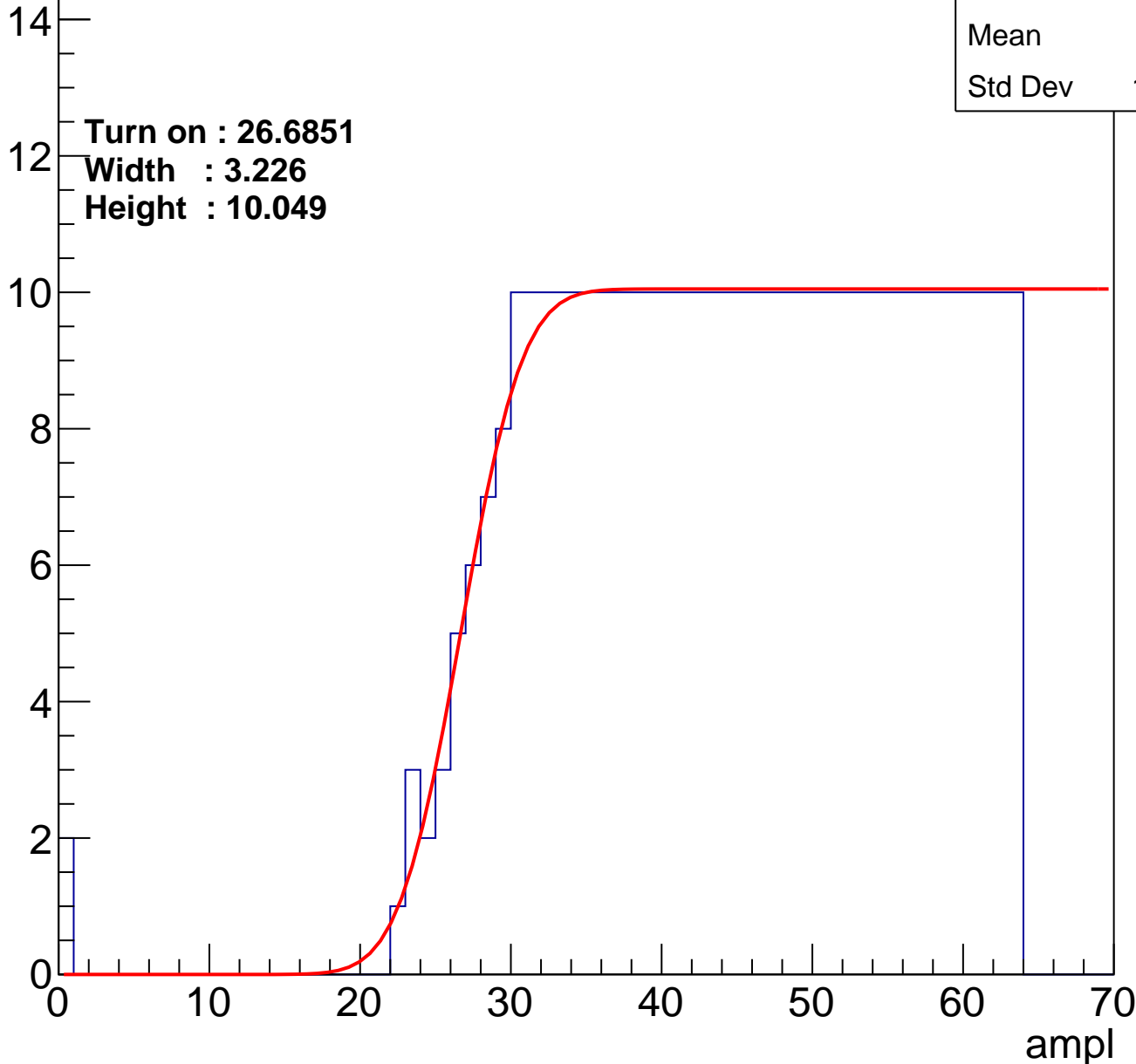
Entries	377
Mean	44.41
Std Dev	11.43

Turn on : 26.6851

Width : 3.226

Height : 10.049

Entry



B0L002S, U9-ch32

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.58
Std Dev	11.46

Turn on : 27.0238

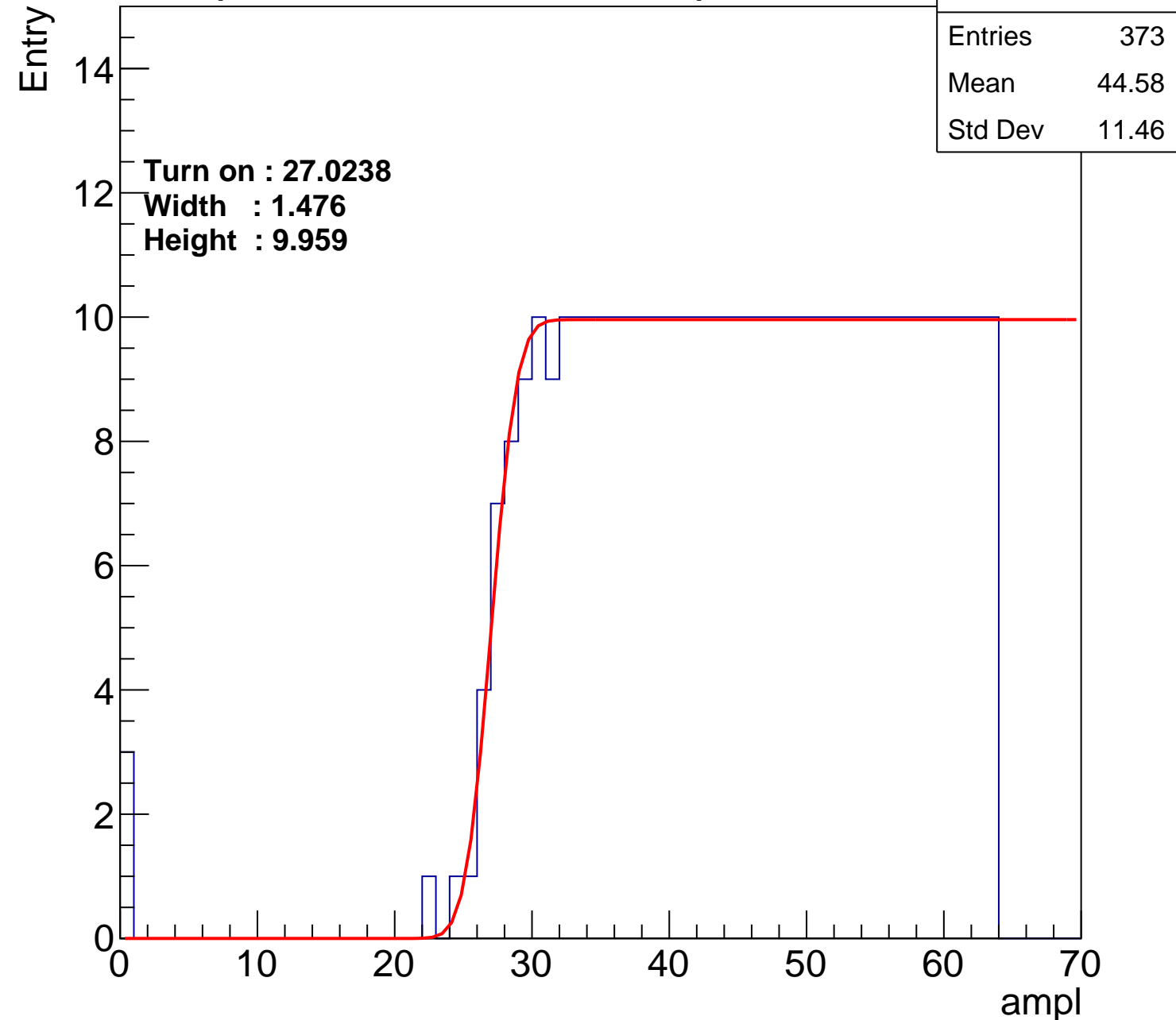
Width : 1.476

Height : 9.959

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch33

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.44
Std Dev	11.26

Turn on : 28.9196

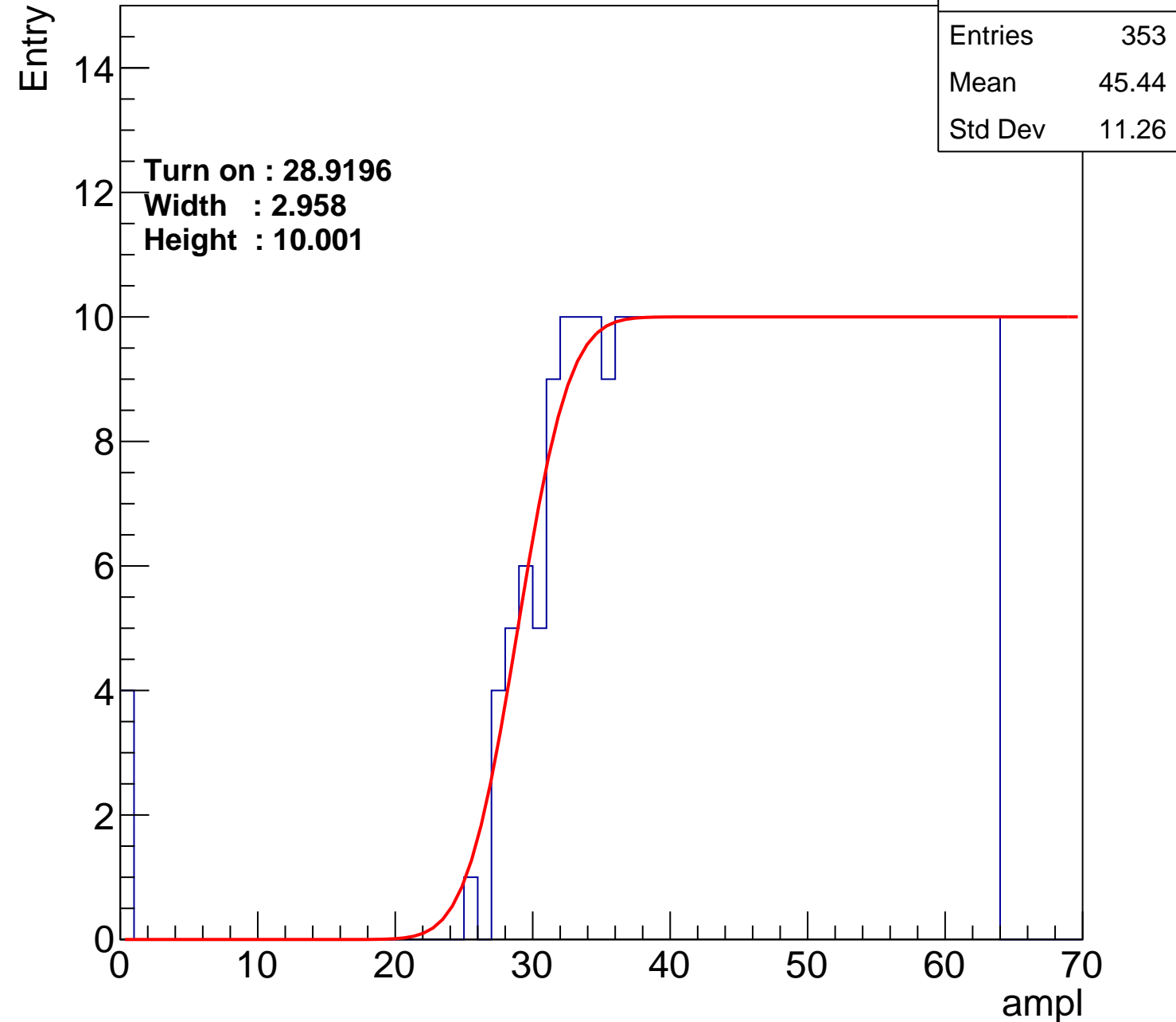
Width : 2.958

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch34

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.87
Std Dev	11.3

Turn on : 28.6961

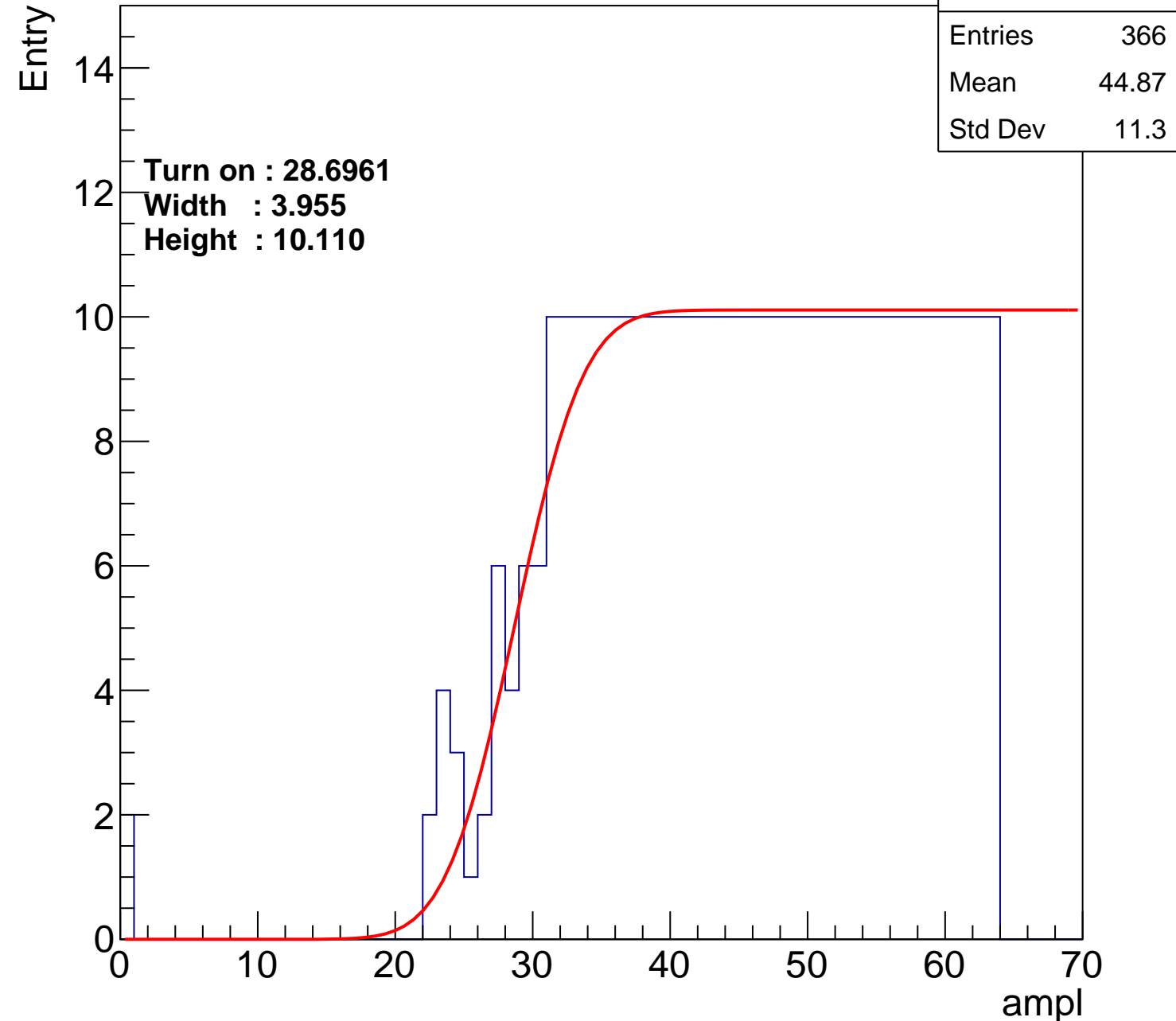
Width : 3.955

Height : 10.110

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch35

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.67
Std Dev	10.61

Turn on : 29.3360

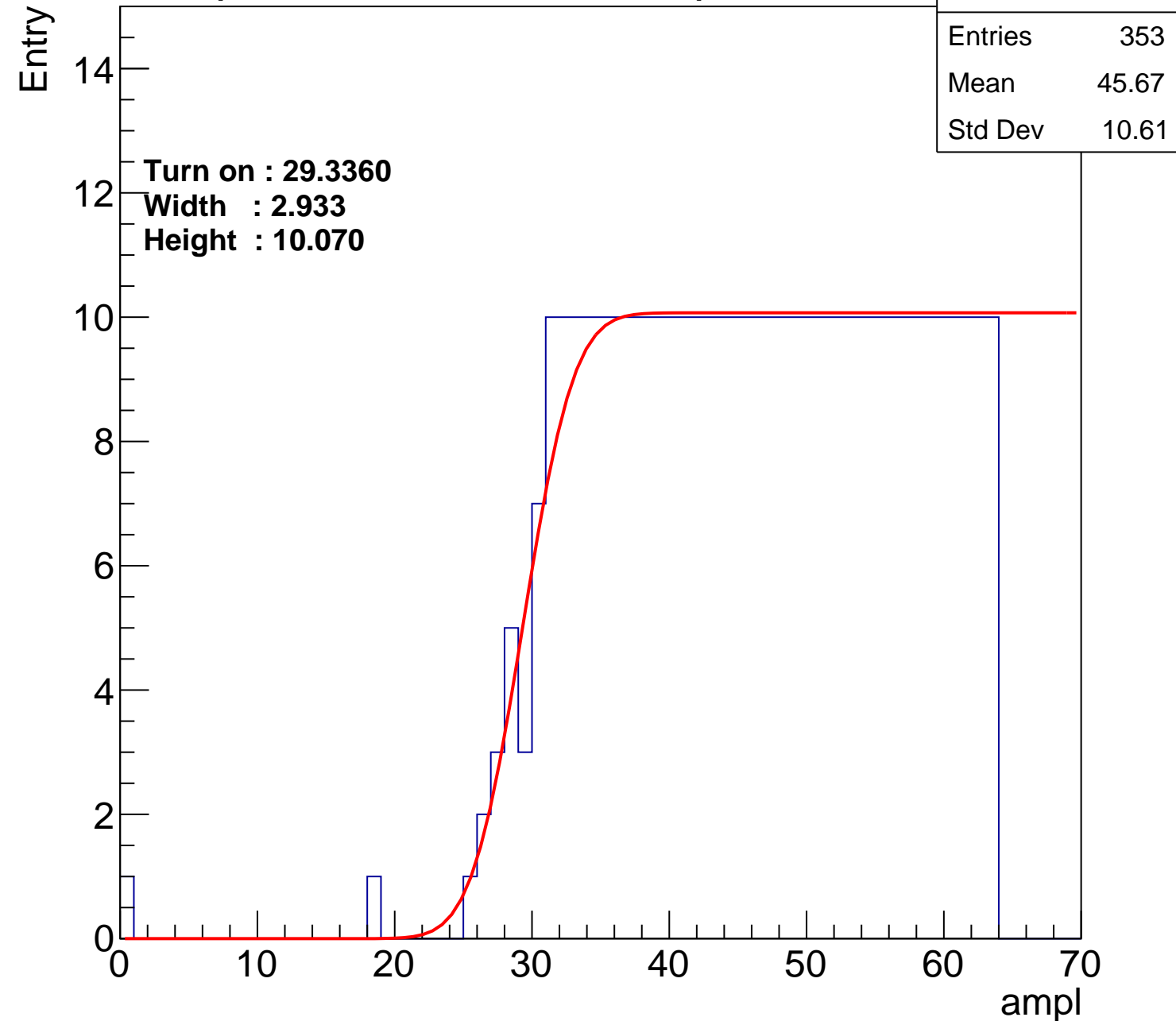
Width : 2.933

Height : 10.070

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch36

calib_packv5_042523_0143.root, FC#8, port C1

Entries	362
Mean	45.06
Std Dev	11.29

Turn on : 28.2194

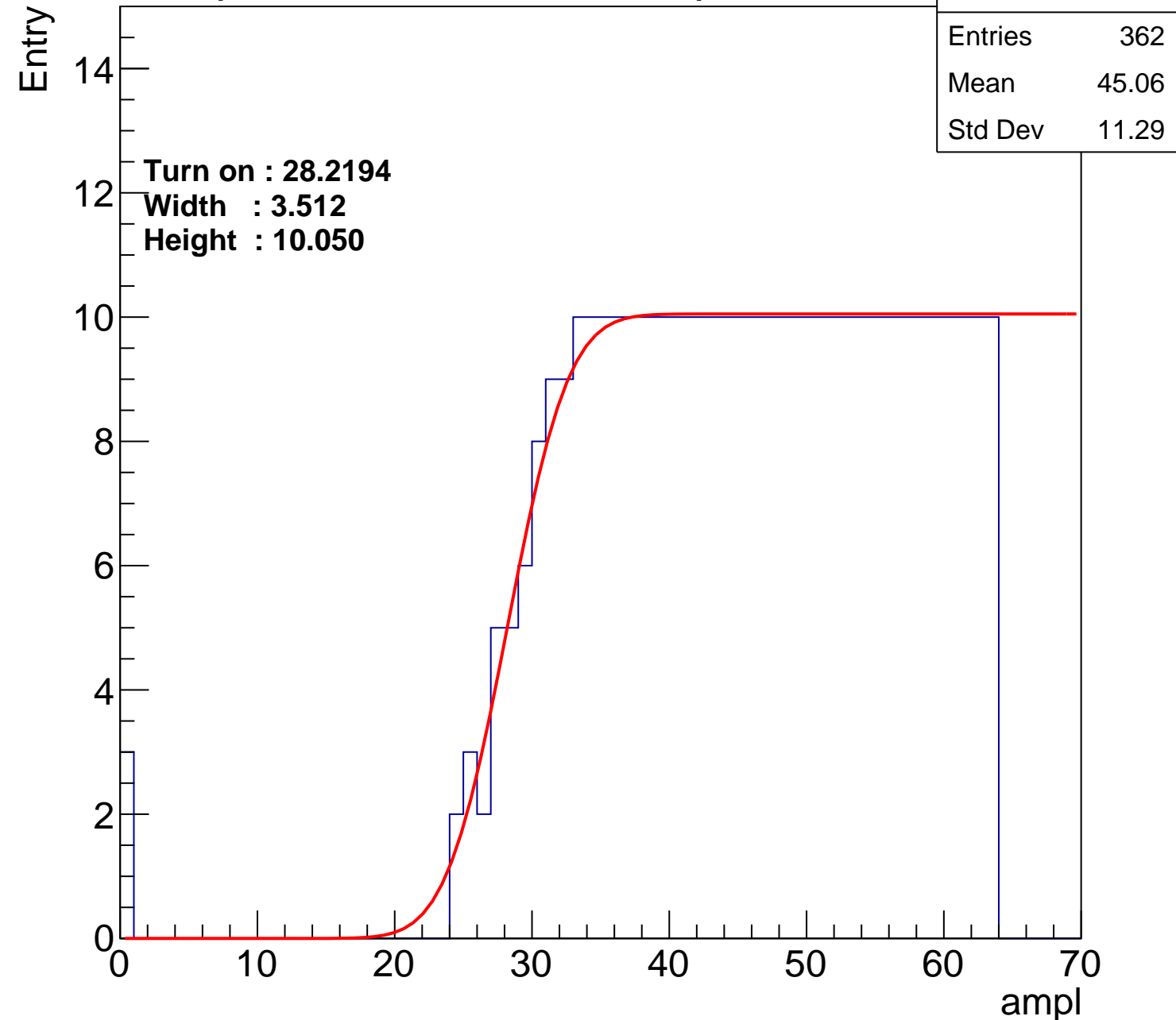
Width : 3.512

Height : 10.050

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch37

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.4
Std Dev	11.59

Turn on : 26.7589

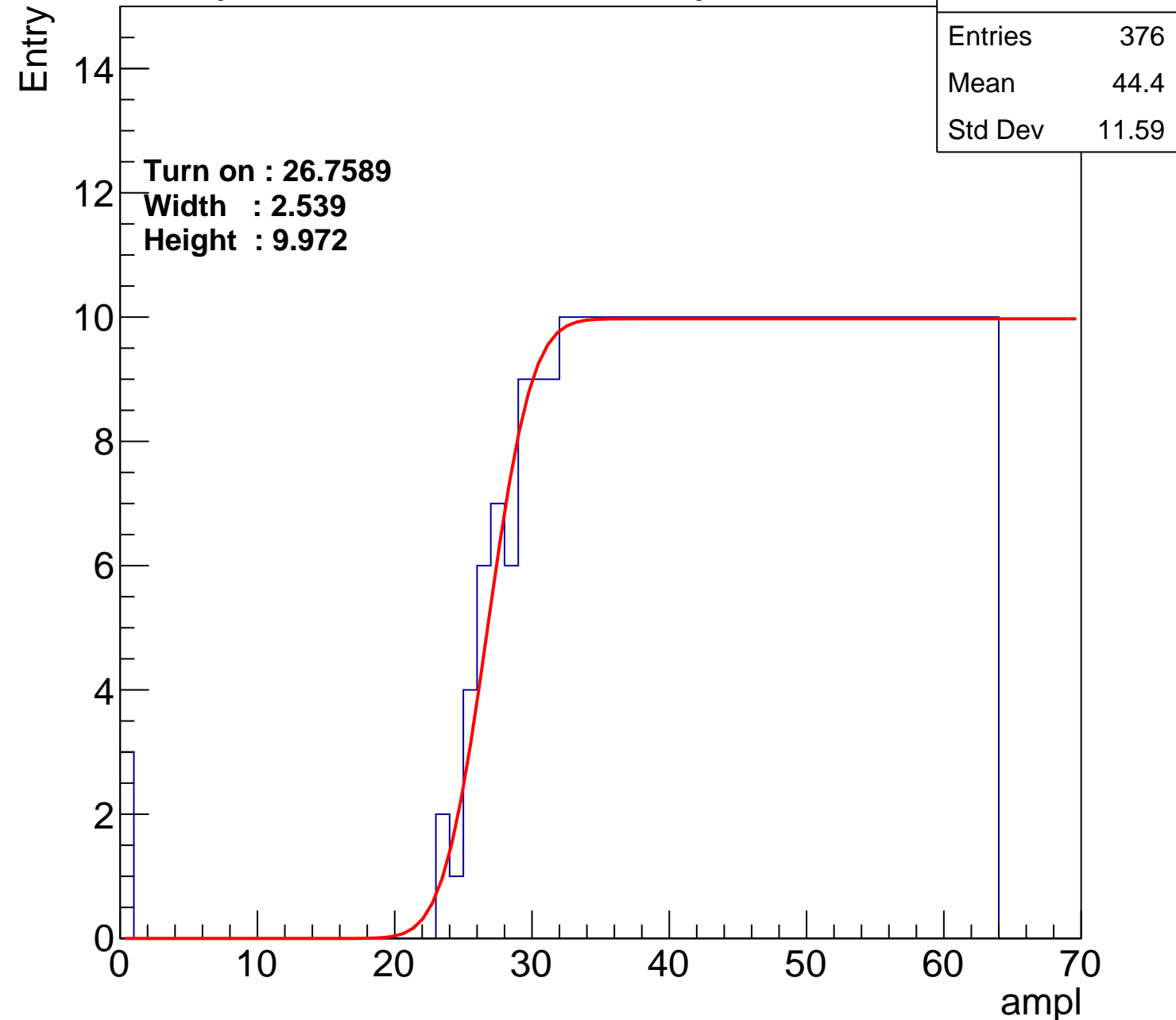
Width : 2.539

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch38

calib_packv5_042523_0143.root, FC#8, port C1

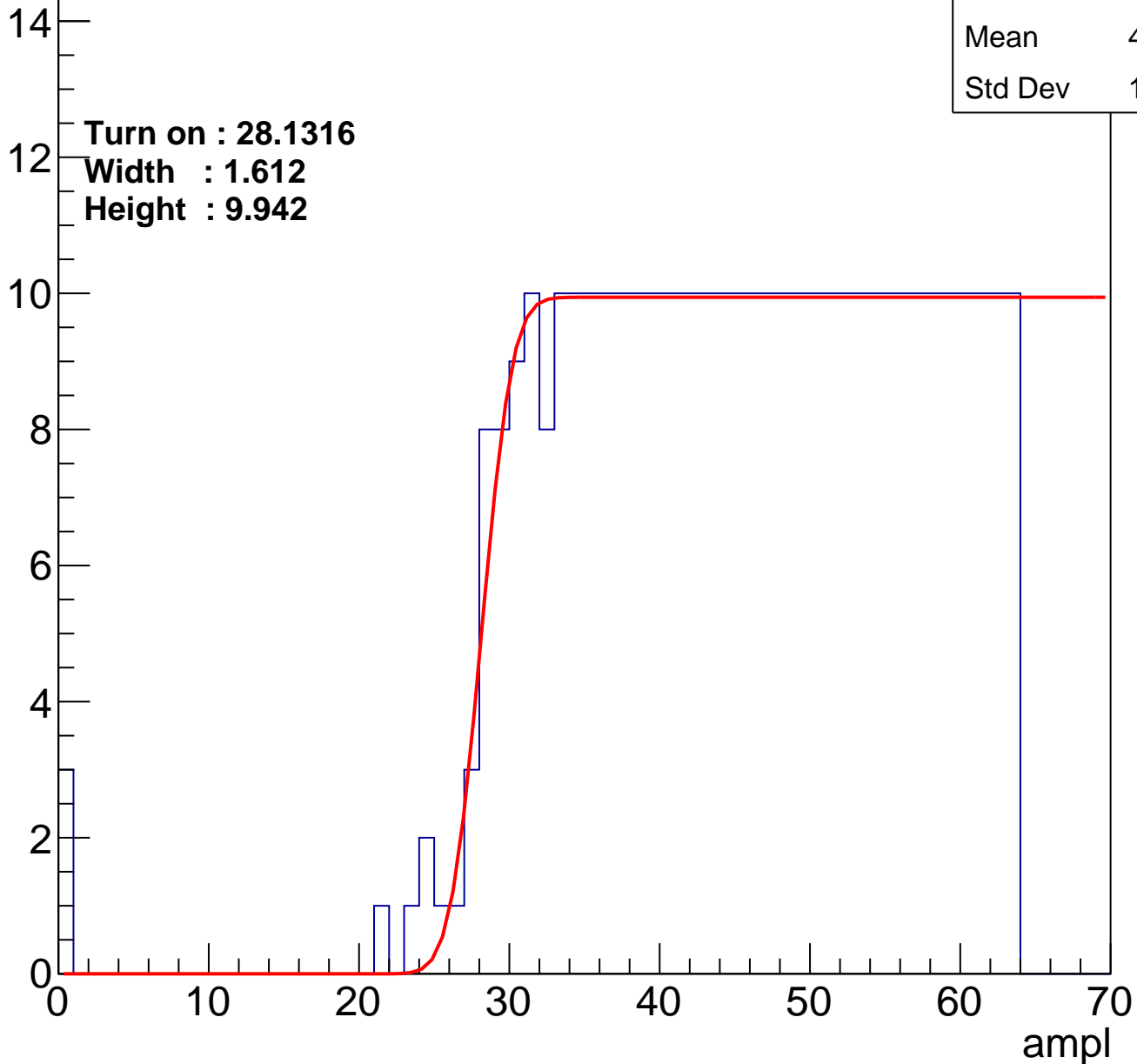
Entry

Entries	365
Mean	44.92
Std Dev	11.35

Turn on : 28.1316

Width : 1.612

Height : 9.942



B0L002S, U9-ch39

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	45
Std Dev	11.14

Turn on : 27.5702

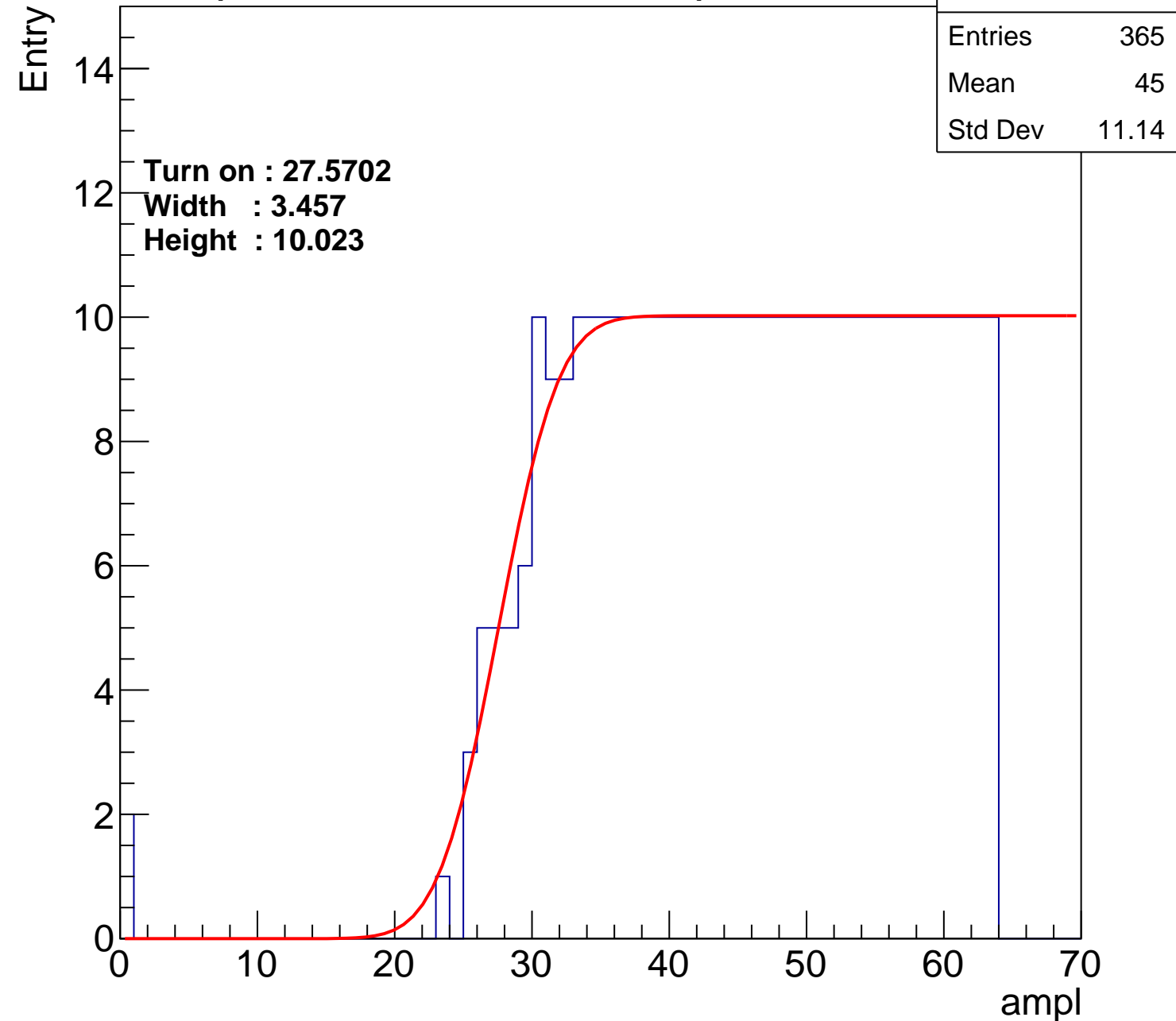
Width : 3.457

Height : 10.023

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch40

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.33
Std Dev	11.52

Turn on : 26.5676

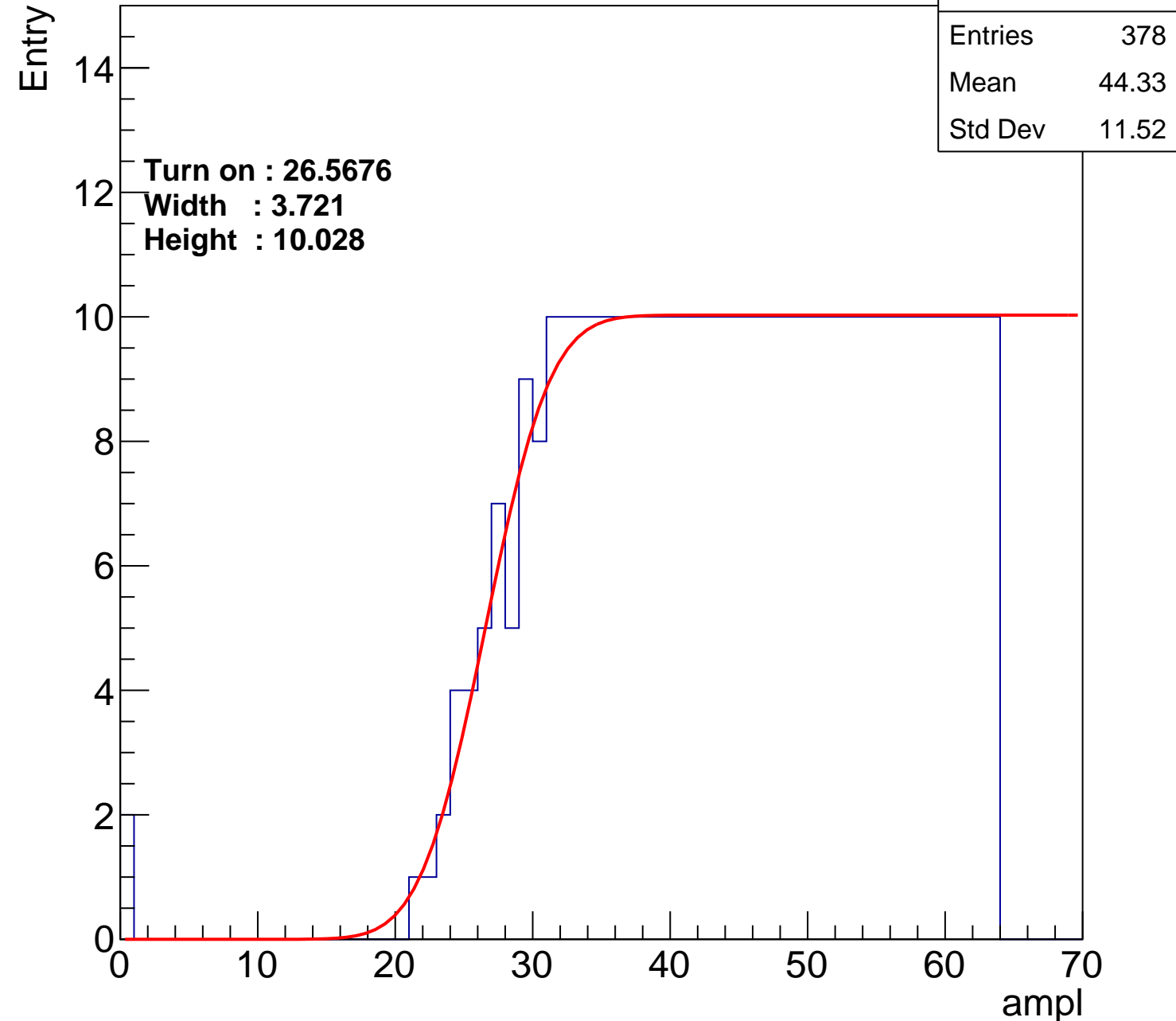
Width : 3.721

Height : 10.028

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch41

calib_packv5_042523_0143.root, FC#8, port C1

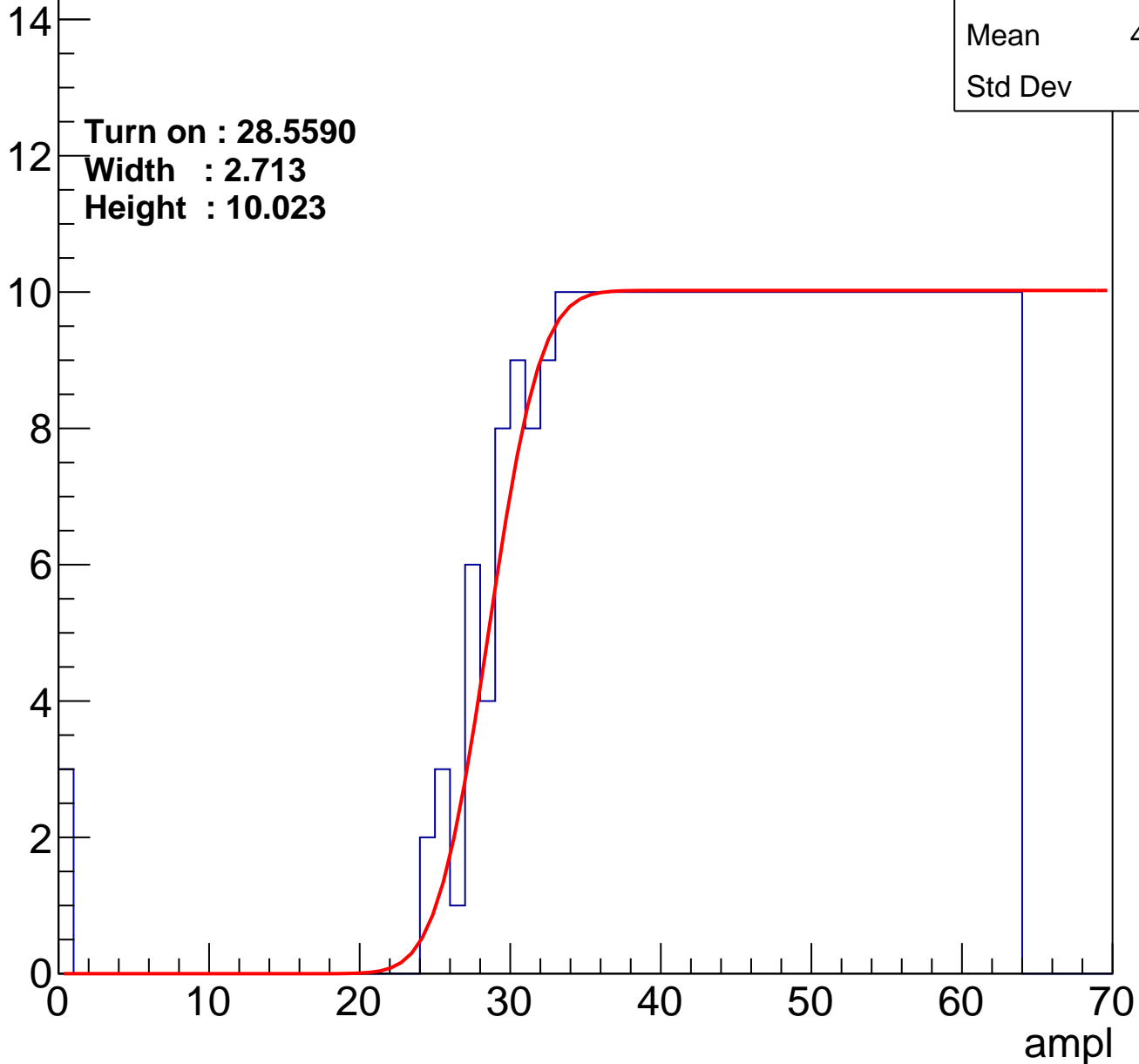
Entries	363
Mean	45.02
Std Dev	11.3

Turn on : 28.5590

Width : 2.713

Height : 10.023

Entry



B0L002S, U9-ch42

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.56
Std Dev	11.04

Turn on : 29.2463

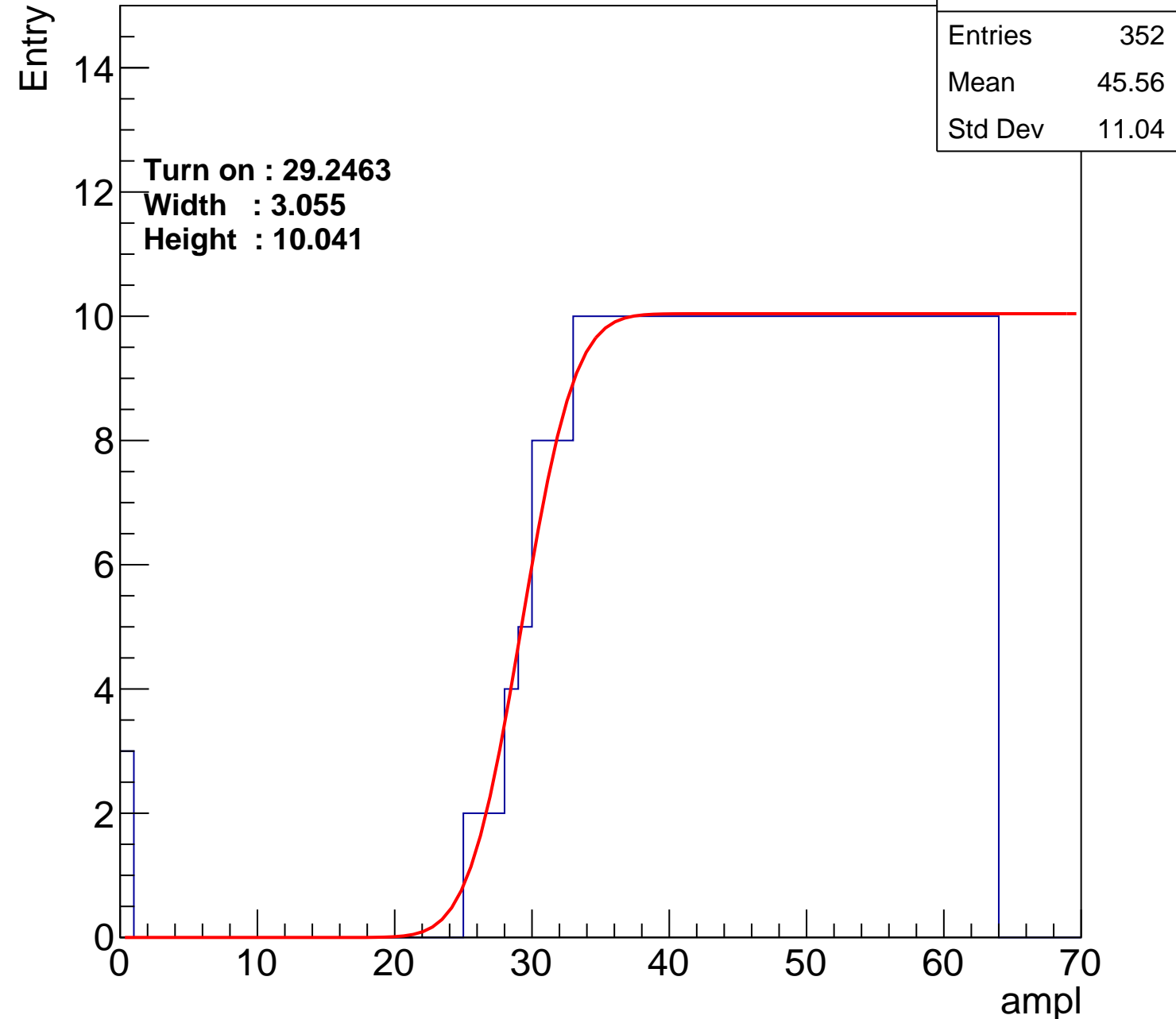
Width : 3.055

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch43

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.36
Std Dev	10.94

Turn on : 28.8303

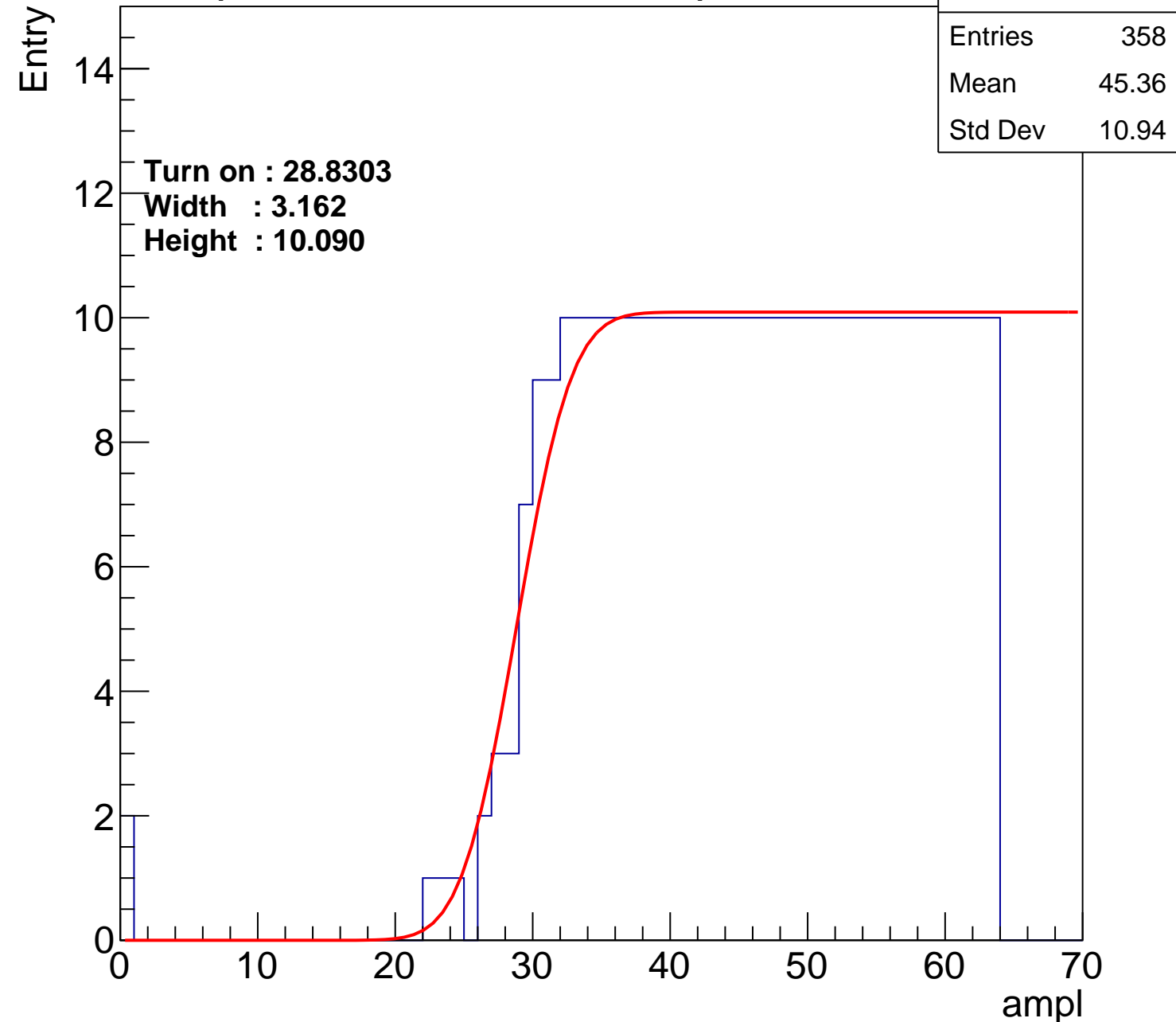
Width : 3.162

Height : 10.090

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch44

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.53
Std Dev	10.66

Turn on : 28.7629

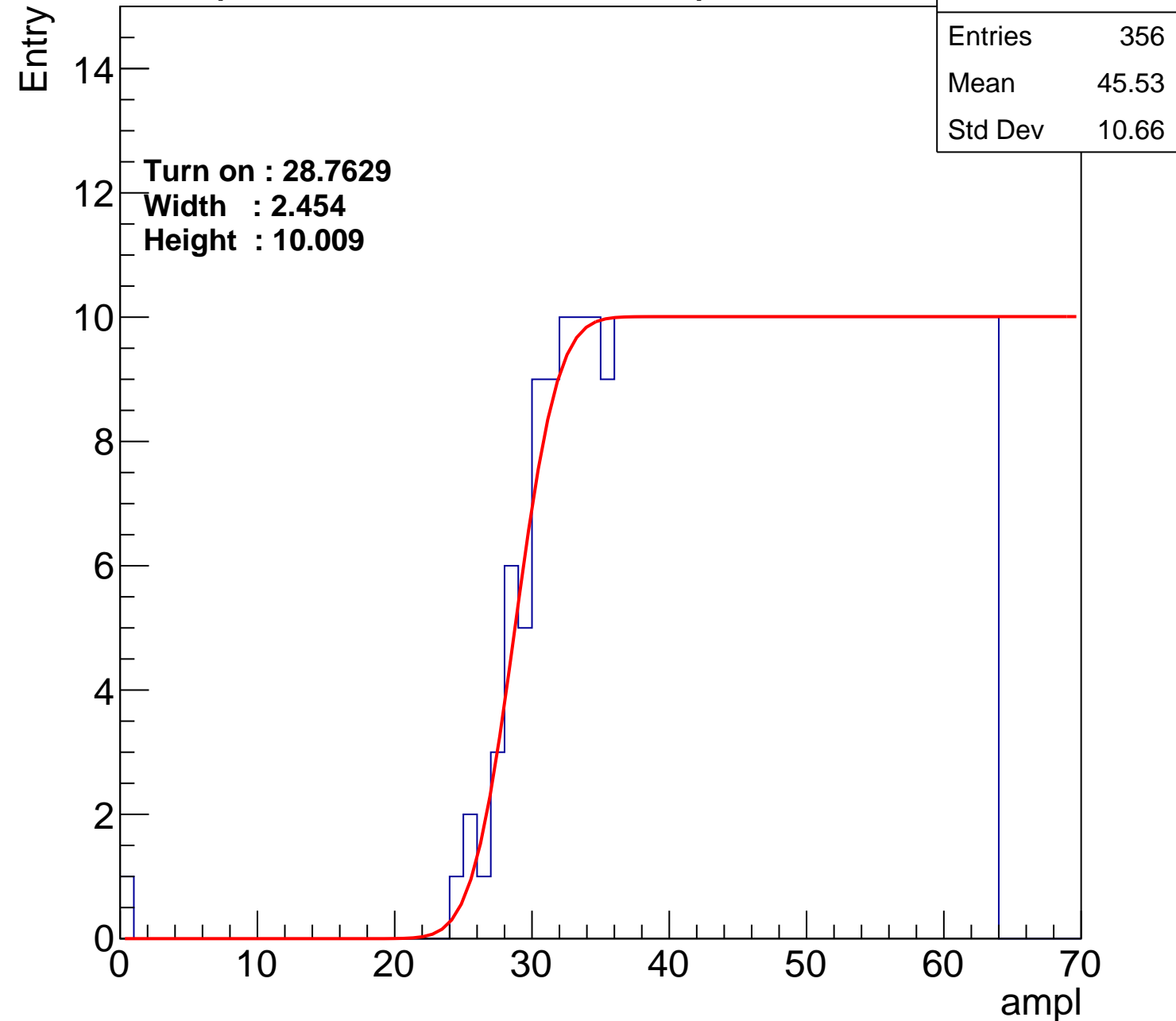
Width : 2.454

Height : 10.009

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch45

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.61
Std Dev	10.65

Turn on : 28.9482

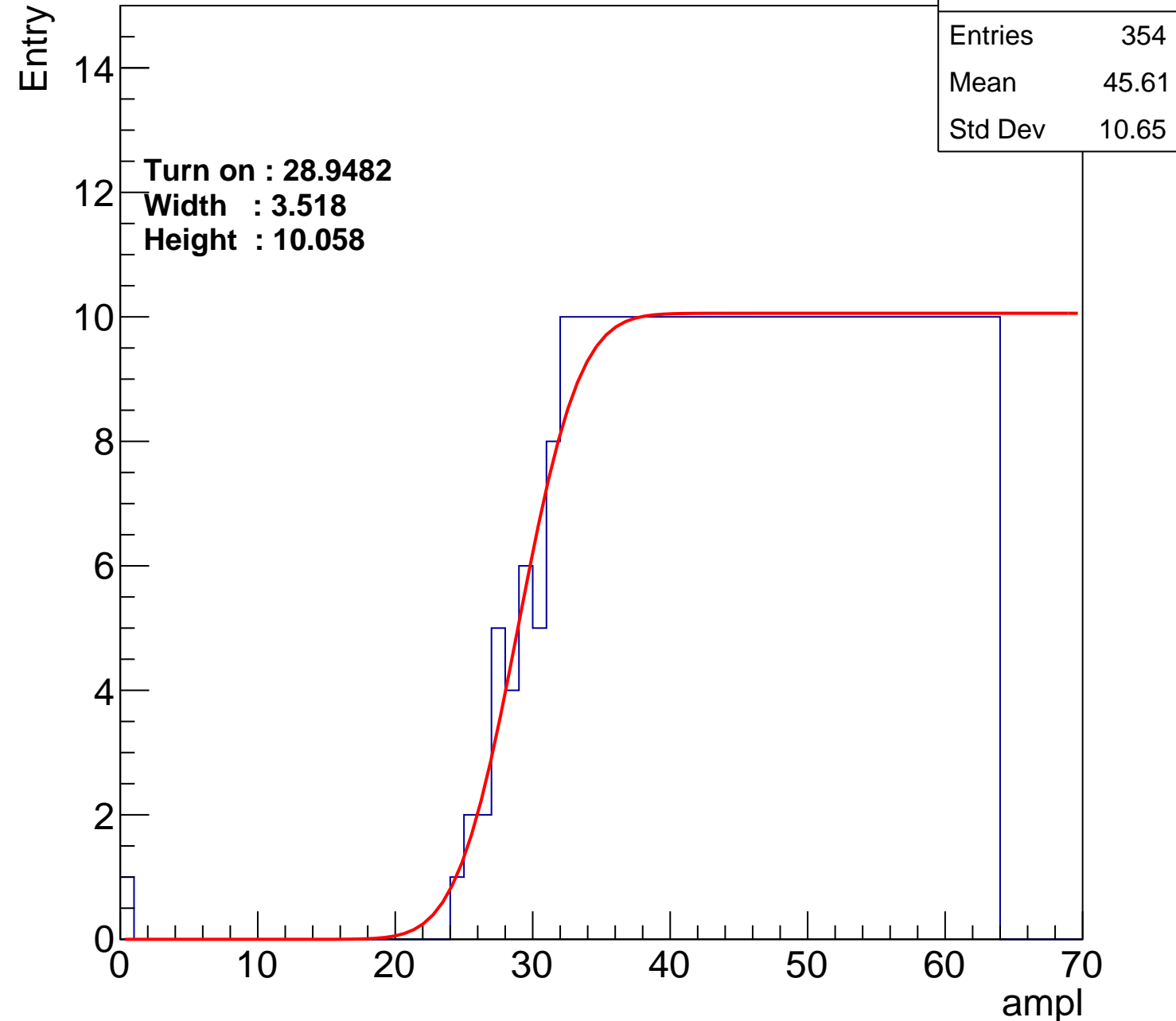
Width : 3.518

Height : 10.058

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch46

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.48
Std Dev	11.38

Turn on : 26.7630

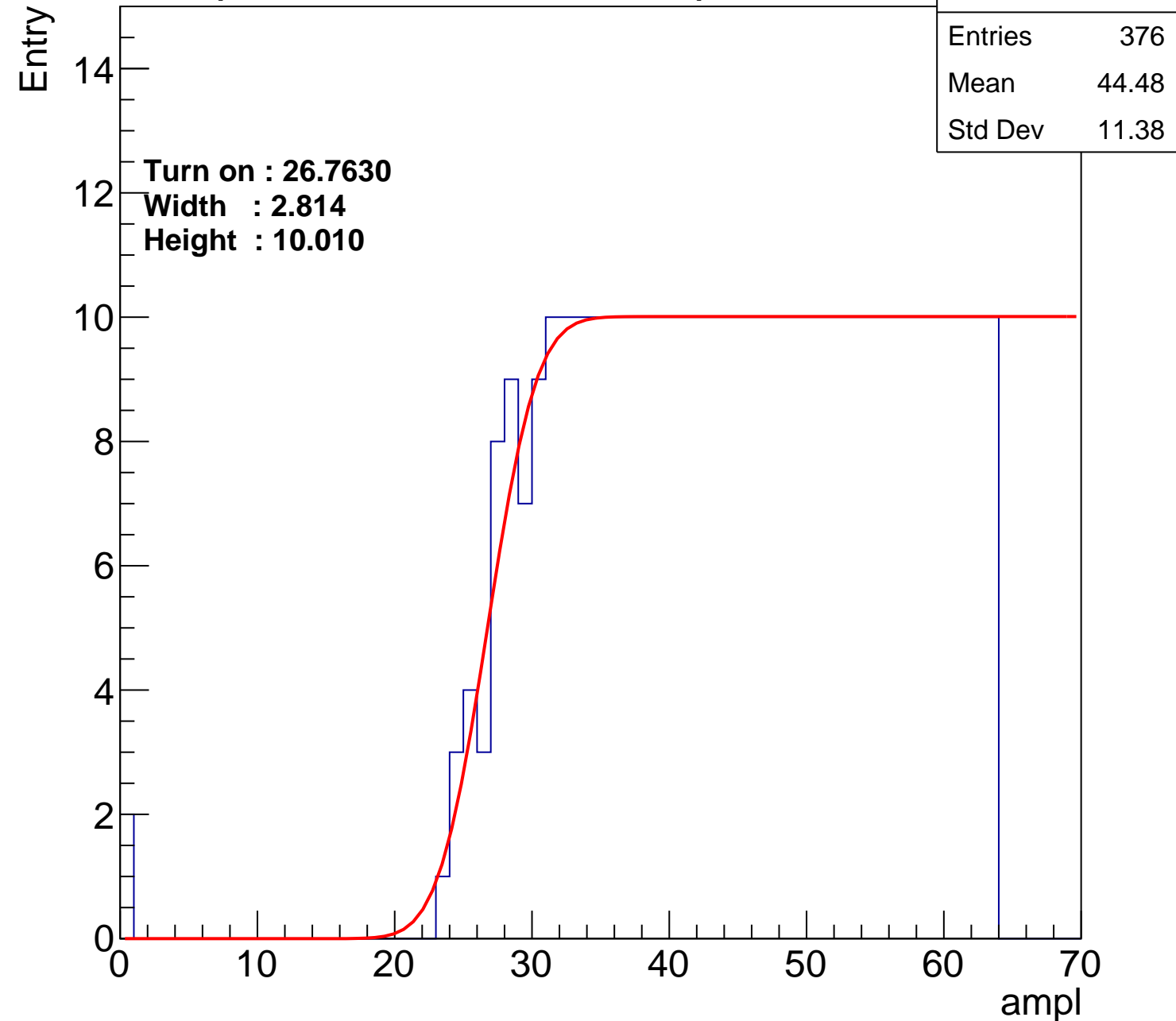
Width : 2.814

Height : 10.010

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch47

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.67
Std Dev	11.15

Turn on : 26.8491

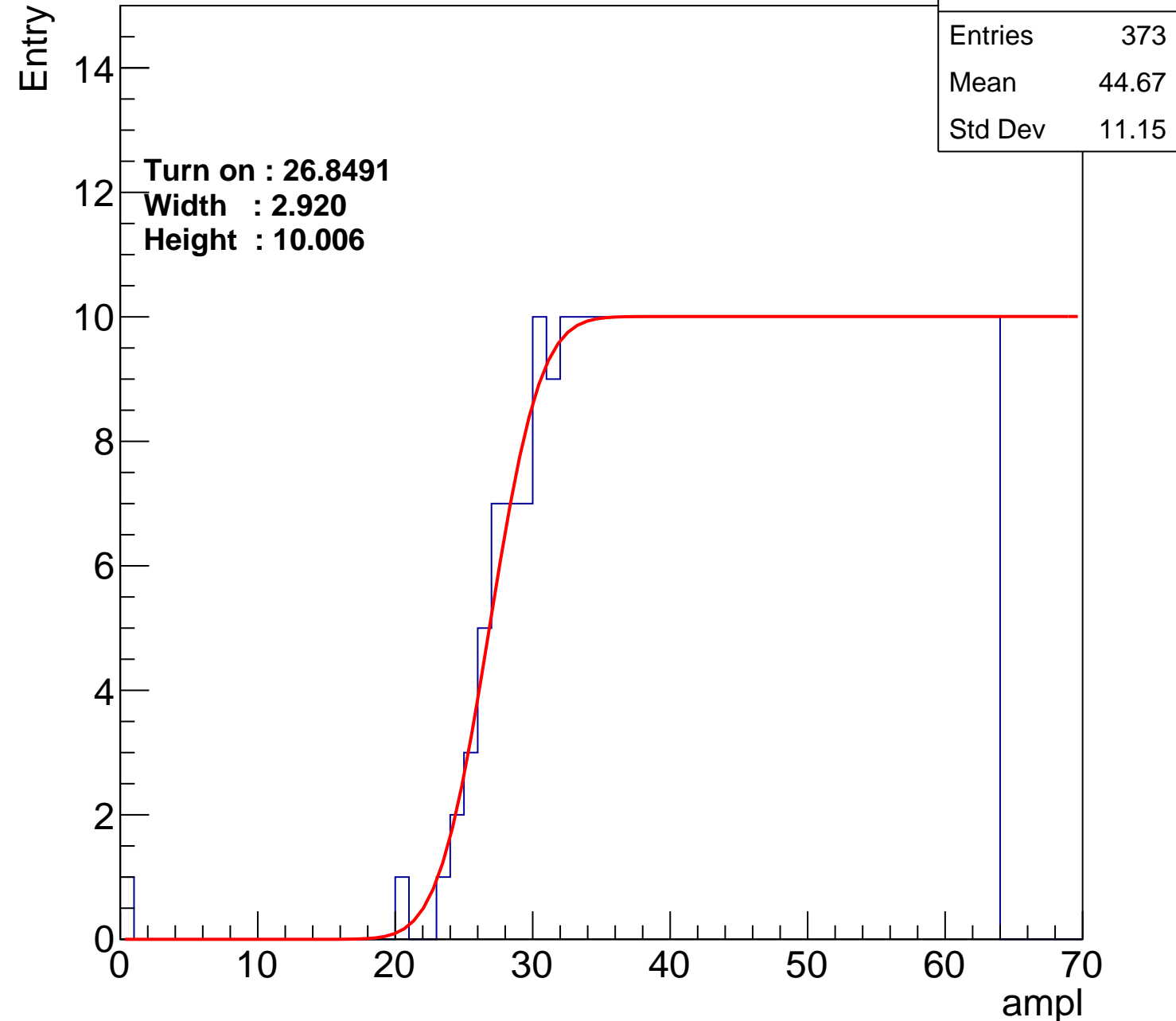
Width : 2.920

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch48

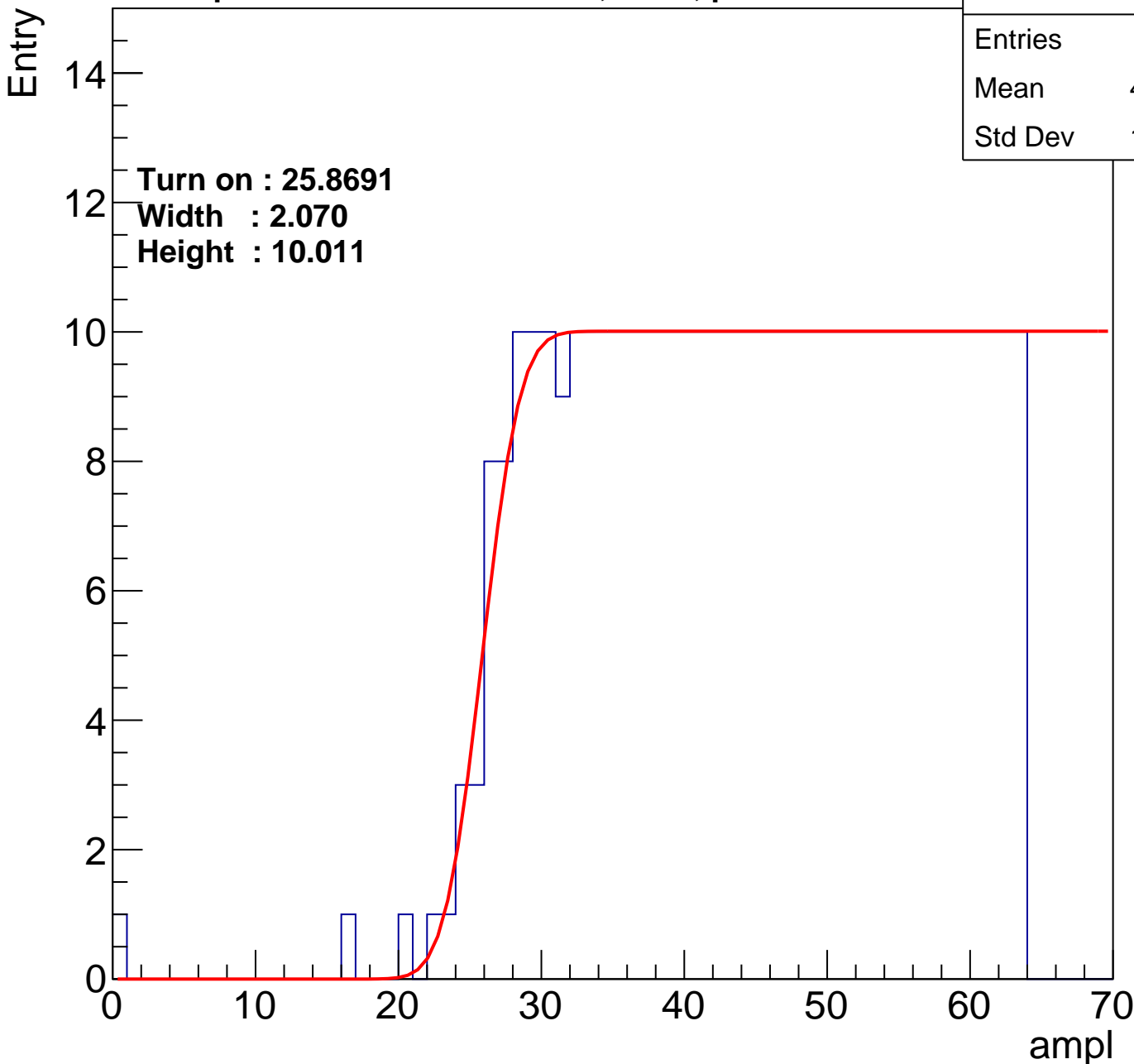
calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	44.04
Std Dev	11.48

Turn on : 25.8691

Width : 2.070

Height : 10.011



B0L002S, U9-ch49

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.22
Std Dev	10.99

Turn on : 28.4350

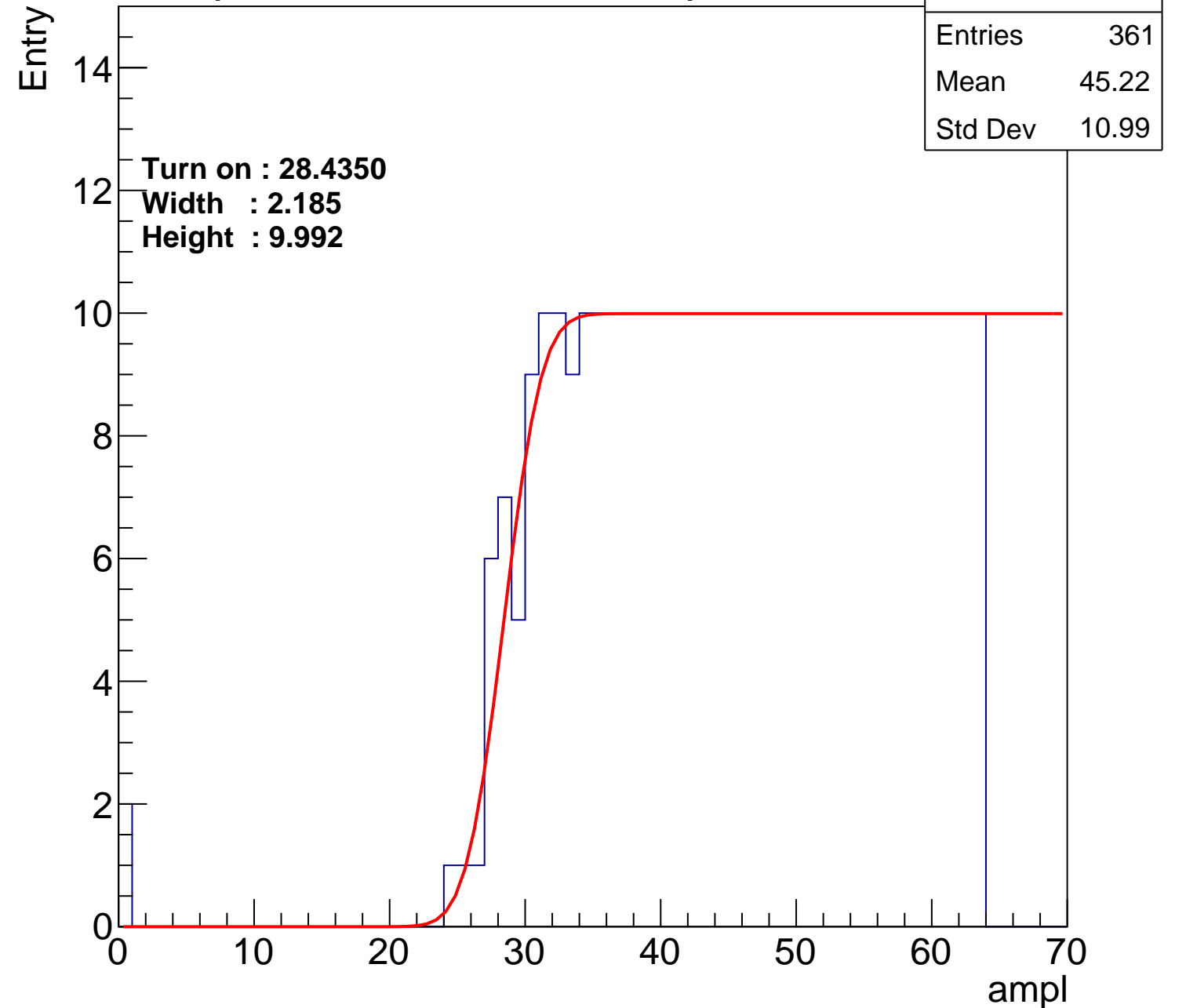
Width : 2.185

Height : 9.992

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch50

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.74
Std Dev	11.09

Turn on : 27.0977

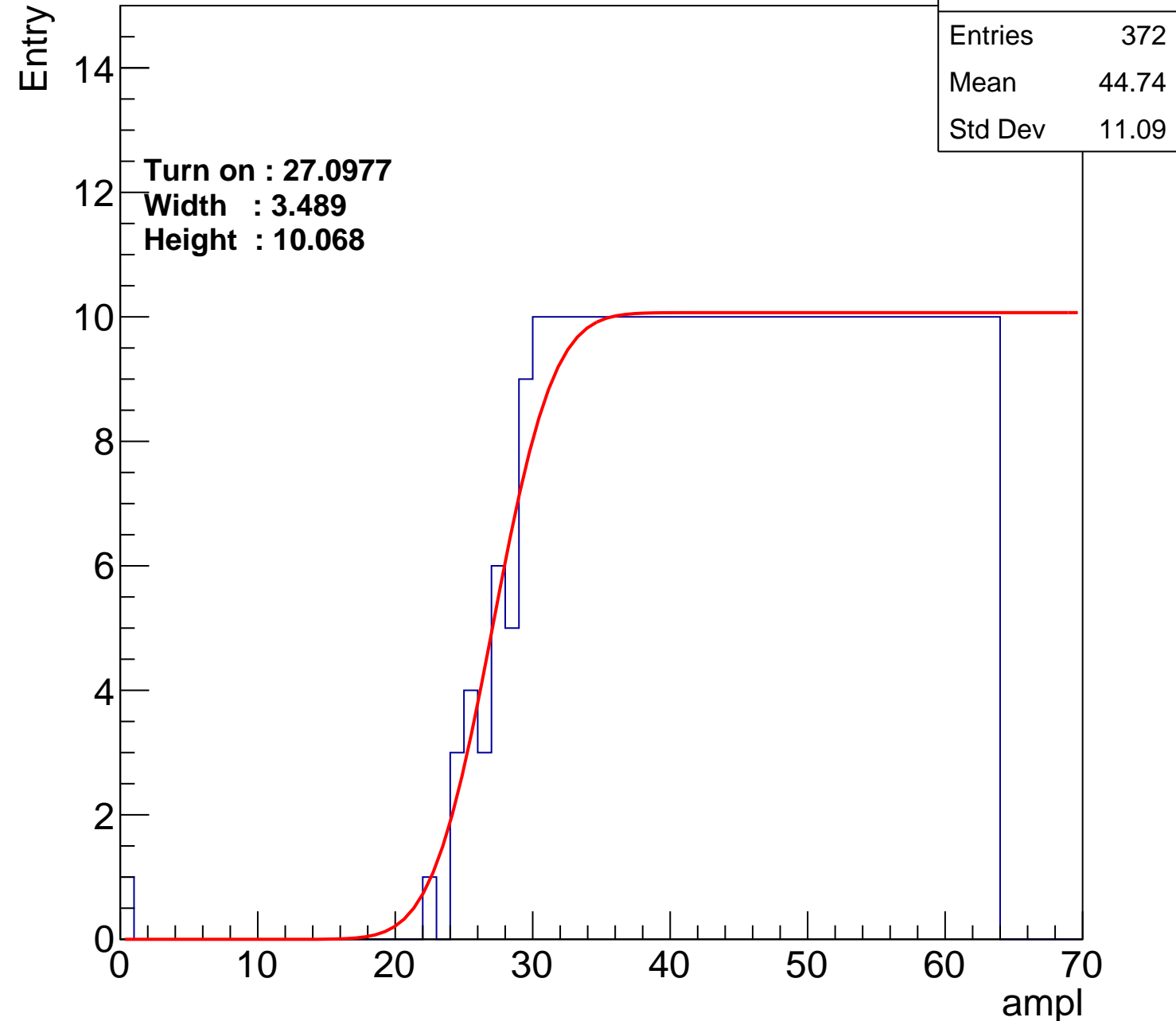
Width : 3.489

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch51

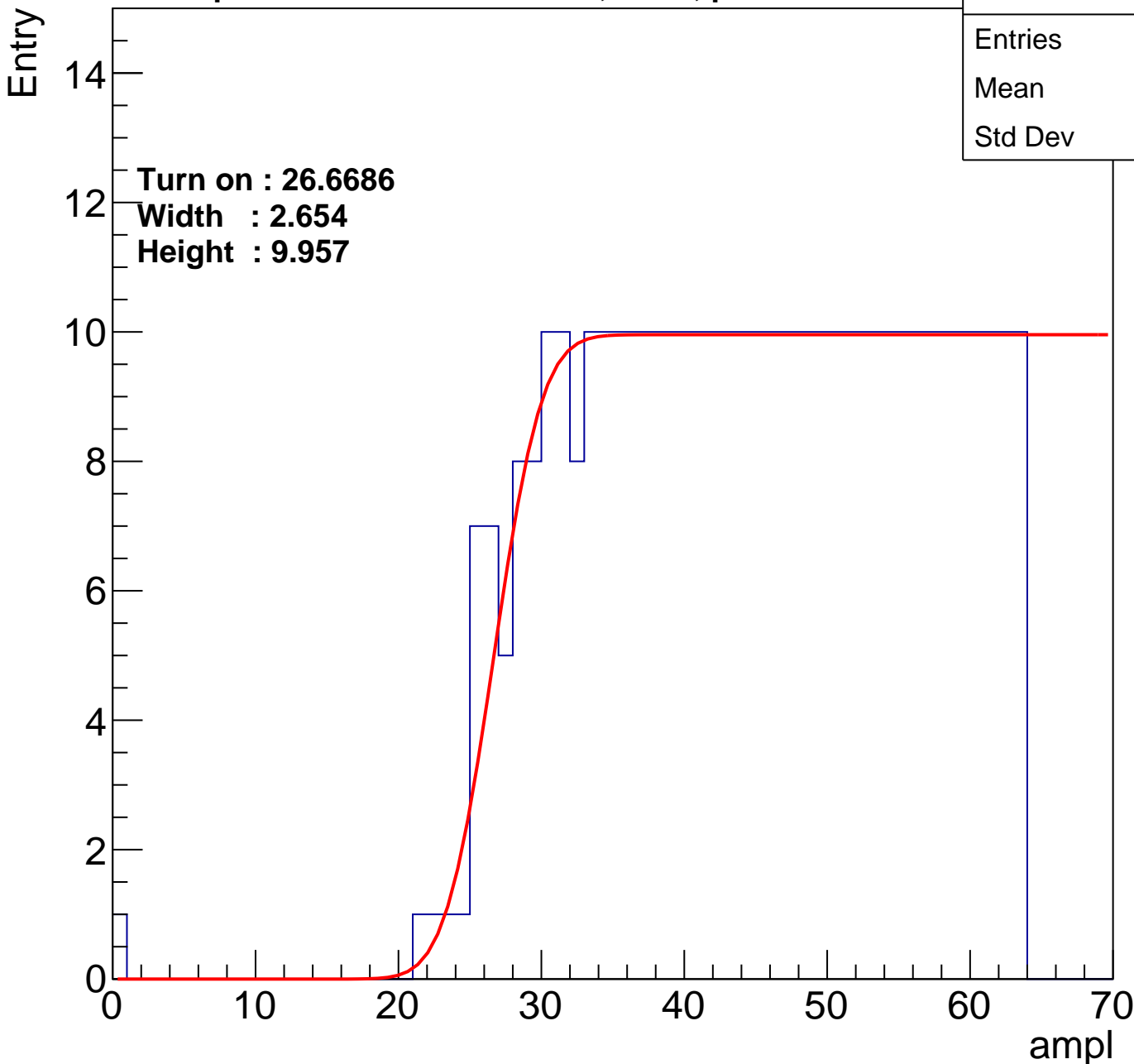
calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.4
Std Dev	11.31

Turn on : 26.6686

Width : 2.654

Height : 9.957



B0L002S, U9-ch52

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.79
Std Dev	11.39

Turn on : 27.9513

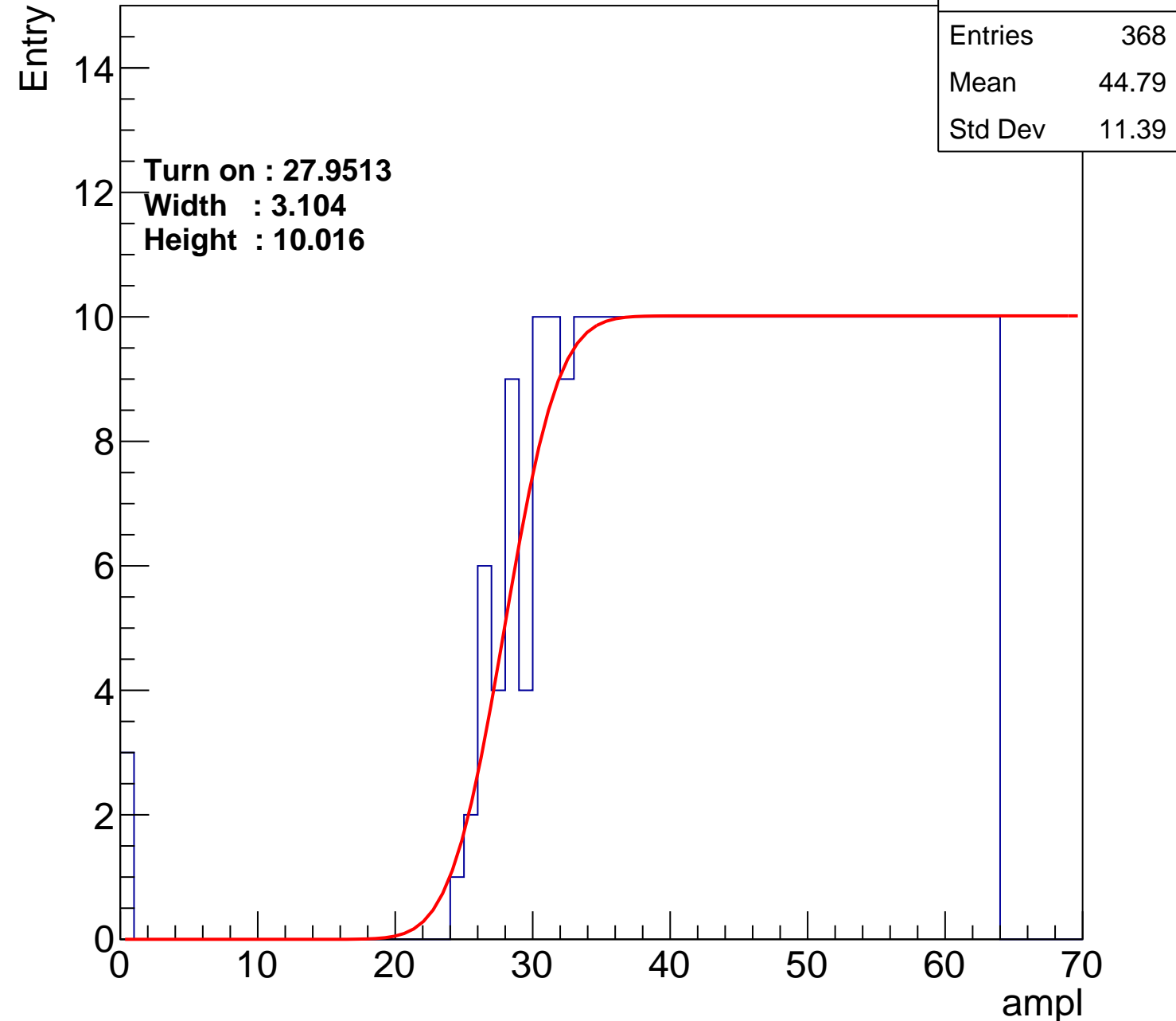
Width : 3.104

Height : 10.016

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch53

calib_packv5_042523_0143.root, FC#8, port C1

Entries	365
Mean	44.86
Std Dev	11.52

Turn on : 27.8120

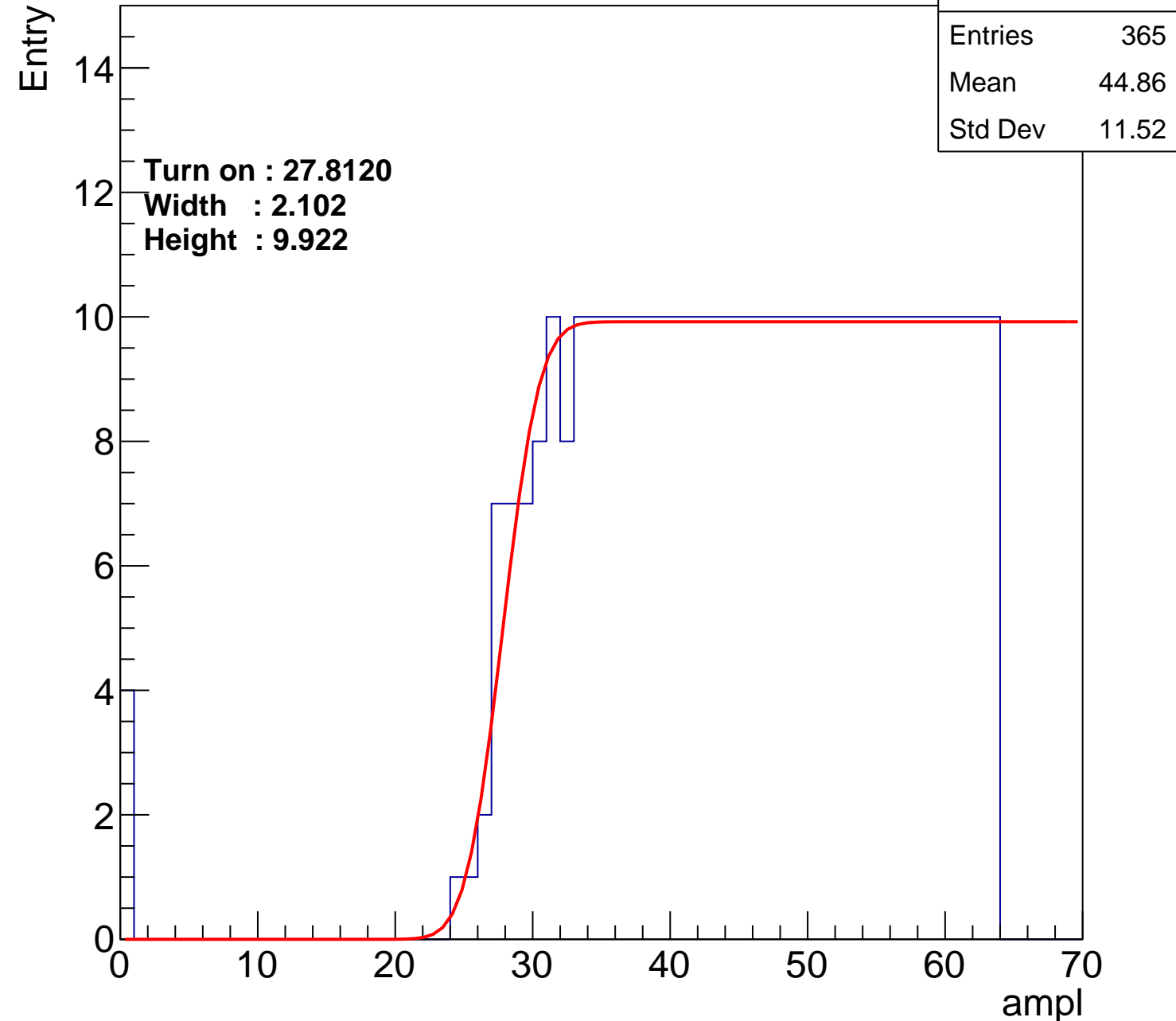
Width : 2.102

Height : 9.922

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch54

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.89
Std Dev	11.35

Turn on : 28.3025

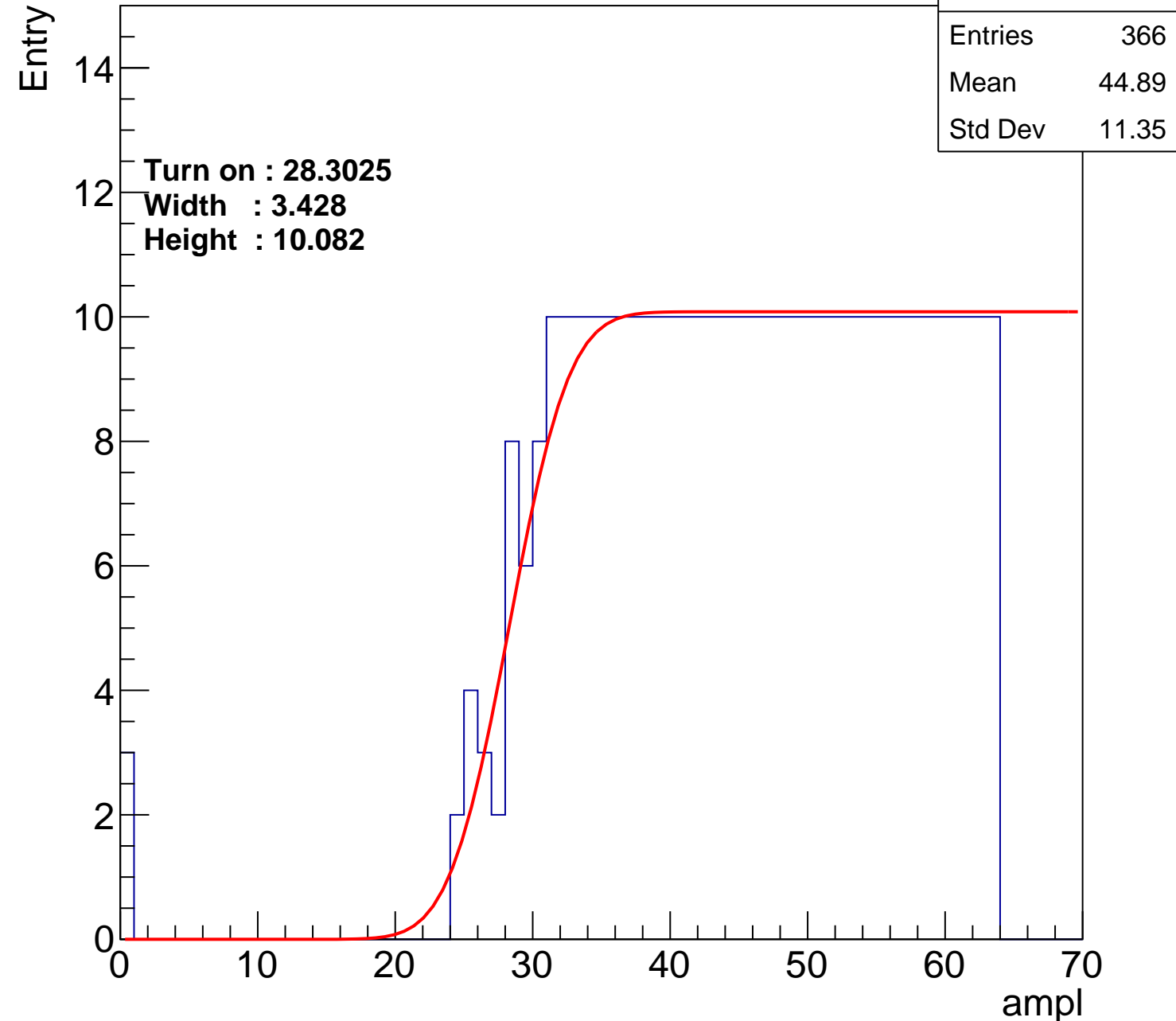
Width : 3.428

Height : 10.082

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch55

calib_packv5_042523_0143.root, FC#8, port C1

Entries	348
Mean	45.88
Std Dev	10.53

Turn on : 29.6844

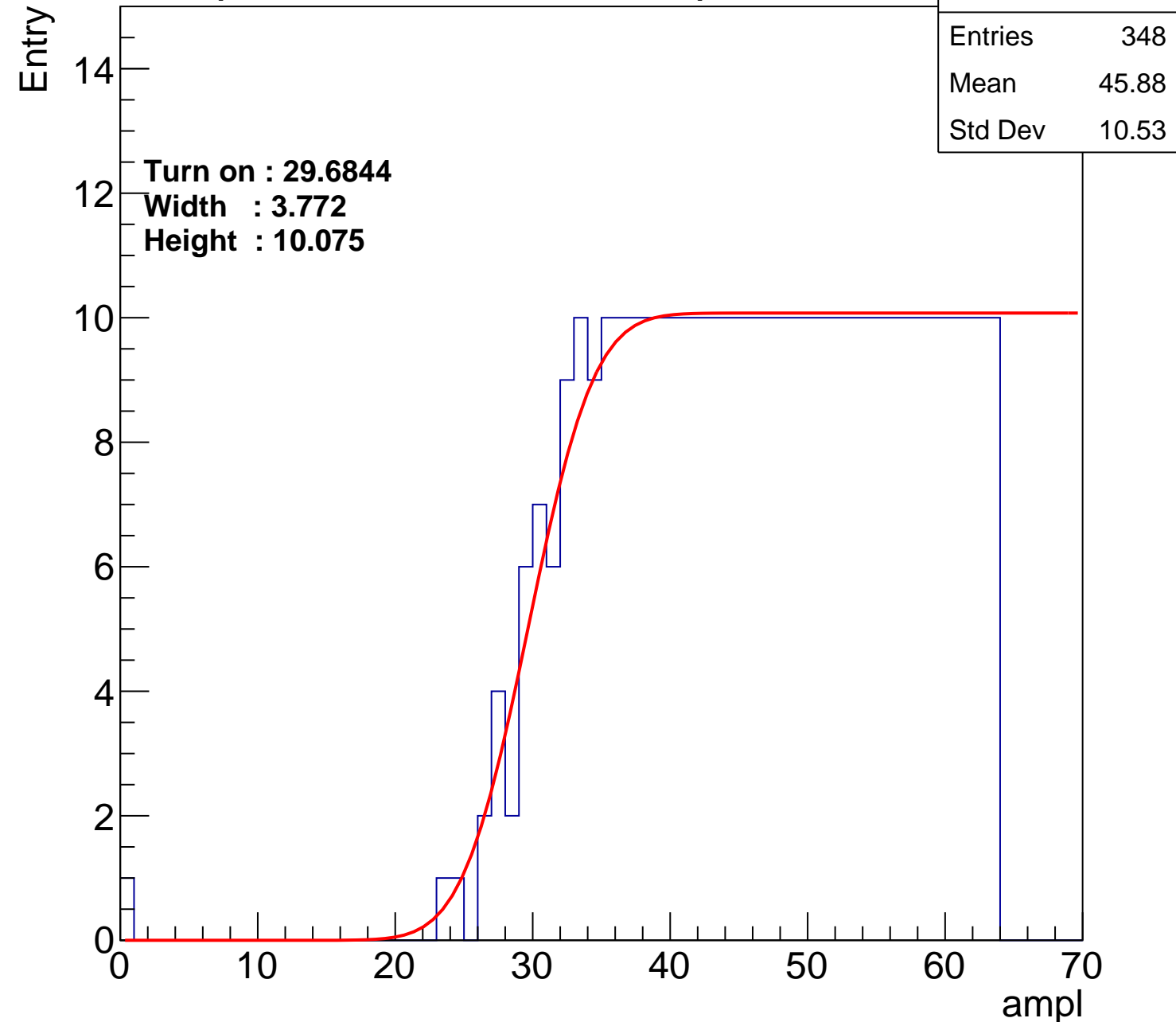
Width : 3.772

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch56

calib_packv5_042523_0143.root, FC#8, port C1

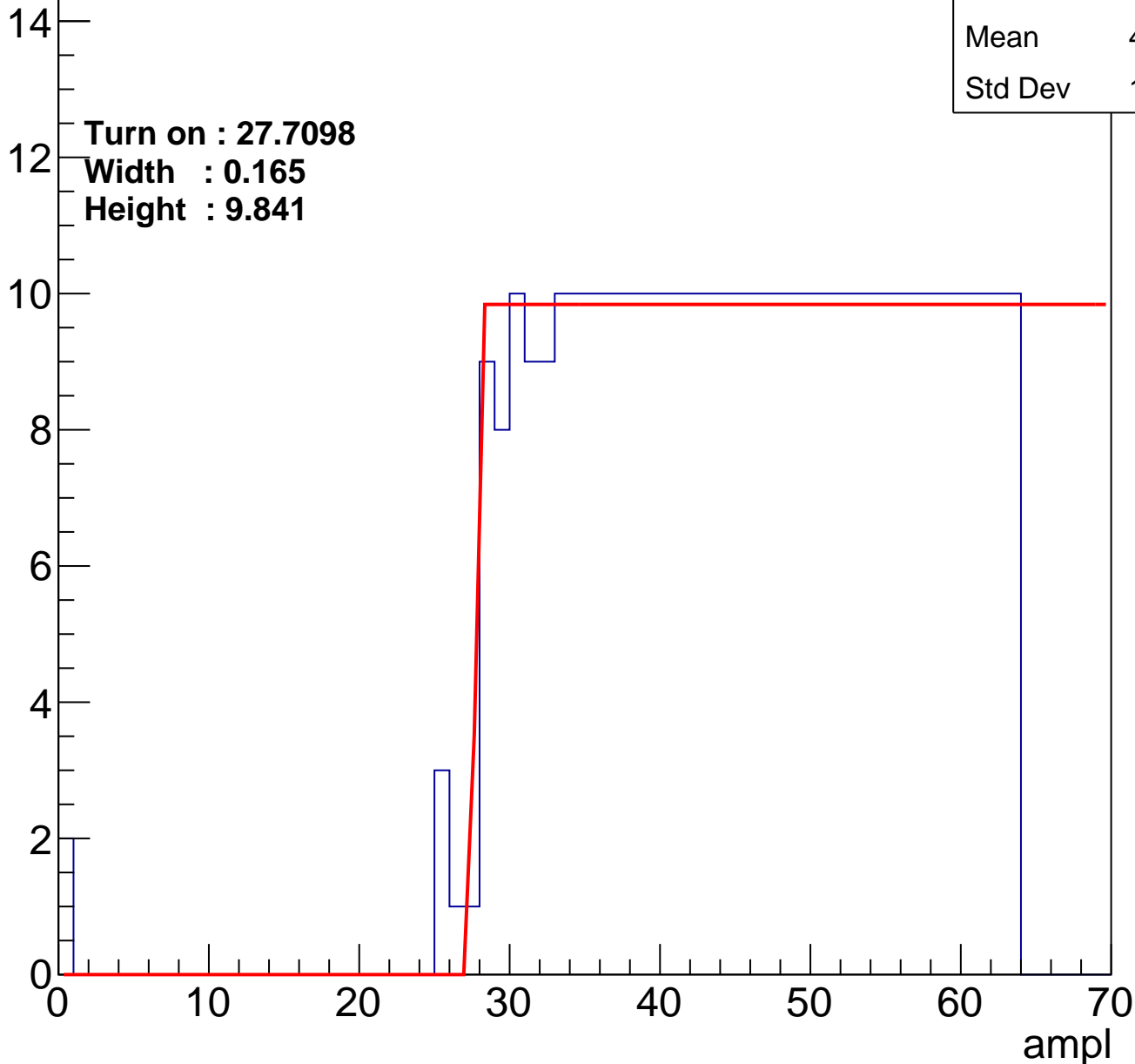
Entry

Entries	362
Mean	45.19
Std Dev	10.99

Turn on : 27.7098

Width : 0.165

Height : 9.841



B0L002S, U9-ch57

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.43
Std Dev	11.54

Turn on : 26.7495

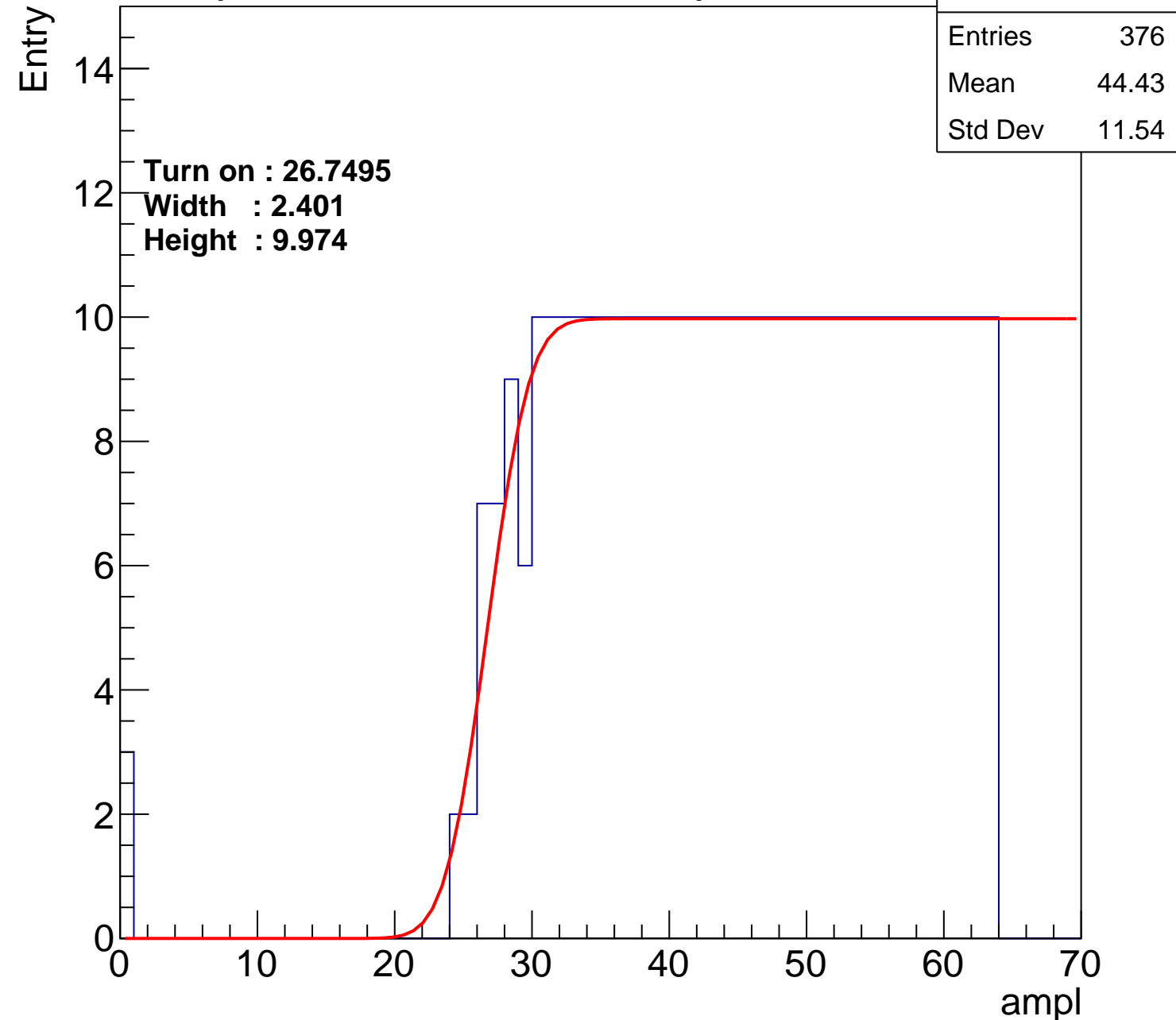
Width : 2.401

Height : 9.974

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch58

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.24
Std Dev	11.81

Turn on : 26.7342

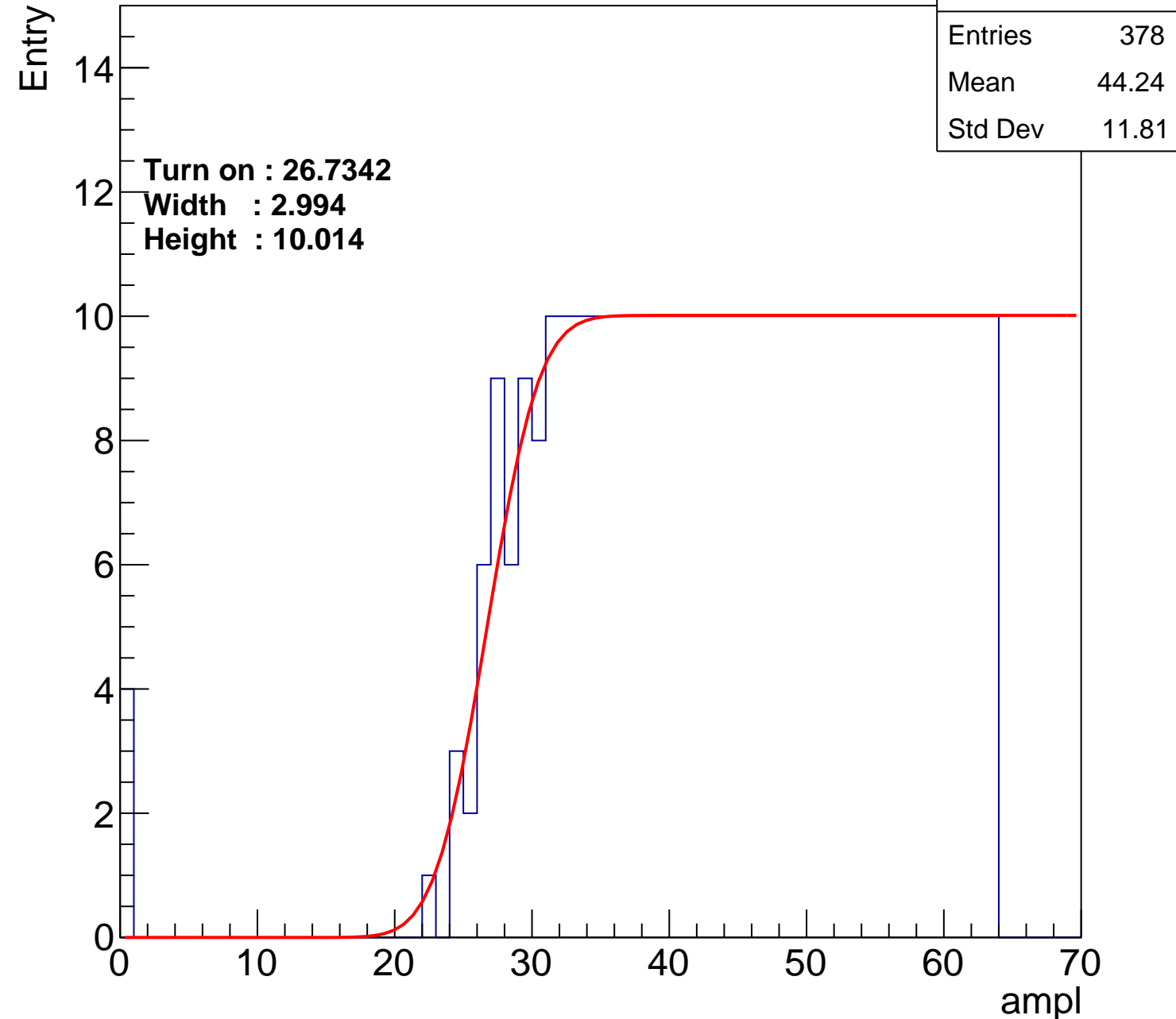
Width : 2.994

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch59

calib_packv5_042523_0143.root, FC#8, port C1

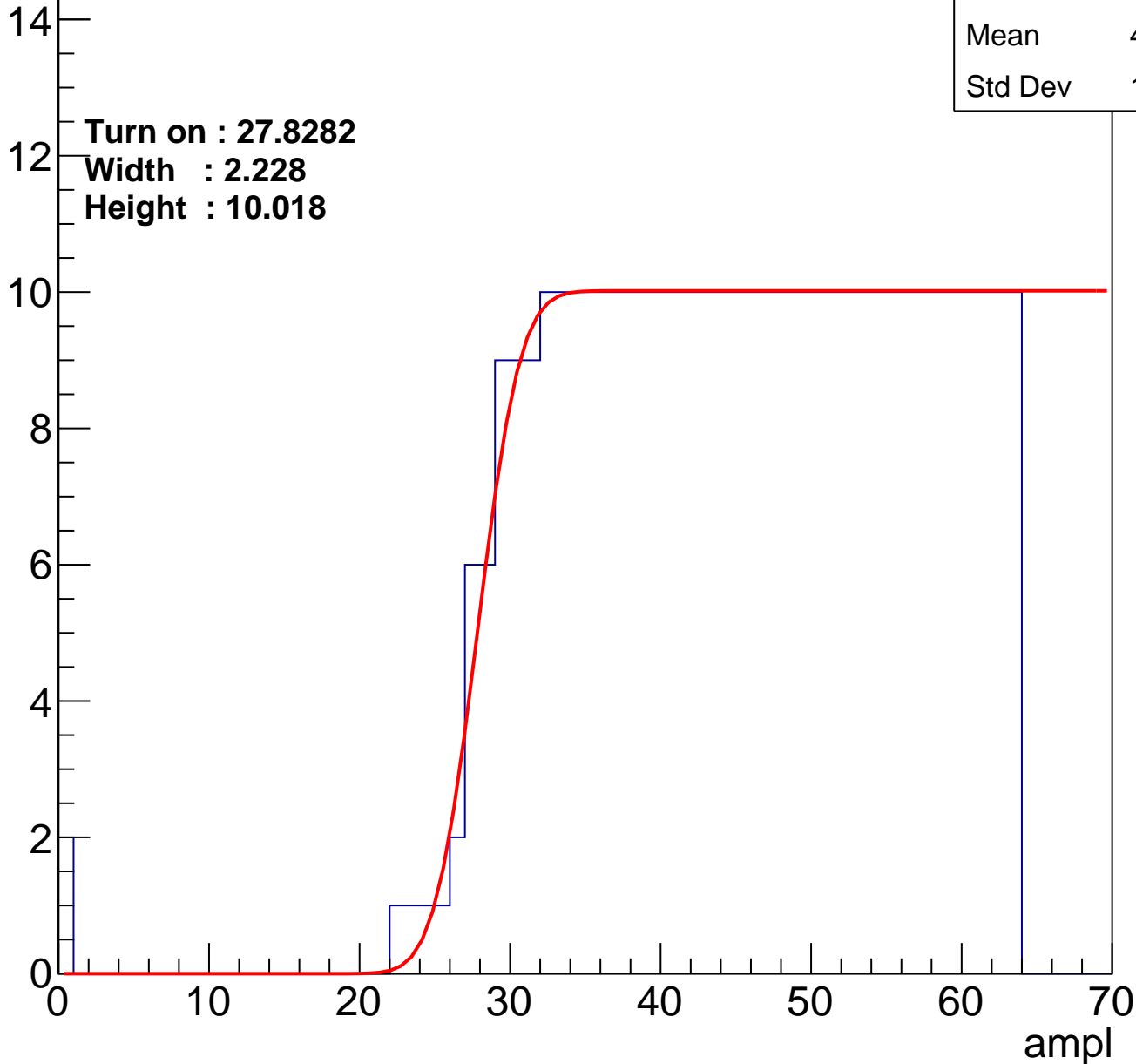
Entry

Entries	367
Mean	44.92
Std Dev	11.16

Turn on : 27.8282

Width : 2.228

Height : 10.018



B0L002S, U9-ch60

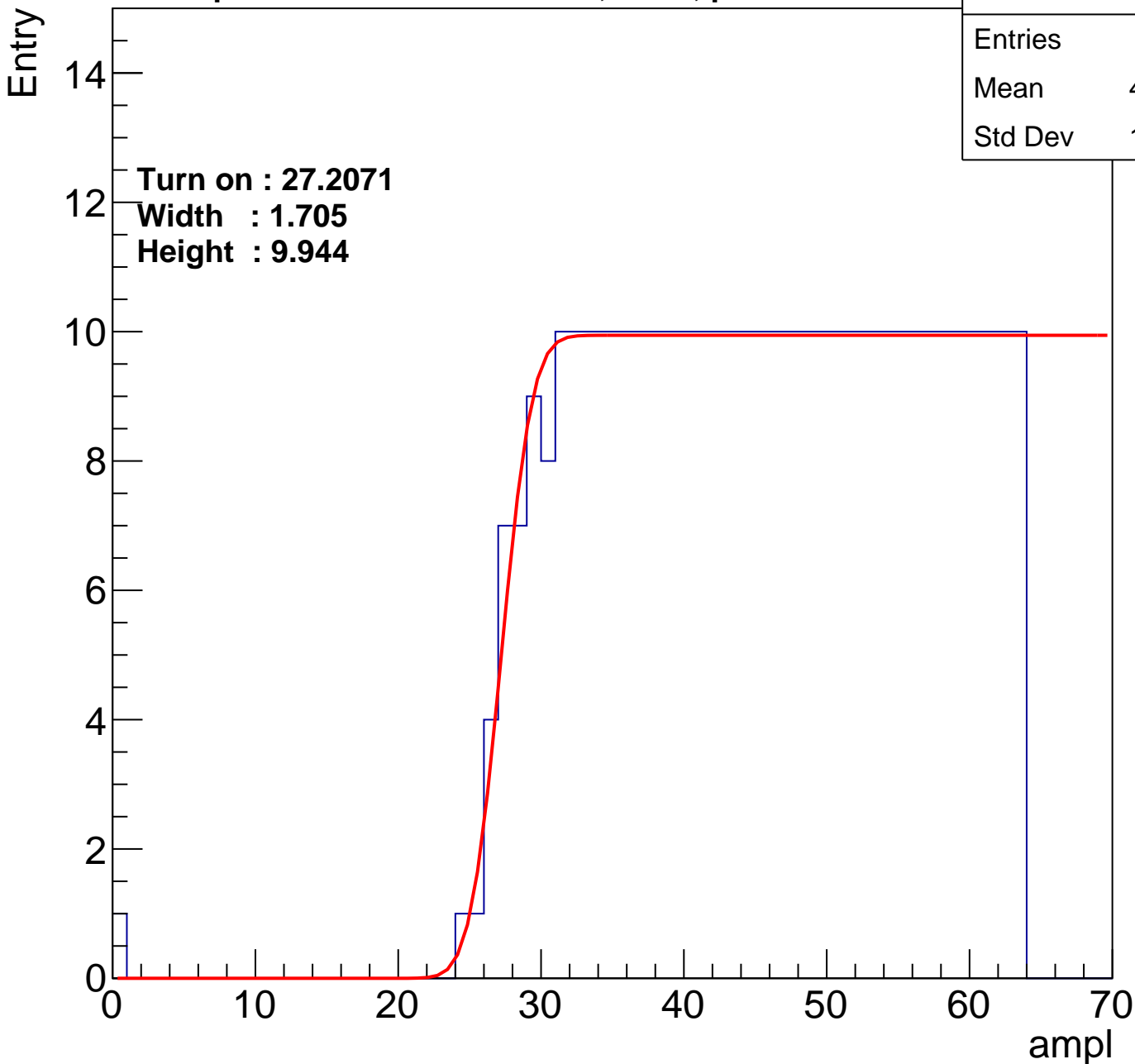
calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.97
Std Dev	10.93

Turn on : 27.2071

Width : 1.705

Height : 9.944



B0L002S, U9-ch61

calib_packv5_042523_0143.root, FC#8, port C1

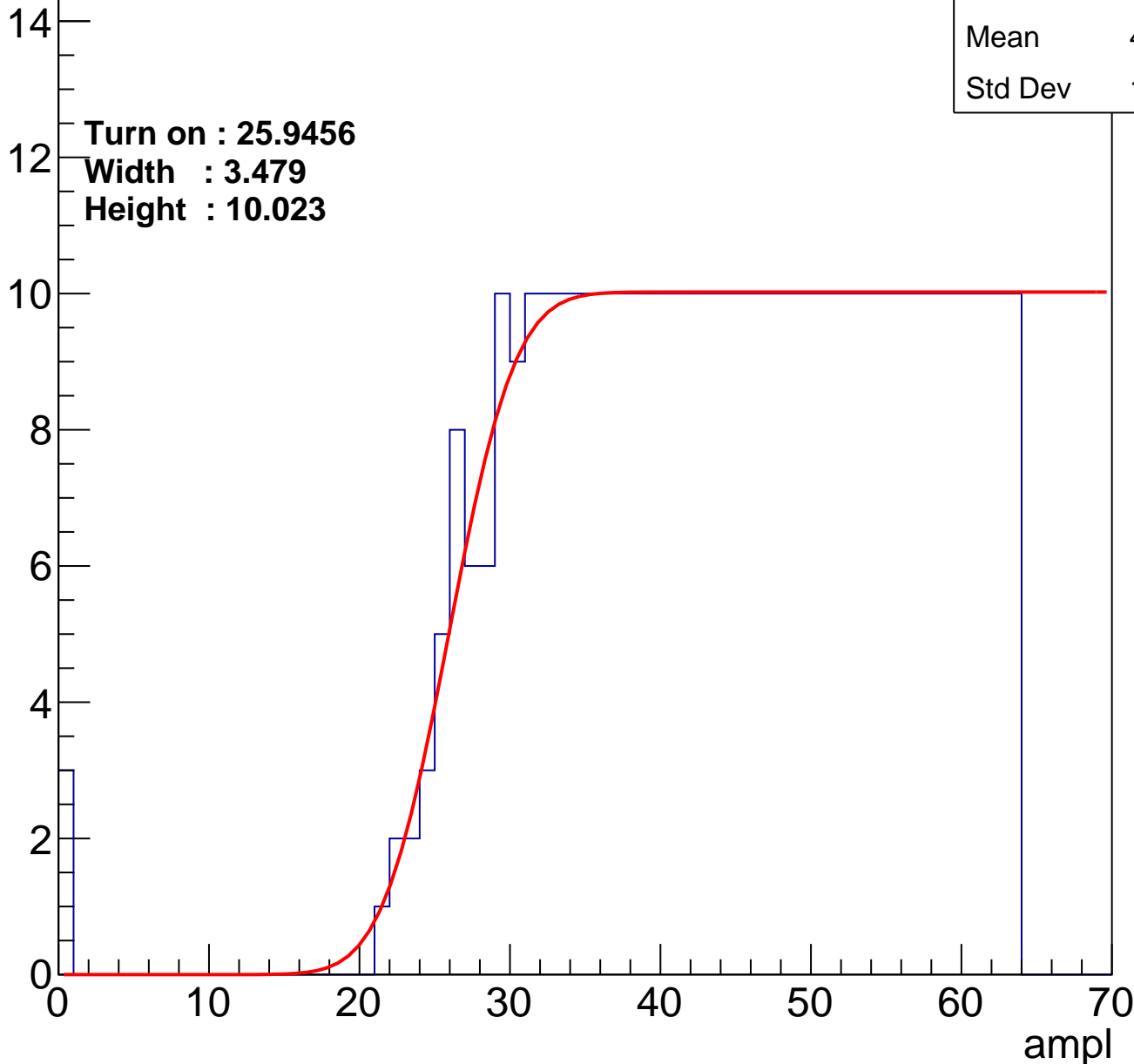
Entries	385
Mean	43.94
Std Dev	11.84

Turn on : 25.9456

Width : 3.479

Height : 10.023

Entry



B0L002S, U9-ch62

calib_packv5_042523_0143.root, FC#8, port C1

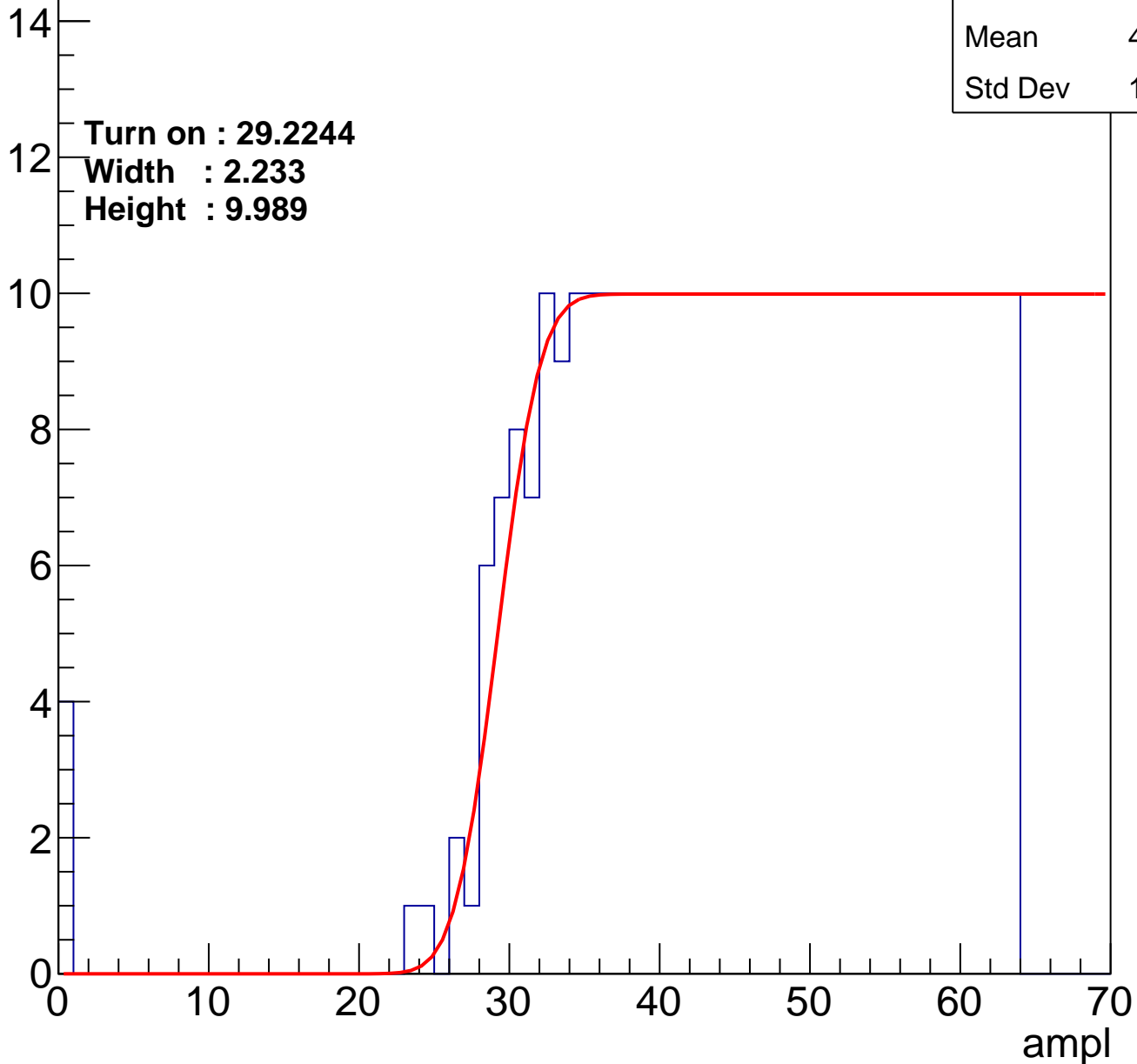
Entry

Entries	356
Mean	45.28
Std Dev	11.35

Turn on : 29.2244

Width : 2.233

Height : 9.989



B0L002S, U9-ch63

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.07
Std Dev	11.87

Turn on : 26.1598

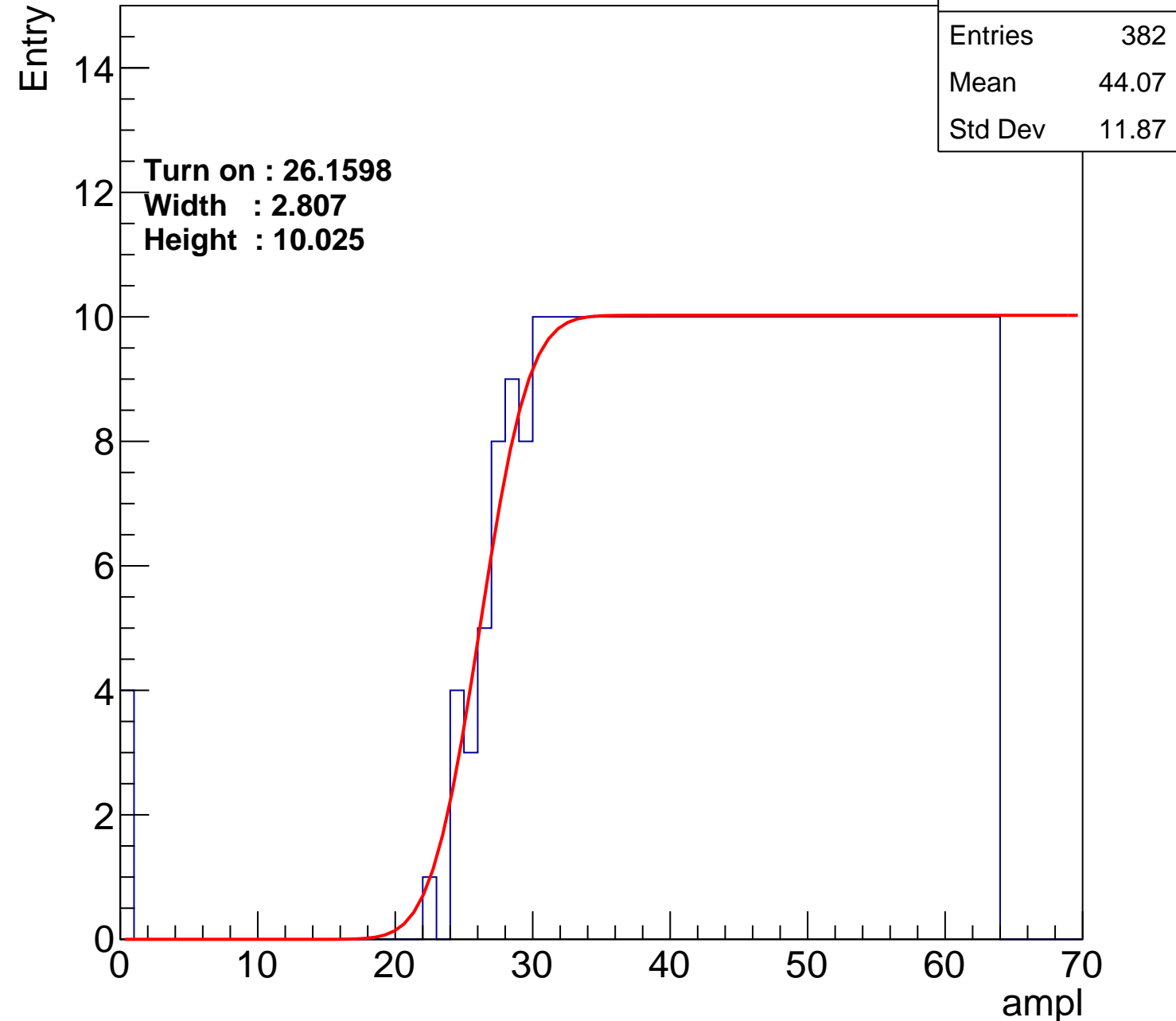
Width : 2.807

Height : 10.025

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch64

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.94
Std Dev	10.98

Turn on : 27.1981

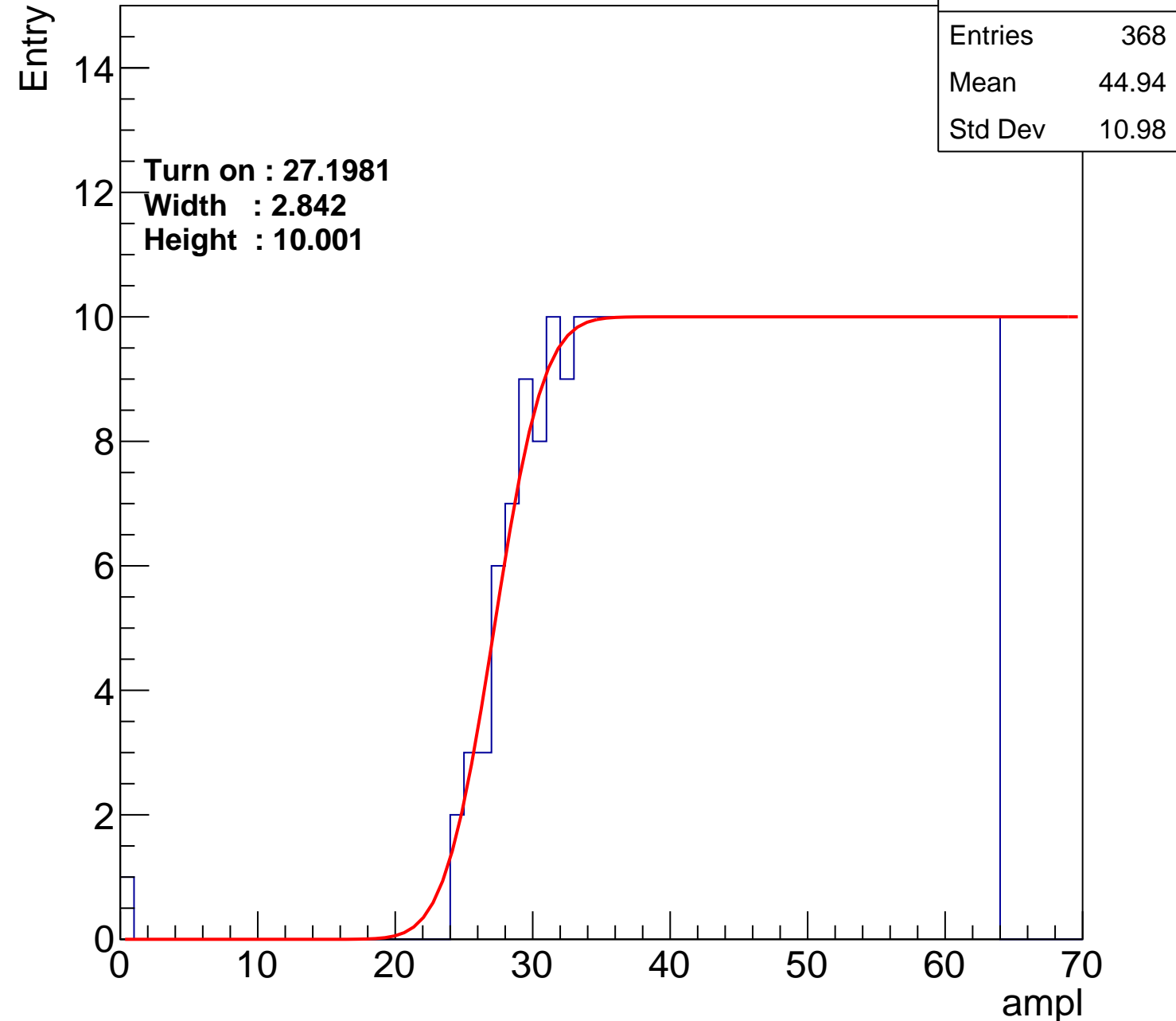
Width : 2.842

Height : 10.001

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch65

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.56
Std Dev	10.82

Turn on : 28.8651

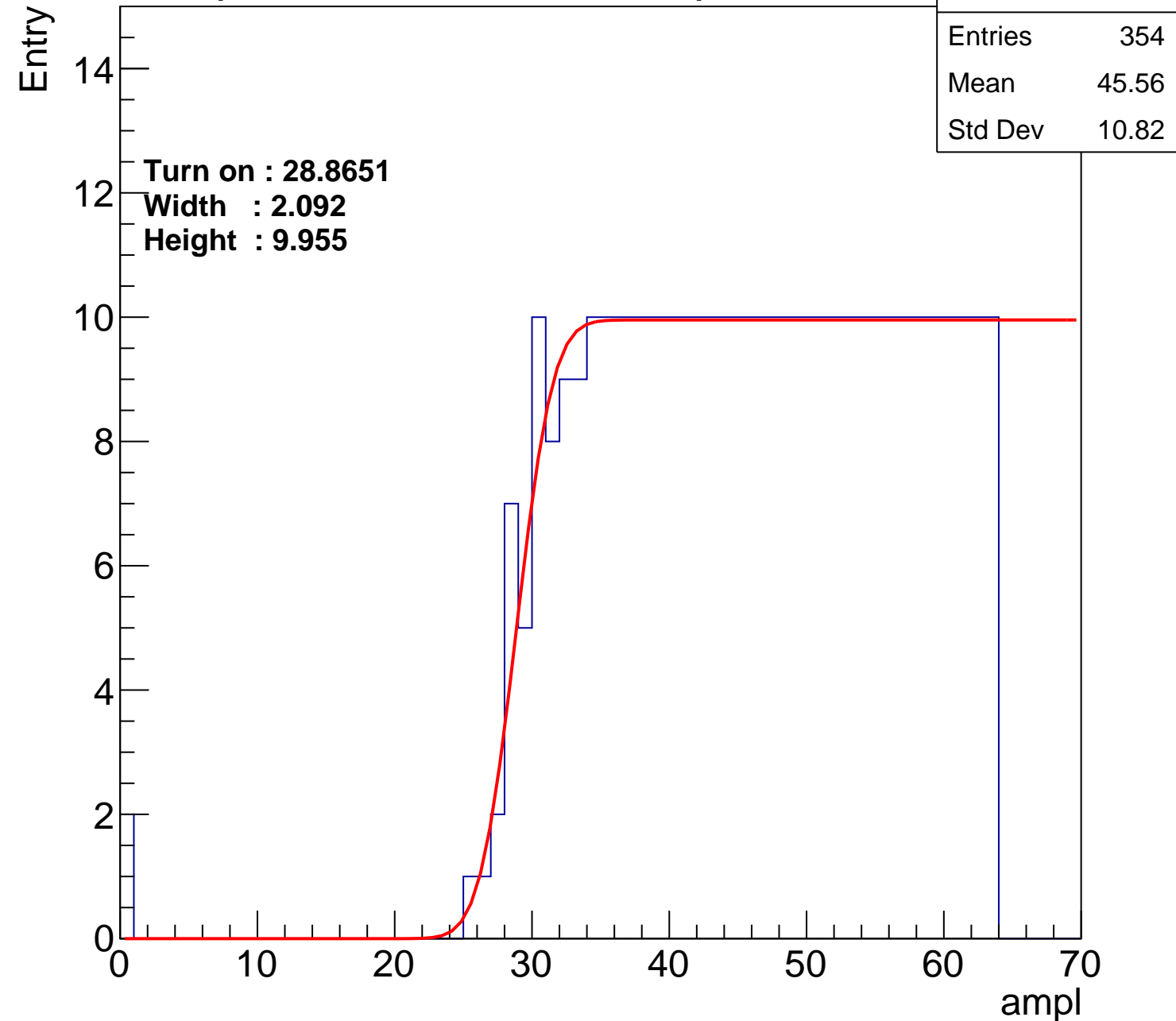
Width : 2.092

Height : 9.955

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch66

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.26
Std Dev	11.33

Turn on : 26.0142

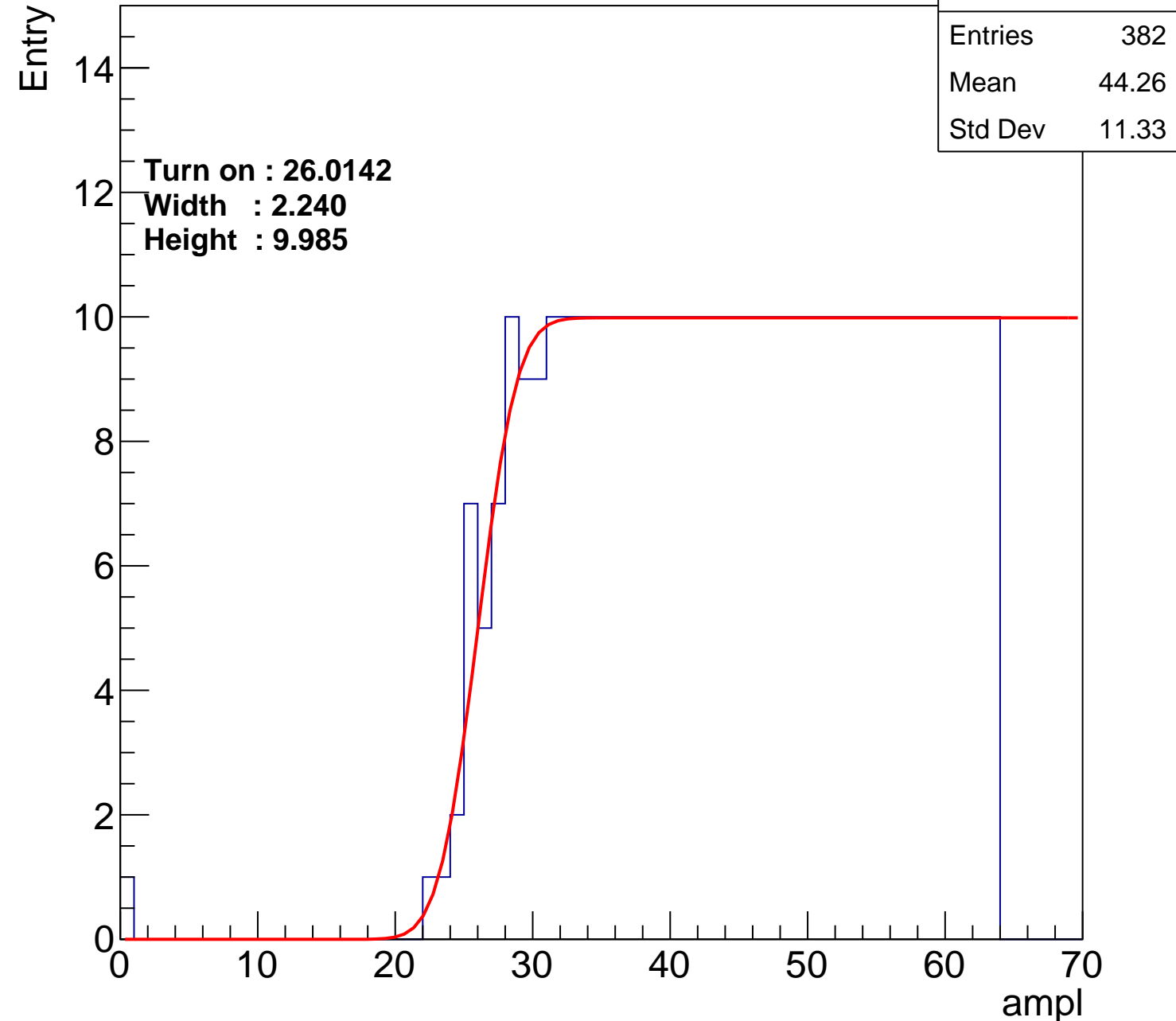
Width : 2.240

Height : 9.985

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch67

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.78
Std Dev	11.08

Turn on : 27.0629

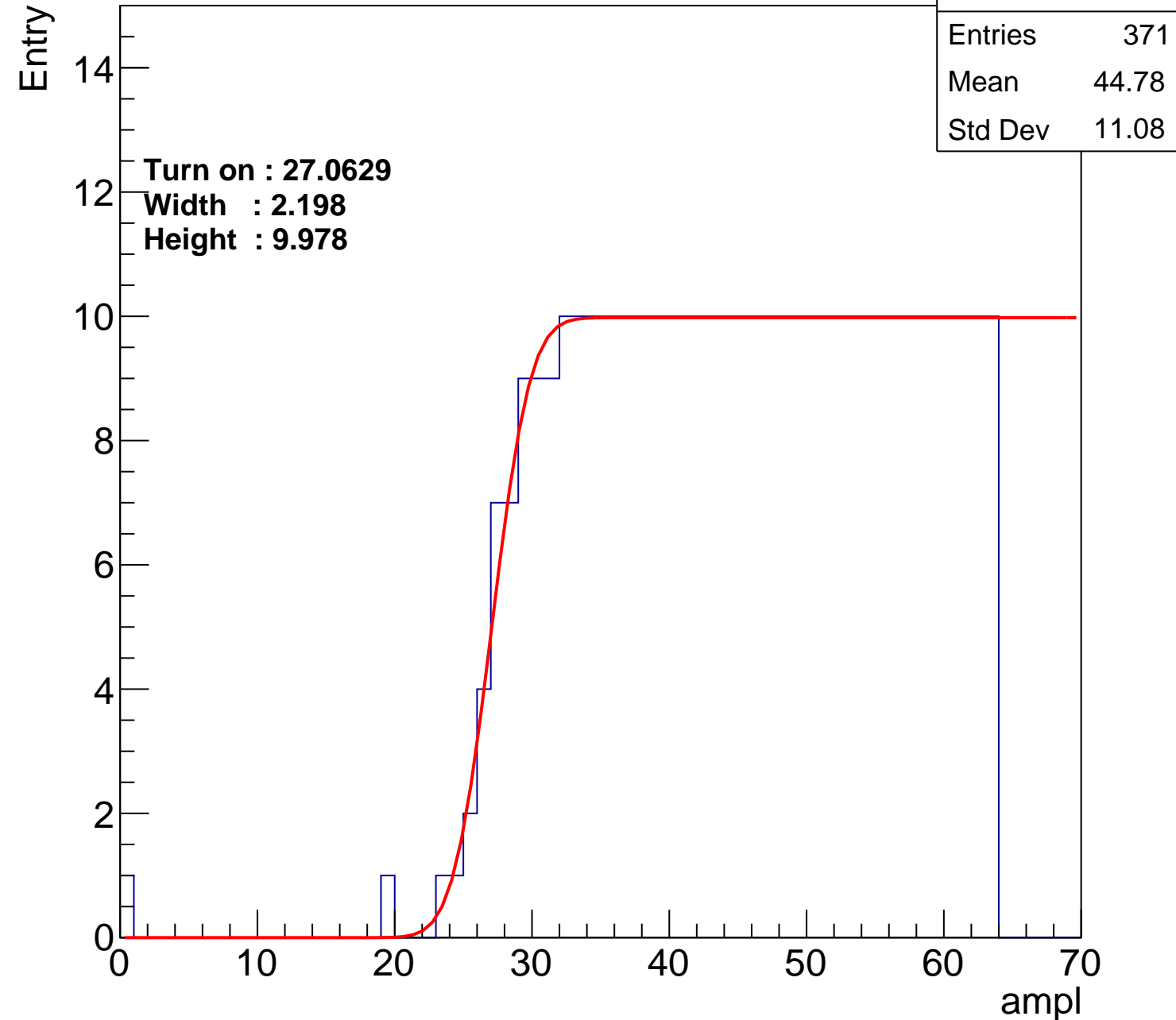
Width : 2.198

Height : 9.978

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch68

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.64
Std Dev	10.76

Turn on : 28.8280

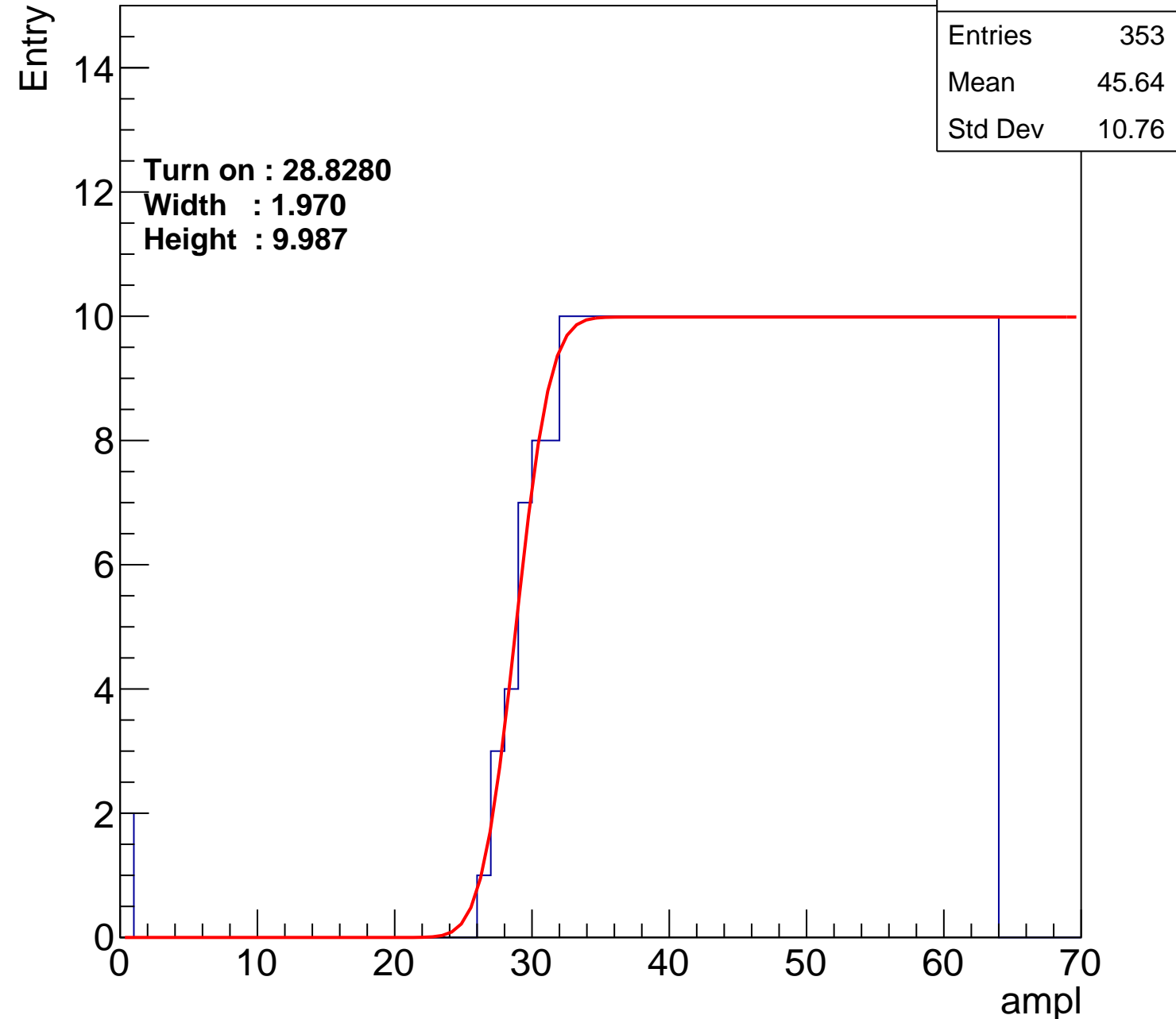
Width : 1.970

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch69

calib_packv5_042523_0143.root, FC#8, port C1

Entries	356
Mean	45.45
Std Dev	10.9

Turn on : 28.8680

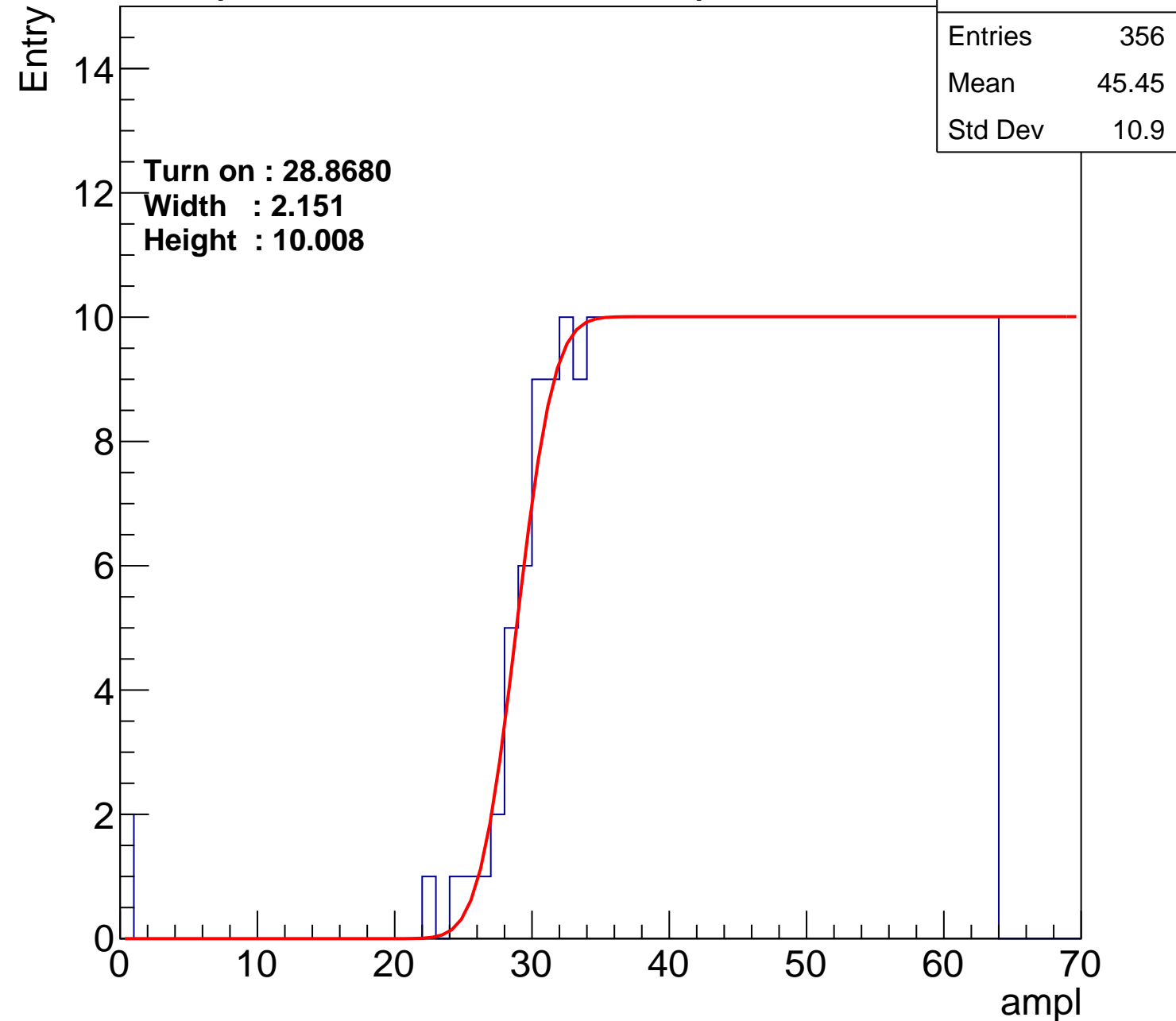
Width : 2.151

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch70

calib_packv5_042523_0143.root, FC#8, port C1

Entries	360
Mean	45.3
Std Dev	10.92

Turn on : 28.1160

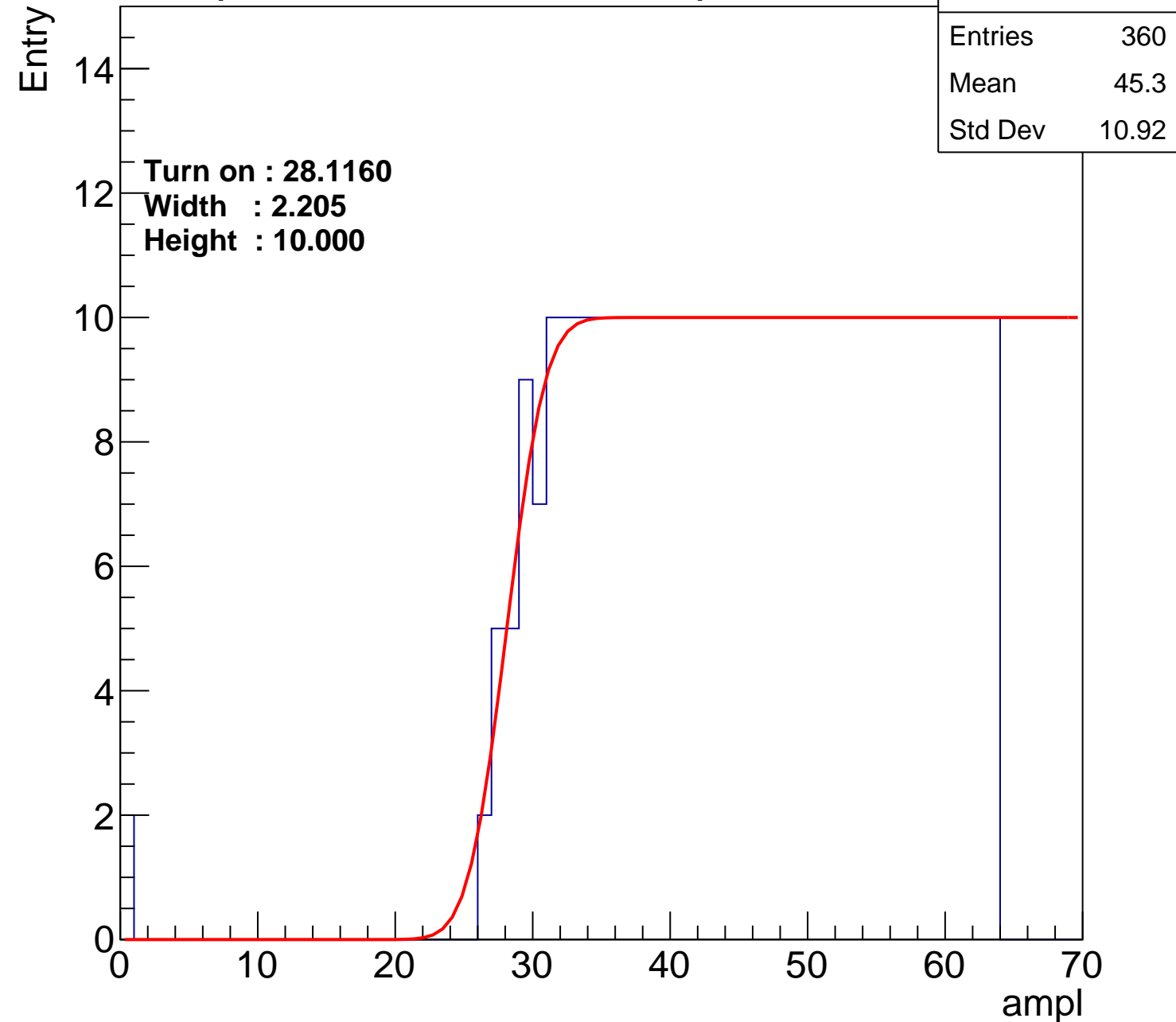
Width : 2.205

Height : 10.000

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch71

calib_packv5_042523_0143.root, FC#8, port C1

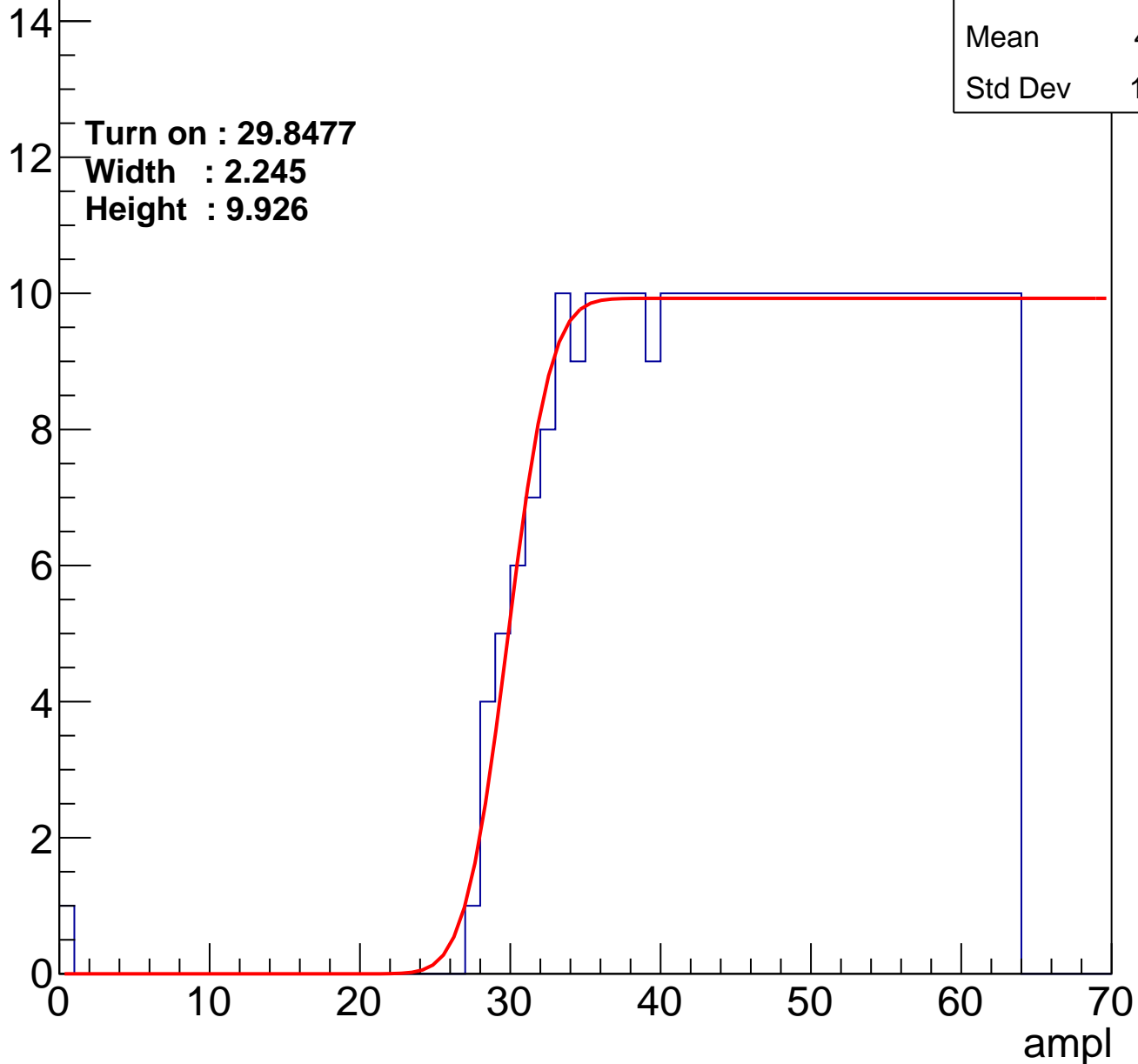
Entries	340
Mean	46.31
Std Dev	10.25

Turn on : 29.8477

Width : 2.245

Height : 9.926

Entry



B0L002S, U9-ch72

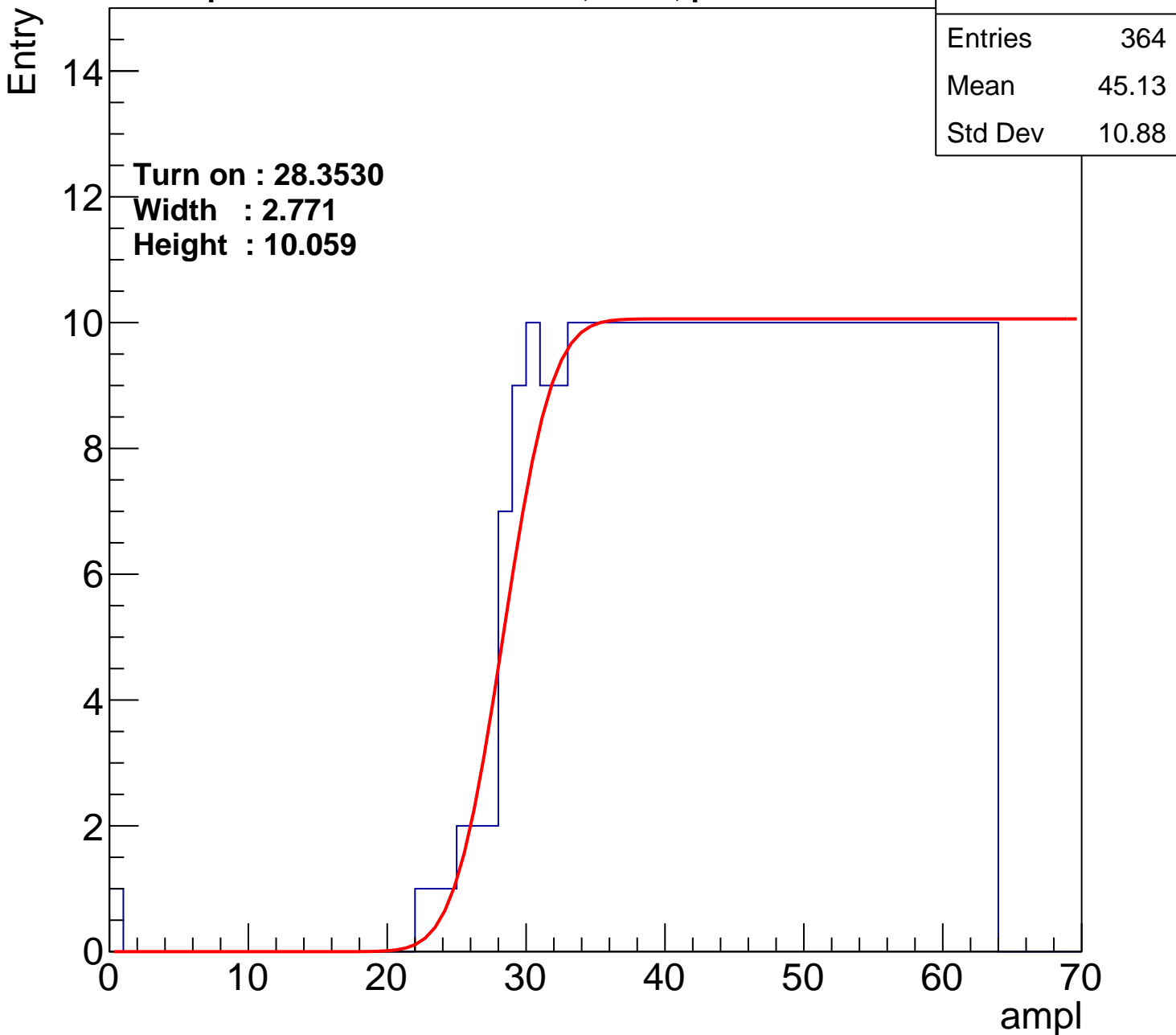
calib_packv5_042523_0143.root, FC#8, port C1

Turn on : 28.3530

Width : 2.771

Height : 10.059

Entries	364
Mean	45.13
Std Dev	10.88



B0L002S, U9-ch73

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.51
Std Dev	11.57

Turn on : 26.9192

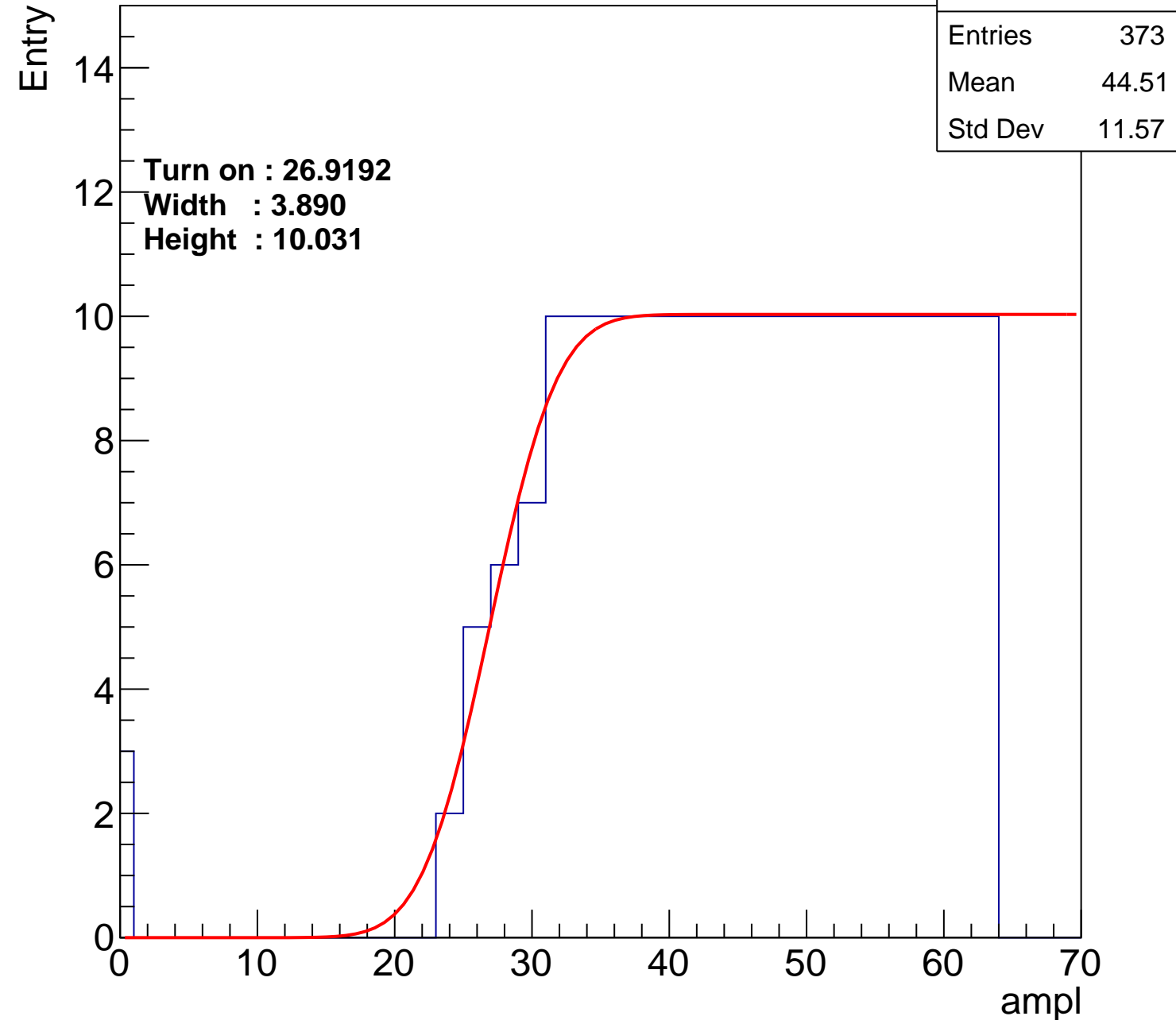
Width : 3.890

Height : 10.031

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch74

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.46
Std Dev	11.73

Turn on : 27.1031

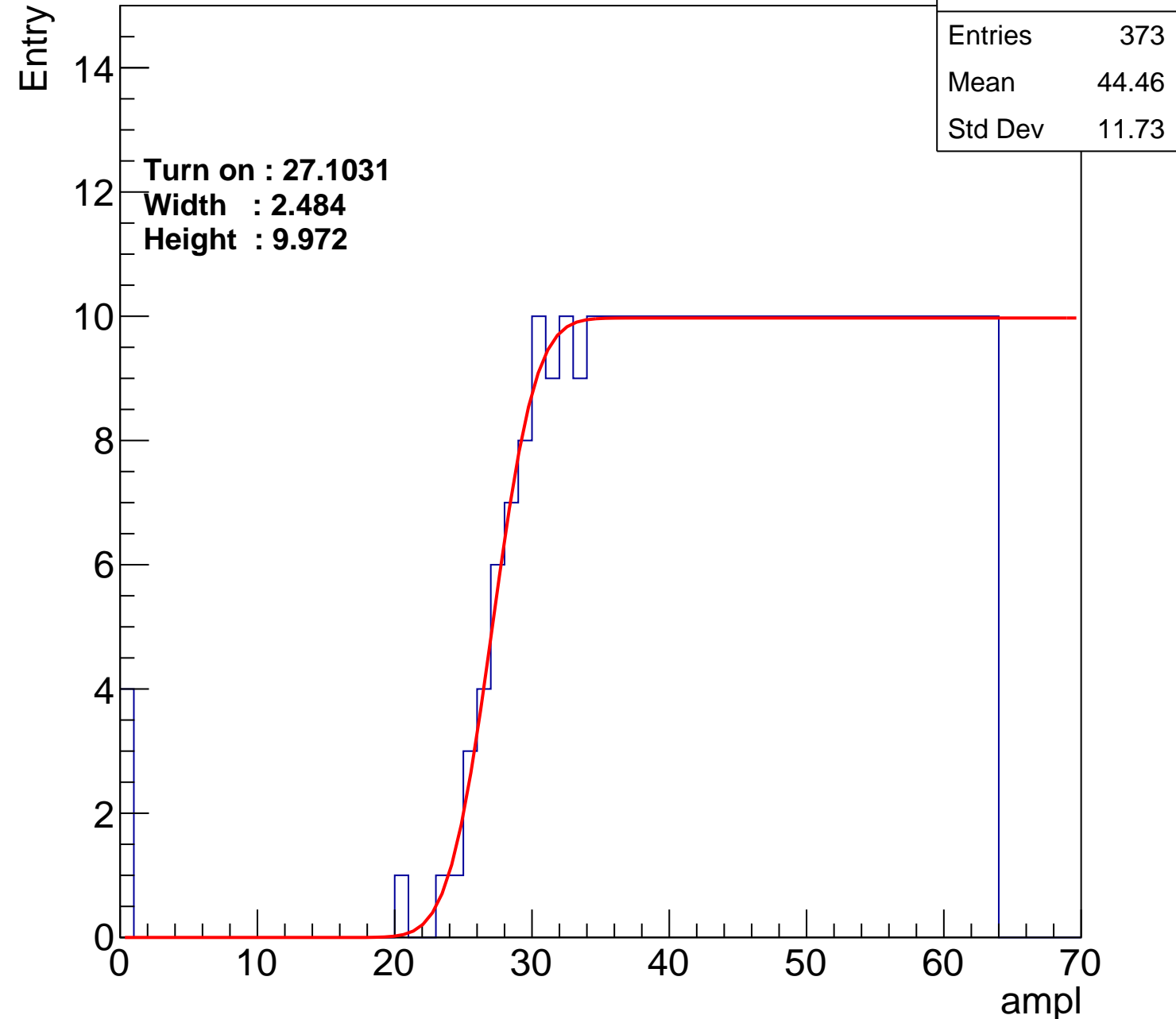
Width : 2.484

Height : 9.972

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch75

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.57
Std Dev	11.53

Turn on : 27.0036

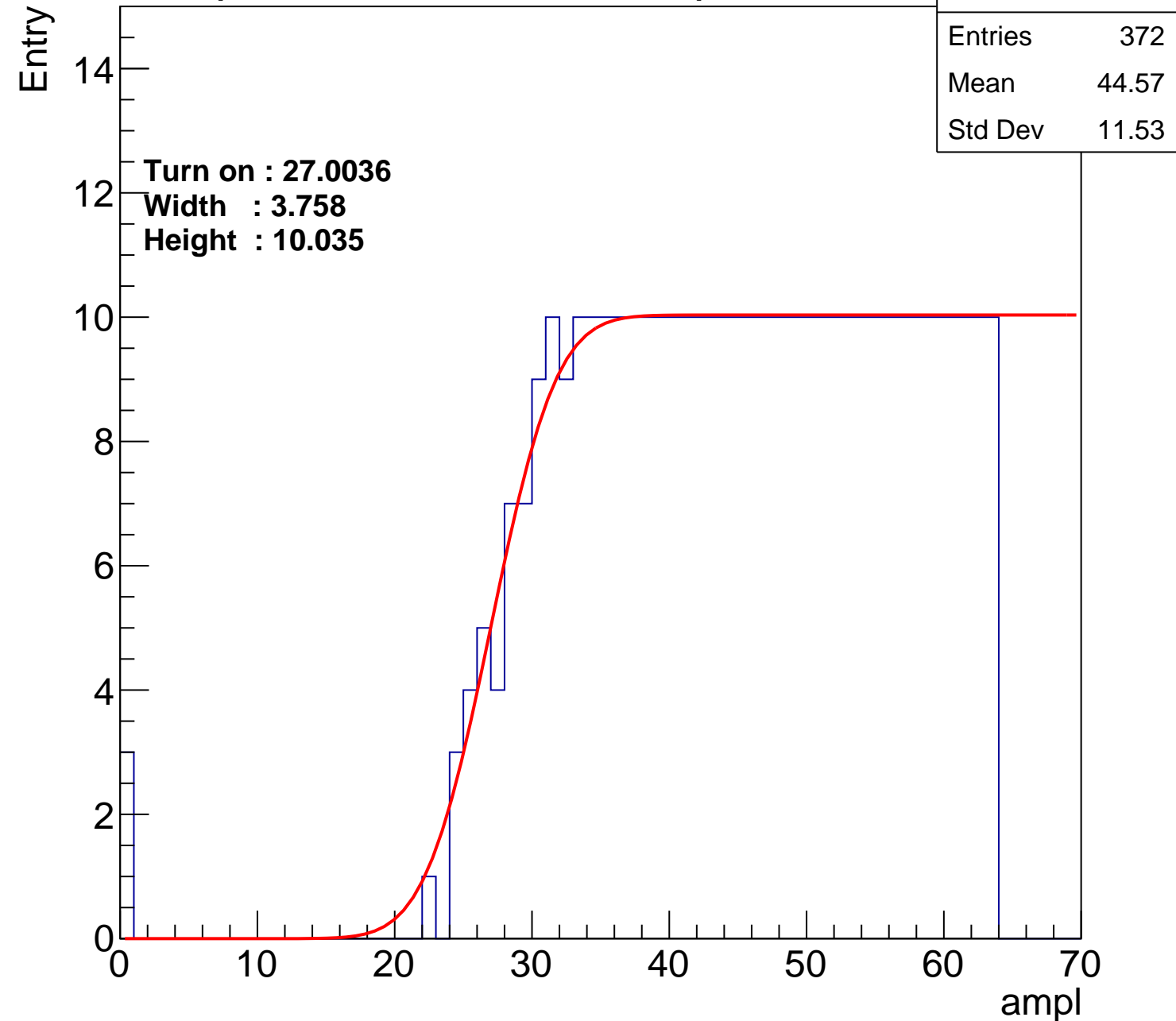
Width : 3.758

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch76

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.08
Std Dev	11.07

Turn on : 28.1369

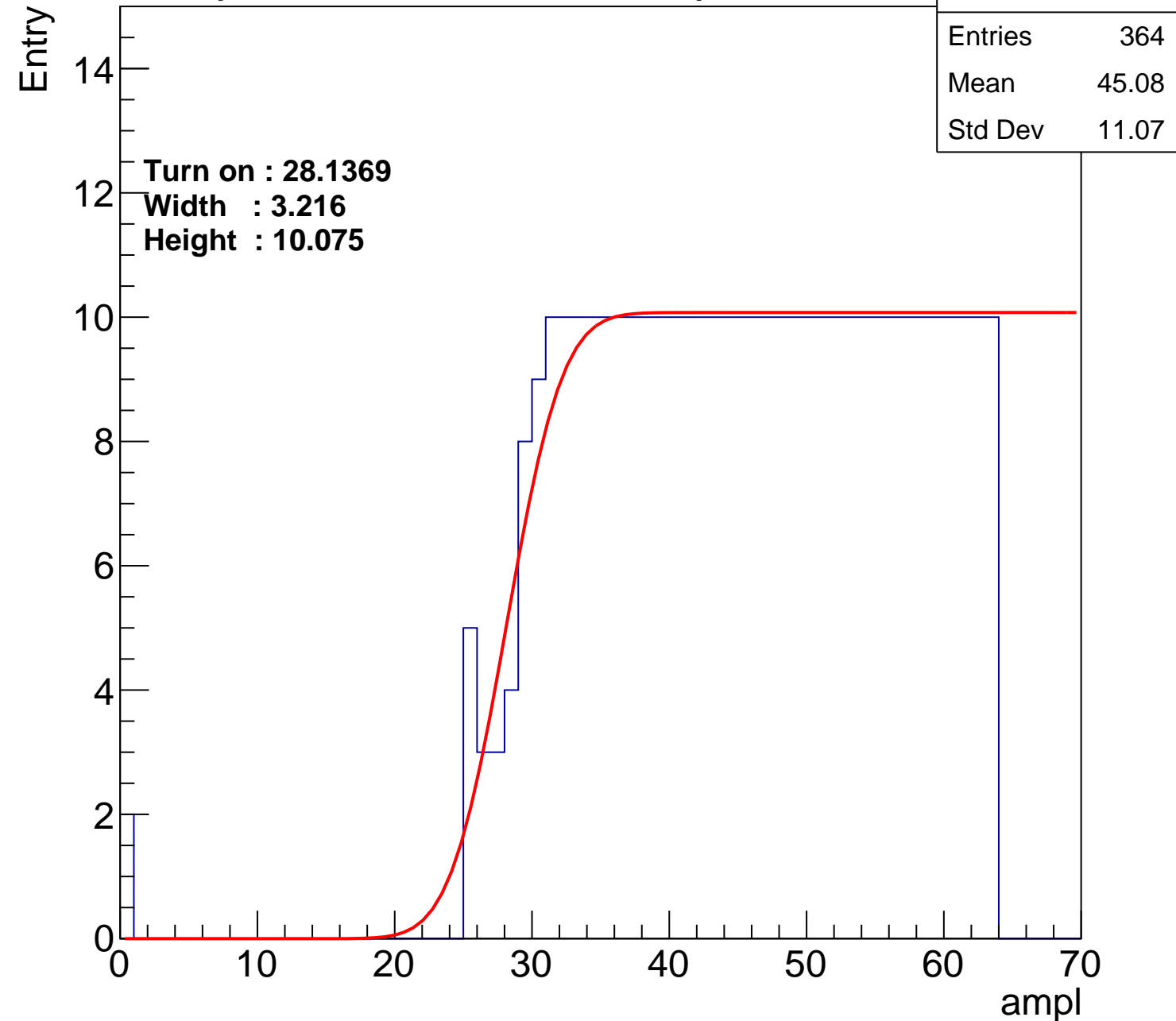
Width : 3.216

Height : 10.075

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch77

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.73
Std Dev	11.43

Turn on : 27.4174

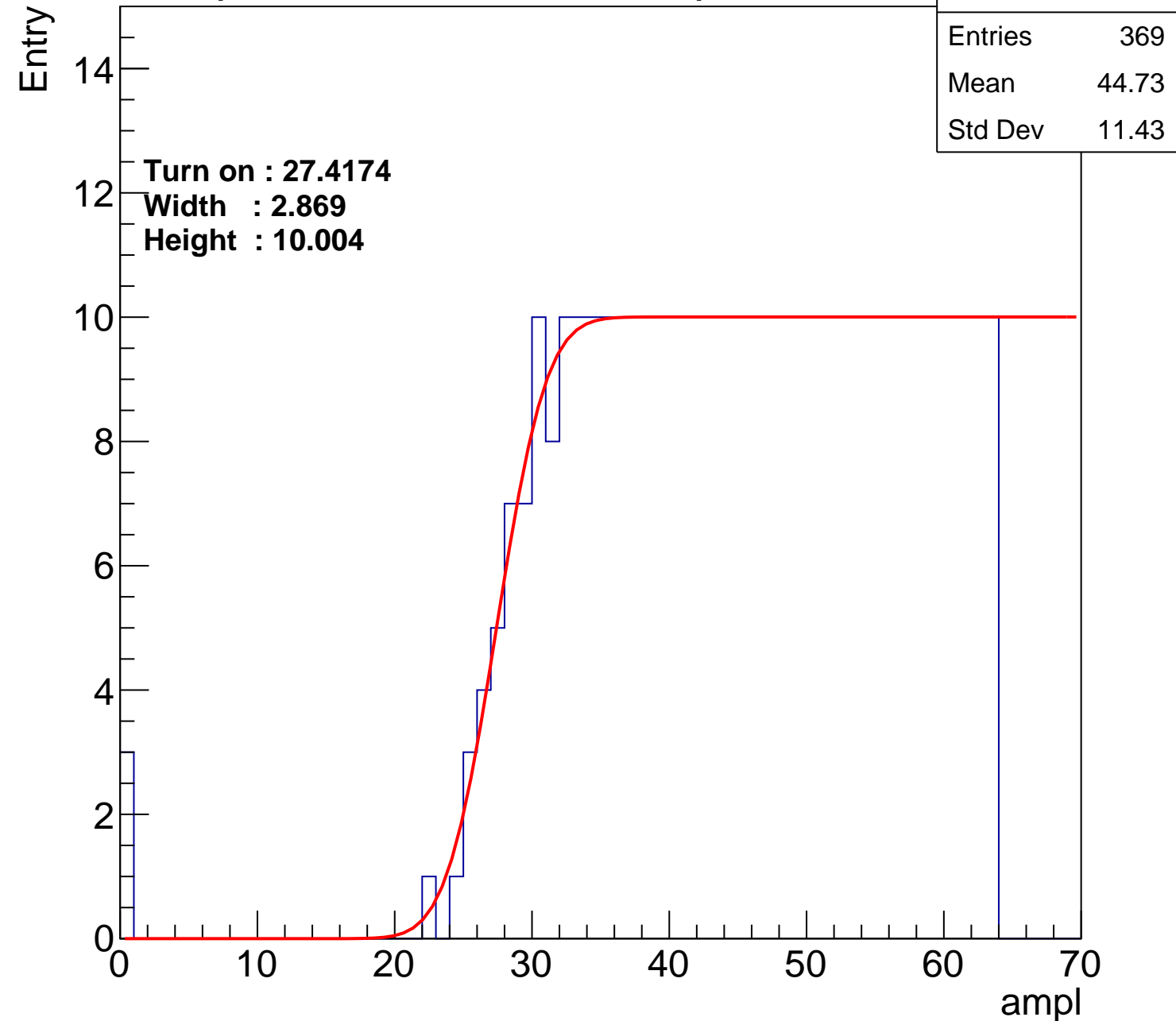
Width : 2.869

Height : 10.004

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch78

calib_packv5_042523_0143.root, FC#8, port C1

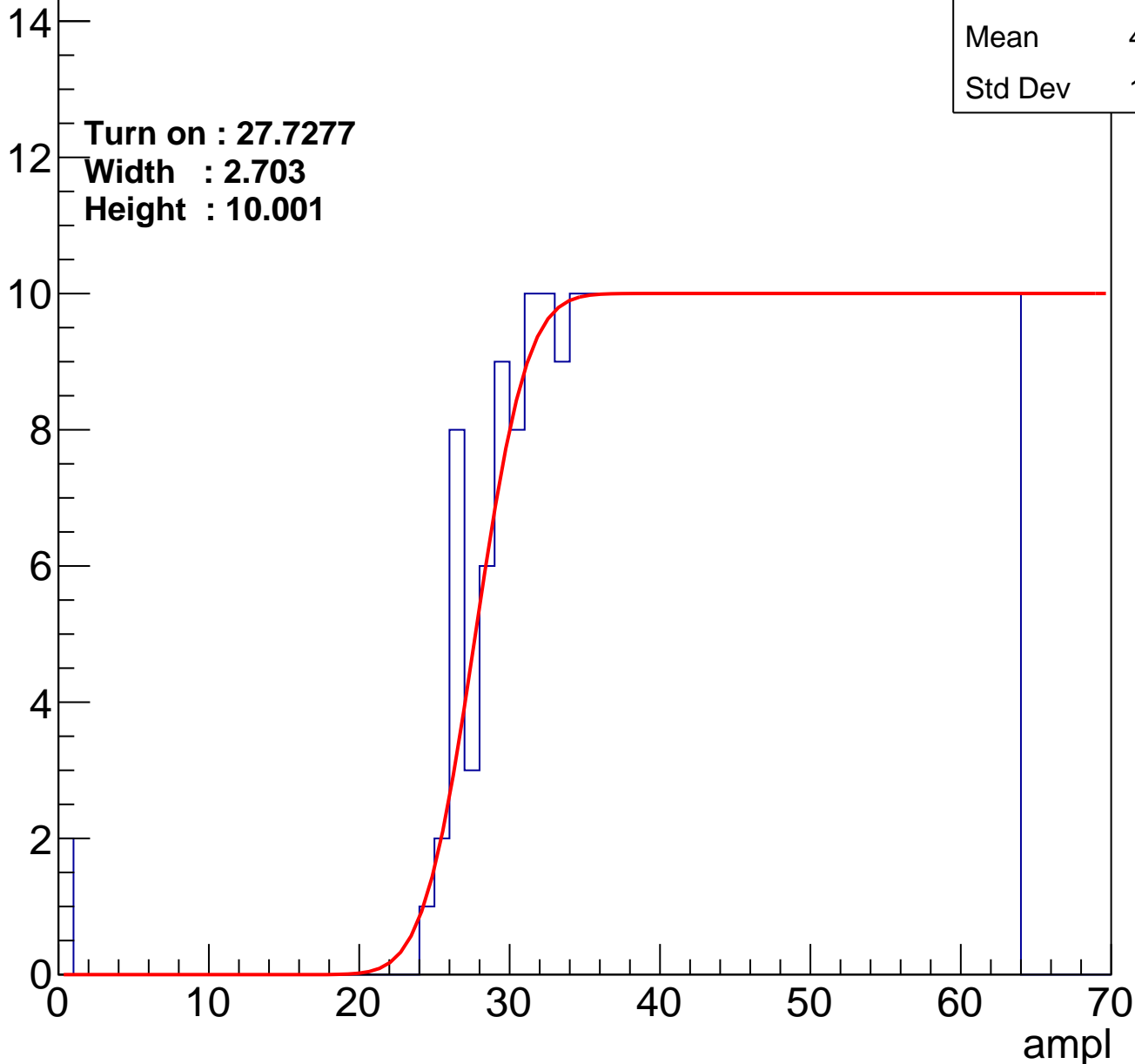
Entry

Entries	368
Mean	44.86
Std Dev	11.19

Turn on : 27.7277

Width : 2.703

Height : 10.001



B0L002S, U9-ch79

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.51
Std Dev	11.06

Turn on : 29.2990

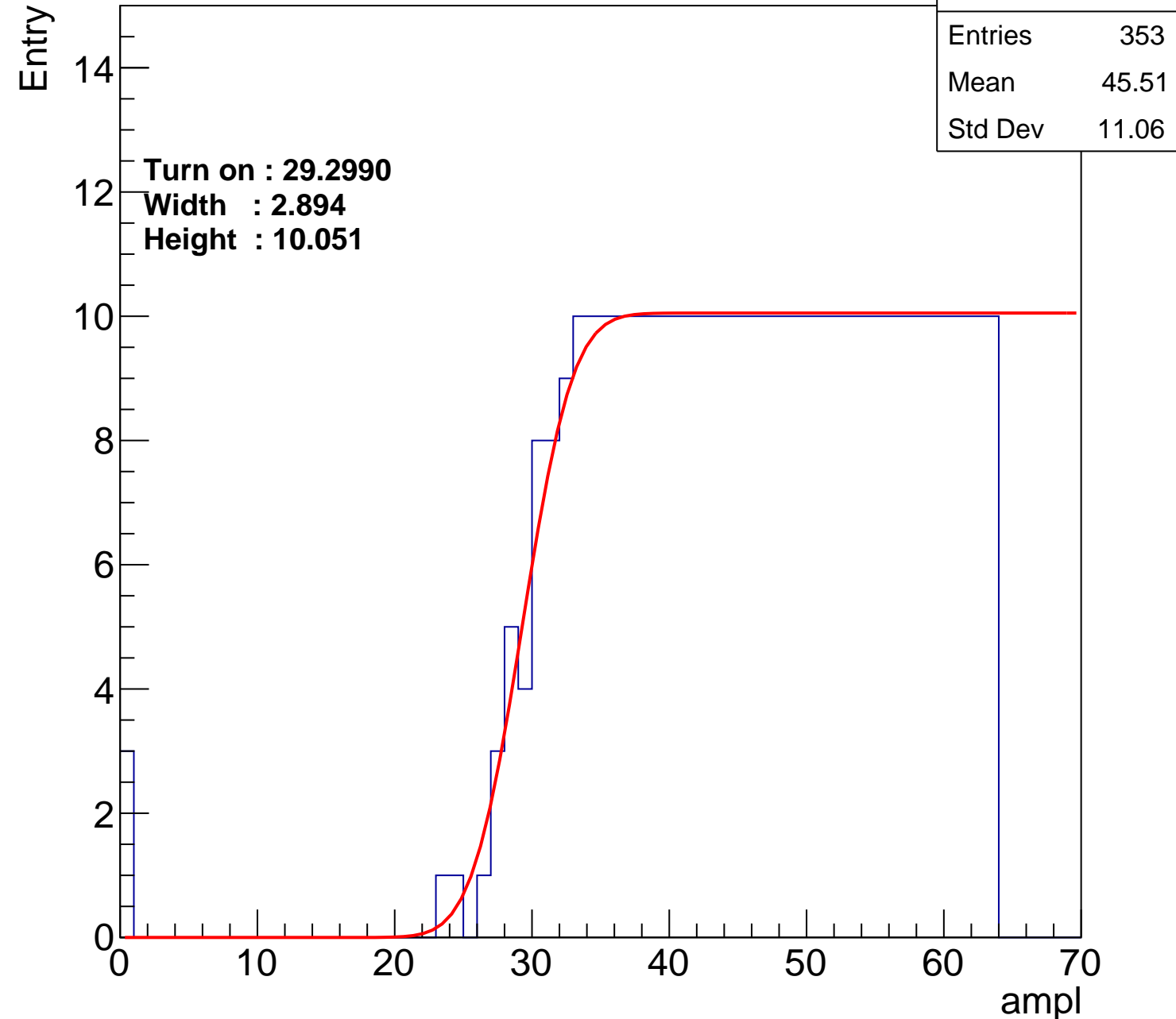
Width : 2.894

Height : 10.051

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch80

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.63
Std Dev	11.5

Turn on : 27.3497

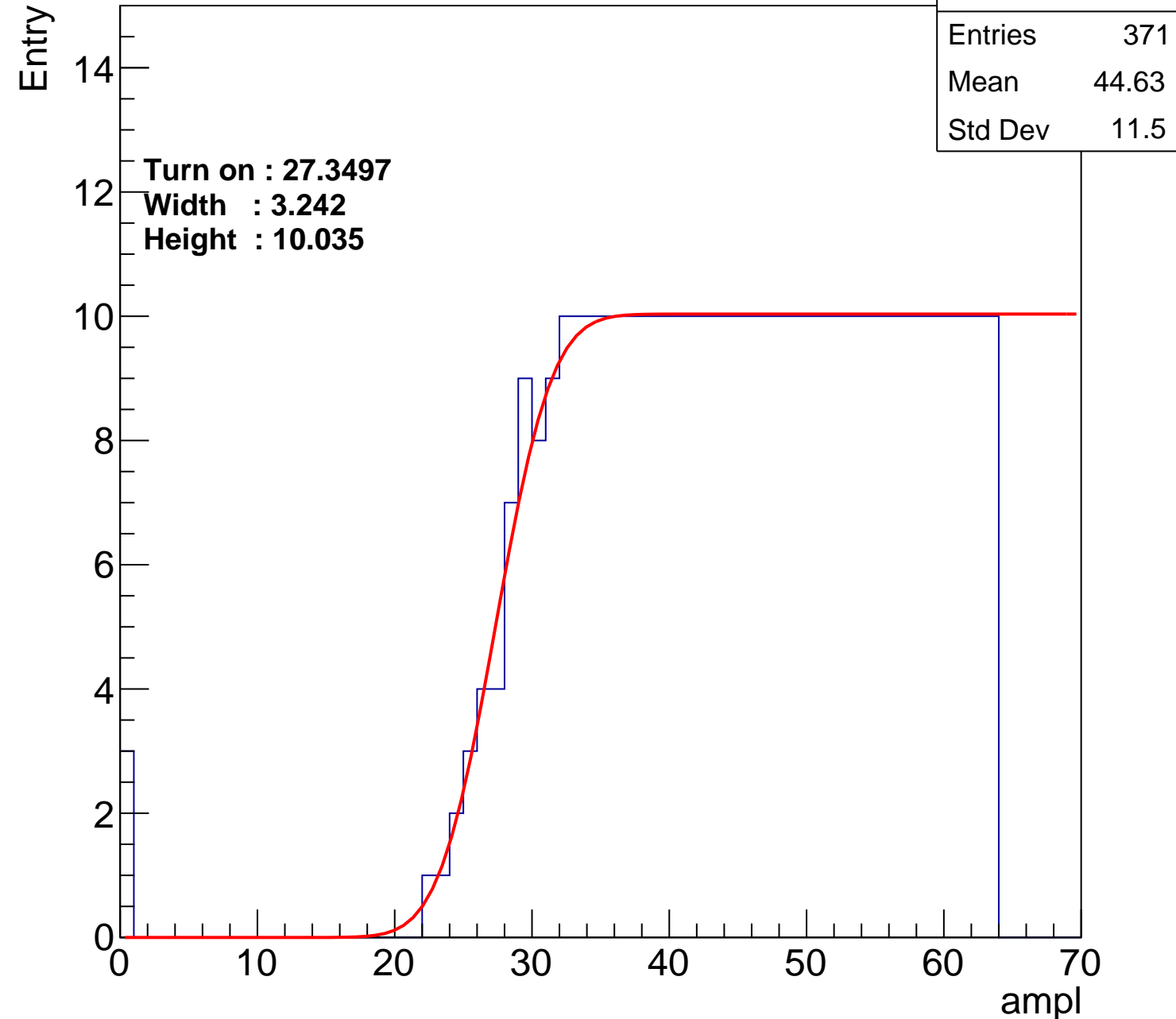
Width : 3.242

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch81

calib_packv5_042523_0143.root, FC#8, port C1

Entries	379
Mean	44.21
Std Dev	11.8

Turn on : 26.7394

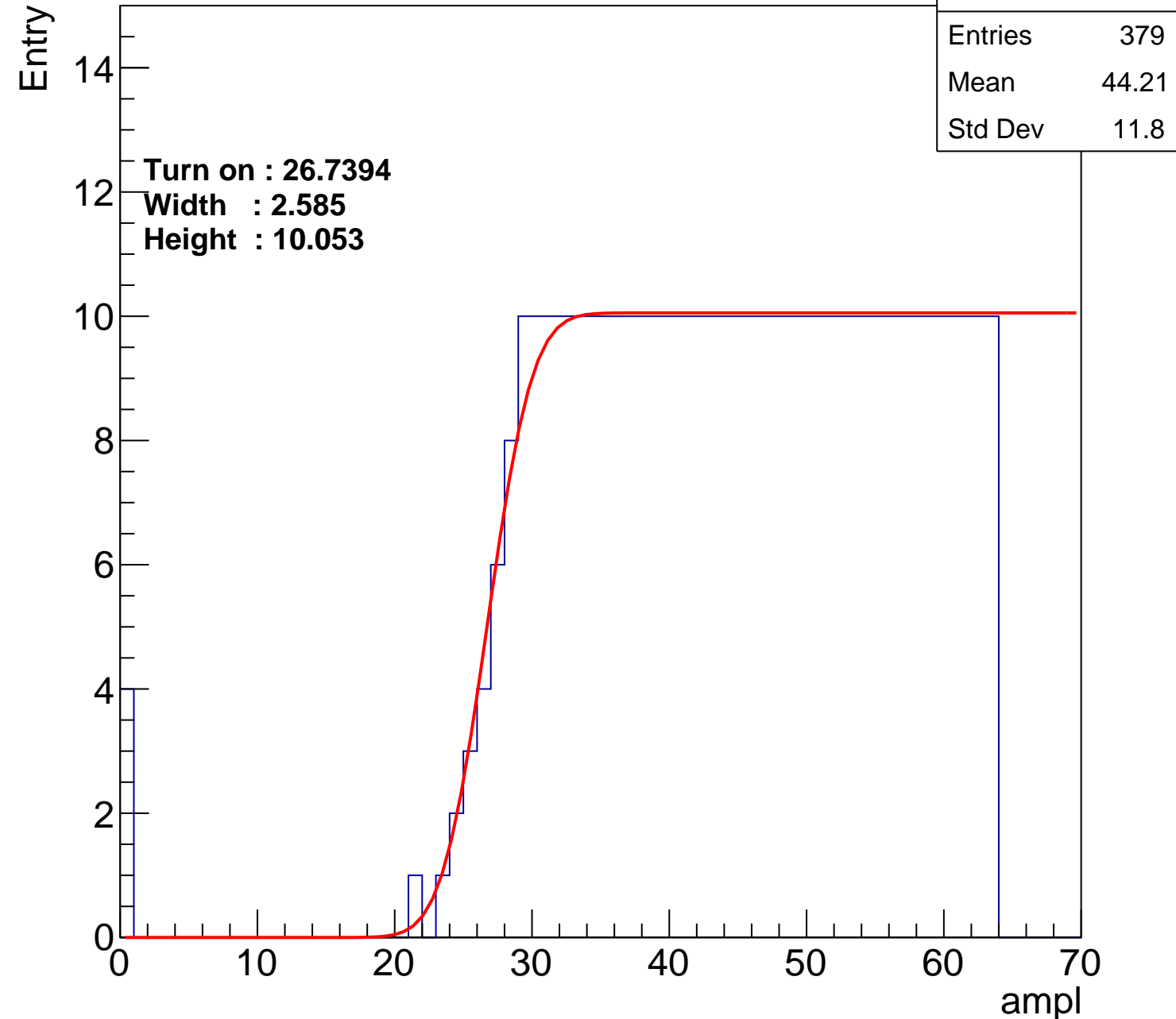
Width : 2.585

Height : 10.053

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch82

calib_packv5_042523_0143.root, FC#8, port C1

Entries	370
Mean	44.7
Std Dev	11.44

Turn on : 27.7707

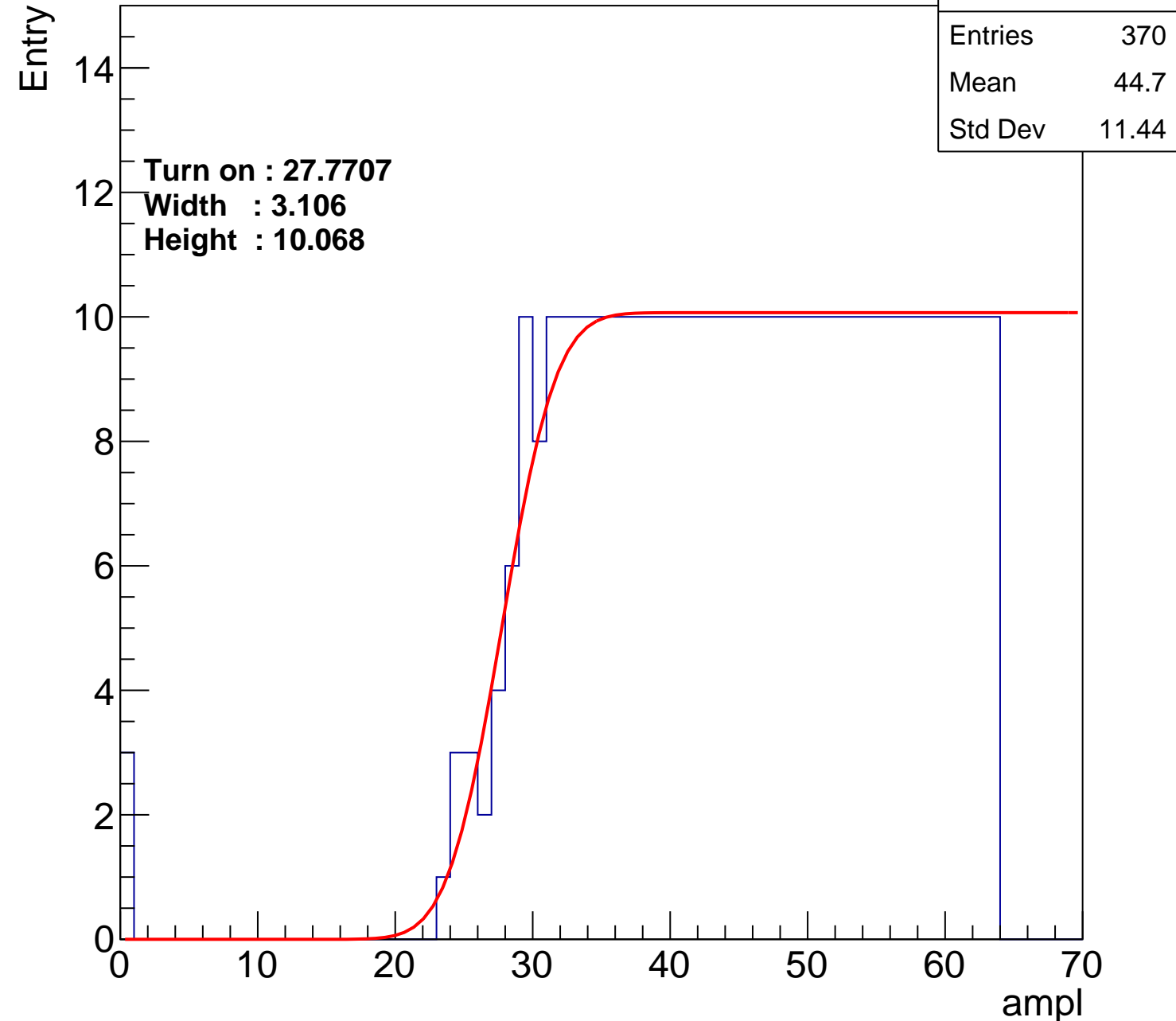
Width : 3.106

Height : 10.068

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch83

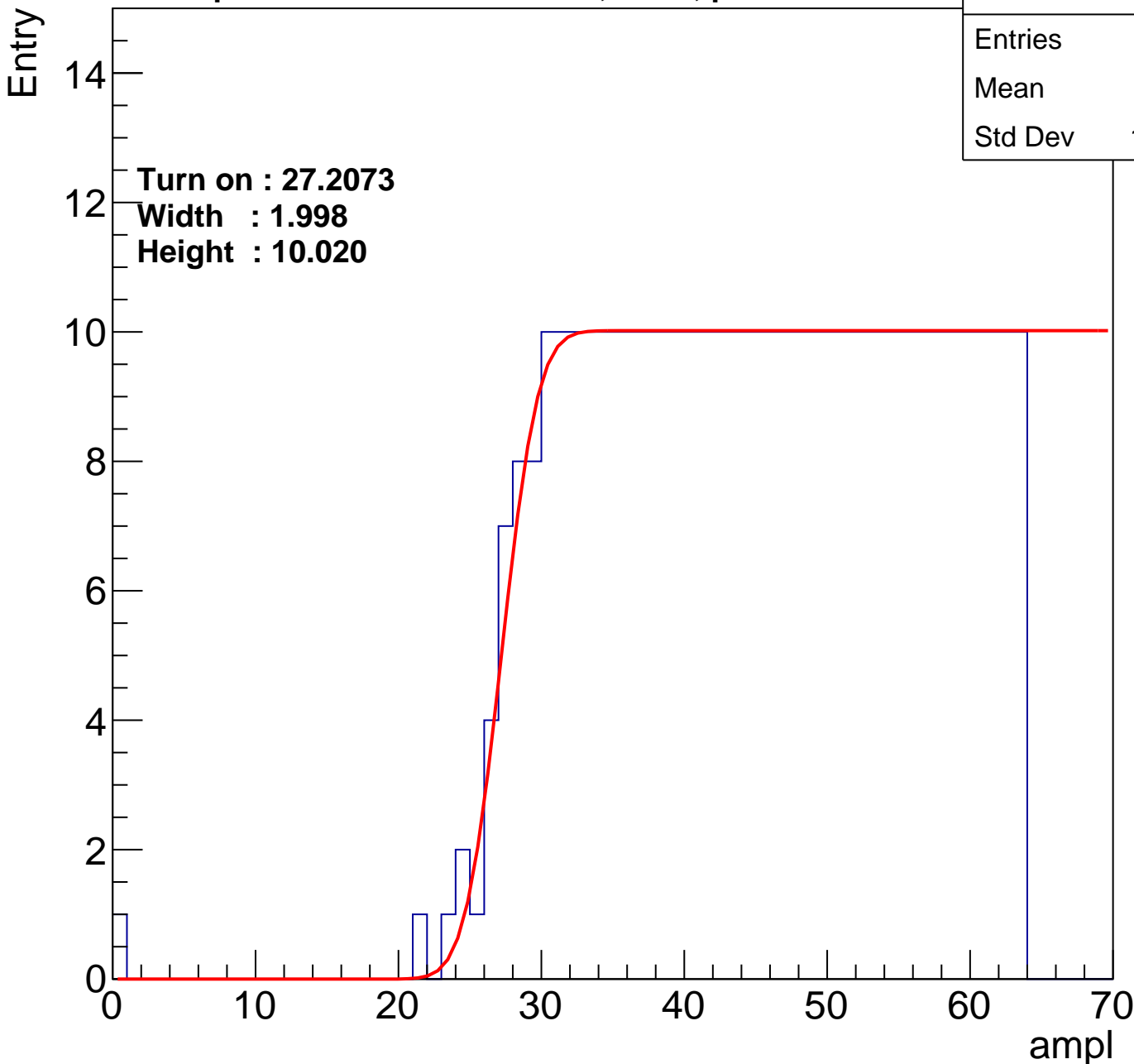
calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.71
Std Dev	11.09

Turn on : 27.2073

Width : 1.998

Height : 10.020



B0L002S, U9-ch84

calib_packv5_042523_0143.root, FC#8, port C1

Entries	383
Mean	44.14
Std Dev	11.55

Turn on : 26.1124

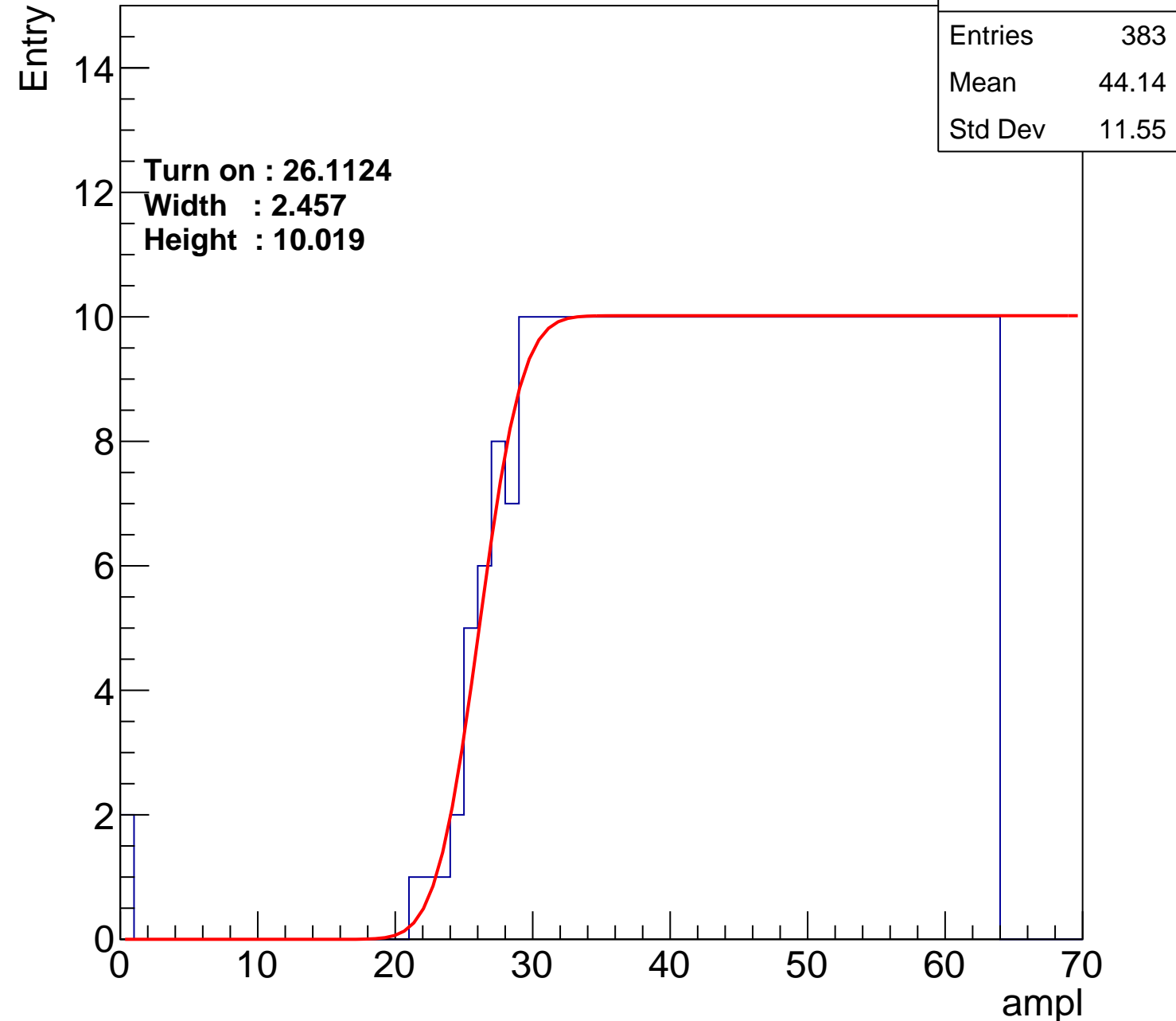
Width : 2.457

Height : 10.019

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch85

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.58
Std Dev	11.5

Turn on : 26.8150

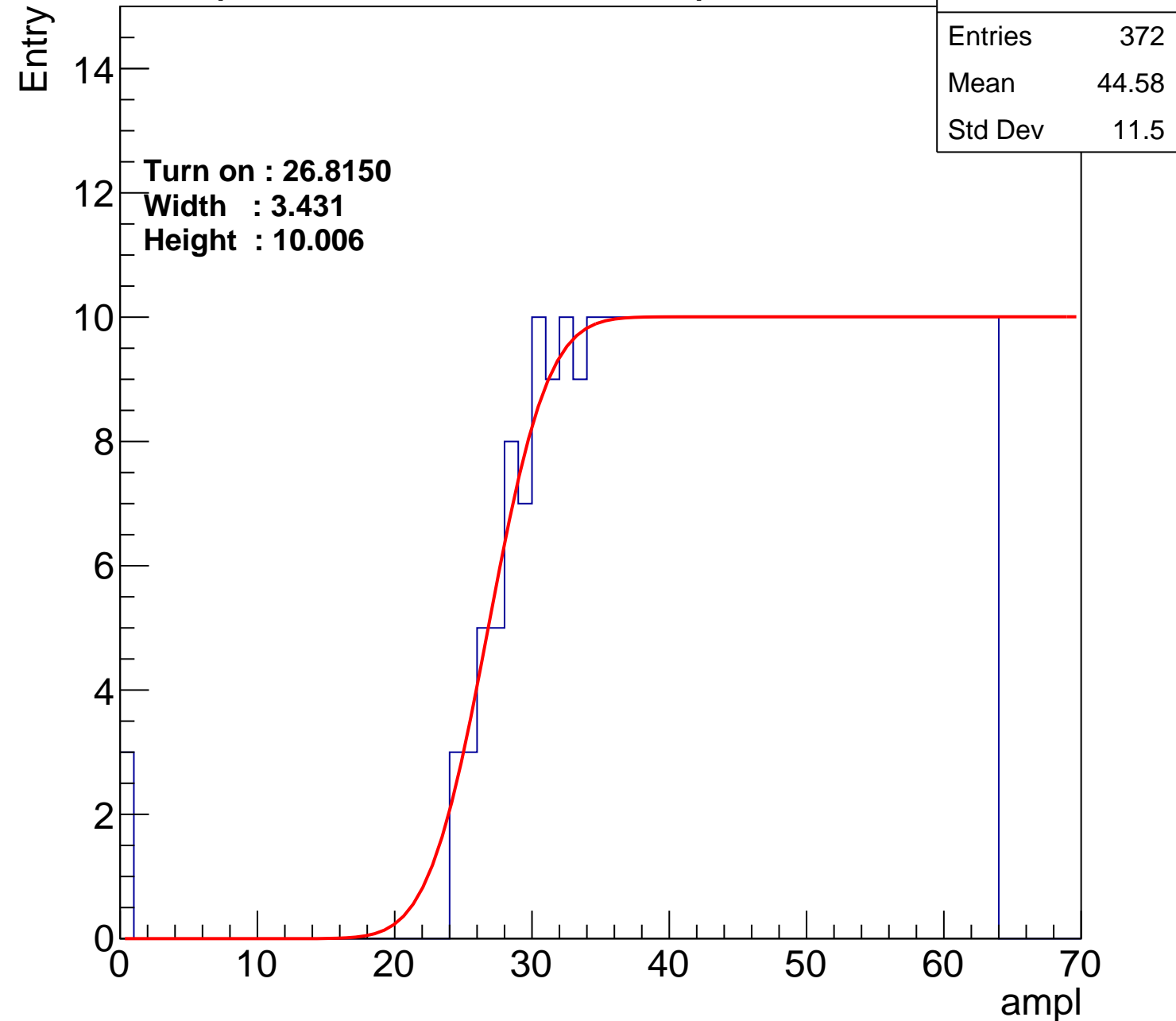
Width : 3.431

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch86

calib_packv5_042523_0143.root, FC#8, port C1

Entries	374
Mean	44.58
Std Dev	11.32

Turn on : 26.5181

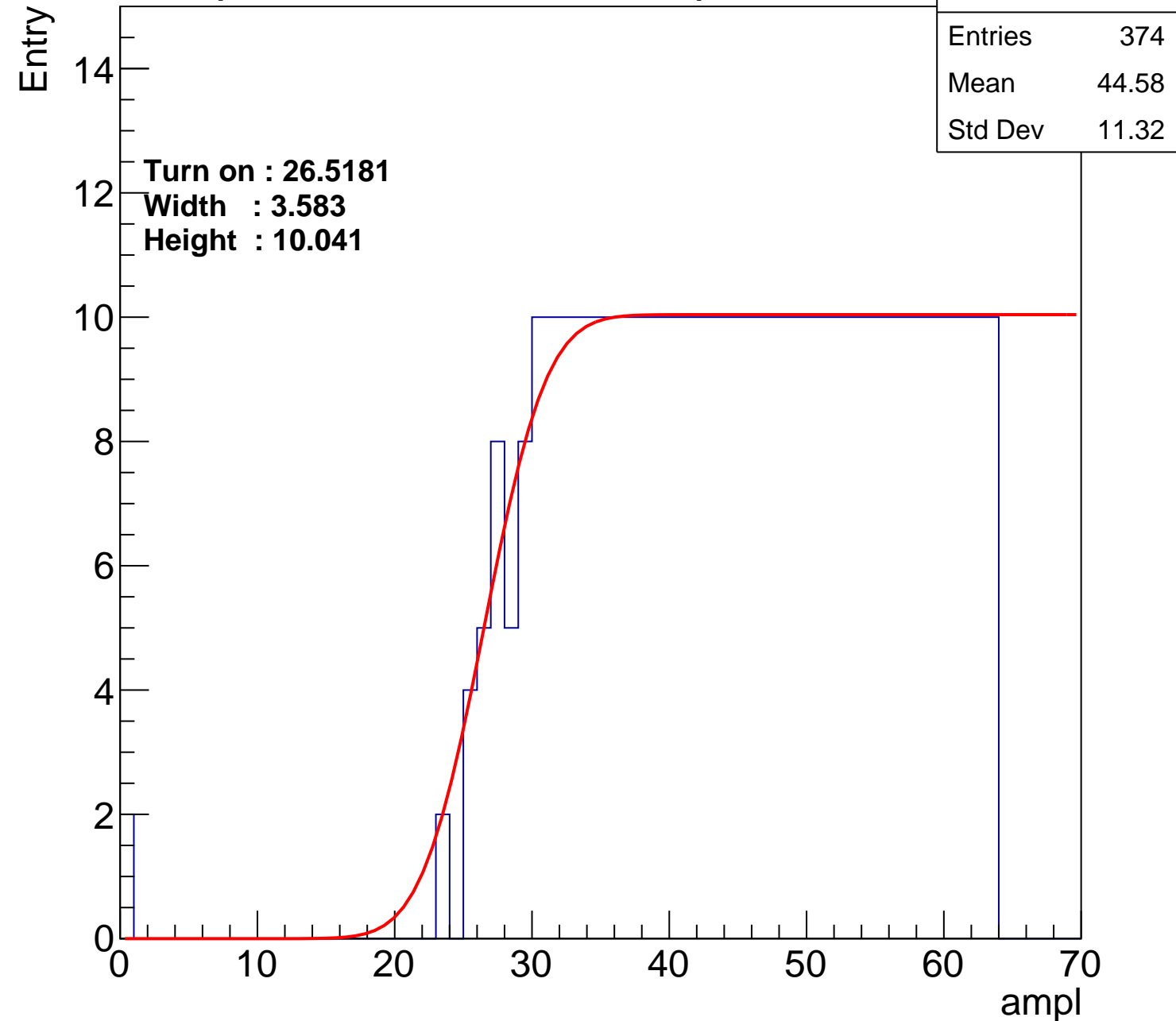
Width : 3.583

Height : 10.041

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch87

calib_packv5_042523_0143.root, FC#8, port C1

Entries	386
Mean	44.03
Std Dev	11.5

Turn on : 25.4722

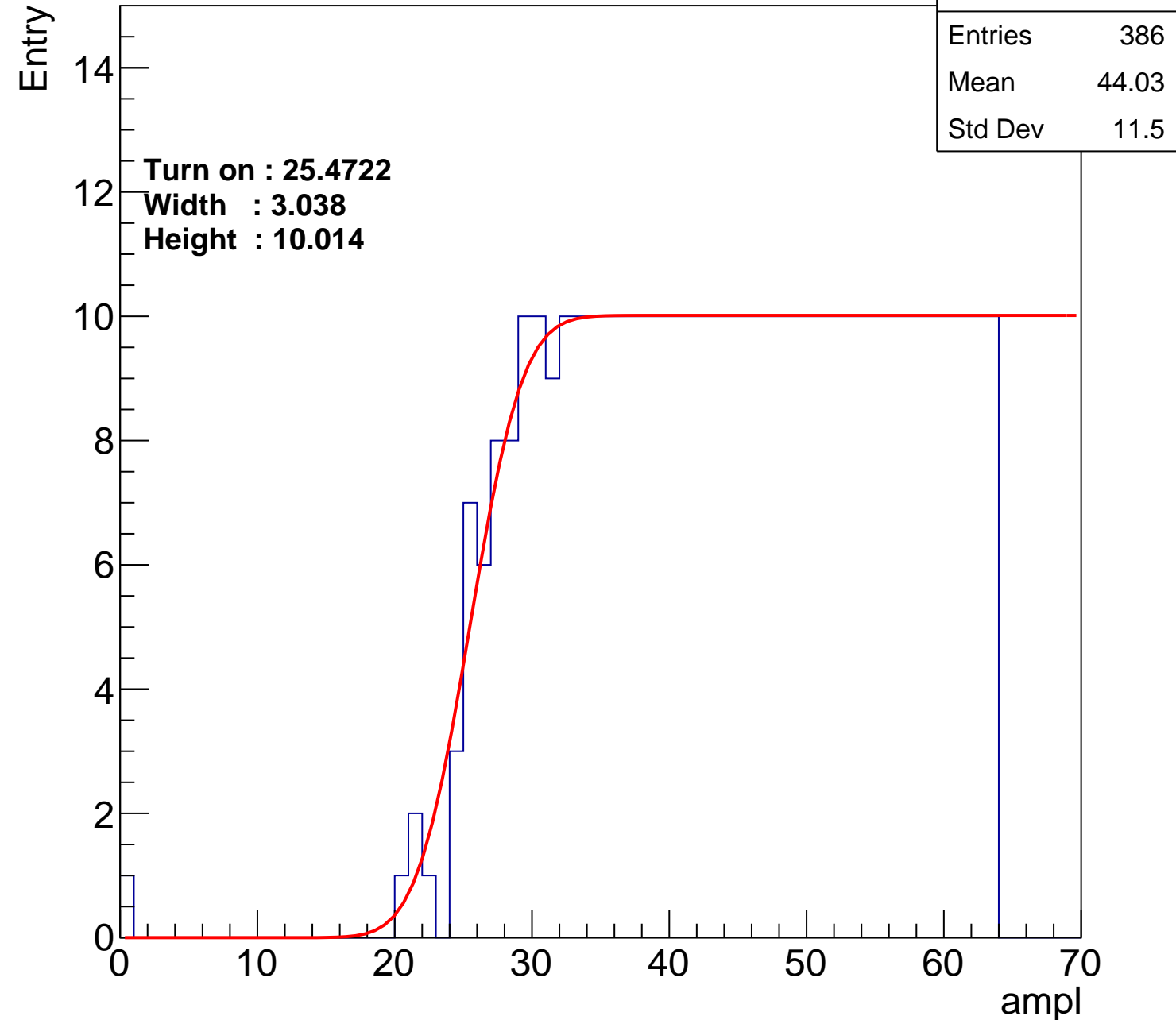
Width : 3.038

Height : 10.014

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch88

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.46
Std Dev	11.42

Turn on : 27.0531

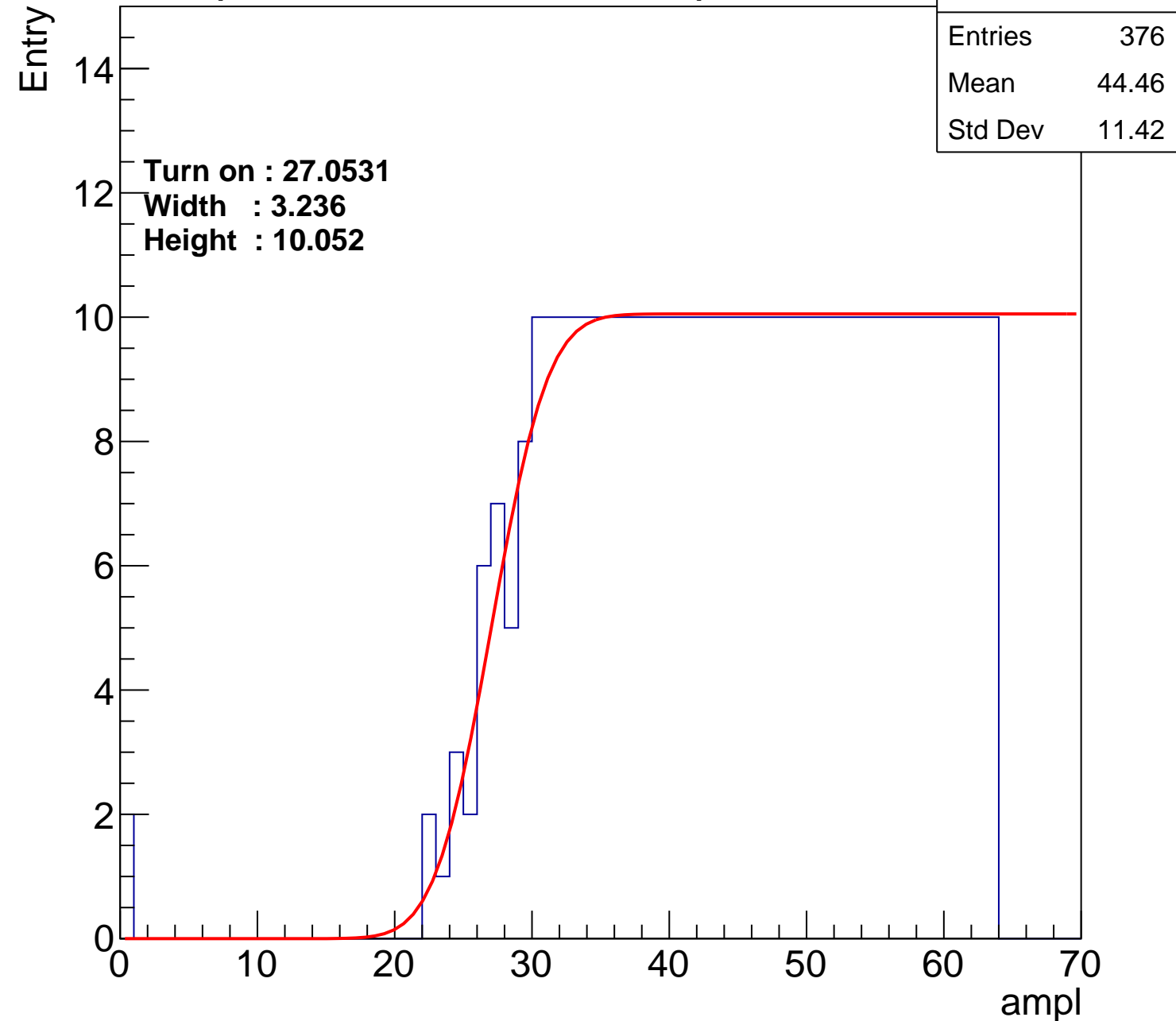
Width : 3.236

Height : 10.052

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch89

calib_packv5_042523_0143.root, FC#8, port C1

Entries	358
Mean	45.36
Std Dev	10.93

Turn on : 29.0606

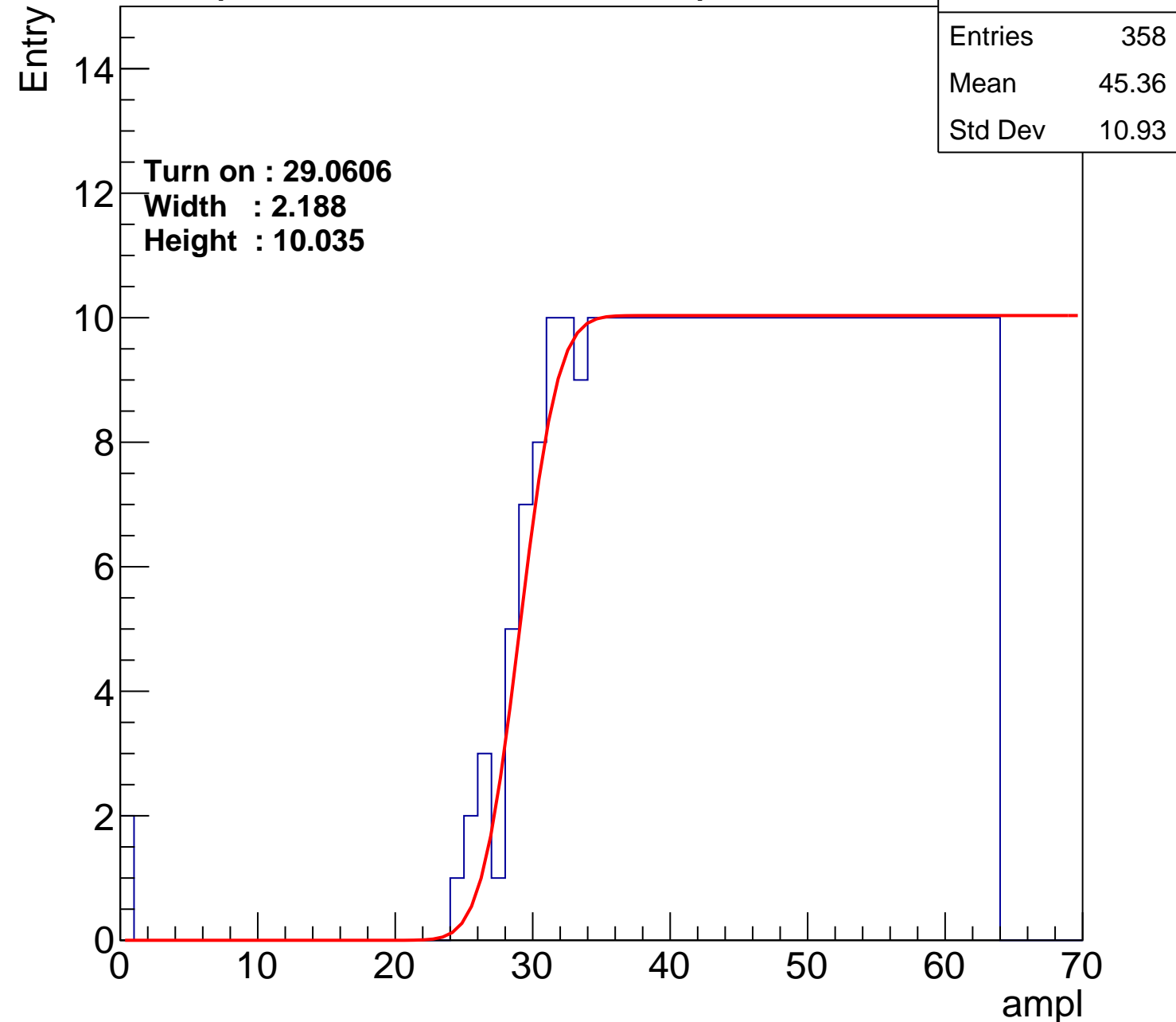
Width : 2.188

Height : 10.035

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch90

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.5613

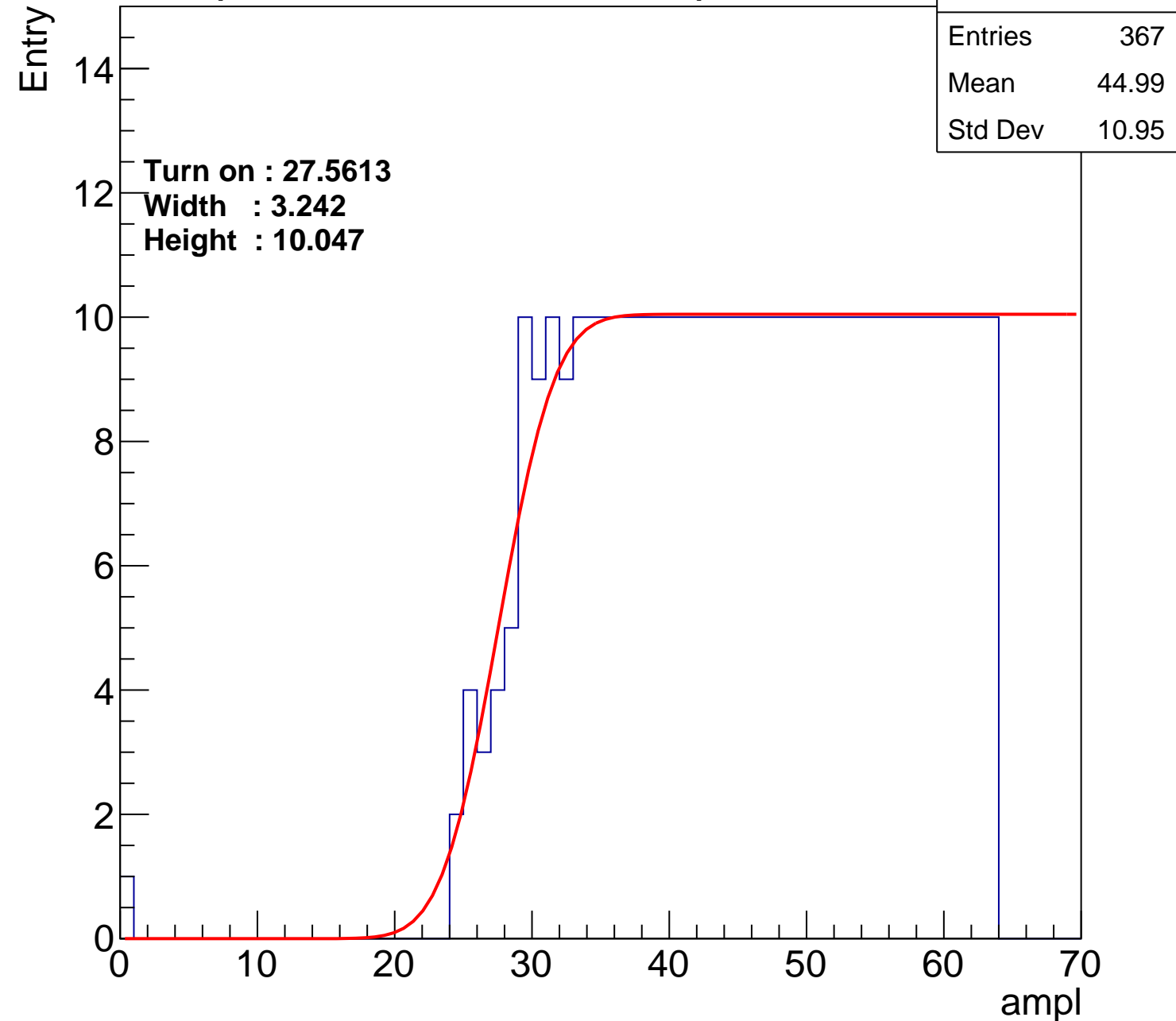
Width : 3.242

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch91

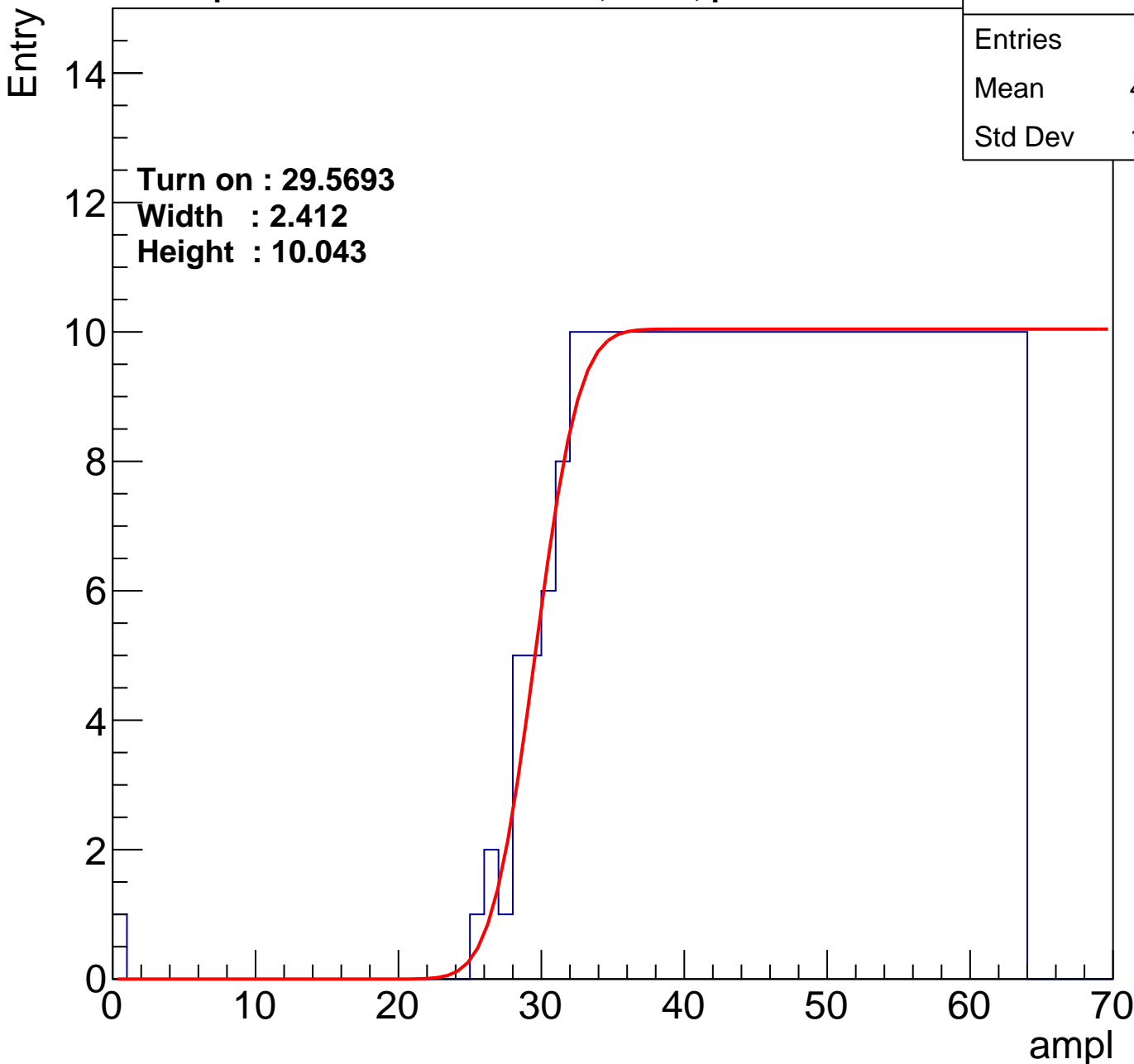
calib_packv5_042523_0143.root, FC#8, port C1

Entries	349
Mean	45.89
Std Dev	10.45

Turn on : 29.5693

Width : 2.412

Height : 10.043



B0L002S, U9-ch92

calib_packv5_042523_0143.root, FC#8, port C1

Entries	368
Mean	44.89
Std Dev	11.07

Turn on : 27.2786

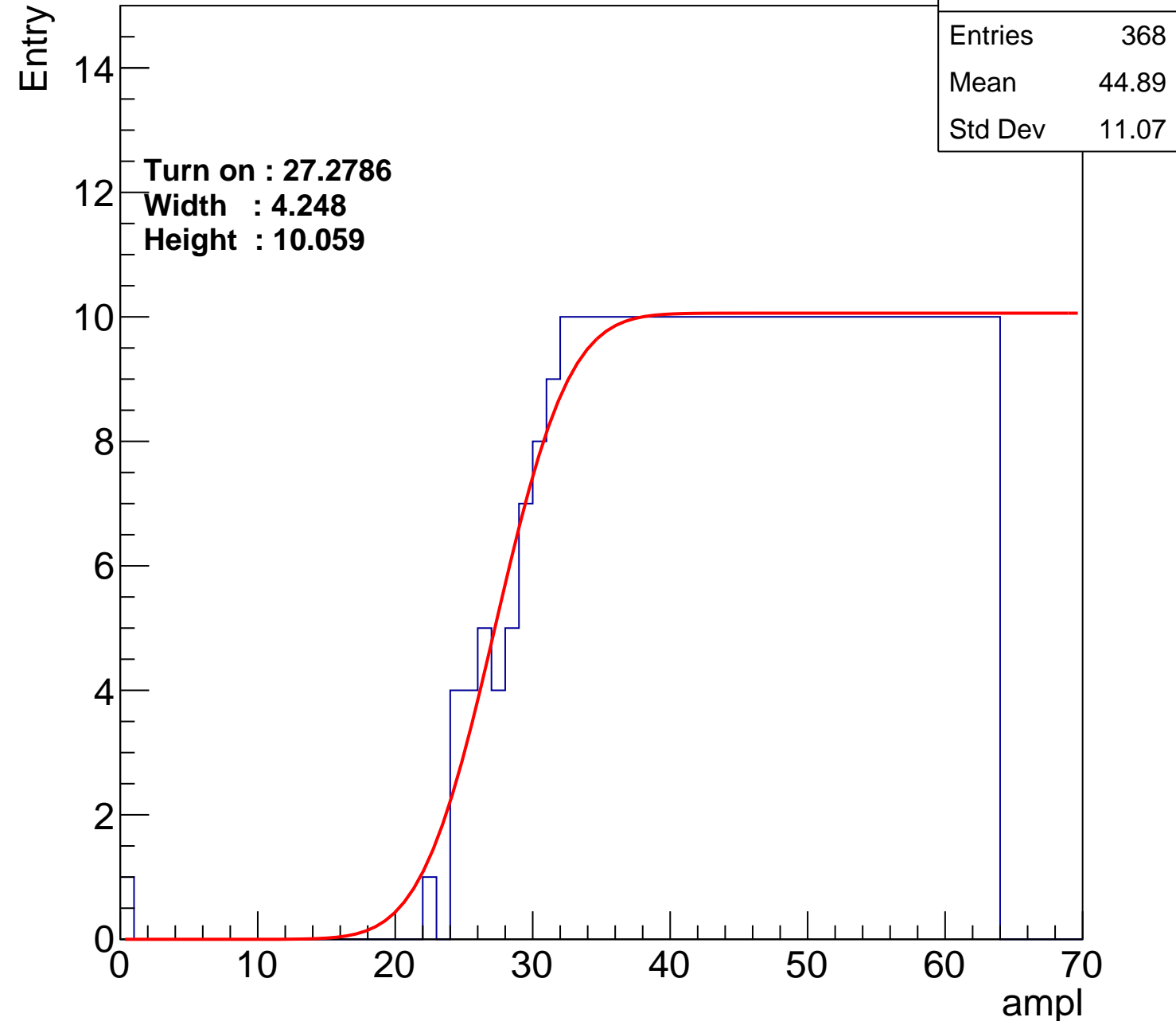
Width : 4.248

Height : 10.059

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch93

calib_packv5_042523_0143.root, FC#8, port C1

Entries	364
Mean	45.15
Std Dev	10.85

Turn on : 27.3912

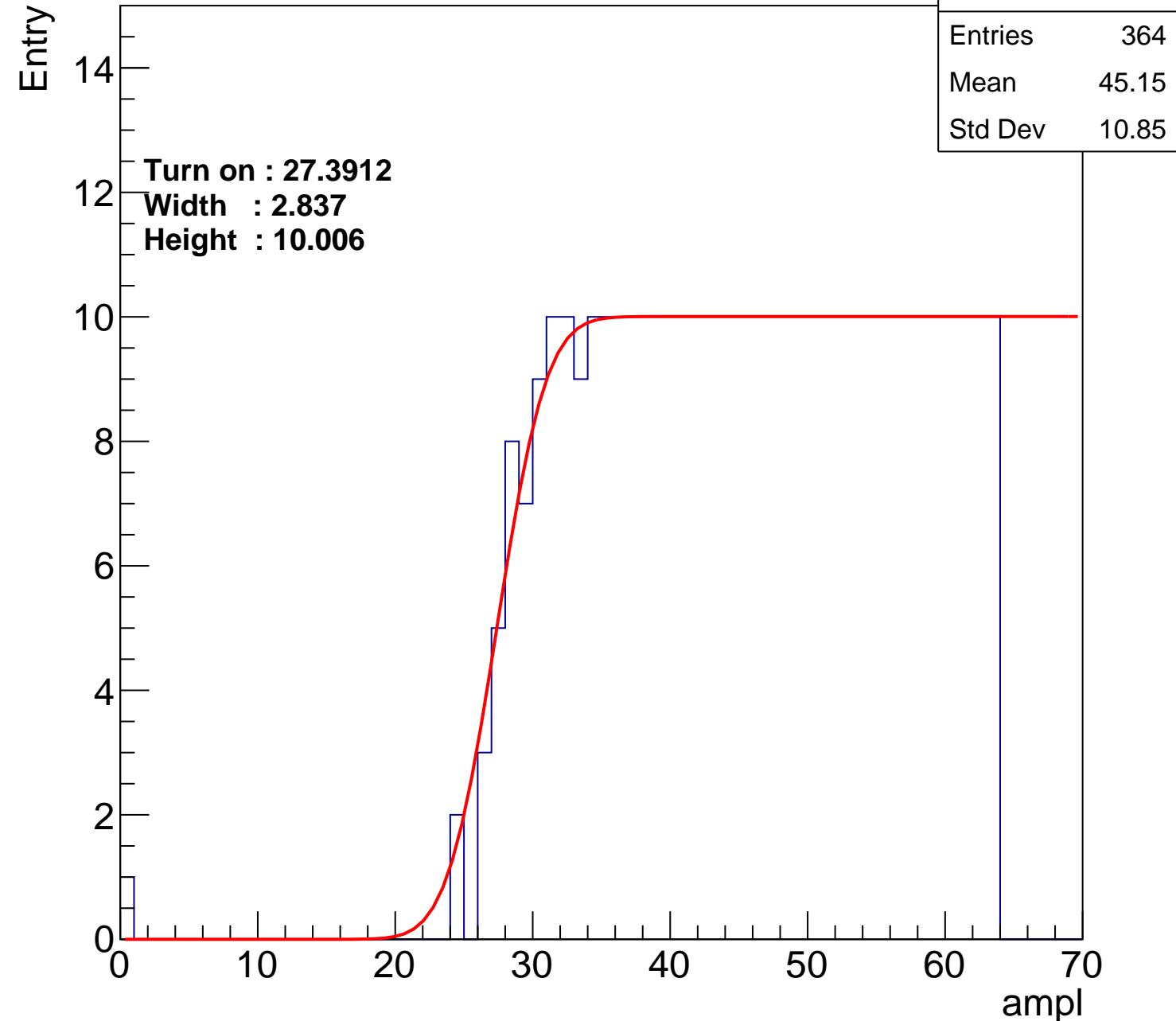
Width : 2.837

Height : 10.006

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch94

calib_packv5_042523_0143.root, FC#8, port C1

Entries	390
Mean	43.66
Std Dev	12.09

Turn on : 25.6285

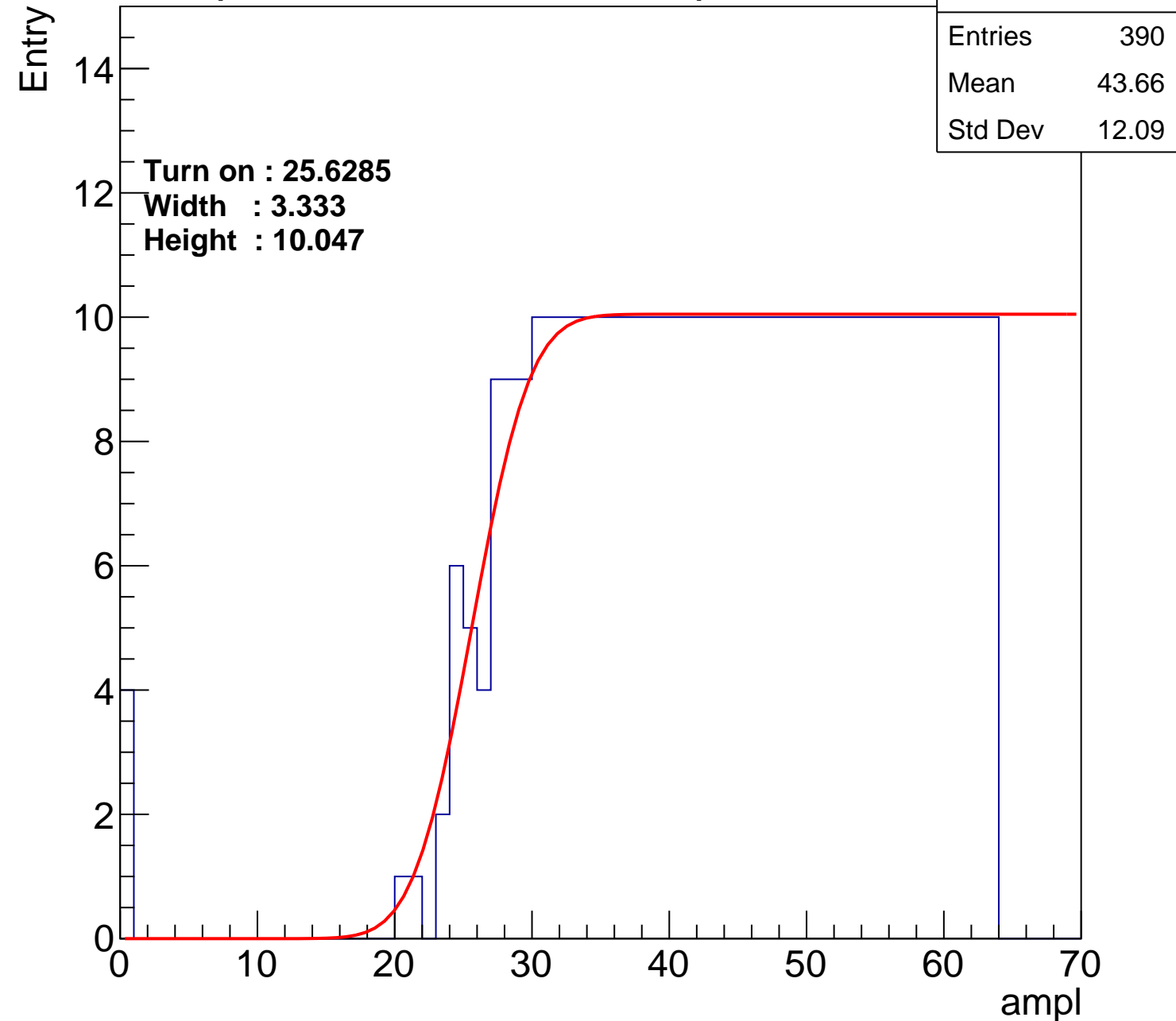
Width : 3.333

Height : 10.047

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch95

calib_packv5_042523_0143.root, FC#8, port C1

Entries	357
Mean	45.32
Std Dev	11.14

Turn on : 28.3874

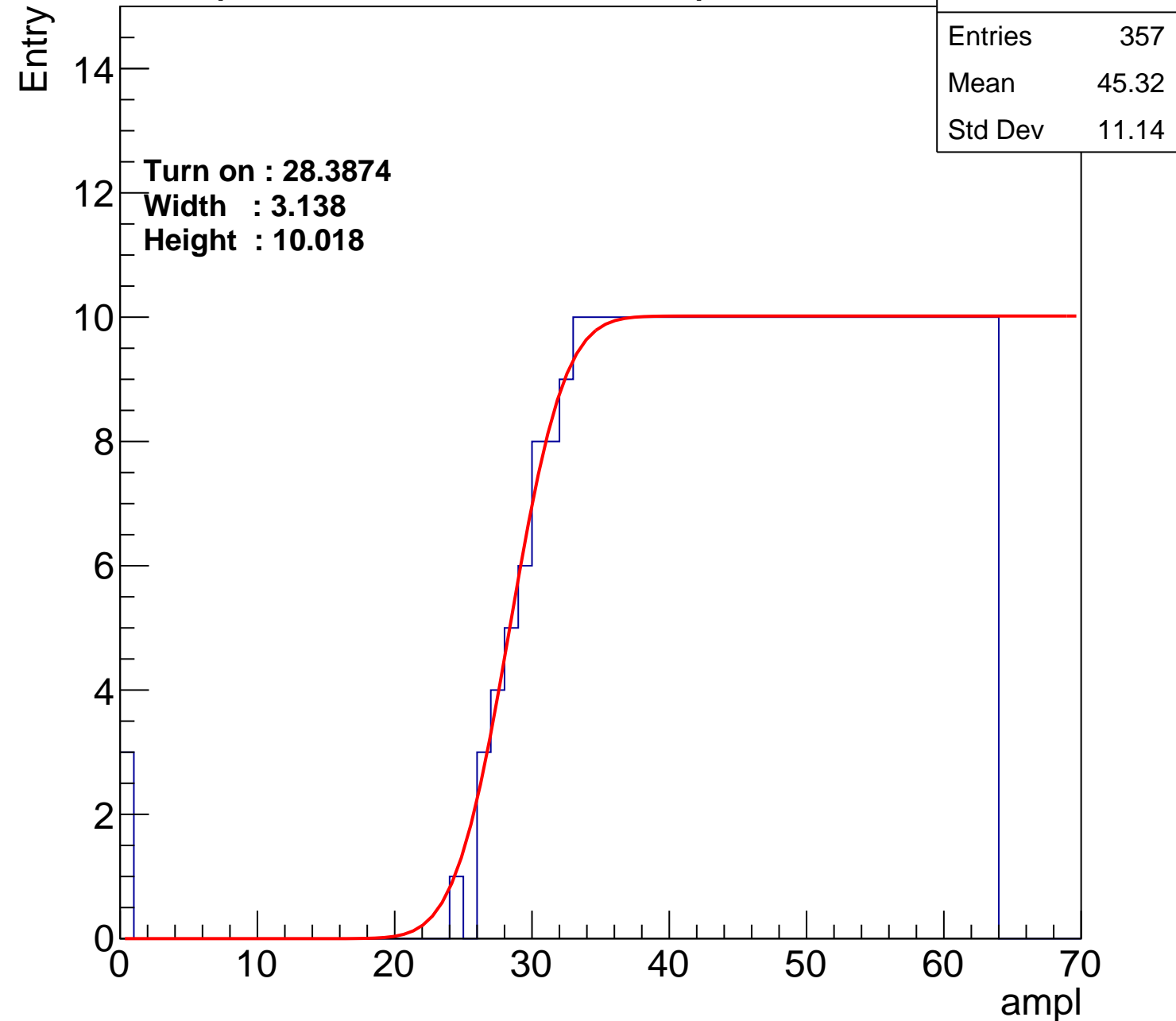
Width : 3.138

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch96

calib_packv5_042523_0143.root, FC#8, port C1

Entries	371
Mean	44.57
Std Dev	11.66

Turn on : 26.9045

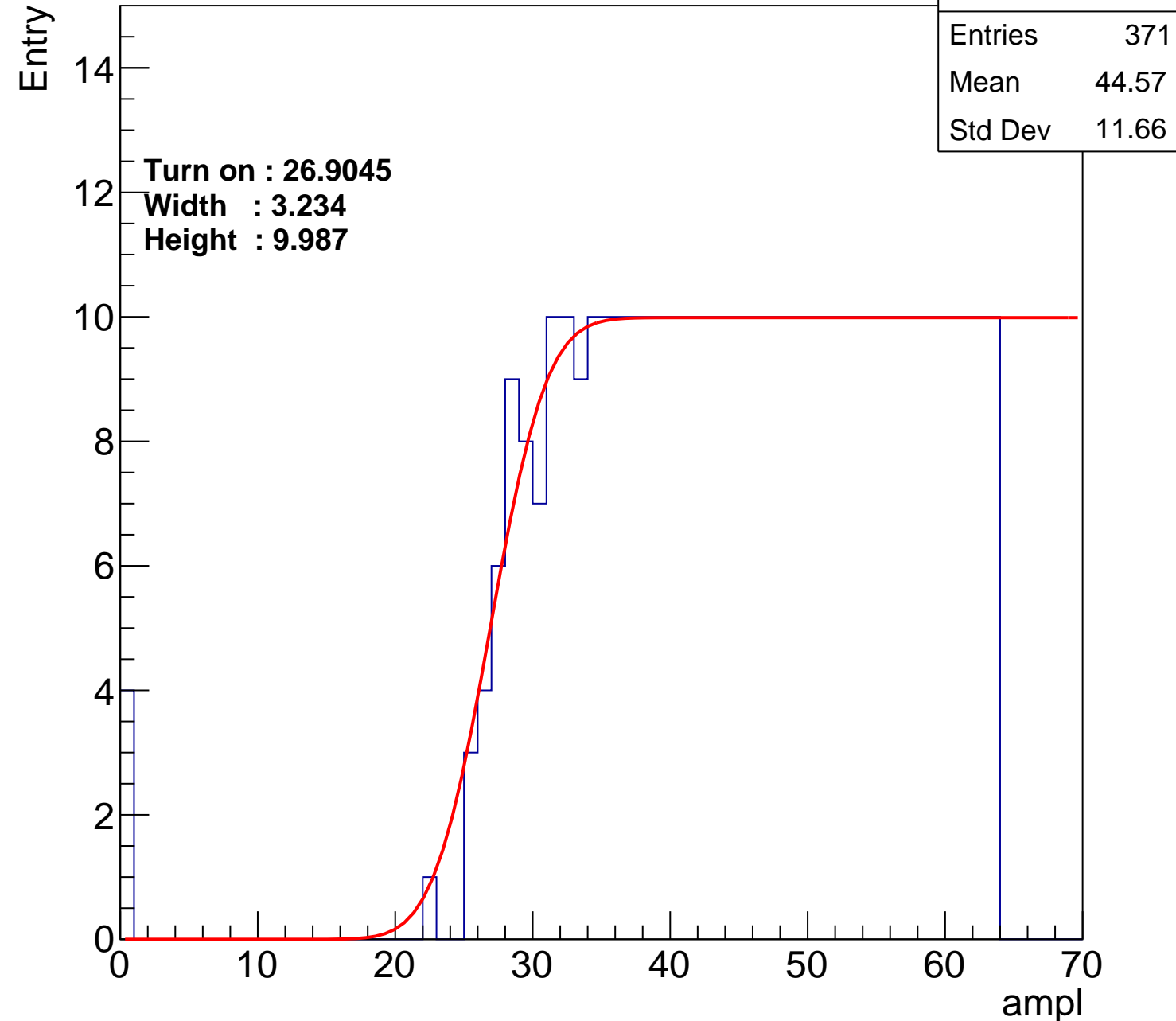
Width : 3.234

Height : 9.987

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch97

calib_packv5_042523_0143.root, FC#8, port C1

Entries	372
Mean	44.68
Std Dev	11.27

Turn on : 27.2178

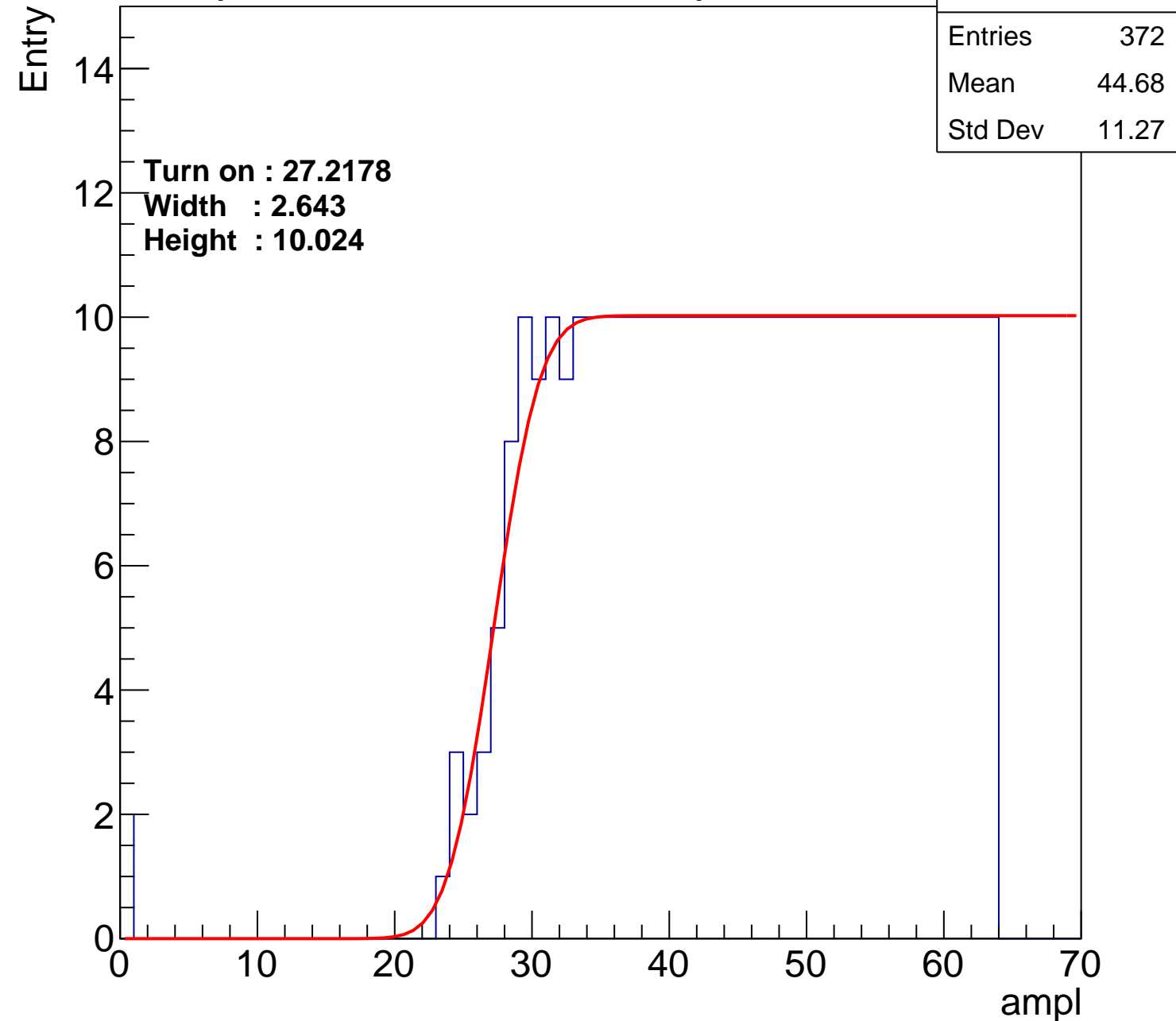
Width : 2.643

Height : 10.024

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch98

calib_packv5_042523_0143.root, FC#8, port C1

Entries	350
Mean	45.74
Std Dev	10.76

Turn on : 29.3244

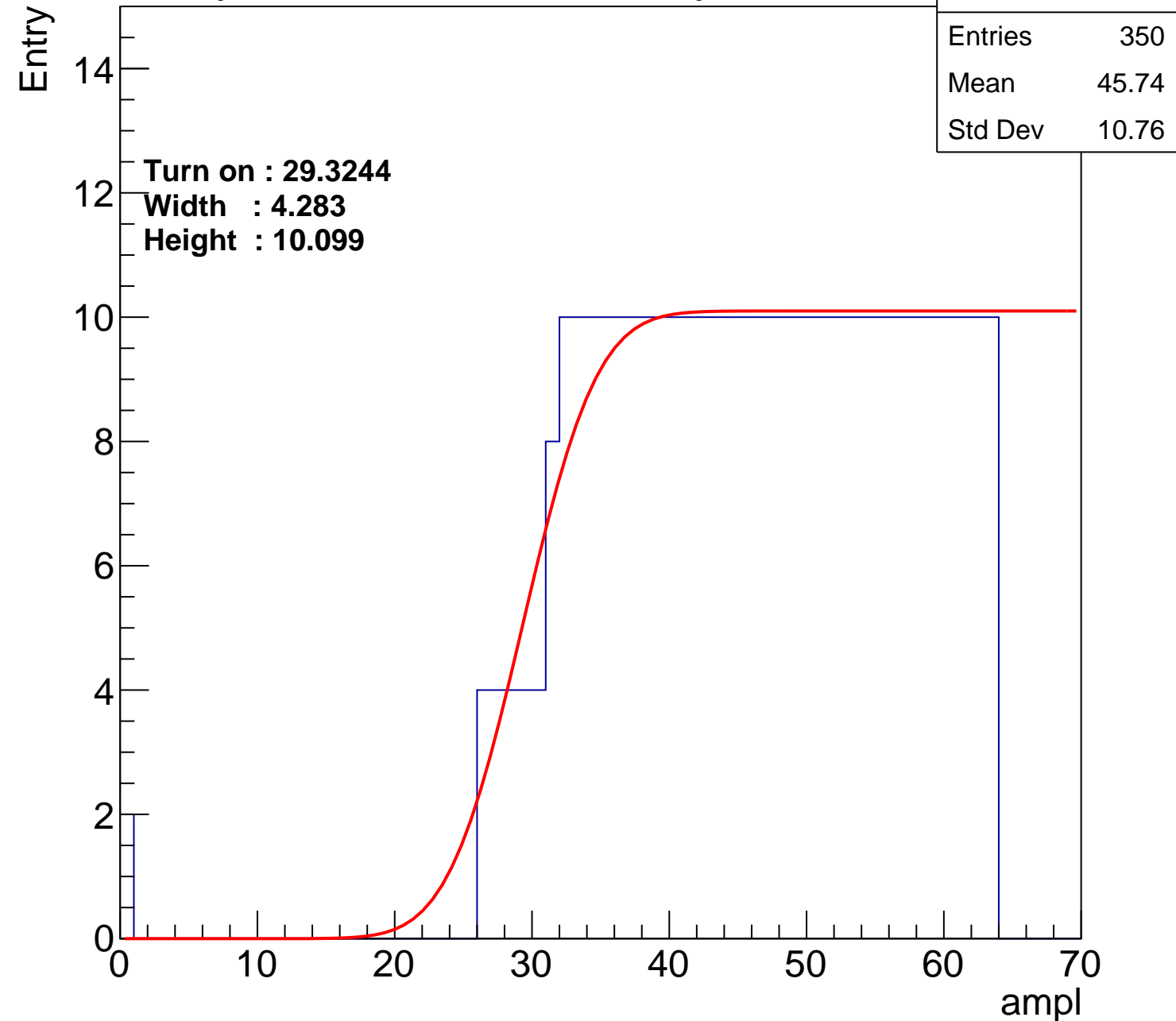
Width : 4.283

Height : 10.099

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch99

calib_packv5_042523_0143.root, FC#8, port C1

Entries	352
Mean	45.72
Std Dev	10.56

Turn on : 29.0851

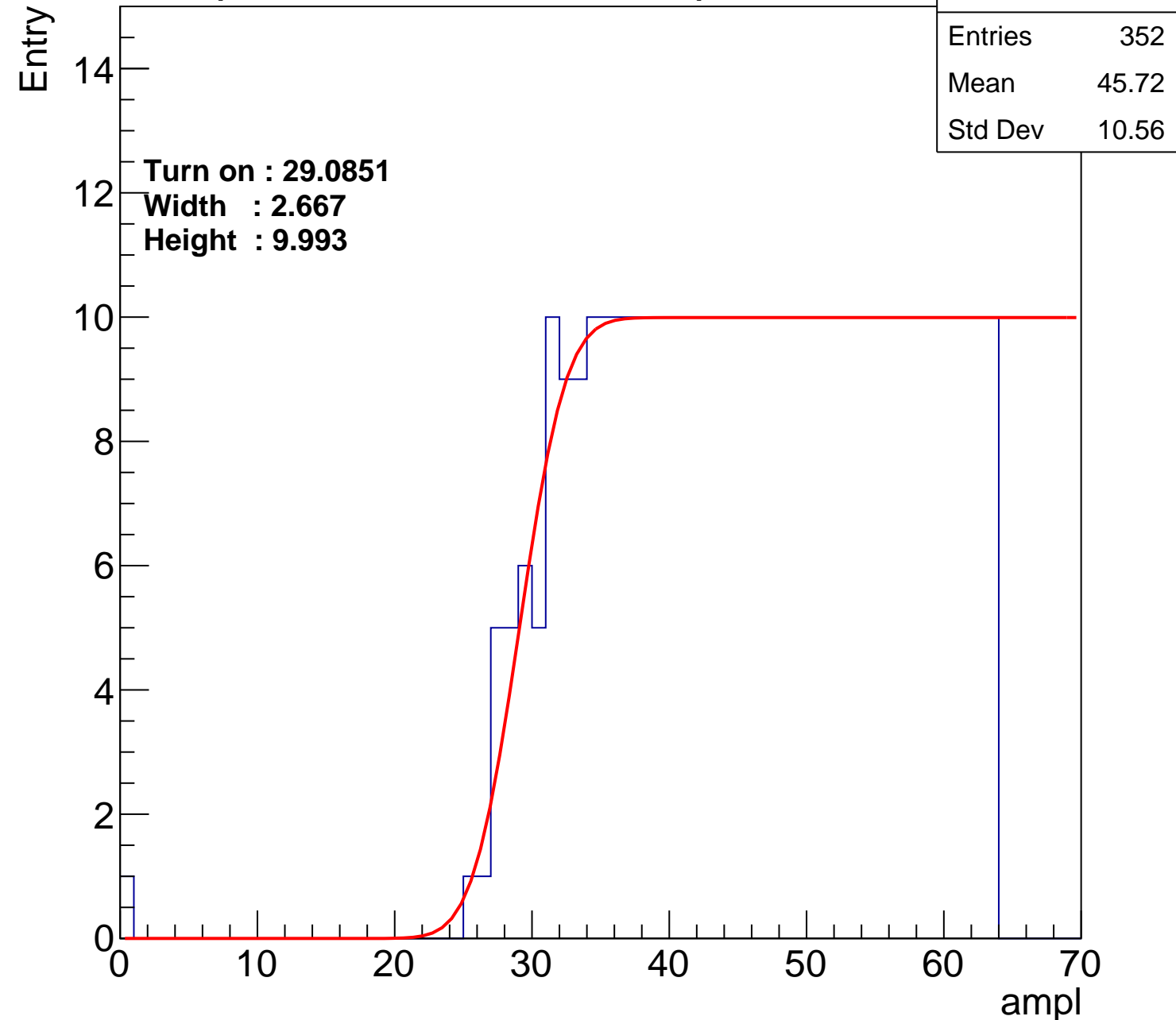
Width : 2.667

Height : 9.993

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch100

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	45.02
Std Dev	10.97

Turn on : 27.5895

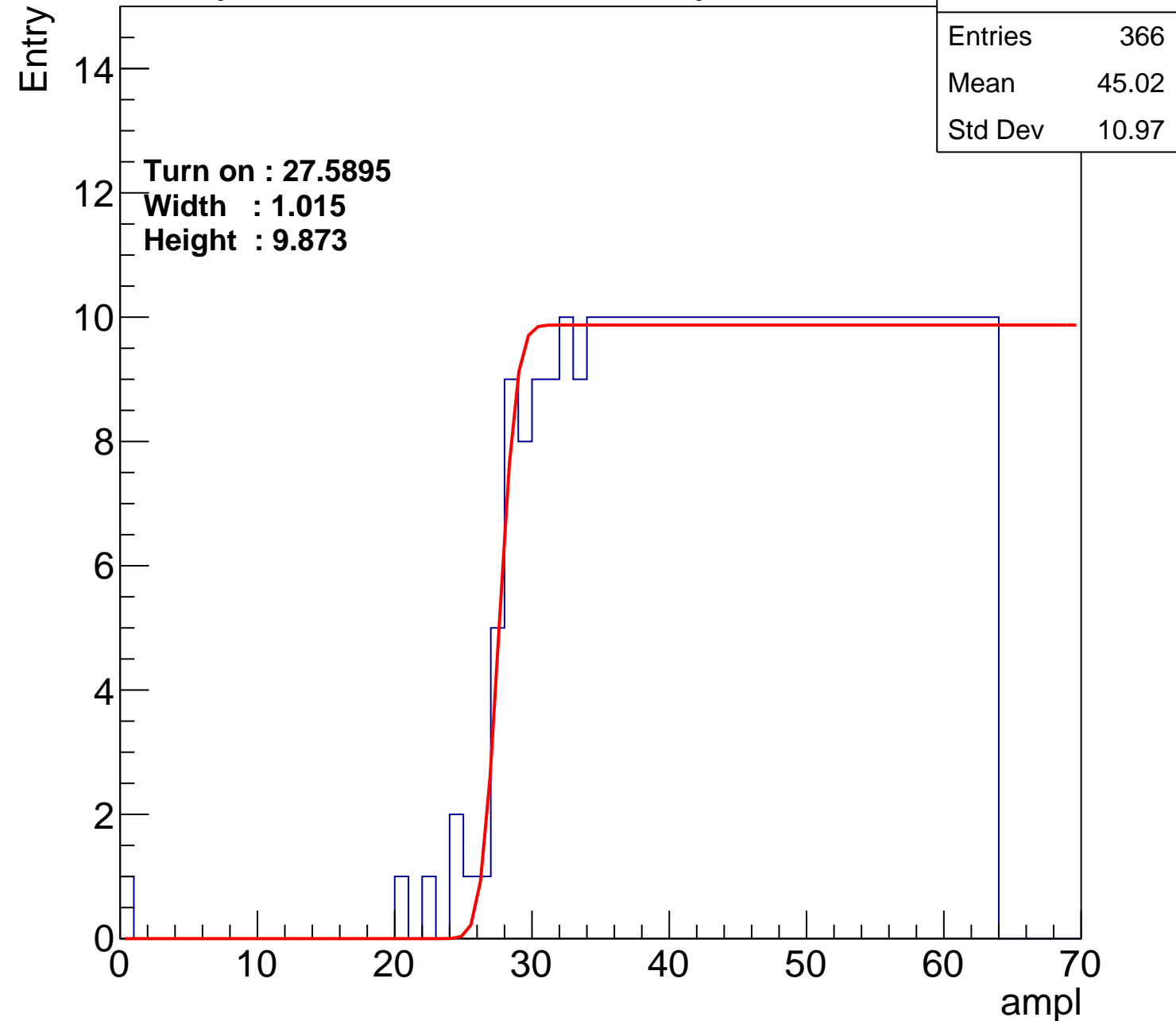
Width : 1.015

Height : 9.873

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch101

calib_packv5_042523_0143.root, FC#8, port C1

Entries	353
Mean	45.65
Std Dev	10.63

Turn on : 29.2197

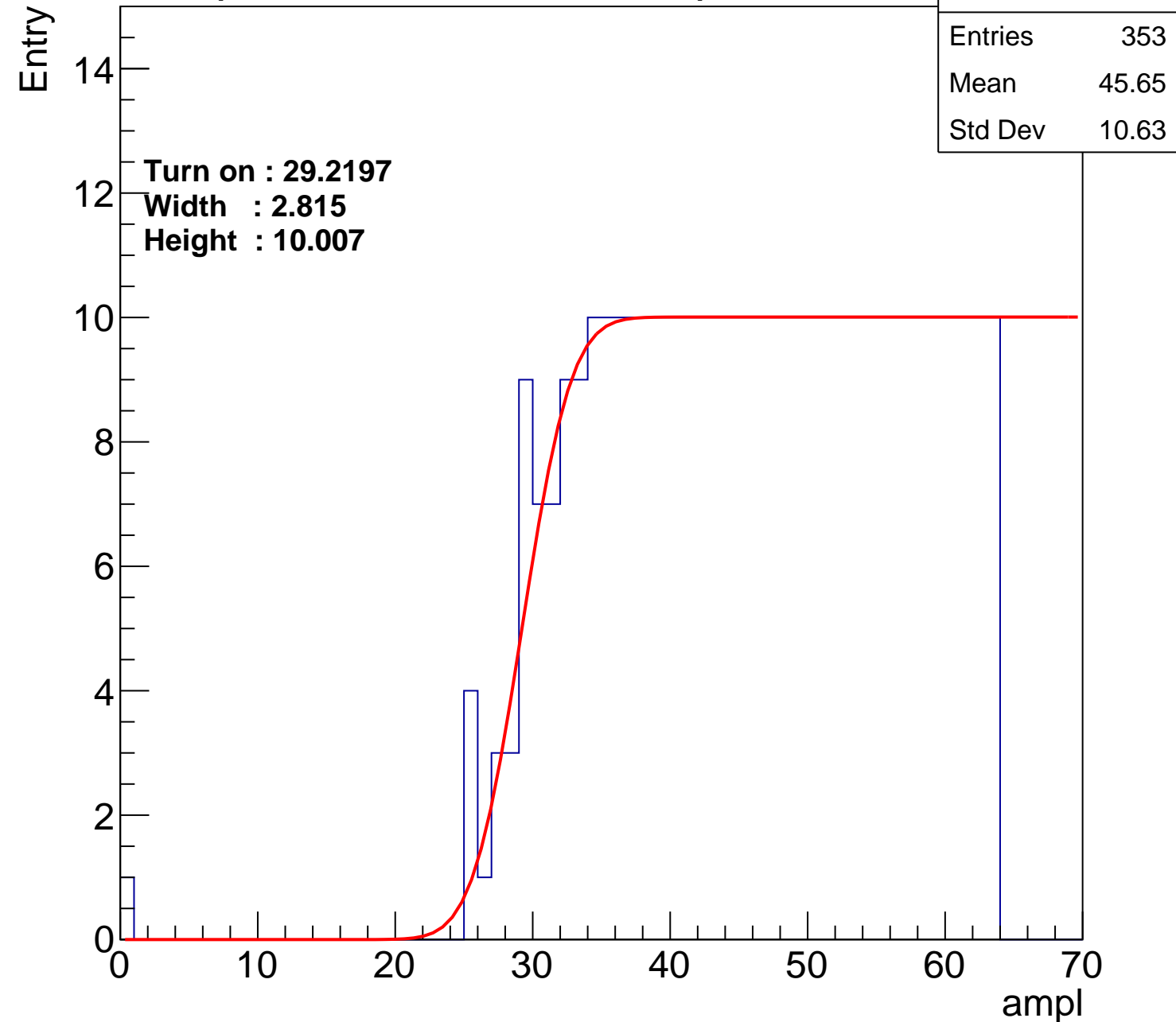
Width : 2.815

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch102

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.31
Std Dev	11.8

Turn on : 26.7826

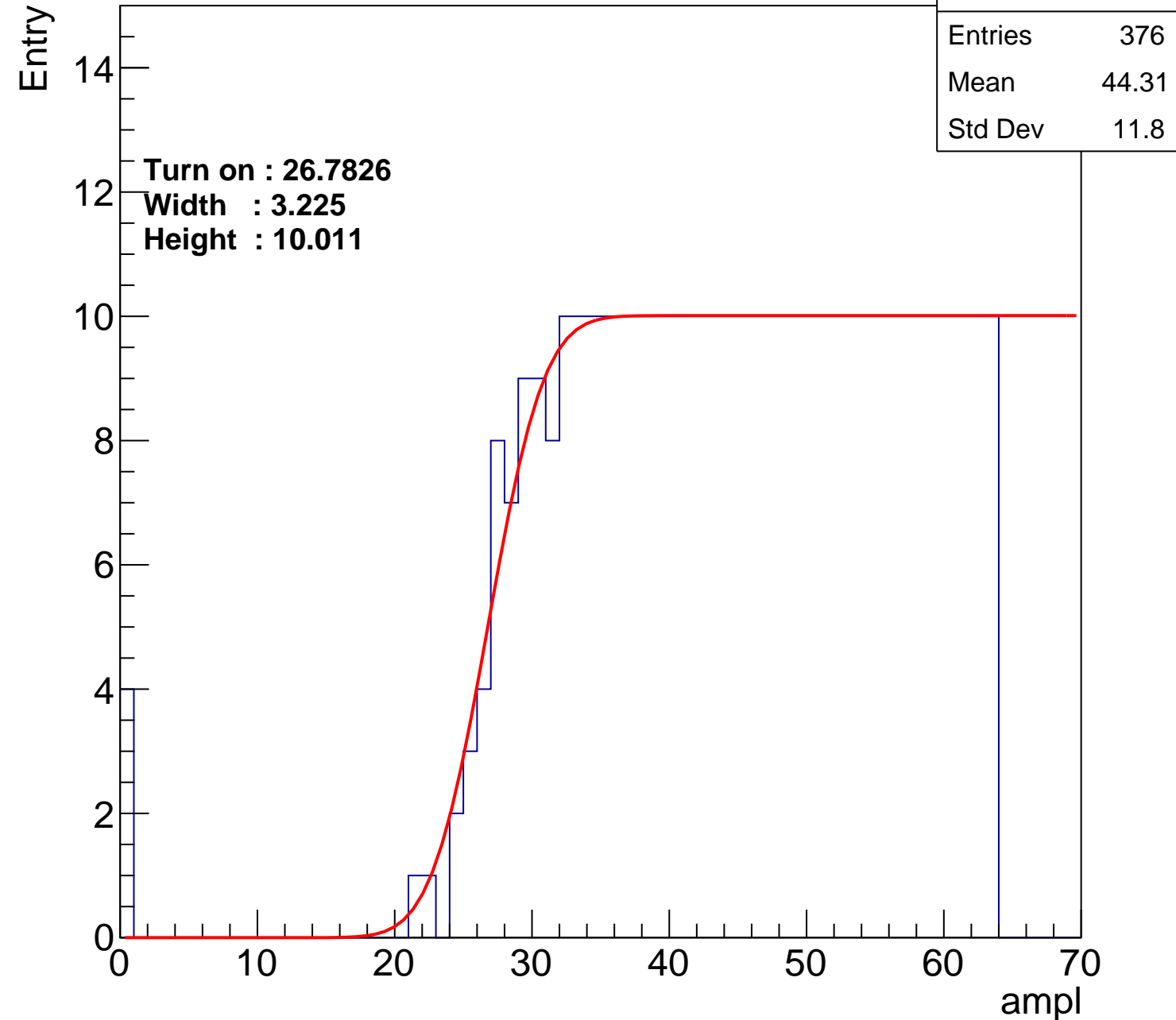
Width : 3.225

Height : 10.011

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch103

calib_packv5_042523_0143.root, FC#8, port C1

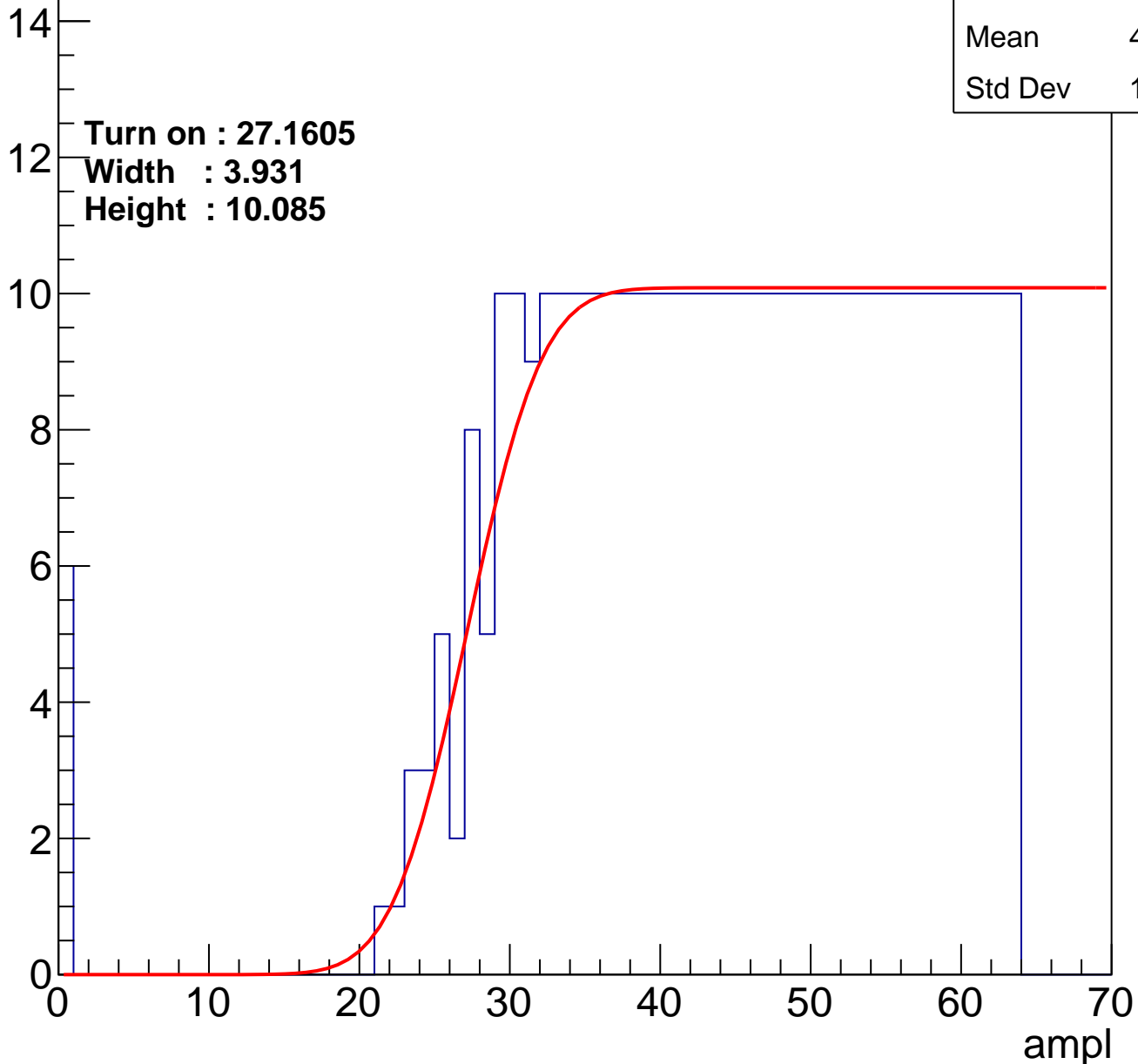
Entries	383
Mean	43.83
Std Dev	12.32

Turn on : 27.1605

Width : 3.931

Height : 10.085

Entry



B0L002S, U9-ch104

calib_packv5_042523_0143.root, FC#8, port C1

Entries	367
Mean	44.99
Std Dev	10.95

Turn on : 27.6827

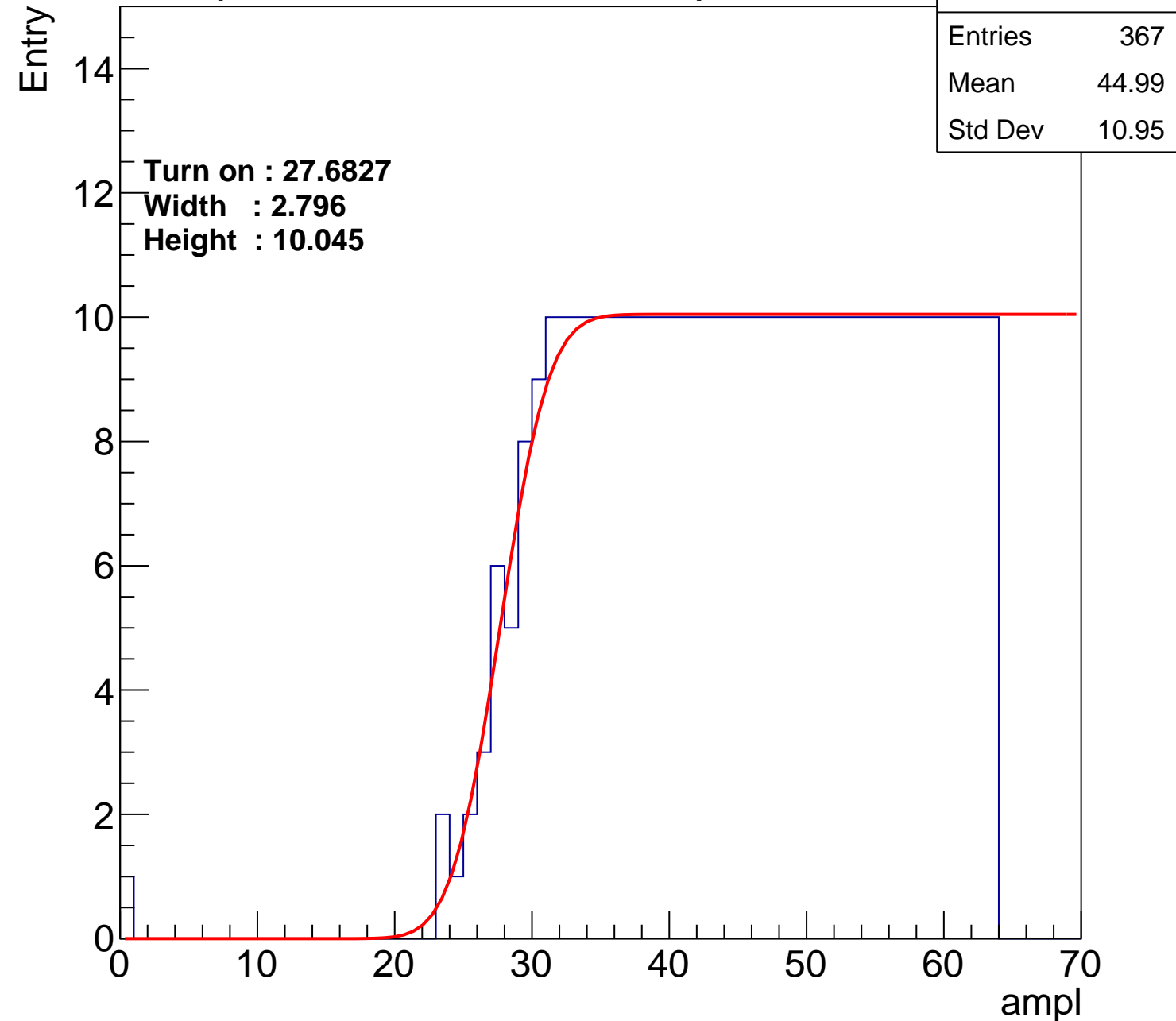
Width : 2.796

Height : 10.045

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch105

calib_packv5_042523_0143.root, FC#8, port C1

Entries	376
Mean	44.26
Std Dev	11.95

Turn on : 27.1163

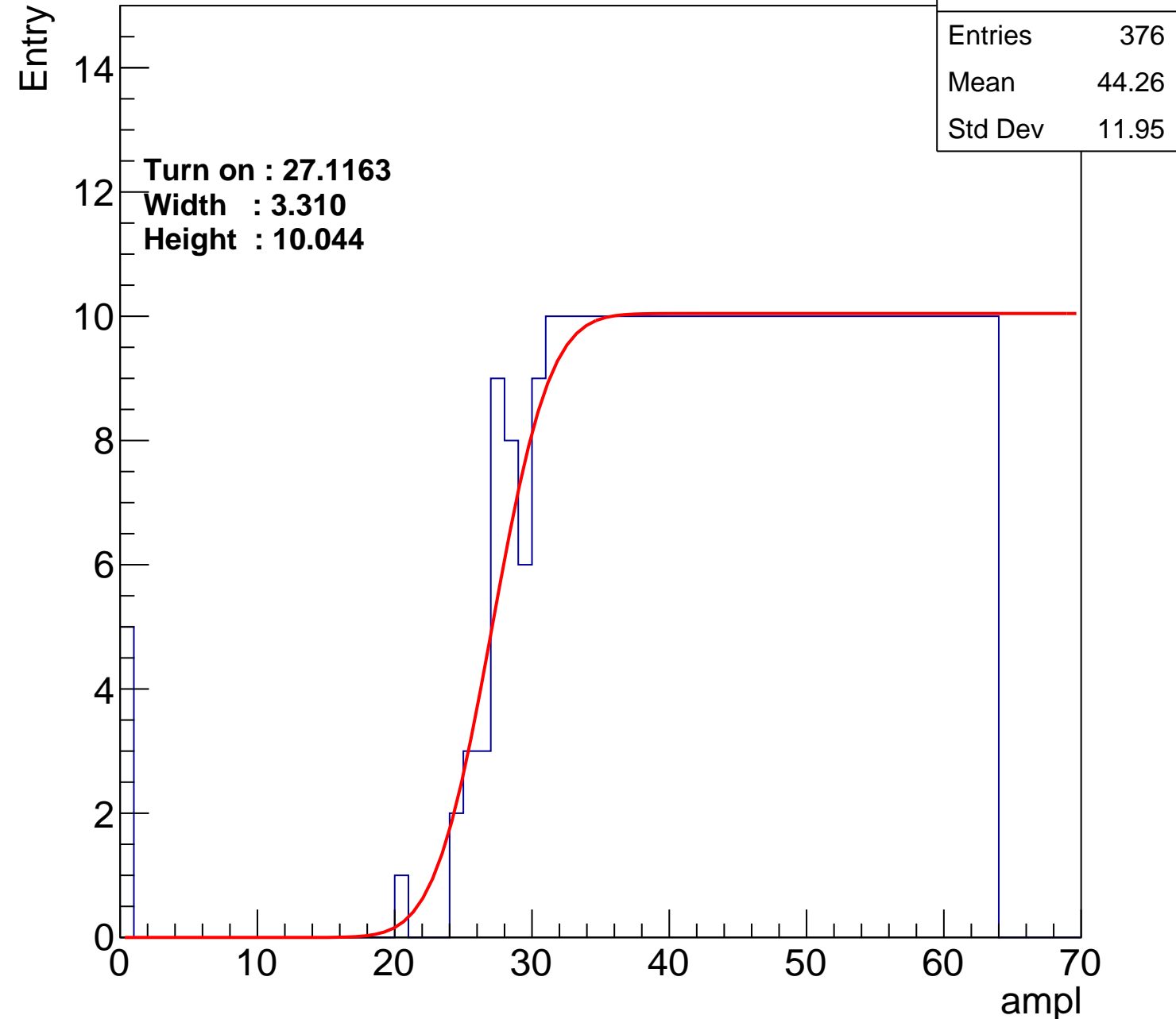
Width : 3.310

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch106

calib_packv5_042523_0143.root, FC#8, port C1

Entries	400
Mean	43.36
Std Dev	11.84

Turn on : 24.1954

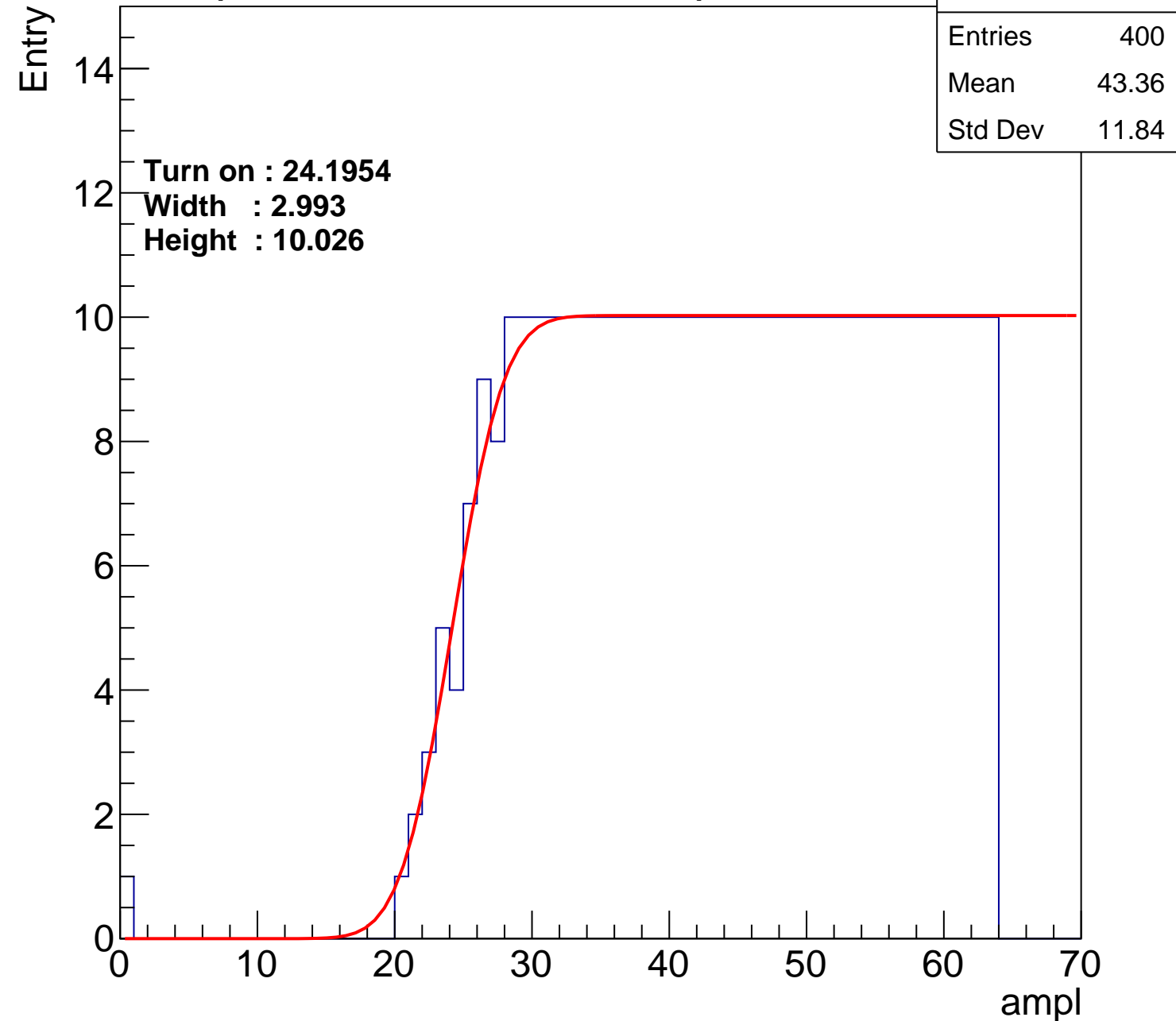
Width : 2.993

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch107

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.34
Std Dev	11.49

Turn on : 26.7194

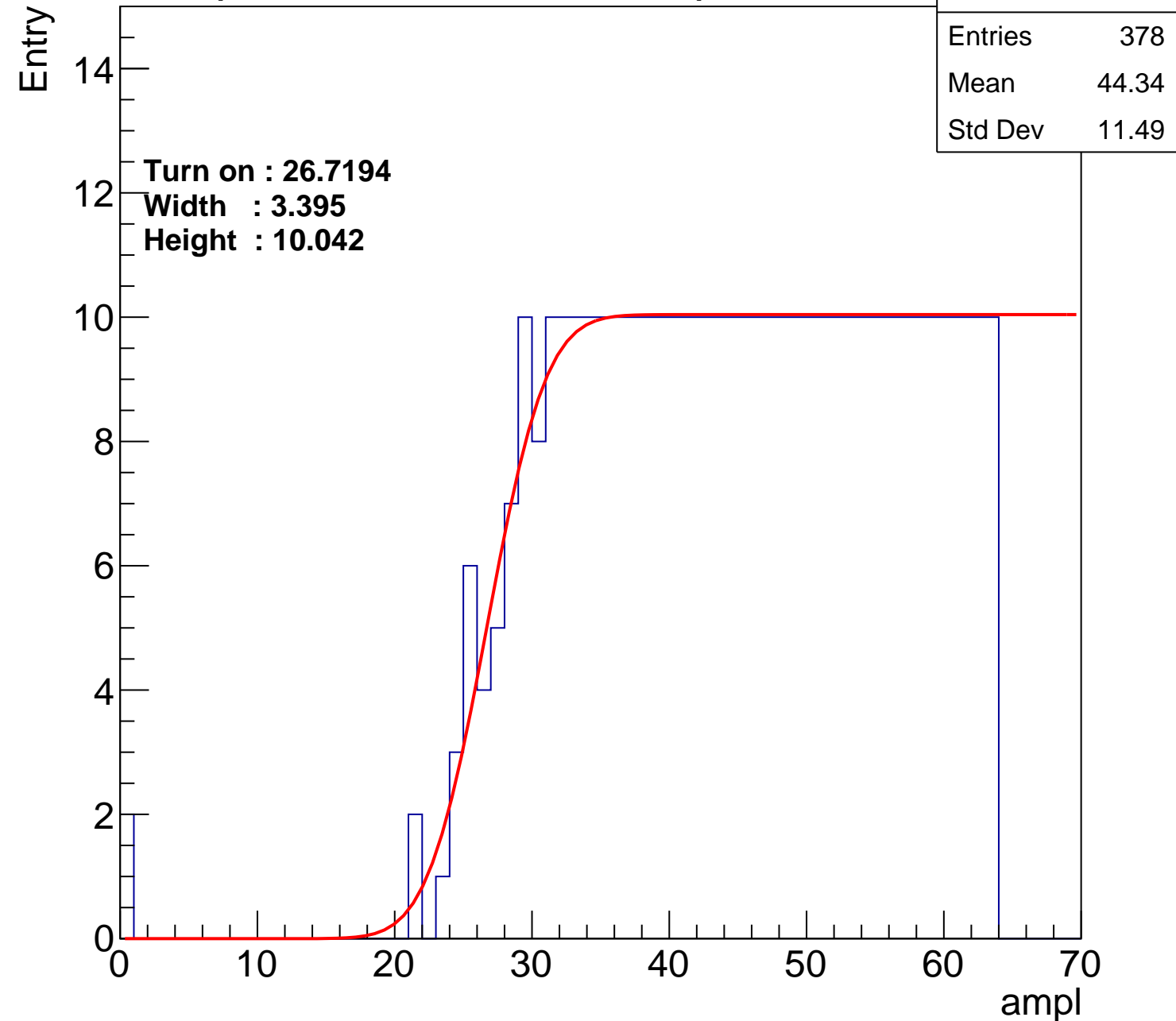
Width : 3.395

Height : 10.042

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch108

calib_packv5_042523_0143.root, FC#8, port C1

Entries	366
Mean	44.84
Std Dev	11.42

Turn on : 28.0994

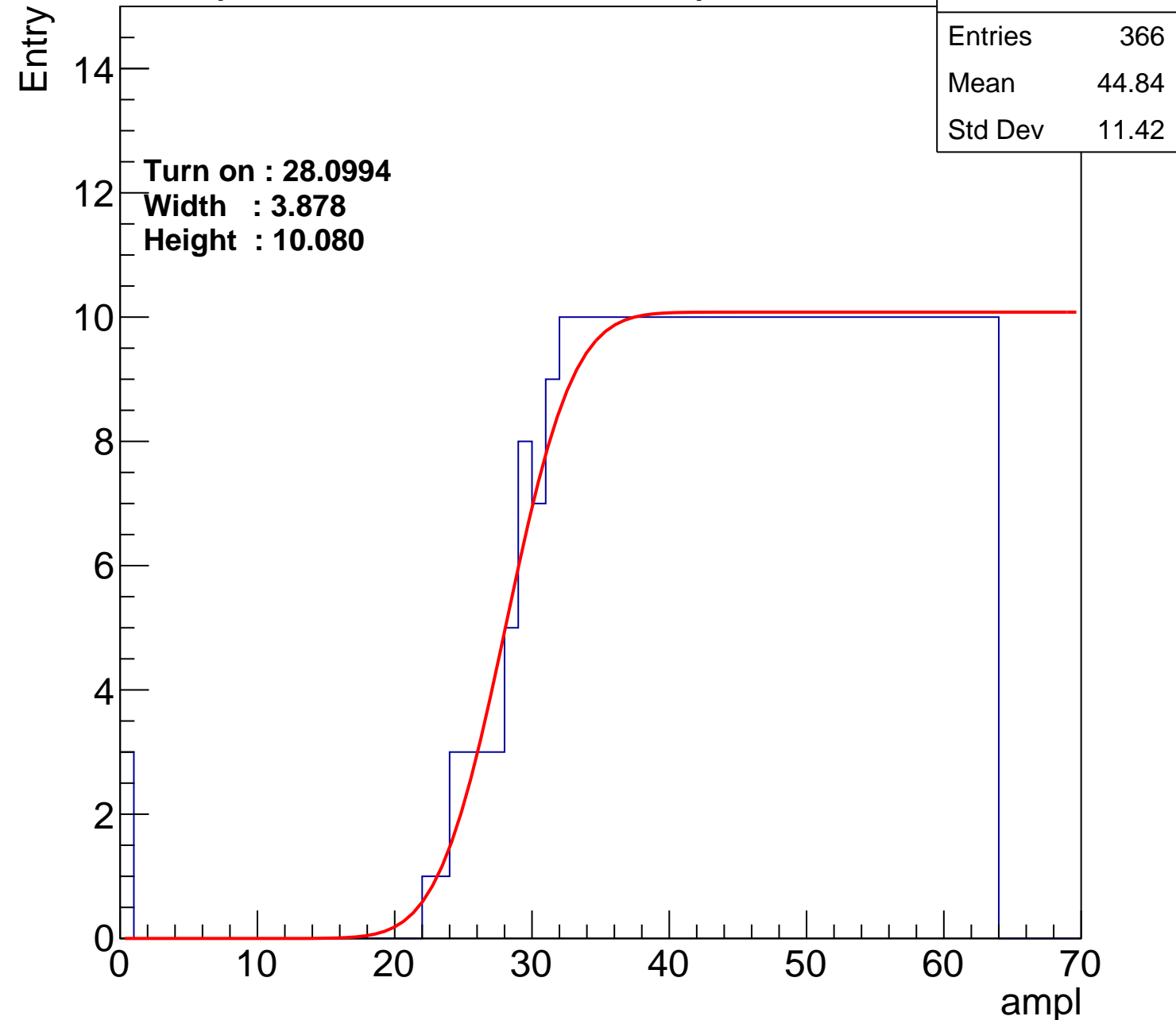
Width : 3.878

Height : 10.080

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch109

calib_packv5_042523_0143.root, FC#8, port C1

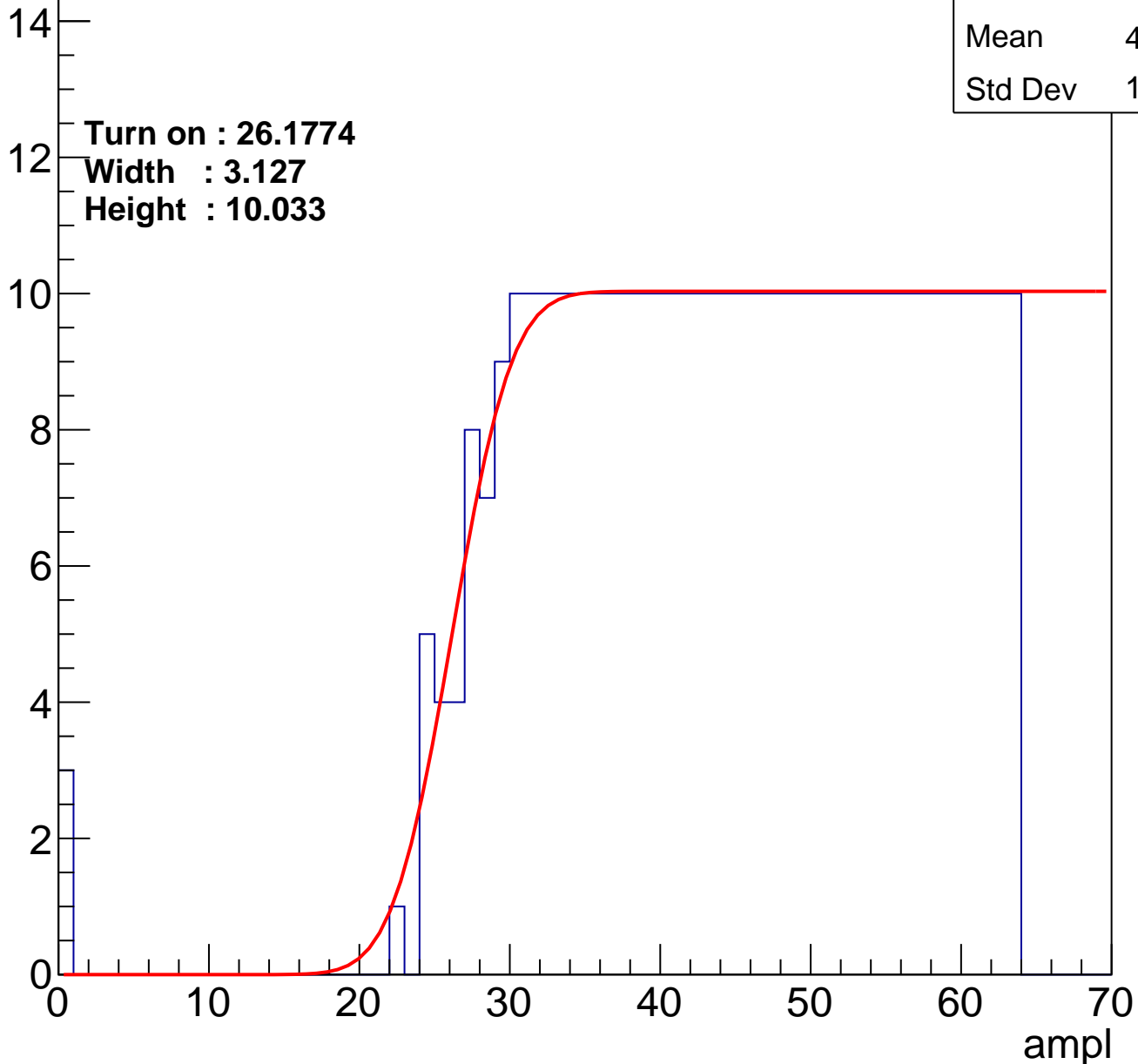
Entries	381
Mean	44.17
Std Dev	11.68

Turn on : 26.1774

Width : 3.127

Height : 10.033

Entry



B0L002S, U9-ch110

calib_packv5_042523_0143.root, FC#8, port C1

Entries	375
Mean	44.52
Std Dev	11.37

Turn on : 26.8863

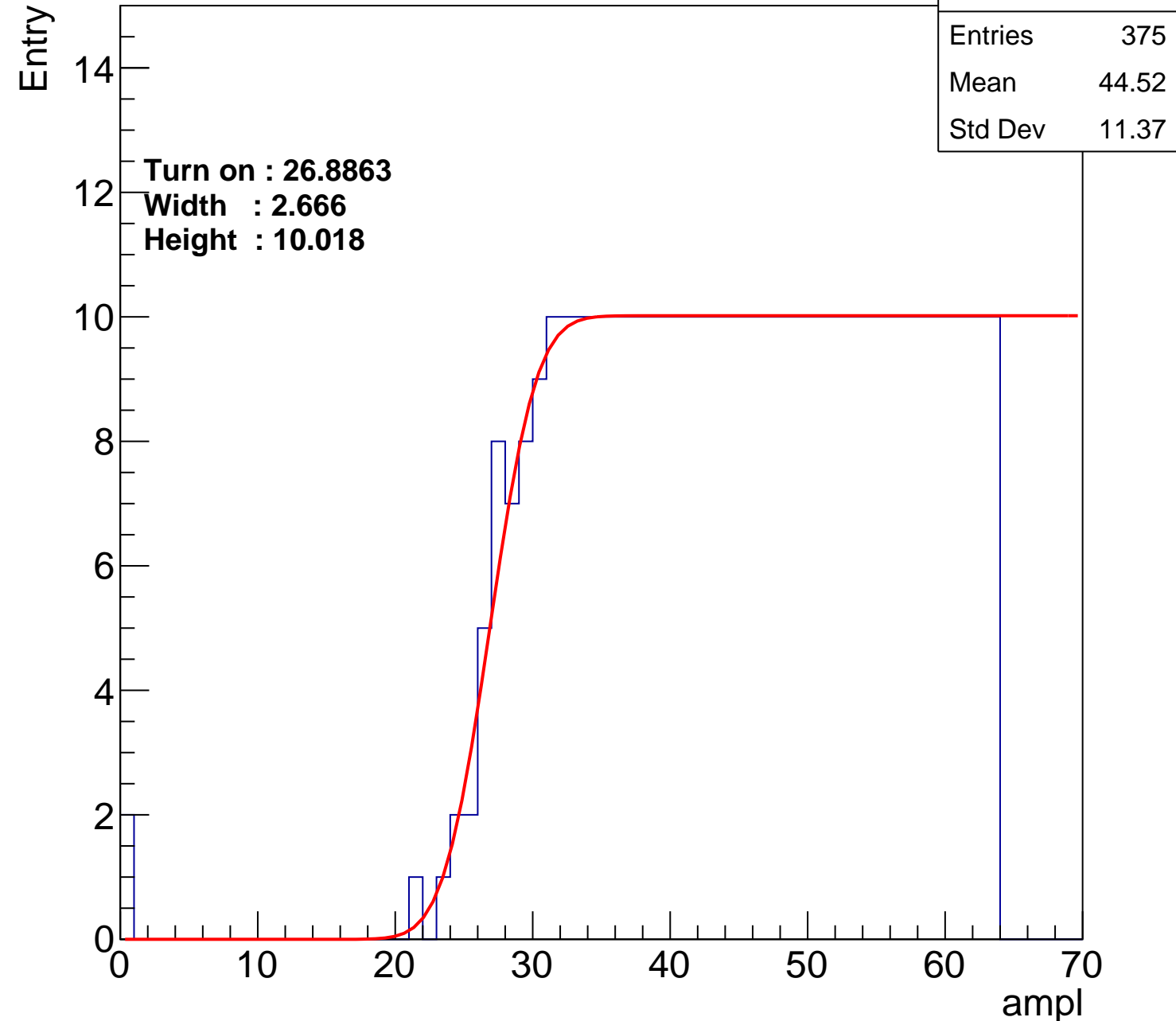
Width : 2.666

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch111

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.82
Std Dev	11.13

Turn on : 27.6814

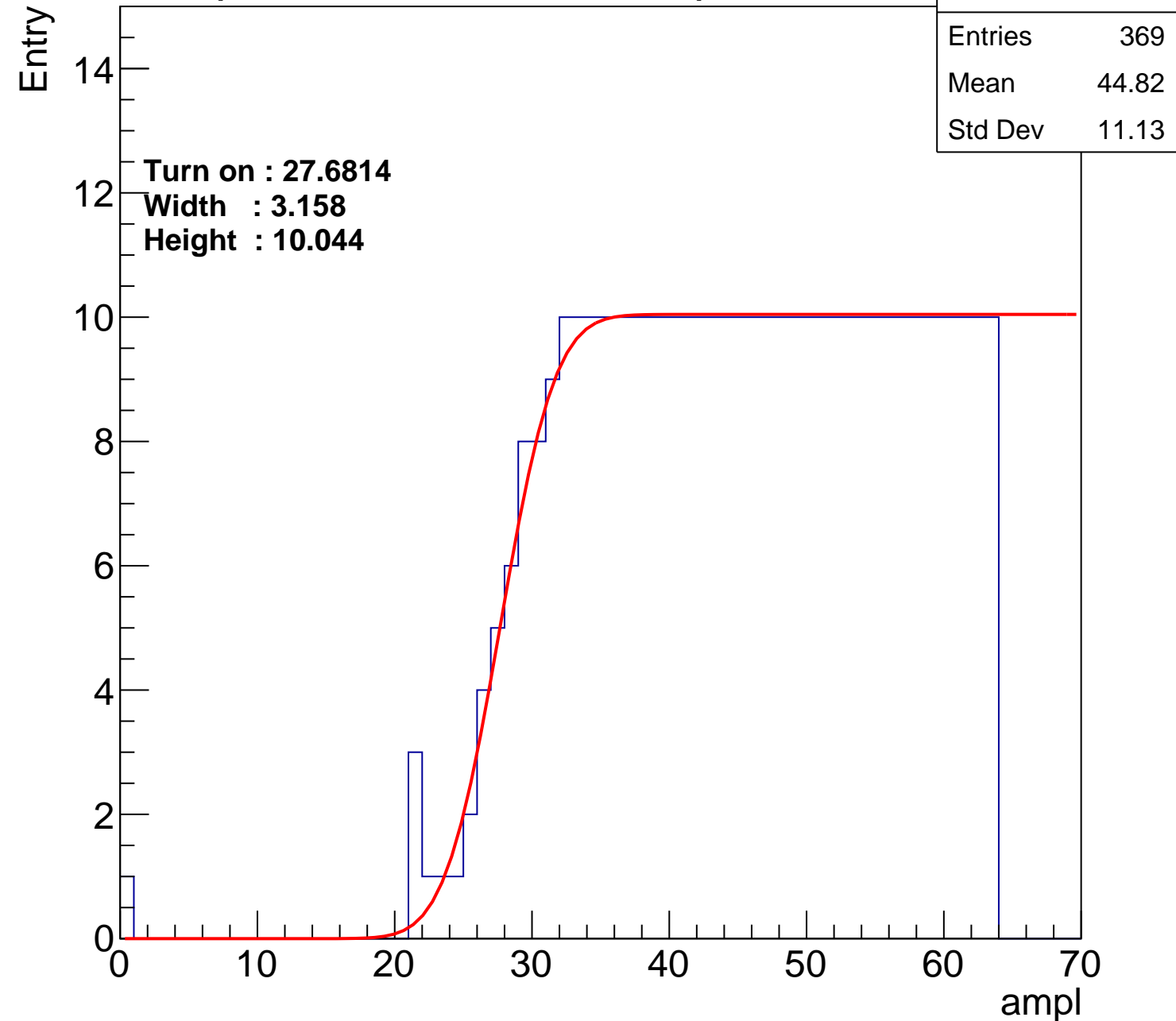
Width : 3.158

Height : 10.044

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch112

calib_packv5_042523_0143.root, FC#8, port C1

Entries	378
Mean	44.39
Std Dev	11.42

Turn on : 26.3478

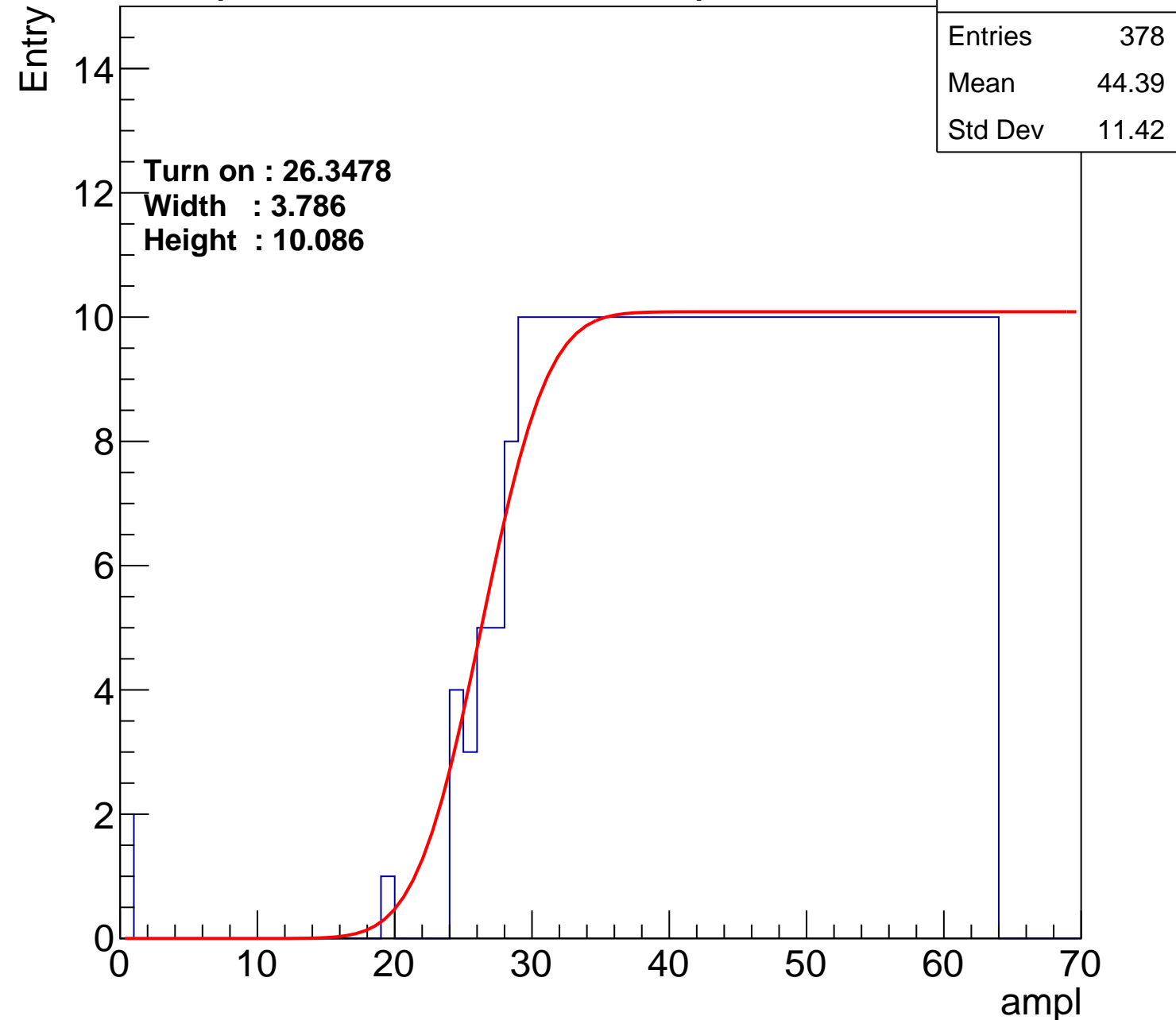
Width : 3.786

Height : 10.086

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch113

calib_packv5_042523_0143.root, FC#8, port C1

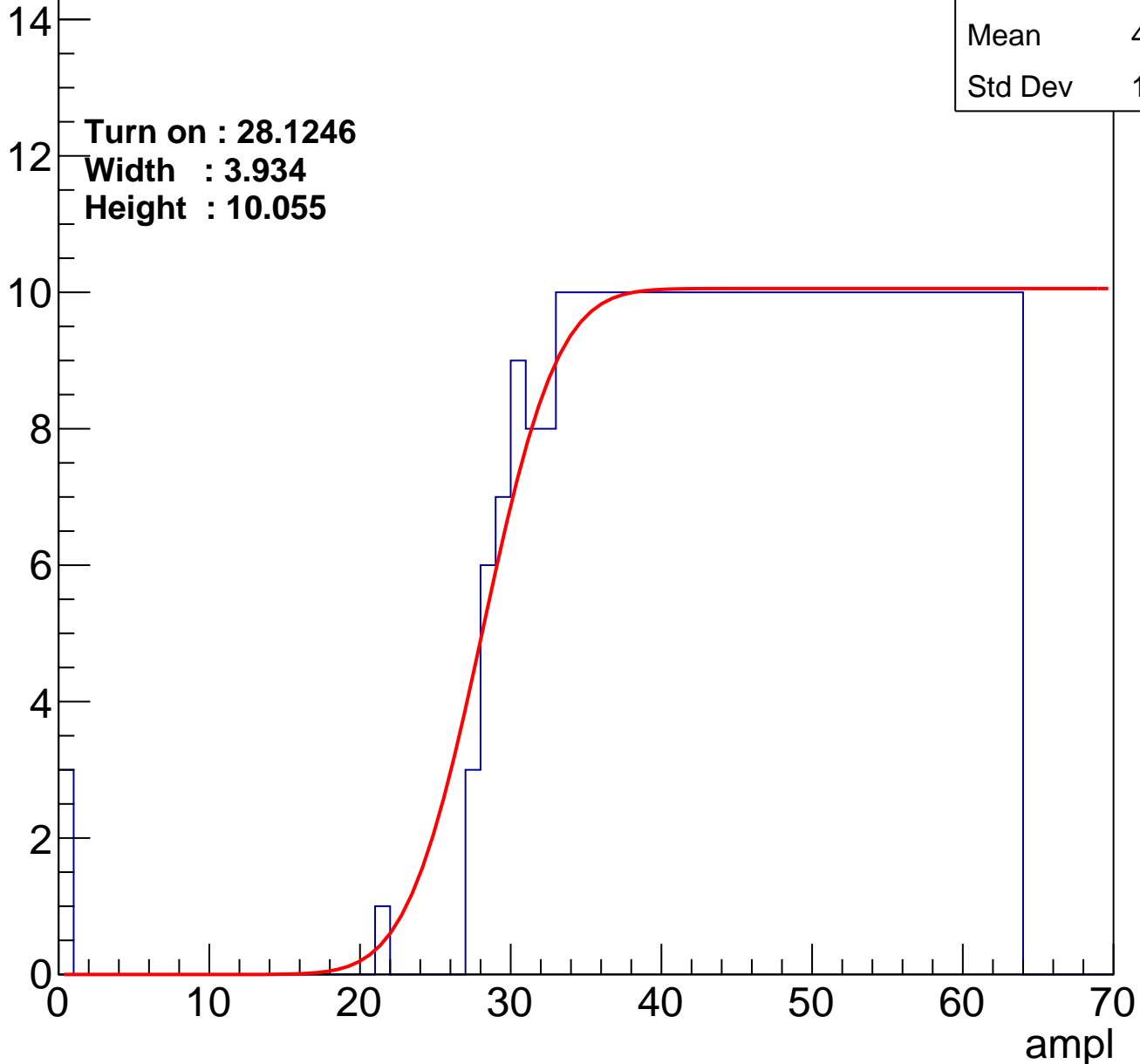
Entries	355
Mean	45.43
Std Dev	11.08

Turn on : 28.1246

Width : 3.934

Height : 10.055

Entry



B0L002S, U9-ch114

calib_packv5_042523_0143.root, FC#8, port C1

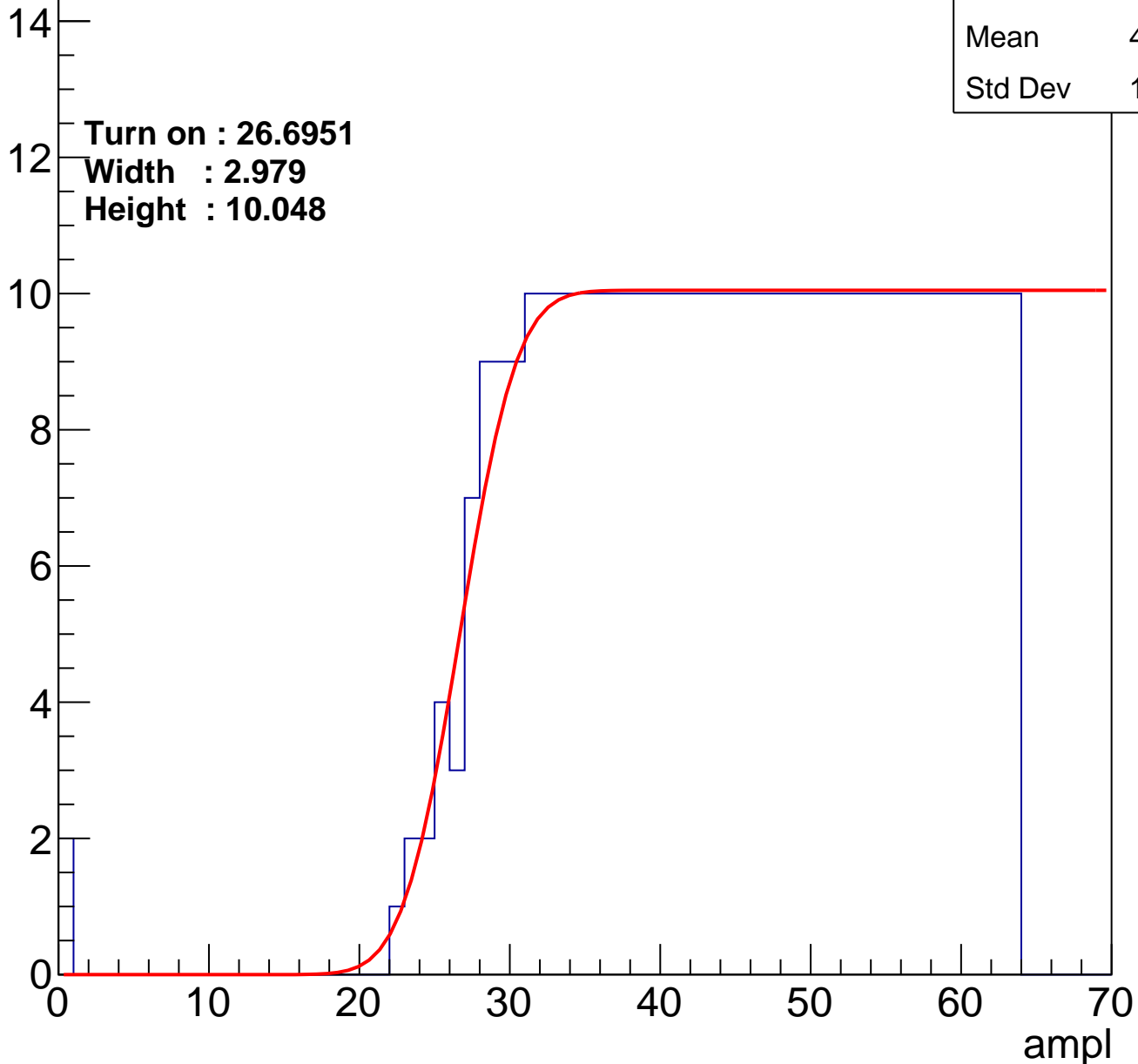
Entries	378
Mean	44.38
Std Dev	11.43

Turn on : 26.6951

Width : 2.979

Height : 10.048

Entry



B0L002S, U9-ch115

calib_packv5_042523_0143.root, FC#8, port C1

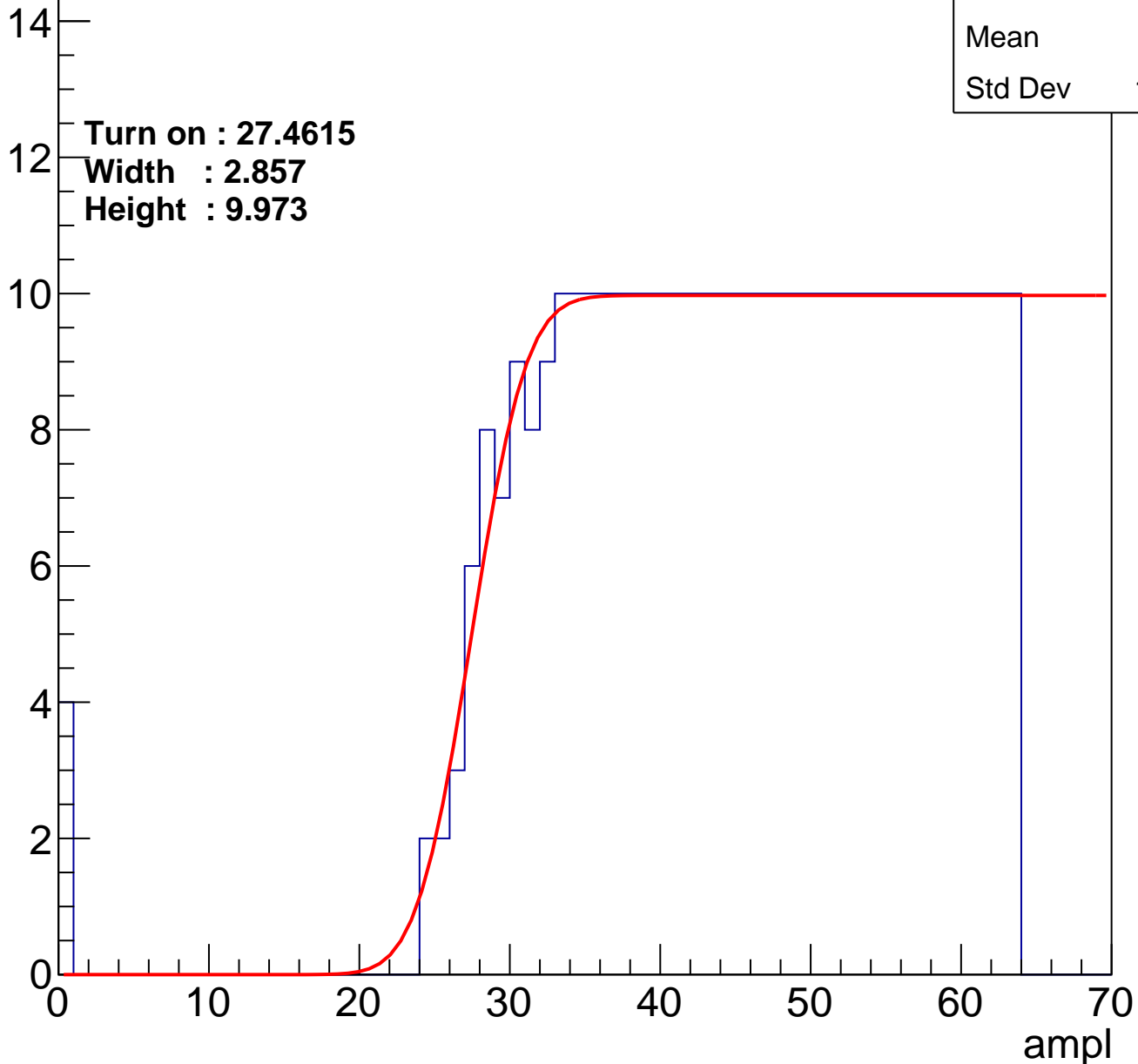
Entries	368
Mean	44.7
Std Dev	11.61

Turn on : 27.4615

Width : 2.857

Height : 9.973

Entry



B0L002S, U9-ch116

calib_packv5_042523_0143.root, FC#8, port C1

Entries	373
Mean	44.65
Std Dev	11.26

Turn on : 27.0034

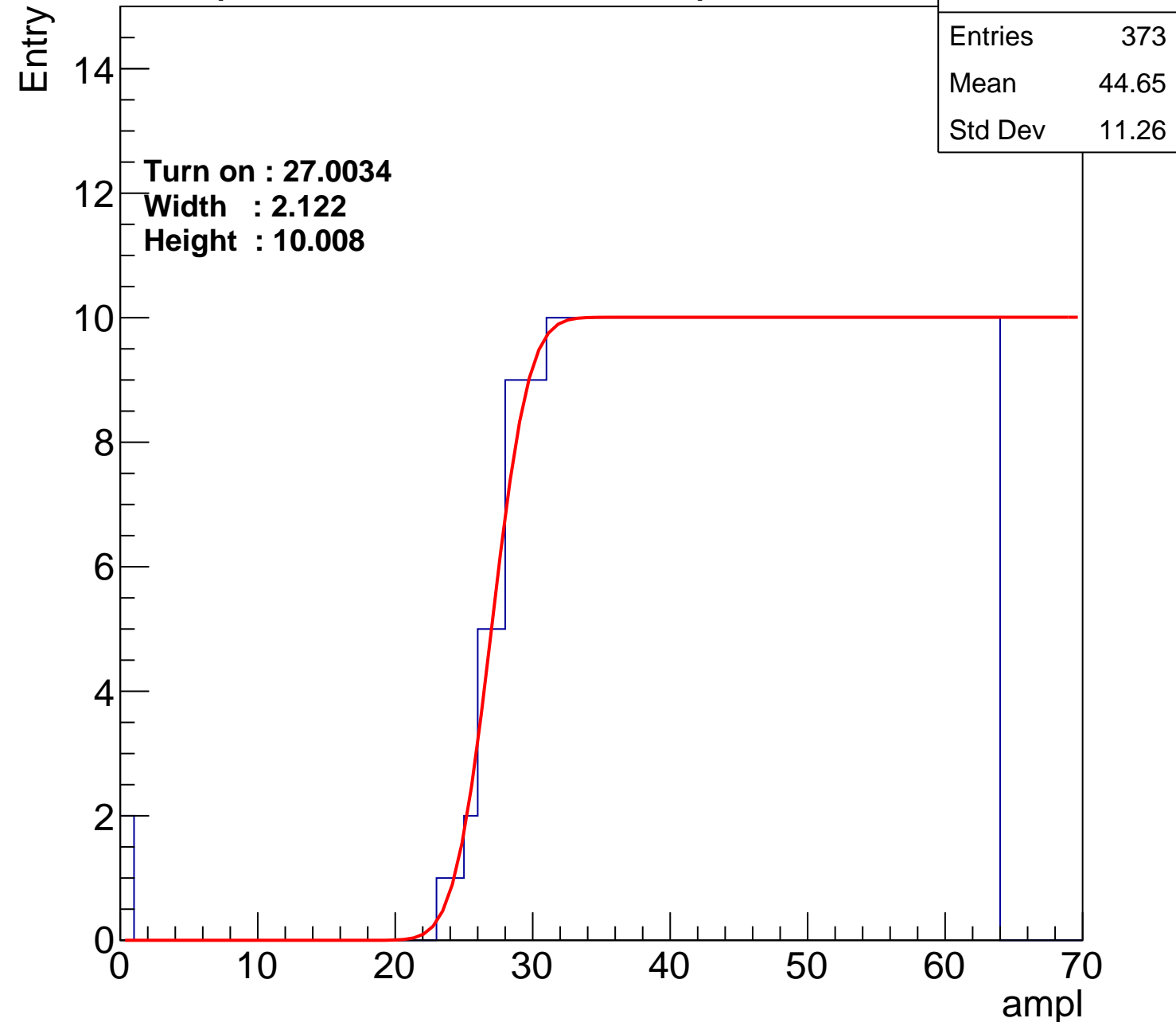
Width : 2.122

Height : 10.008

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch117

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.27
Std Dev	10.82

Turn on : 27.8766

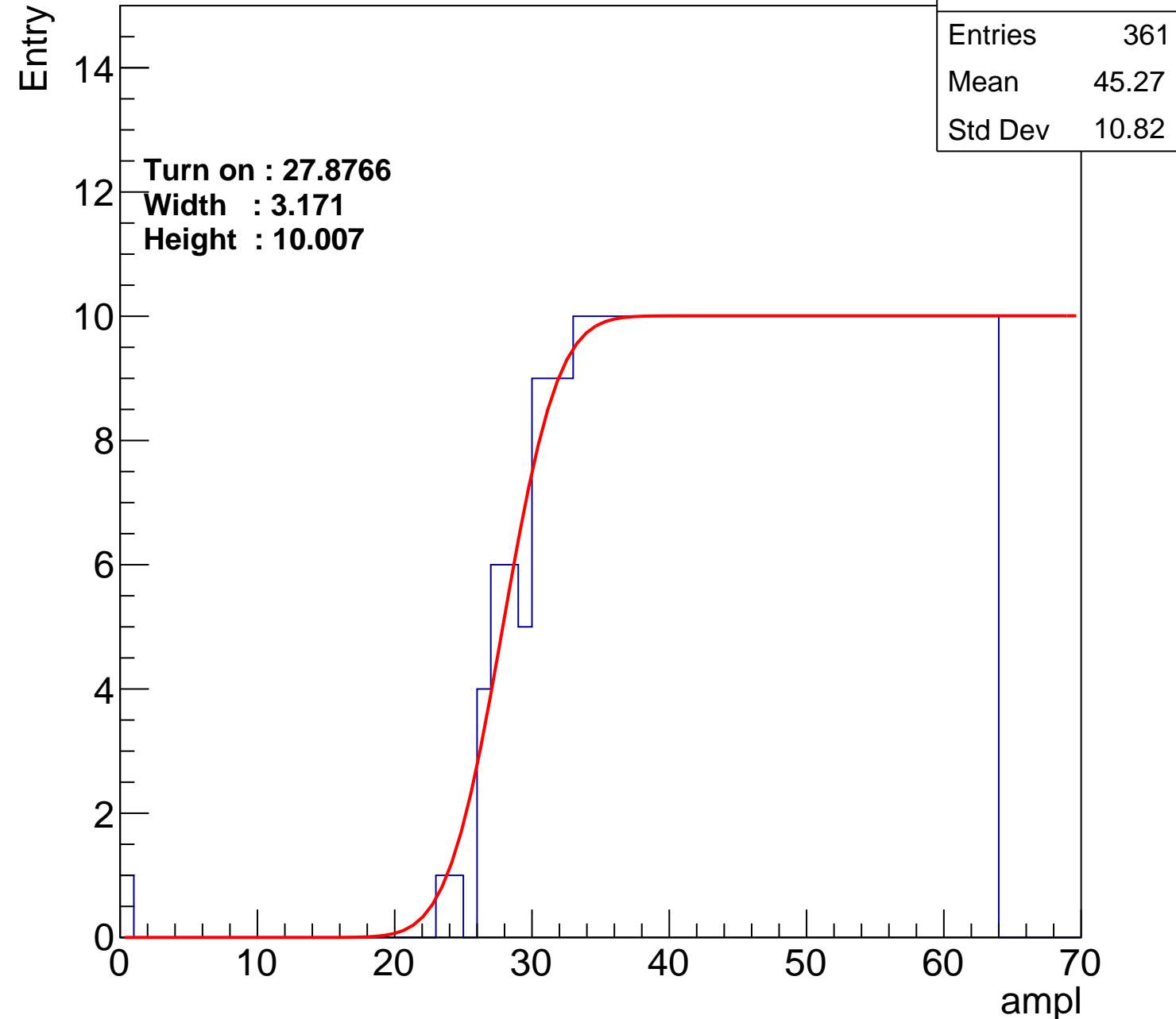
Width : 3.171

Height : 10.007

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch118

calib_packv5_042523_0143.root, FC#8, port C1

Entries	387
Mean	43.77
Std Dev	12.07

Turn on : 25.8459

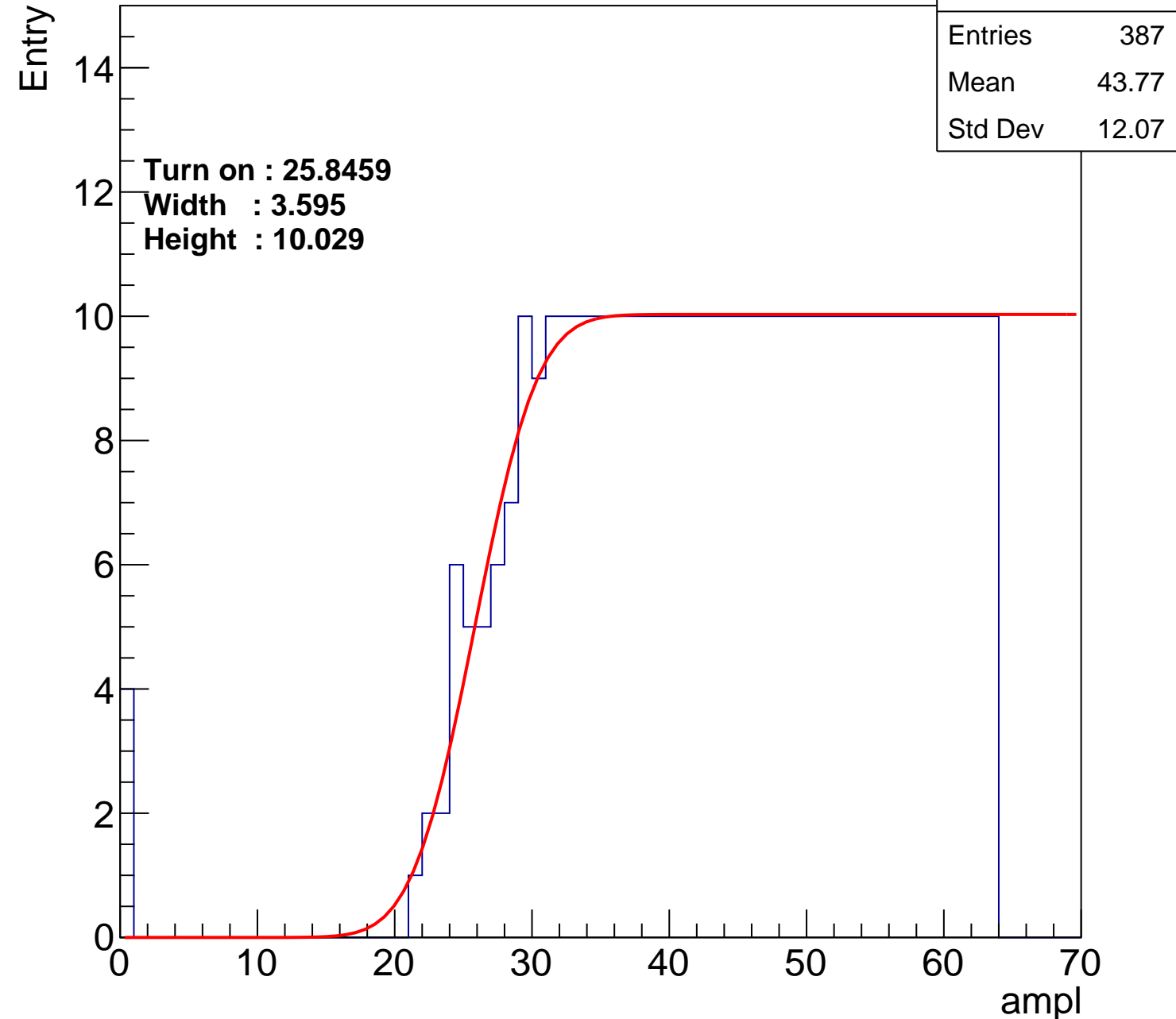
Width : 3.595

Height : 10.029

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch119

calib_packv5_042523_0143.root, FC#8, port C1

Entries	389
Mean	43.61
Std Dev	12.28

Turn on : 26.0425

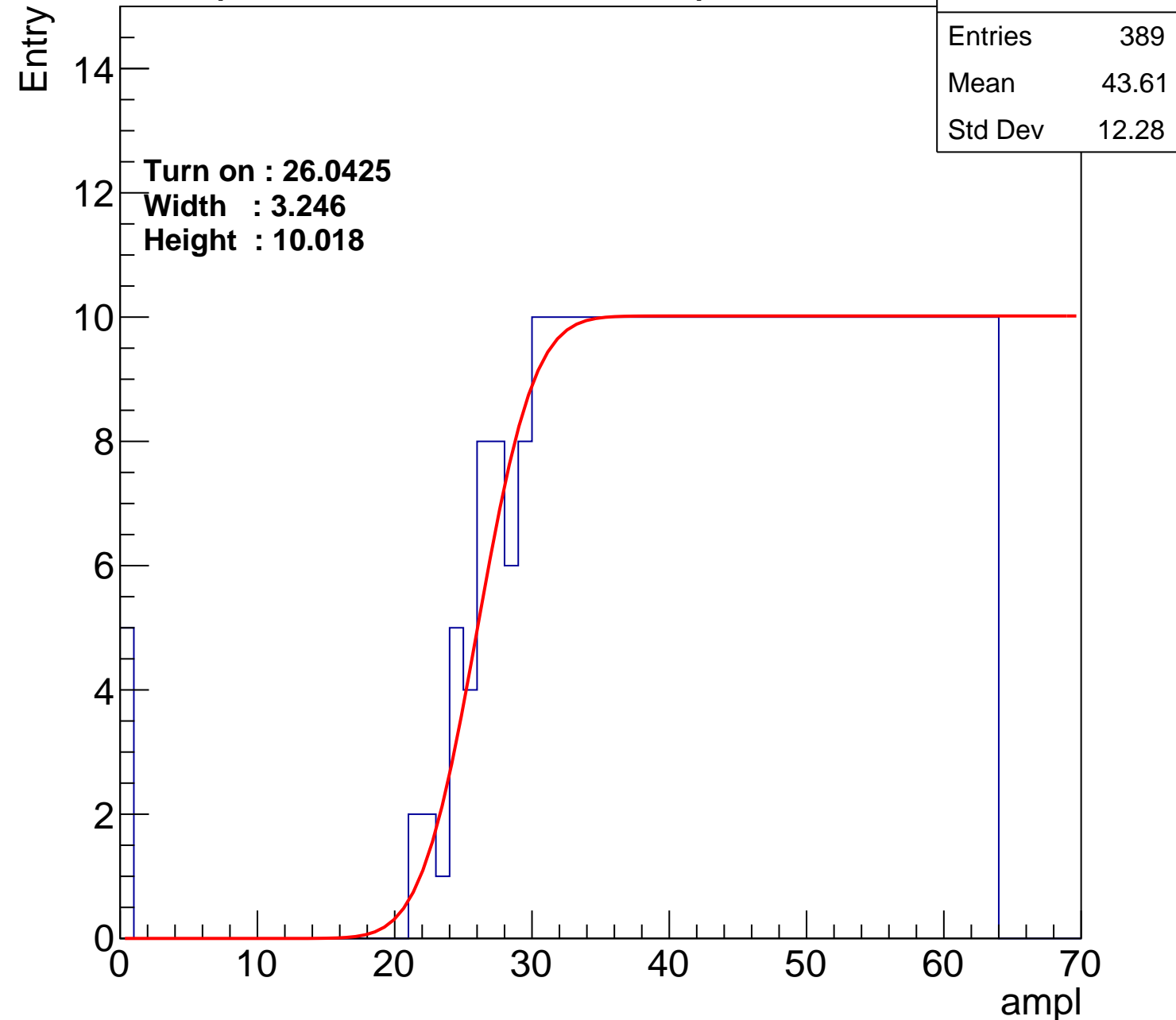
Width : 3.246

Height : 10.018

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch120

calib_packv5_042523_0143.root, FC#8, port C1

Entries	380
Mean	44.3
Std Dev	11.46

Turn on : 26.0668

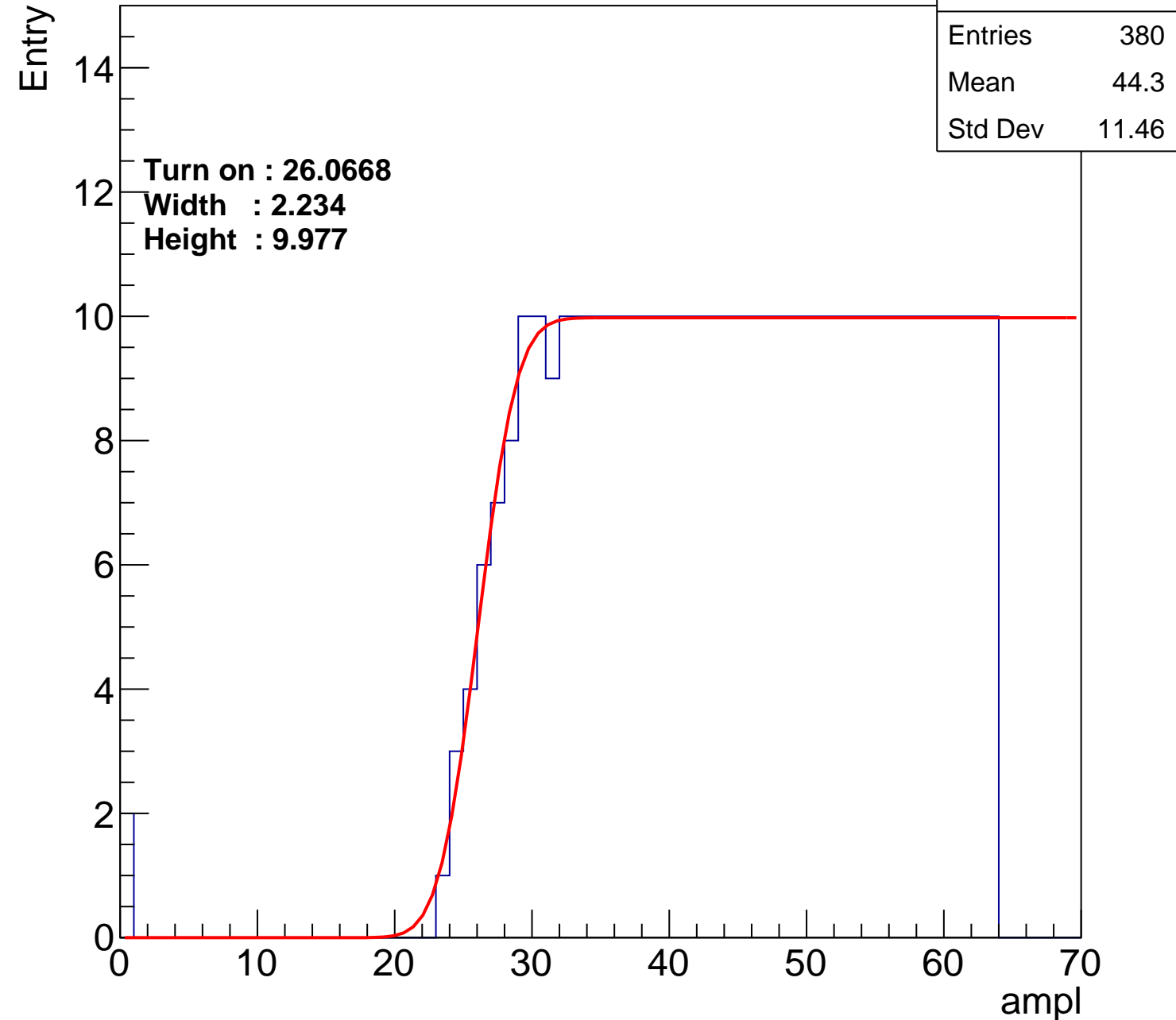
Width : 2.234

Height : 9.977

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch121

calib_packv5_042523_0143.root, FC#8, port C1

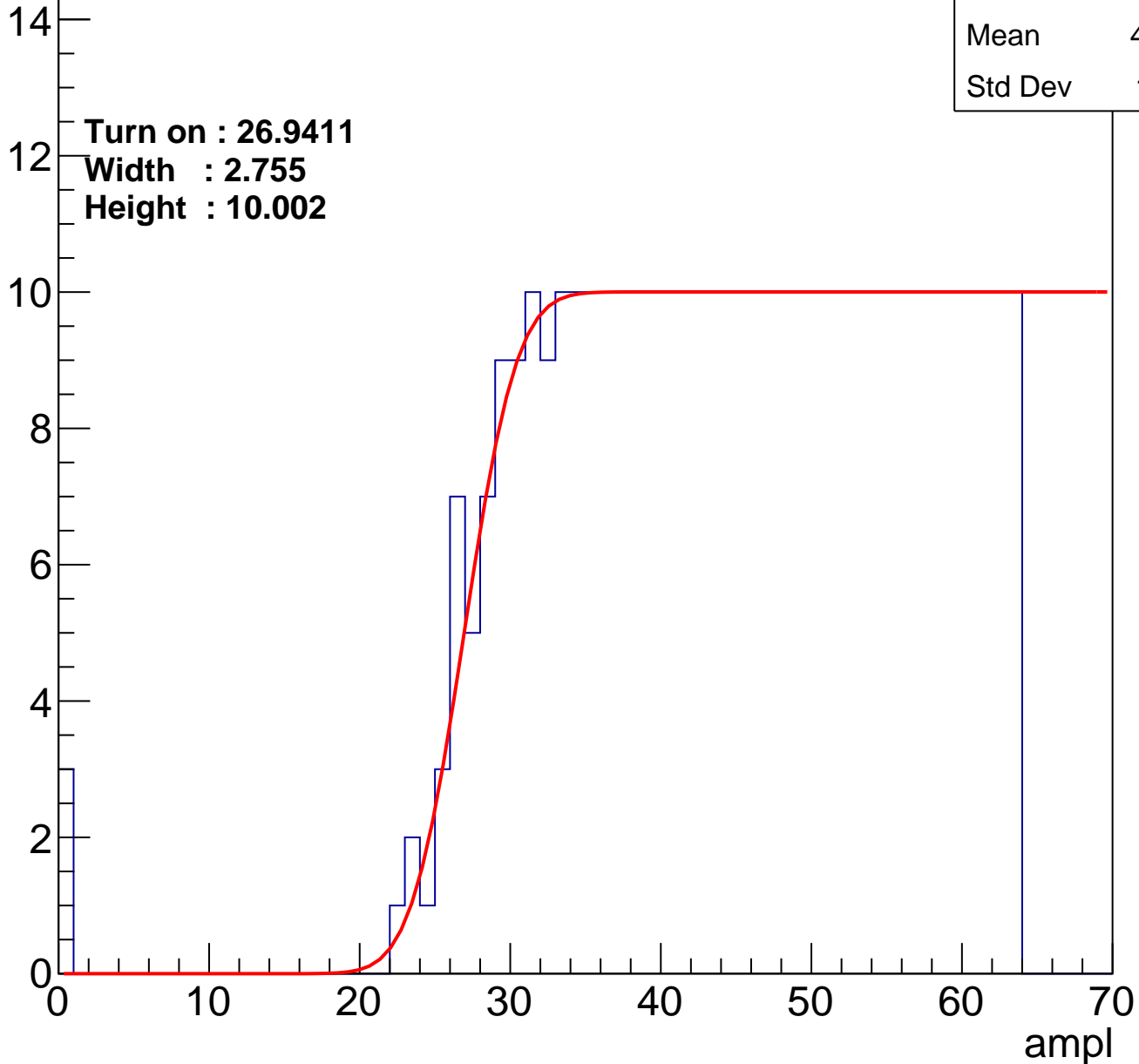
Entries	376
Mean	44.39
Std Dev	11.61

Turn on : 26.9411

Width : 2.755

Height : 10.002

Entry



B0L002S, U9-ch122

calib_packv5_042523_0143.root, FC#8, port C1

Entries	369
Mean	44.93
Std Dev	10.94

Turn on : 27.2660

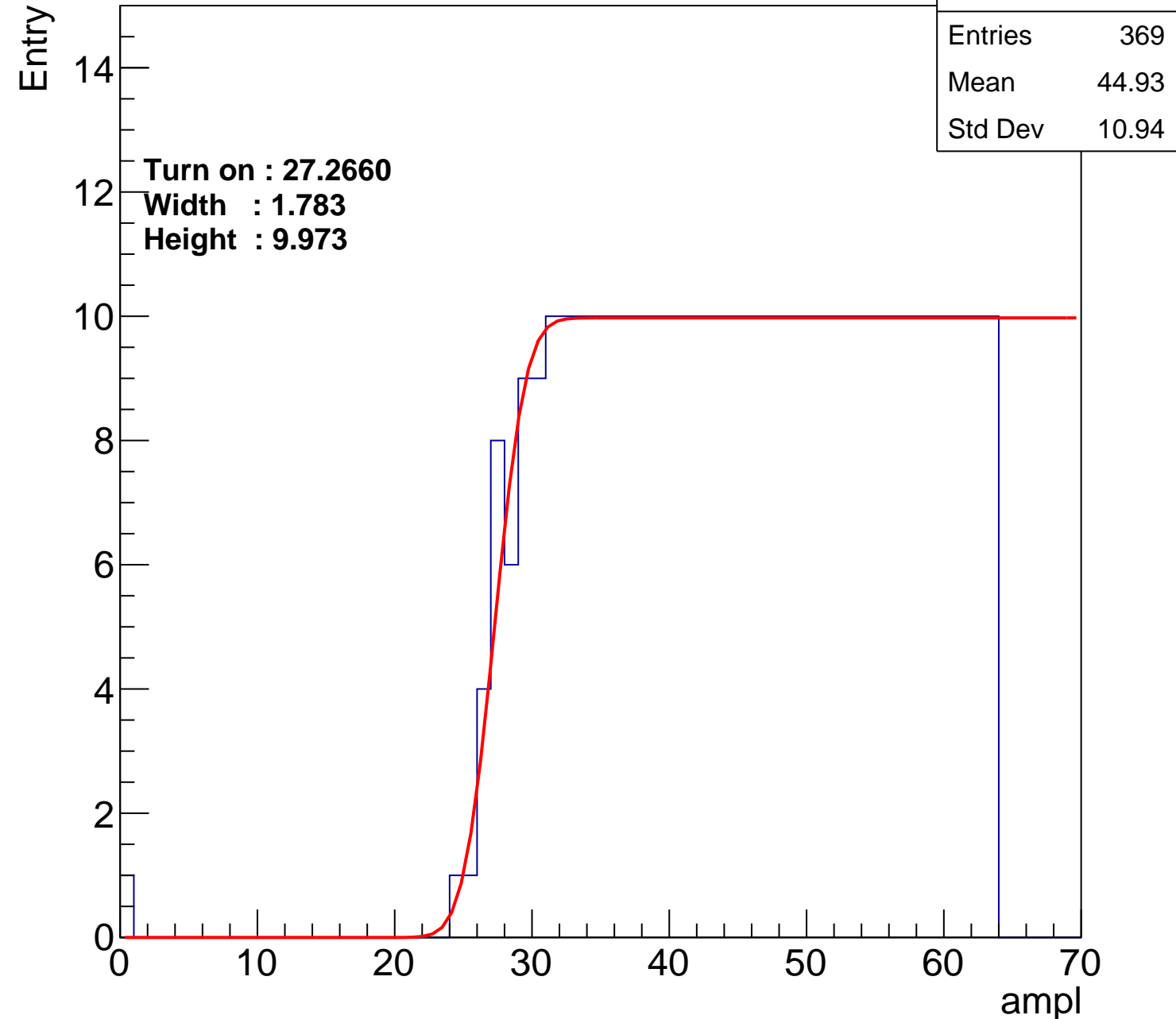
Width : 1.783

Height : 9.973

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch123

calib_packv5_042523_0143.root, FC#8, port C1

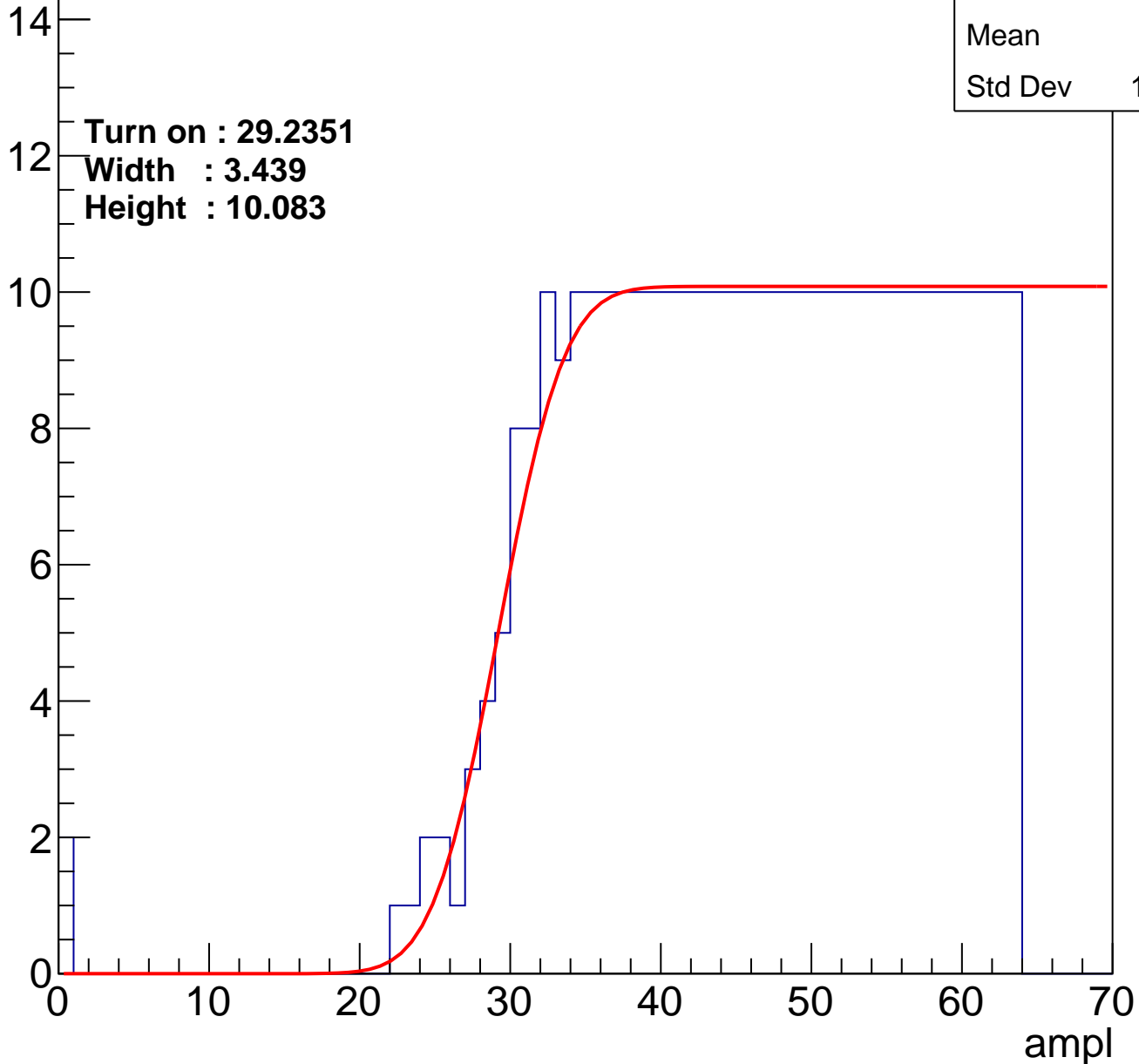
Entries	356
Mean	45.4
Std Dev	10.99

Turn on : 29.2351

Width : 3.439

Height : 10.083

Entry



B0L002S, U9-ch124

calib_packv5_042523_0143.root, FC#8, port C1

Entries	361
Mean	45.31
Std Dev	10.75

Turn on : 28.2736

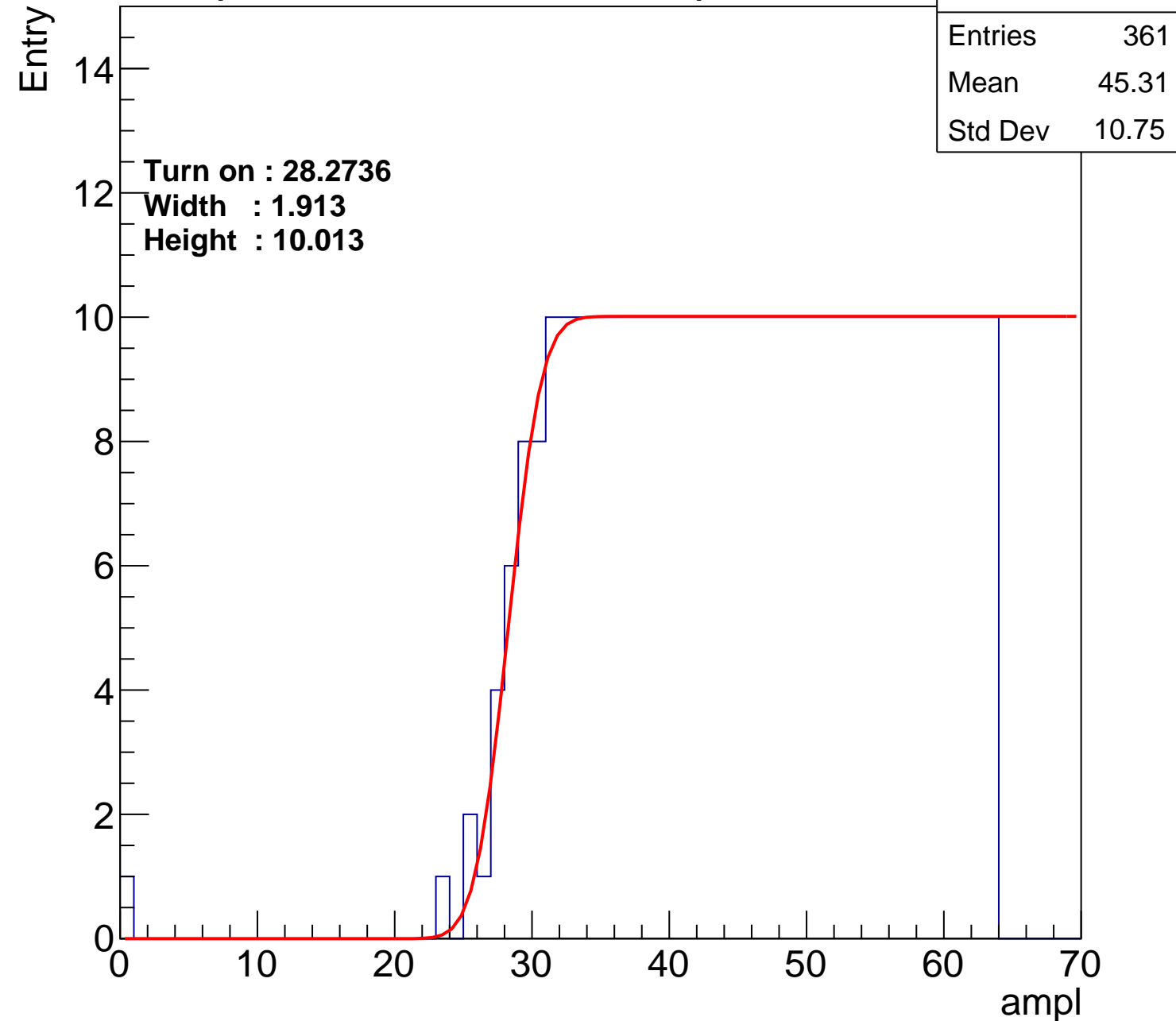
Width : 1.913

Height : 10.013

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch125

calib_packv5_042523_0143.root, FC#8, port C1

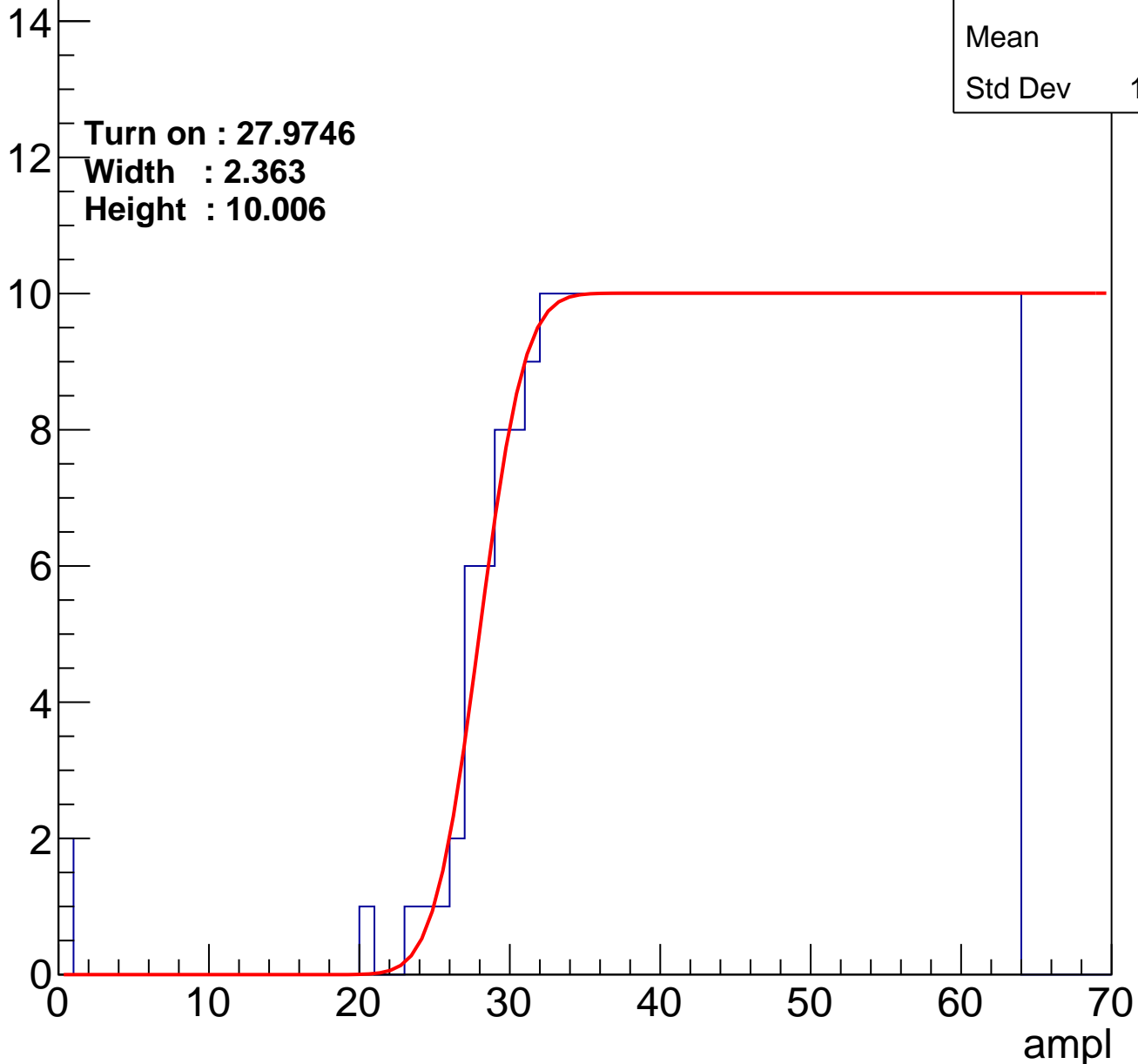
Entries	365
Mean	45
Std Dev	11.14

Turn on : 27.9746

Width : 2.363

Height : 10.006

Entry



B0L002S, U9-ch126

calib_packv5_042523_0143.root, FC#8, port C1

Entries	382
Mean	44.04
Std Dev	11.91

Turn on : 26.5935

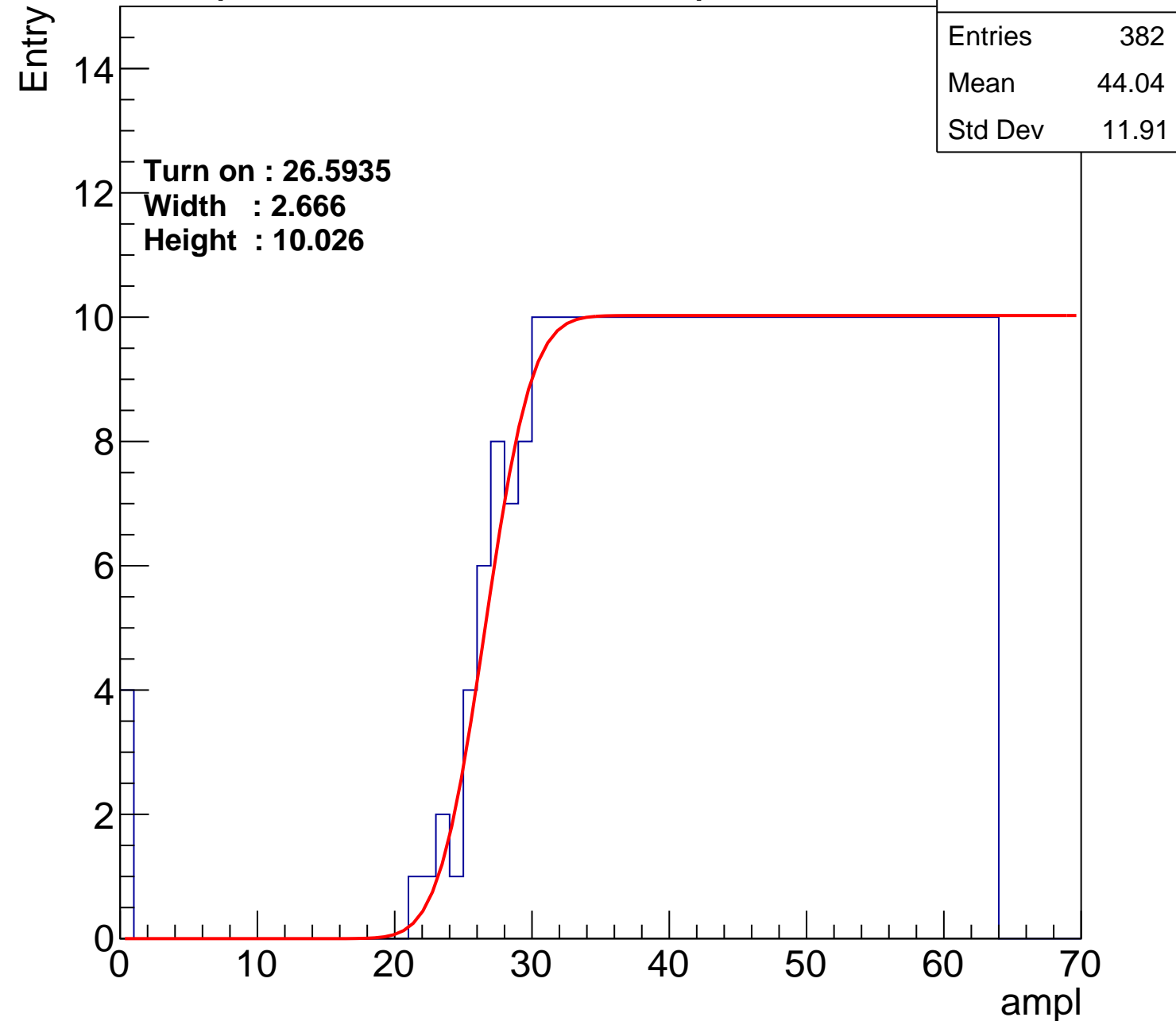
Width : 2.666

Height : 10.026

Entry

14
12
10
8
6
4
2
0

ampl



B0L002S, U9-ch127

calib_packv5_042523_0143.root, FC#8, port C1

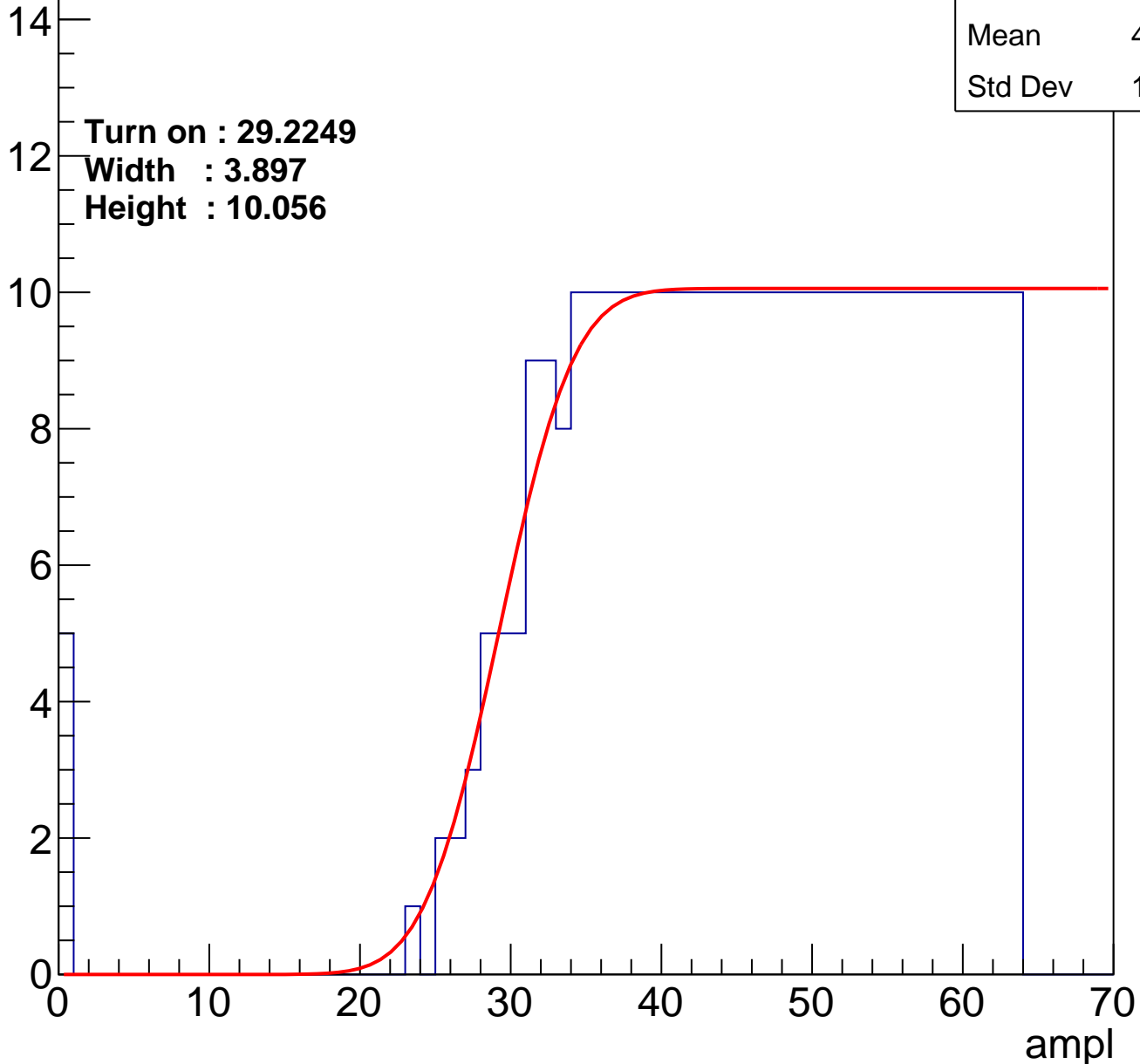
Entries	354
Mean	45.26
Std Dev	11.59

Turn on : 29.2249

Width : 3.897

Height : 10.056

Entry



B0L002S, U9-ch127

calib_packv5_042523_0143.root, FC#8, port C1

Entries	354
Mean	45.26
Std Dev	11.59

Turn on : 29.2249

Width : 3.897

Height : 10.056

Entry

