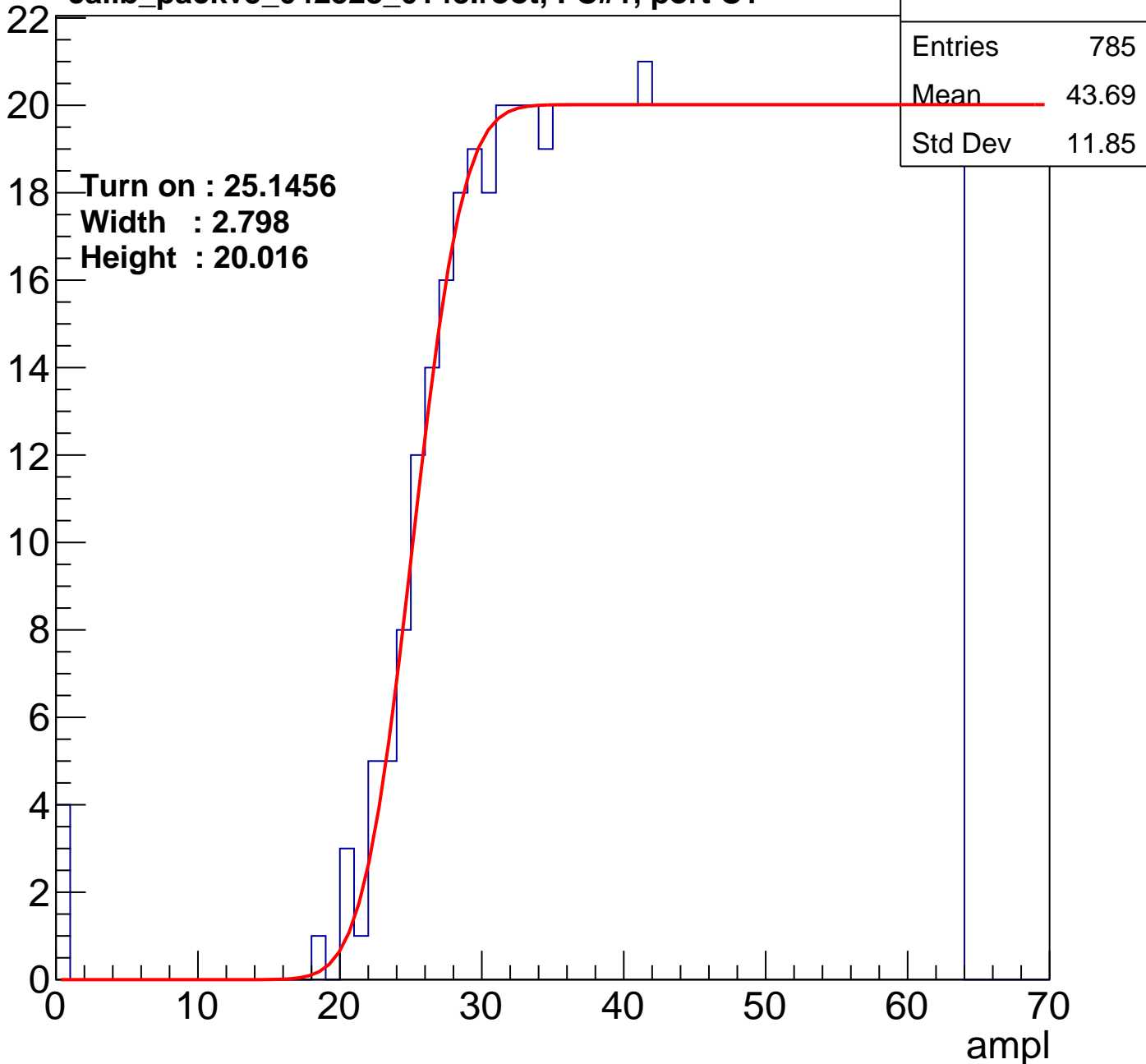


B0L101S, U14-ch0

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch1

calib_packv5_042523_0143.root, FC#1, port C1

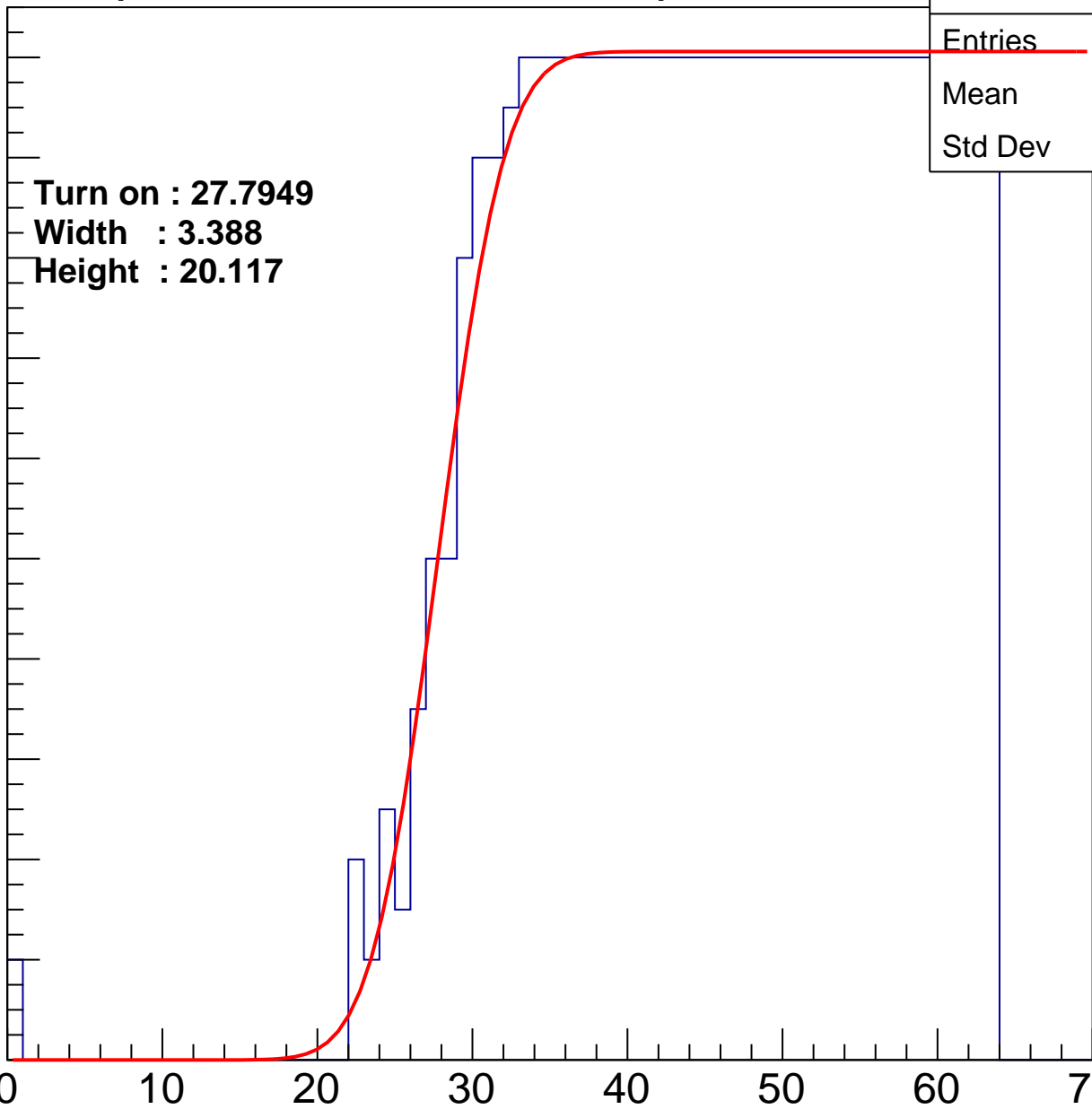
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7949
Width : 3.388
Height : 20.117

Entries	734
Mean	44.95
Std Dev	11.03

ampl



B0L101S, U14-ch2

calib_packv5_042523_0143.root, FC#1, port C1

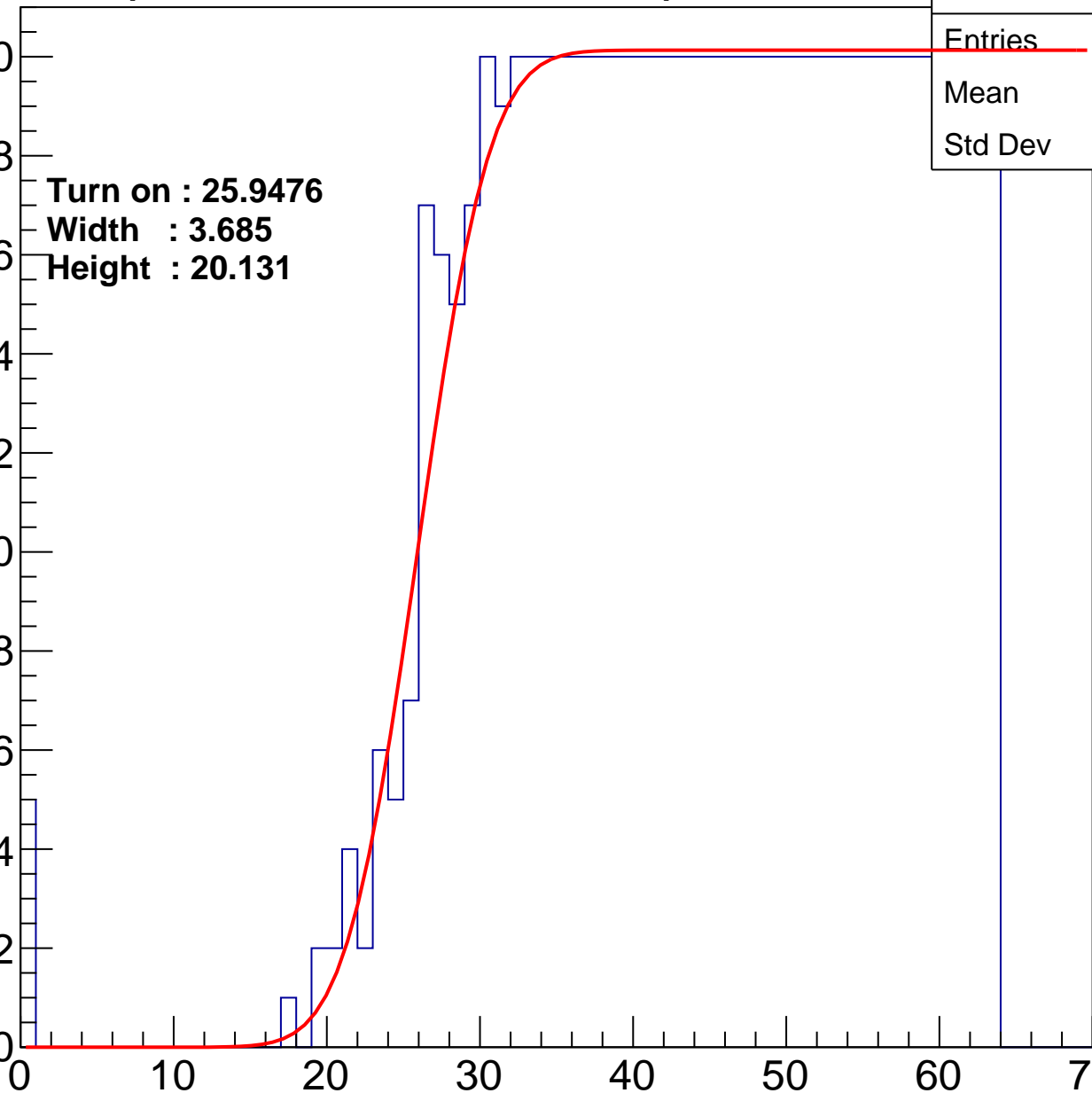
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9476
Width : 3.685
Height : 20.131

Entries	778
Mean	43.74
Std Dev	11.91

ampl



B0L101S, U14-ch3

calib_packv5_042523_0143.root, FC#1, port C1

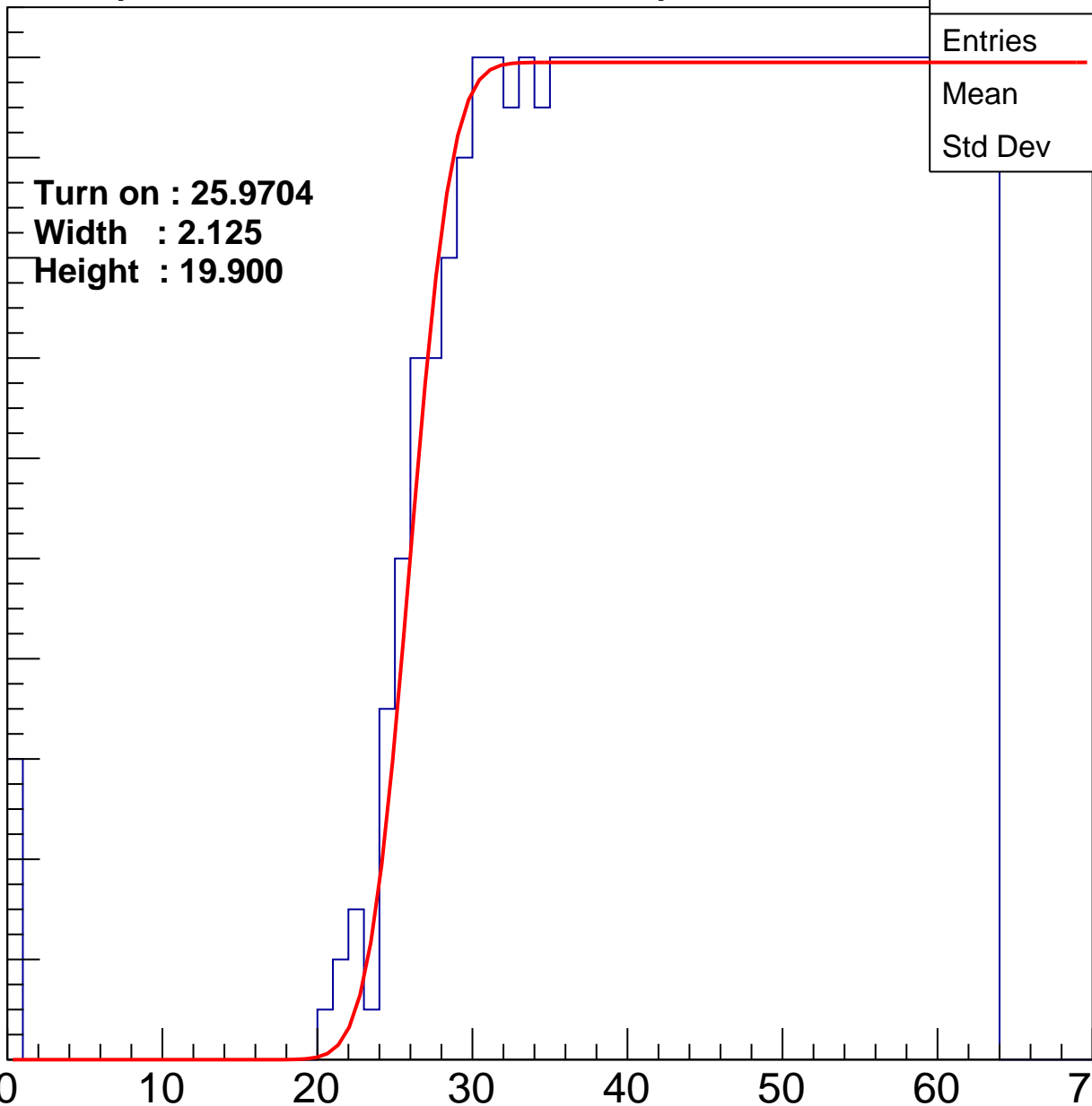
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9704
Width : 2.125
Height : 19.900

Entries	770
Mean	43.94
Std Dev	11.83

ampl



B0L101S, U14-ch4

calib_packv5_042523_0143.root, FC#1, port C1

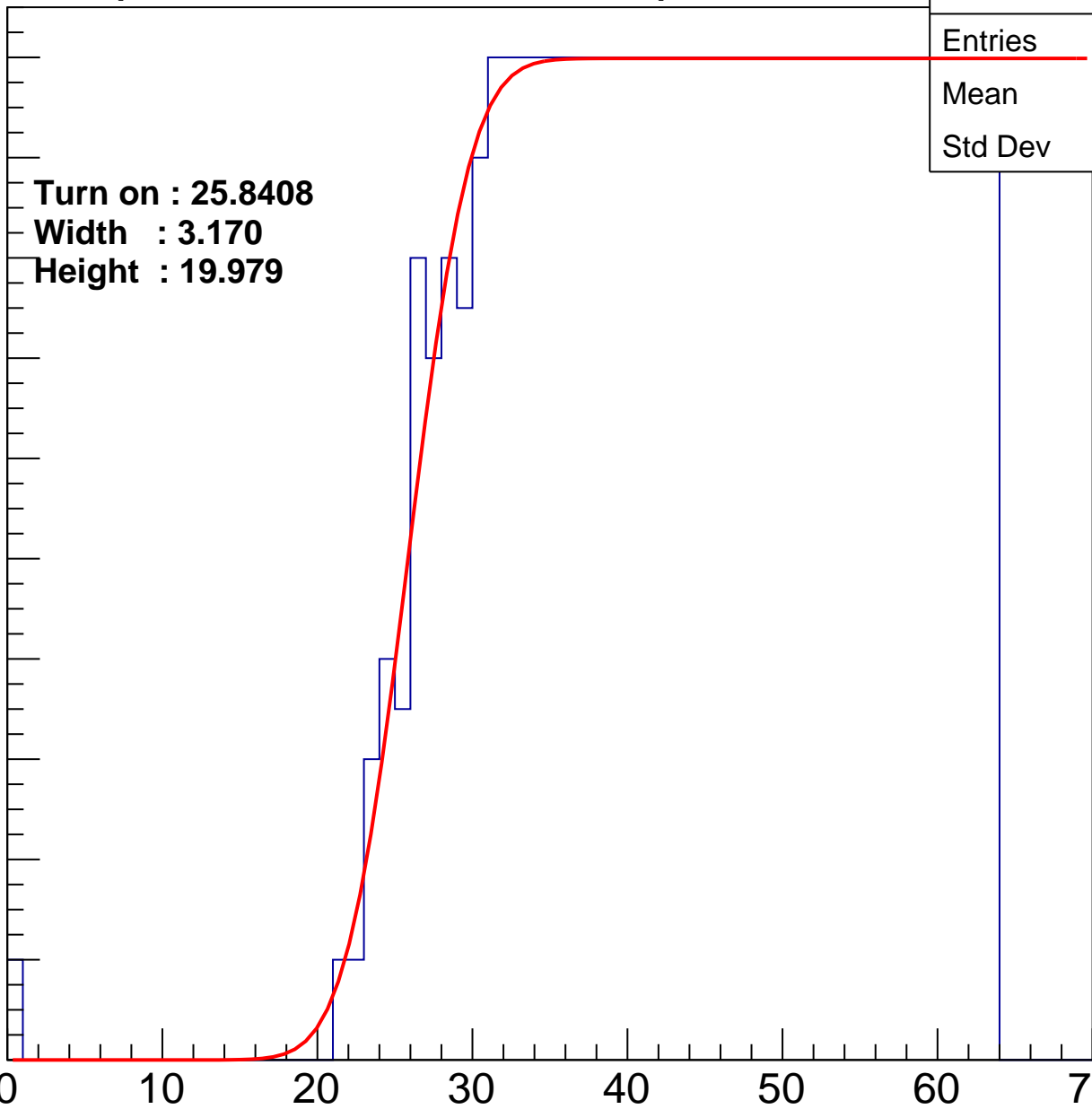
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8408
Width : 3.170
Height : 19.979

Entries	766
Mean	44.16
Std Dev	11.44

ampl



B0L101S, U14-ch5

calib_packv5_042523_0143.root, FC#1, port C1

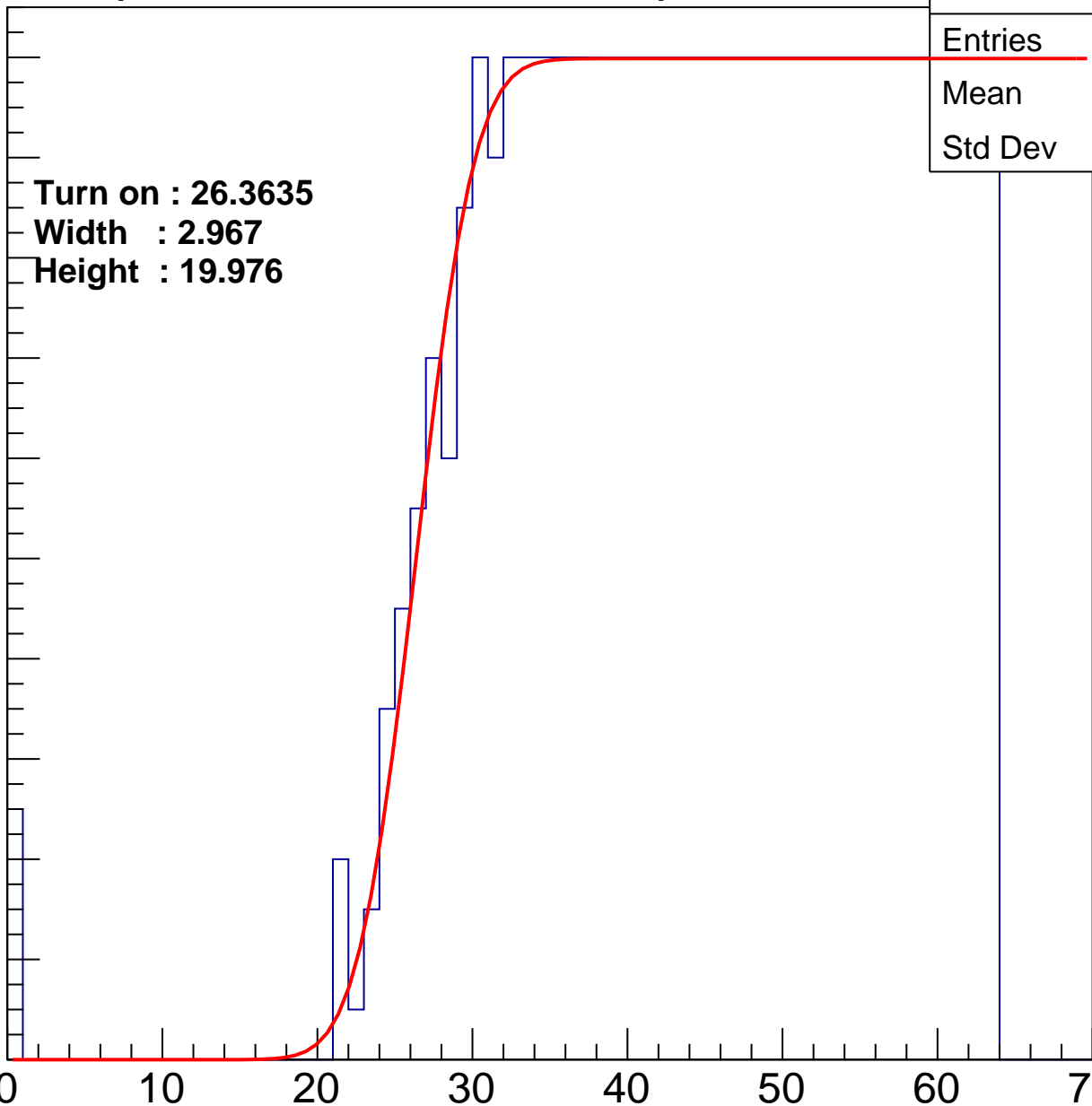
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3635
Width : 2.967
Height : 19.976

Entries	761
Mean	44.18
Std Dev	11.66

ampl



B0L101S, U14-ch6

calib_packv5_042523_0143.root, FC#1, port C1

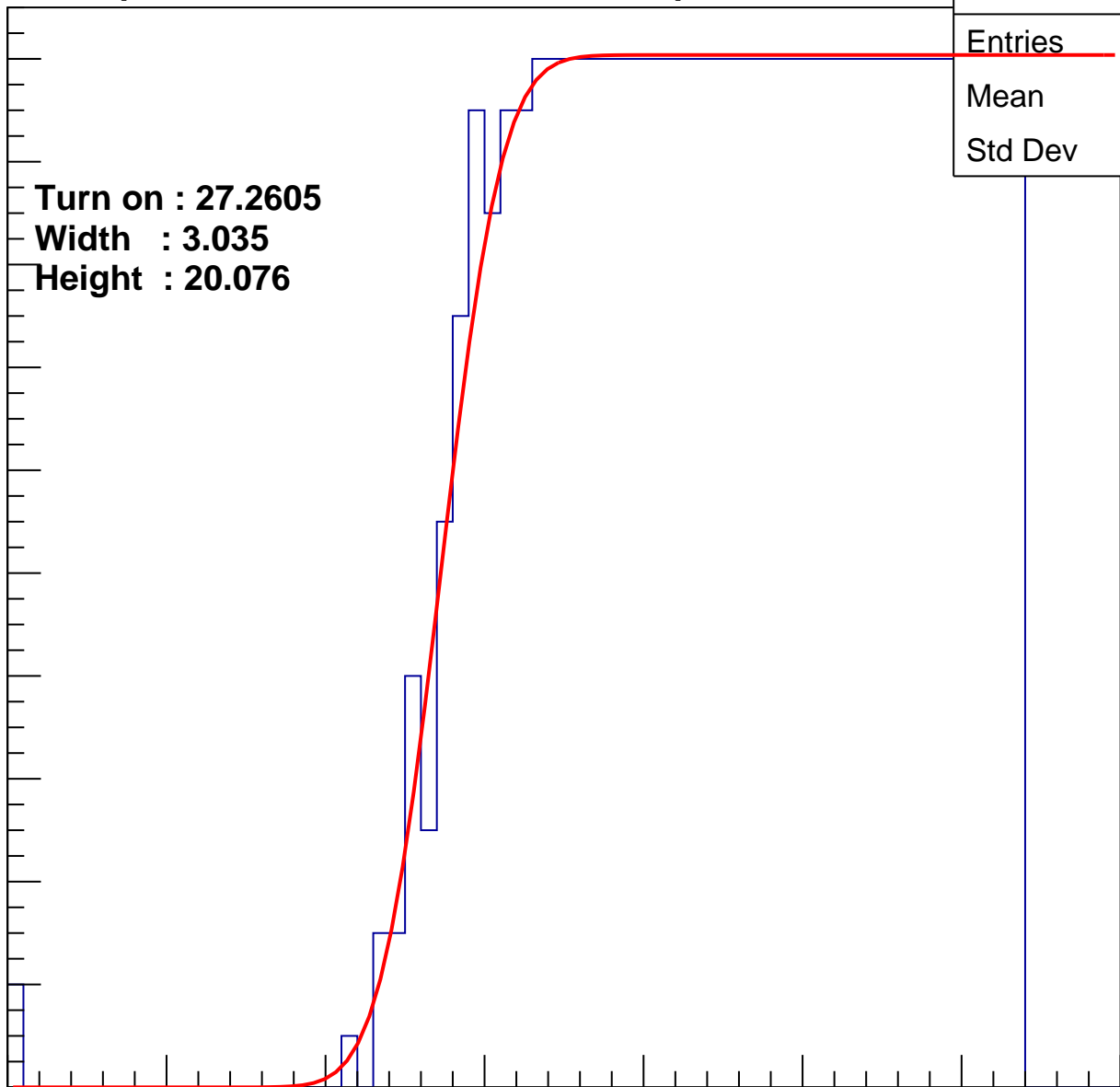
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2605
Width : 3.035
Height : 20.076

Entries	742
Mean	44.78
Std Dev	11.08

ampl



B0L101S, U14-ch7

calib_packv5_042523_0143.root, FC#1, port C1

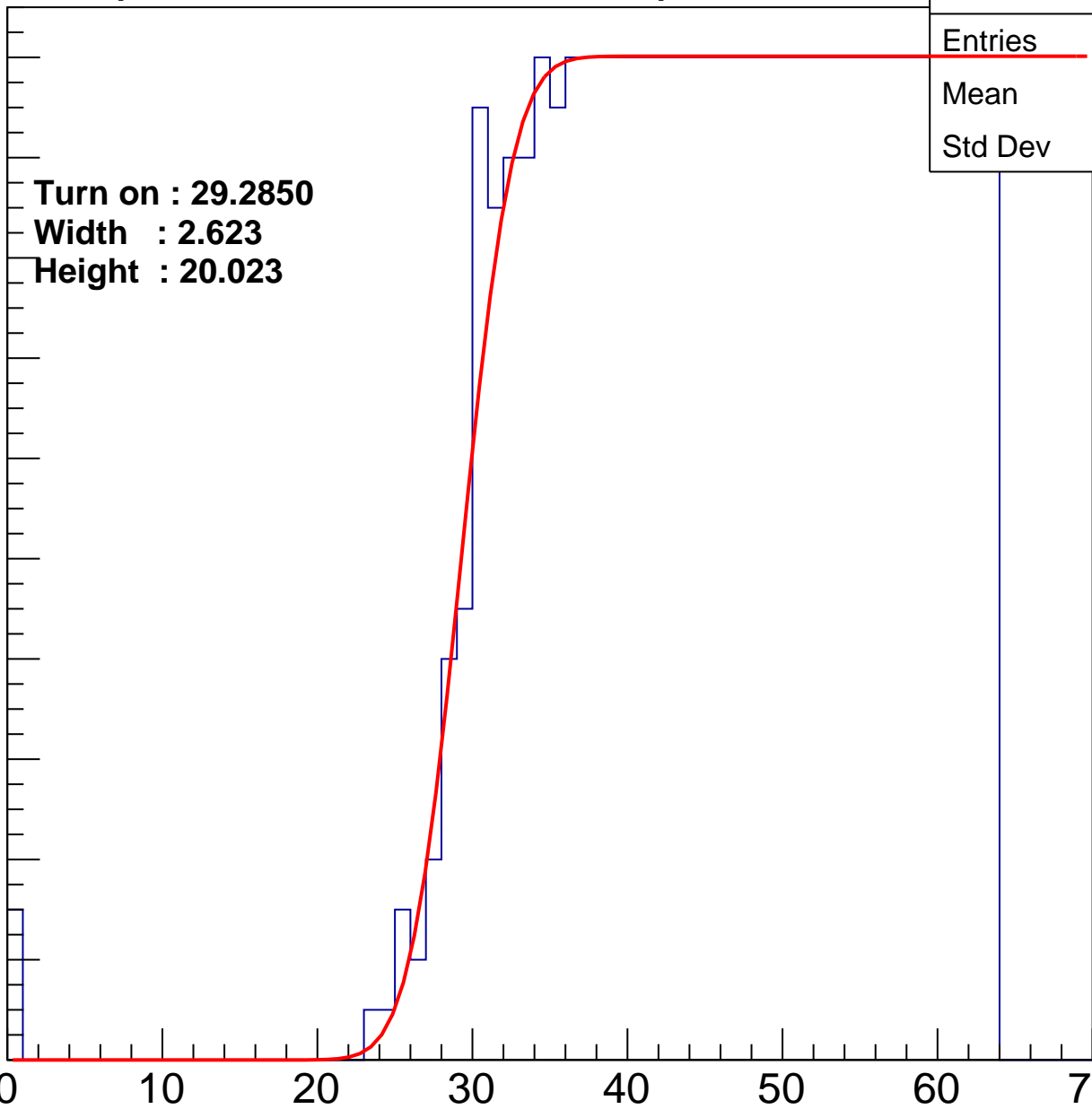
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 29.2850
Width : 2.623
Height : 20.023

Entries	702
Mean	45.73
Std Dev	10.67

ampl



B0L101S, U14-ch8

calib_packv5_042523_0143.root, FC#1, port C1

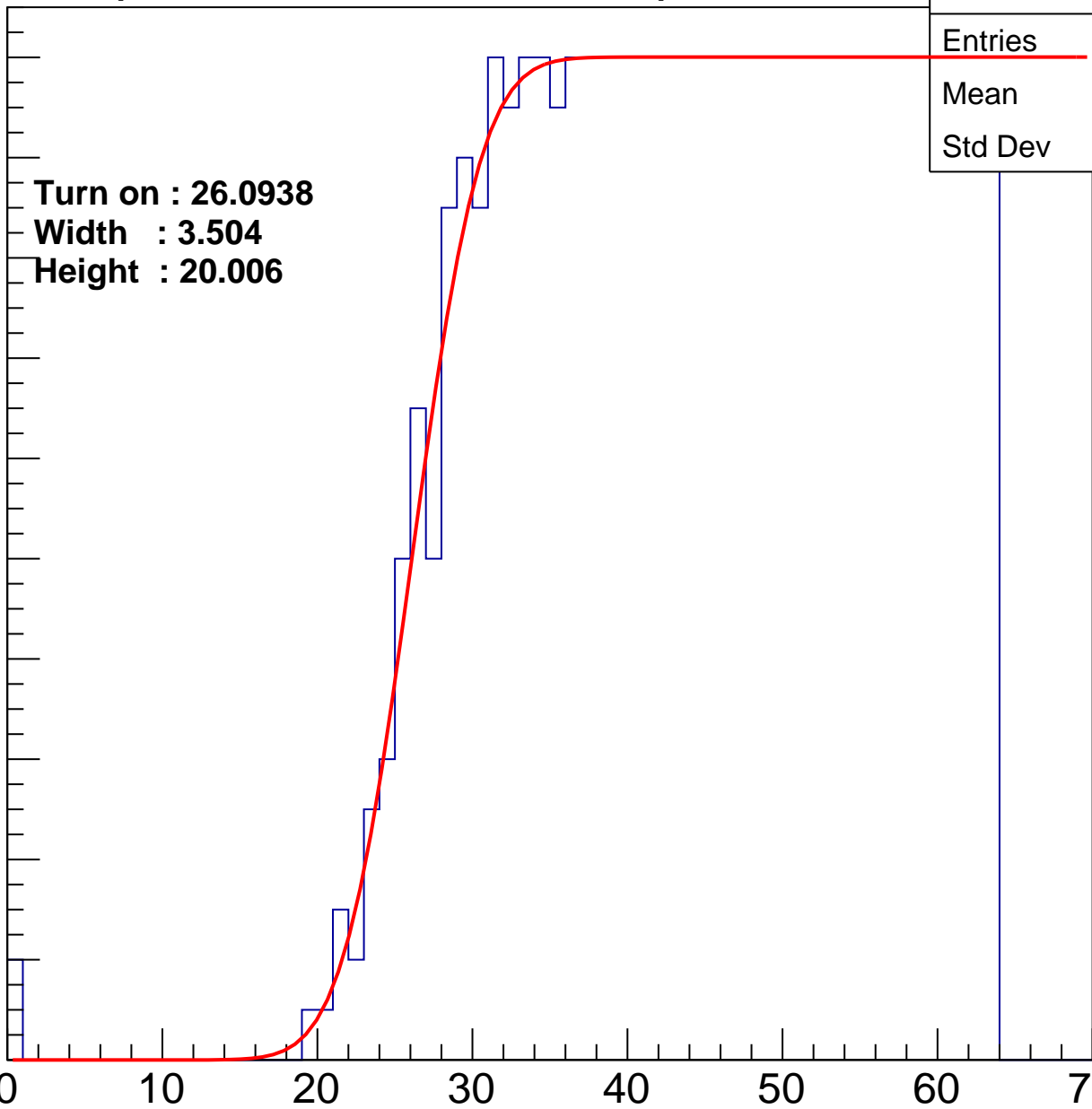
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0938
Width : 3.504
Height : 20.006

Entries	763
Mean	44.2
Std Dev	11.46

ampl



B0L101S, U14-ch9

calib_packv5_042523_0143.root, FC#1, port C1

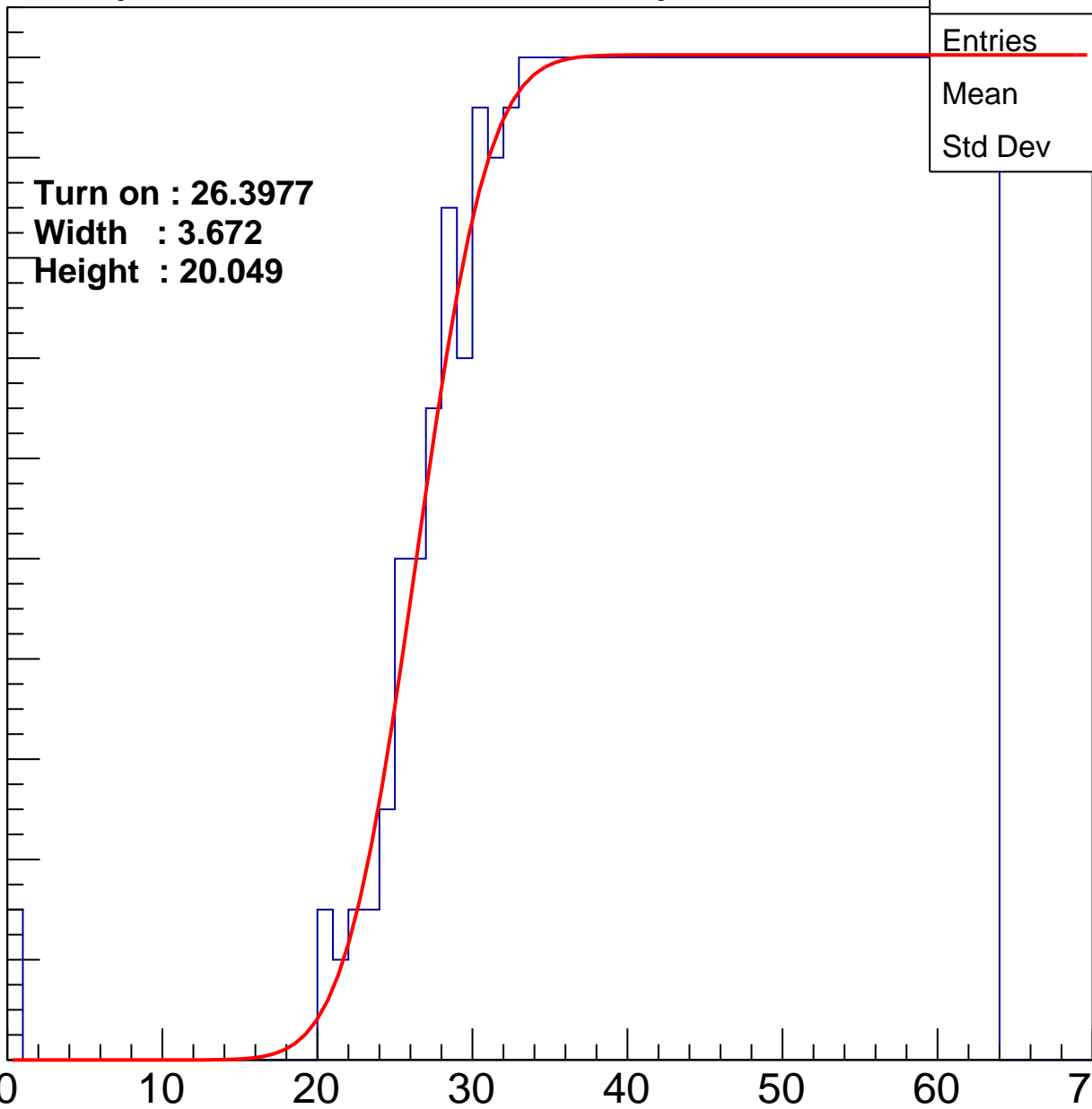
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3977
Width : 3.672
Height : 20.049

Entries	759
Mean	44.26
Std Dev	11.5

ampl



B0L101S, U14-ch10

calib_packv5_042523_0143.root, FC#1, port C1

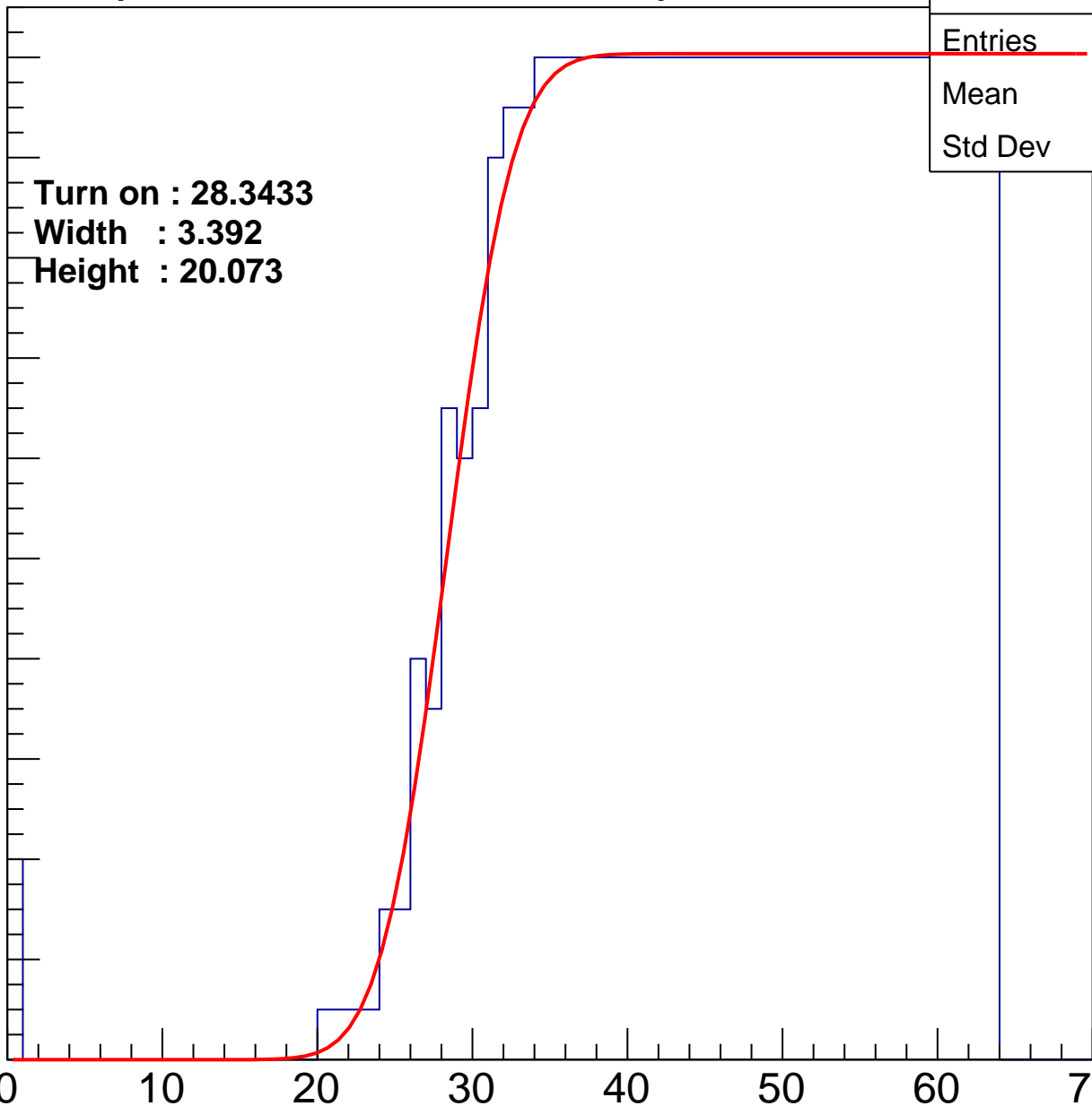
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.3433
Width : 3.392
Height : 20.073

Entries	723
Mean	45.12
Std Dev	11.13

ampl



B0L101S, U14-ch11

calib_packv5_042523_0143.root, FC#1, port C1

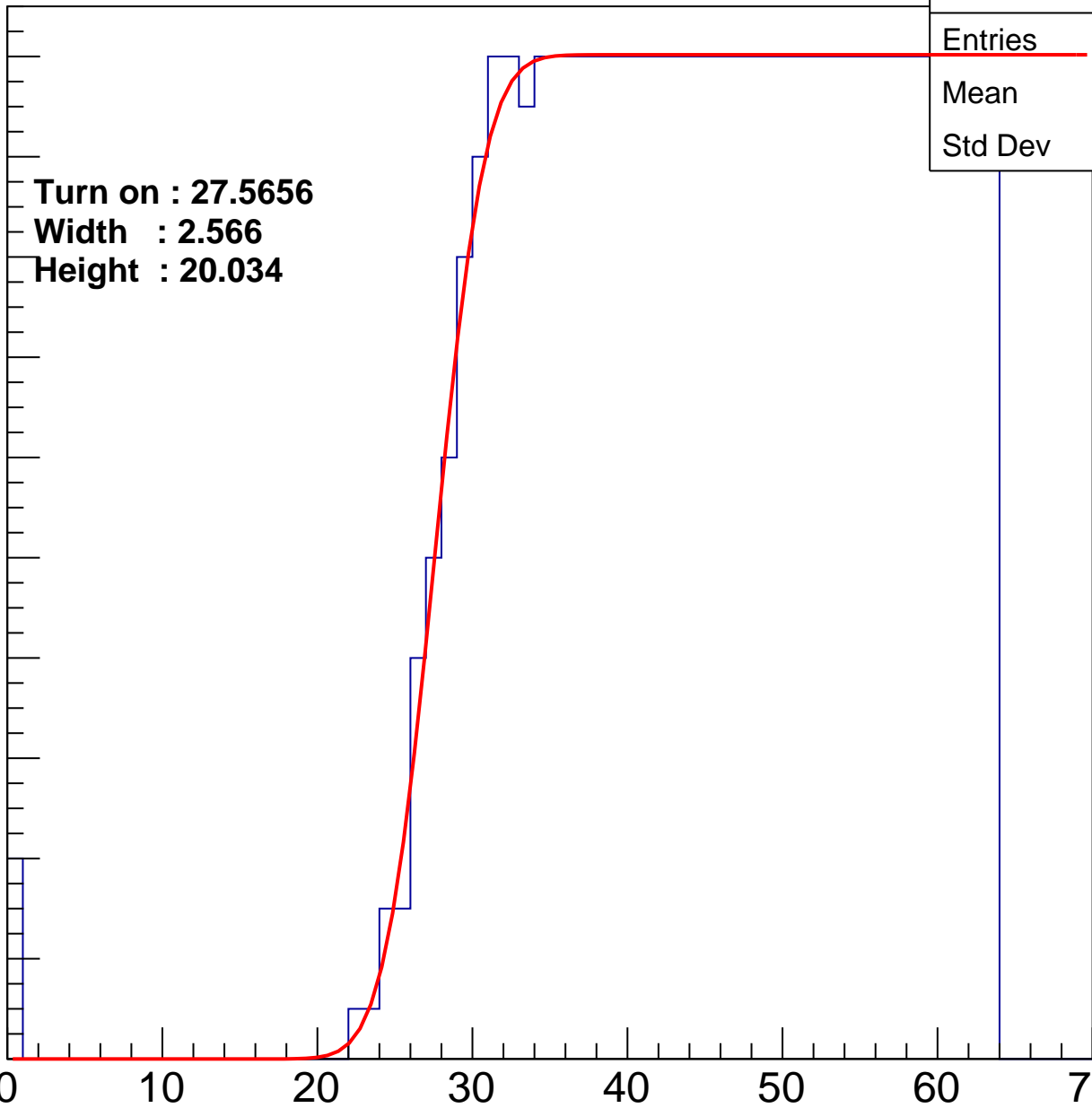
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5656
Width : 2.566
Height : 20.034

Entries	735
Mean	44.89
Std Dev	11.17

ampl



B0L101S, U14-ch12

calib_packv5_042523_0143.root, FC#1, port C1

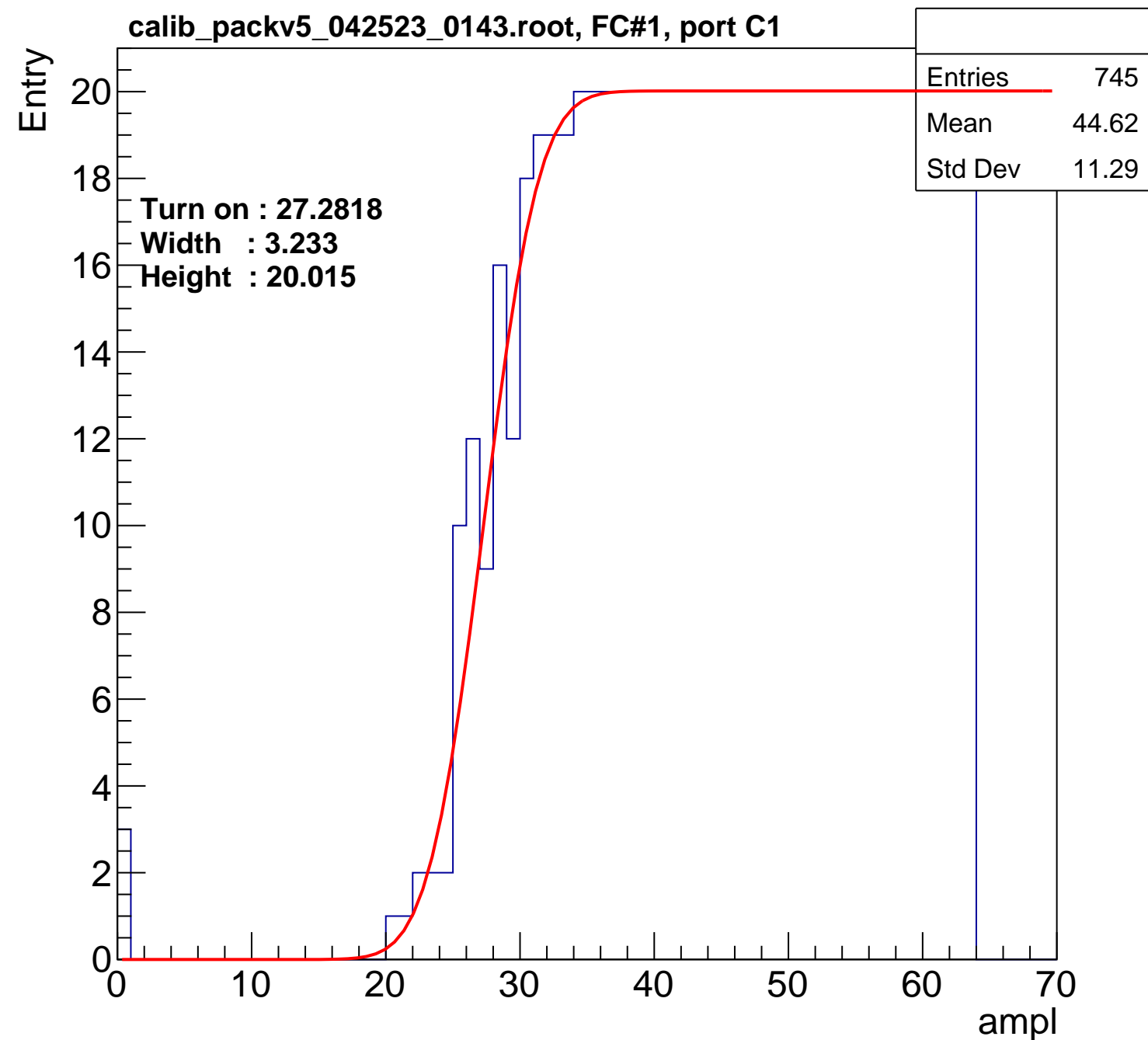
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2818
Width : 3.233
Height : 20.015

Entries	745
Mean	44.62
Std Dev	11.29

ampl



B0L101S, U14-ch13

calib_packv5_042523_0143.root, FC#1, port C1

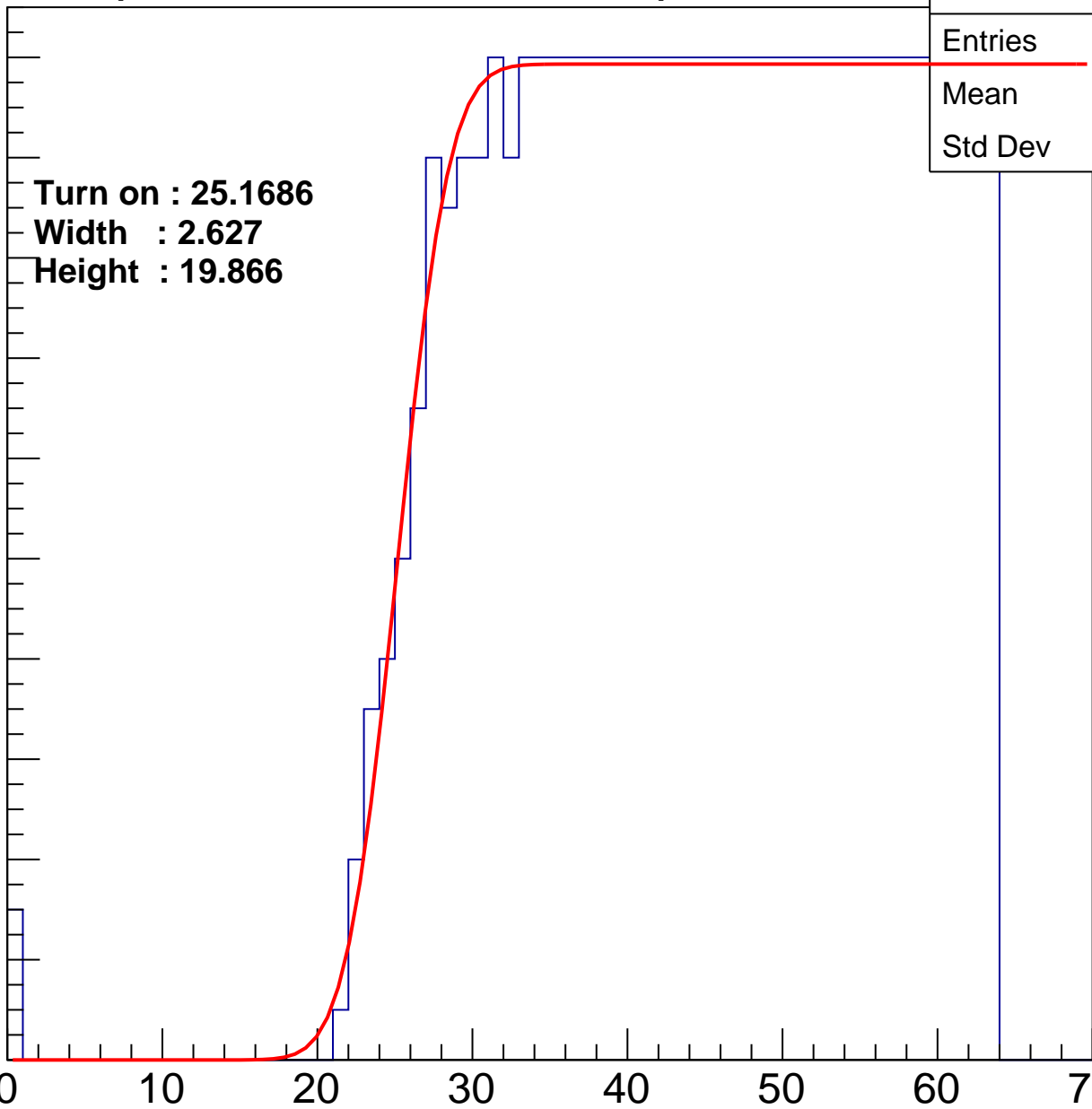
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1686
Width : 2.627
Height : 19.866

Entries	775
Mean	43.91
Std Dev	11.64

ampl



B0L101S, U14-ch14

calib_packv5_042523_0143.root, FC#1, port C1

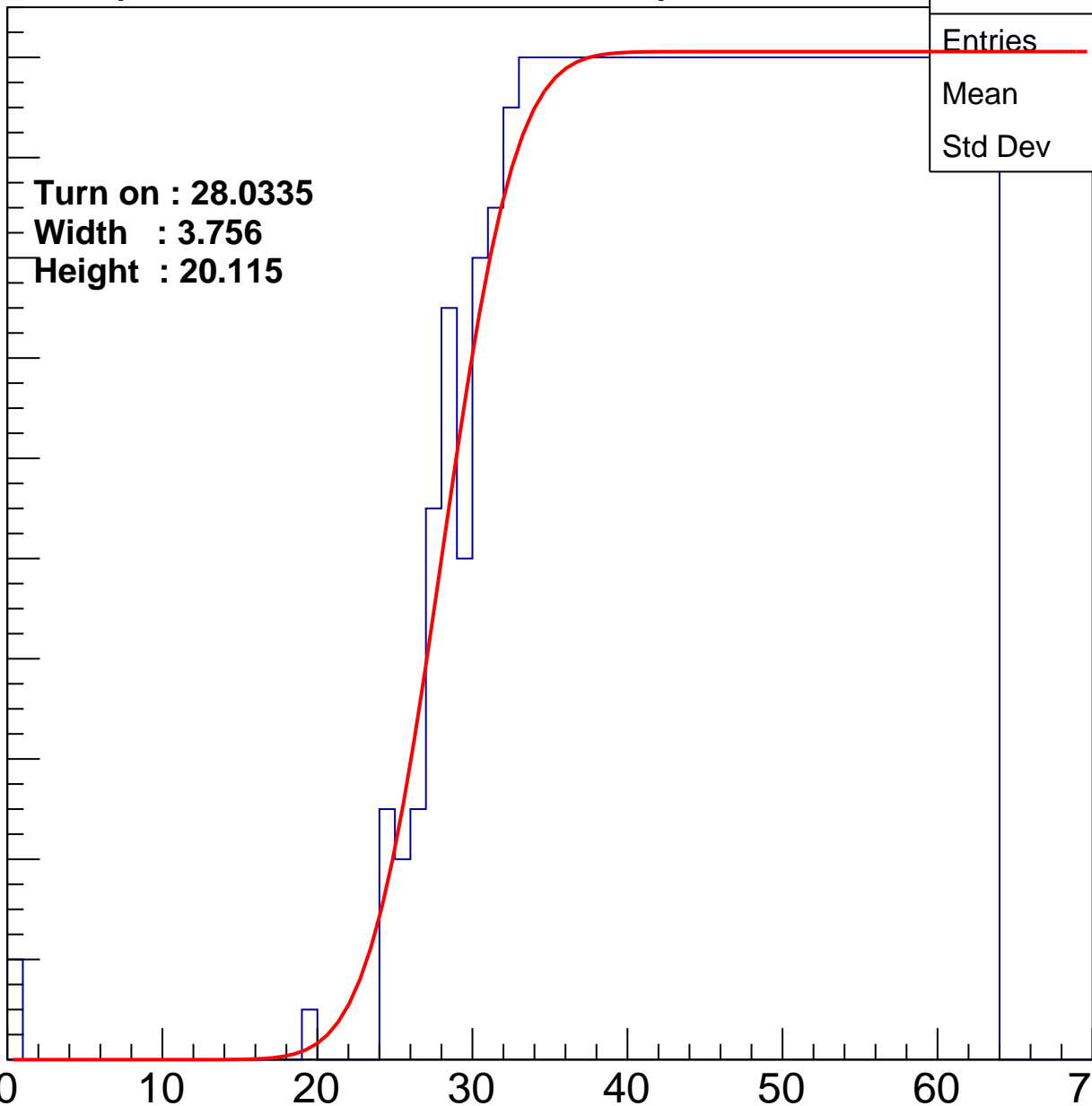
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.0335
Width : 3.756
Height : 20.115

Entries	725
Mean	45.17
Std Dev	10.9

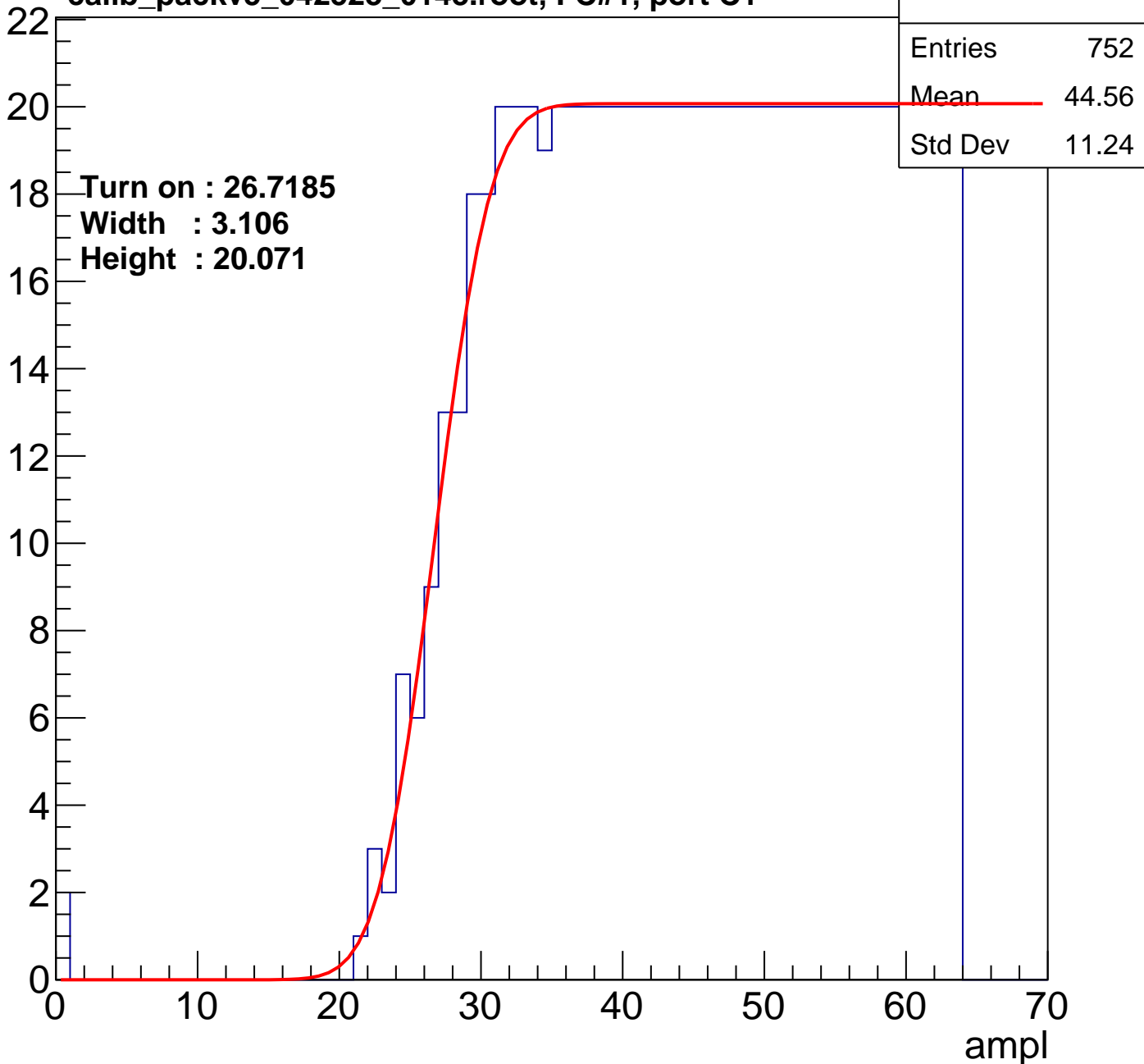
ampl



B0L101S, U14-ch15

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch16

calib_packv5_042523_0143.root, FC#1, port C1

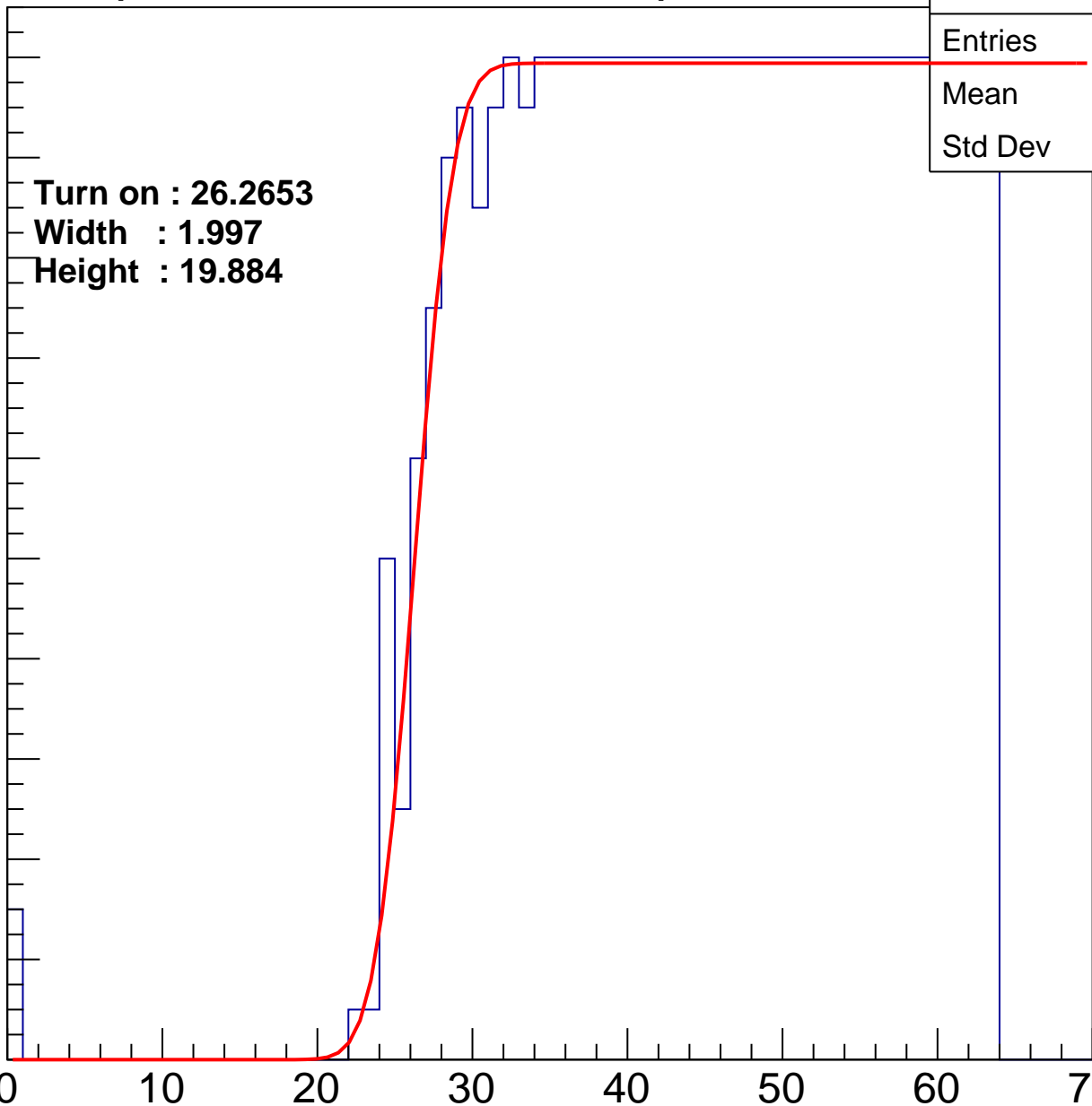
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2653
Width : 1.997
Height : 19.884

Entries	759
Mean	44.33
Std Dev	11.39

ampl



B0L101S, U14-ch17

calib_packv5_042523_0143.root, FC#1, port C1

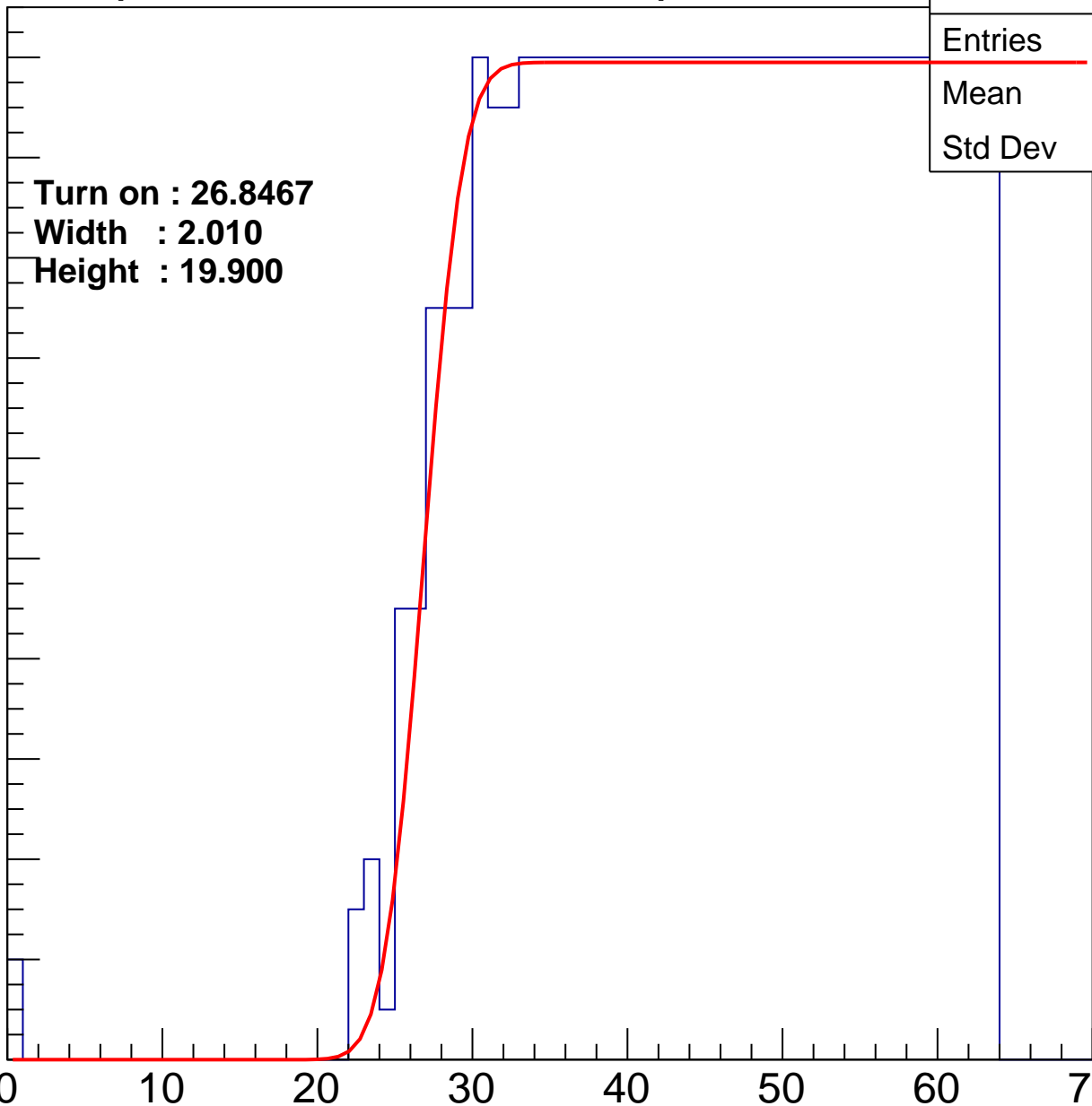
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8467
Width : 2.010
Height : 19.900

Entries	751
Mean	44.55
Std Dev	11.21

ampl



B0L101S, U14-ch18

calib_packv5_042523_0143.root, FC#1, port C1

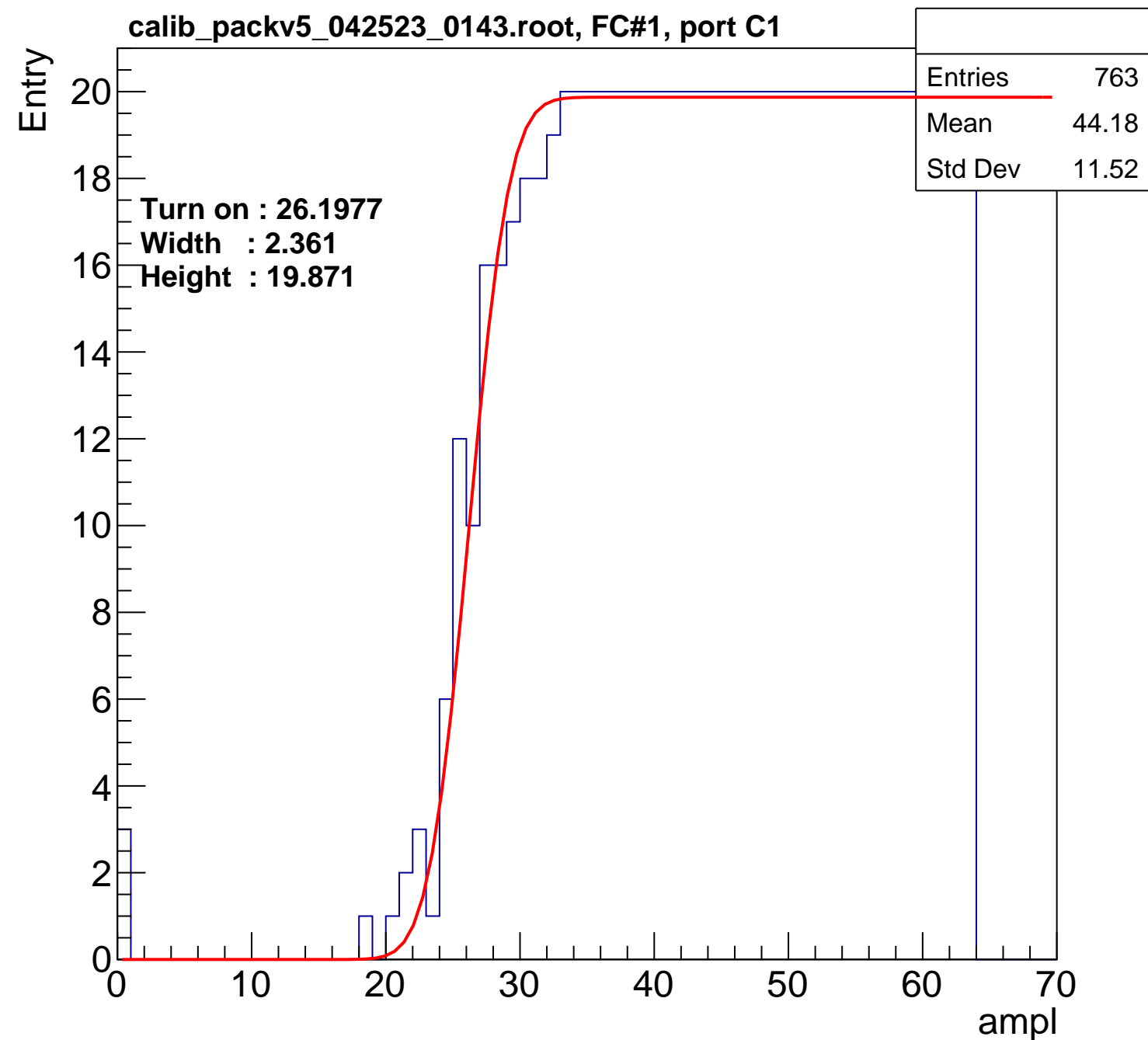
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.1977
Width : 2.361
Height : 19.871

Entries	763
Mean	44.18
Std Dev	11.52

ampl



B0L101S, U14-ch19

calib_packv5_042523_0143.root, FC#1, port C1

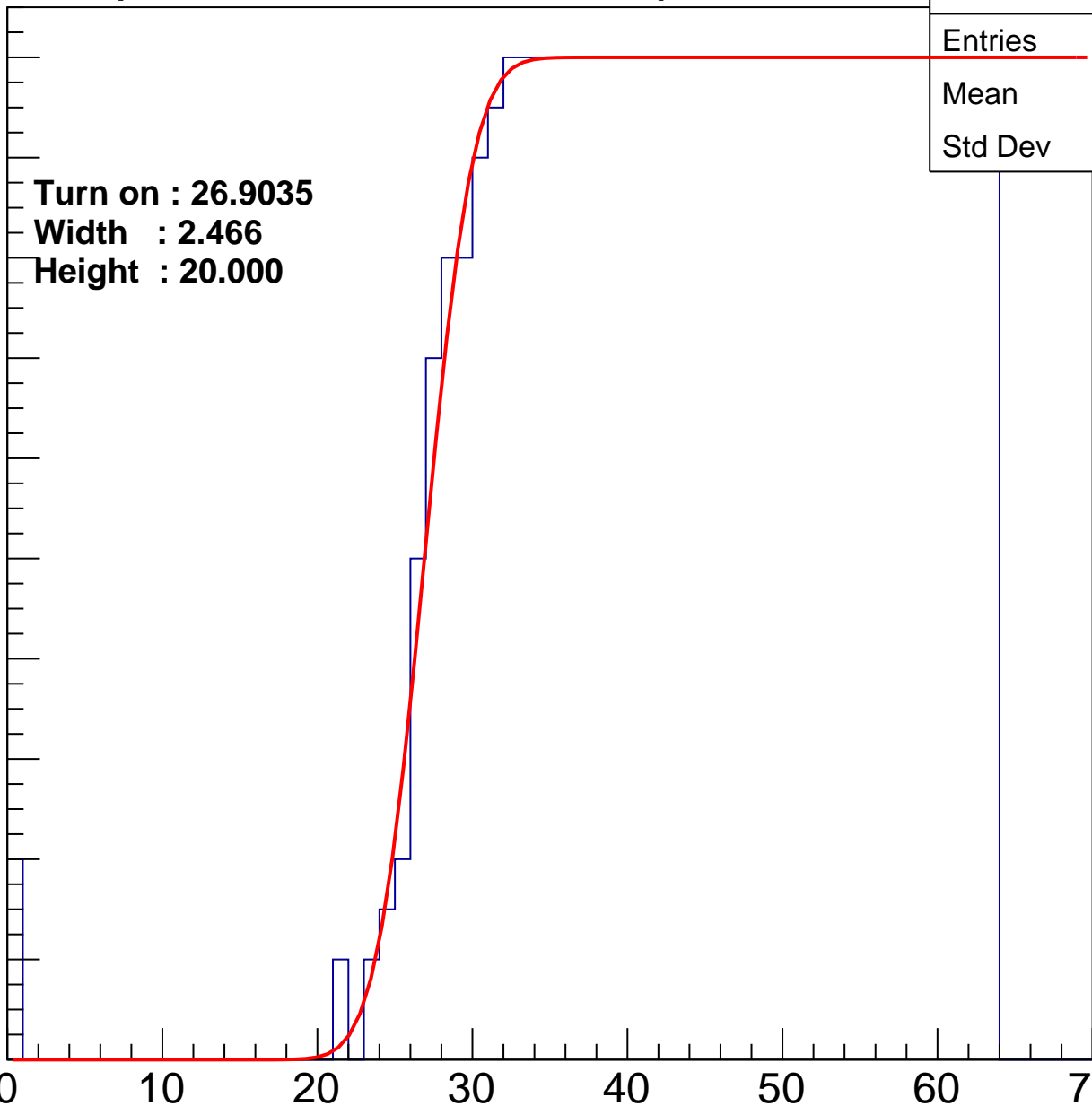
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.9035
Width : 2.466
Height : 20.000

Entries	748
Mean	44.57
Std Dev	11.34

ampl



B0L101S, U14-ch20

calib_packv5_042523_0143.root, FC#1, port C1

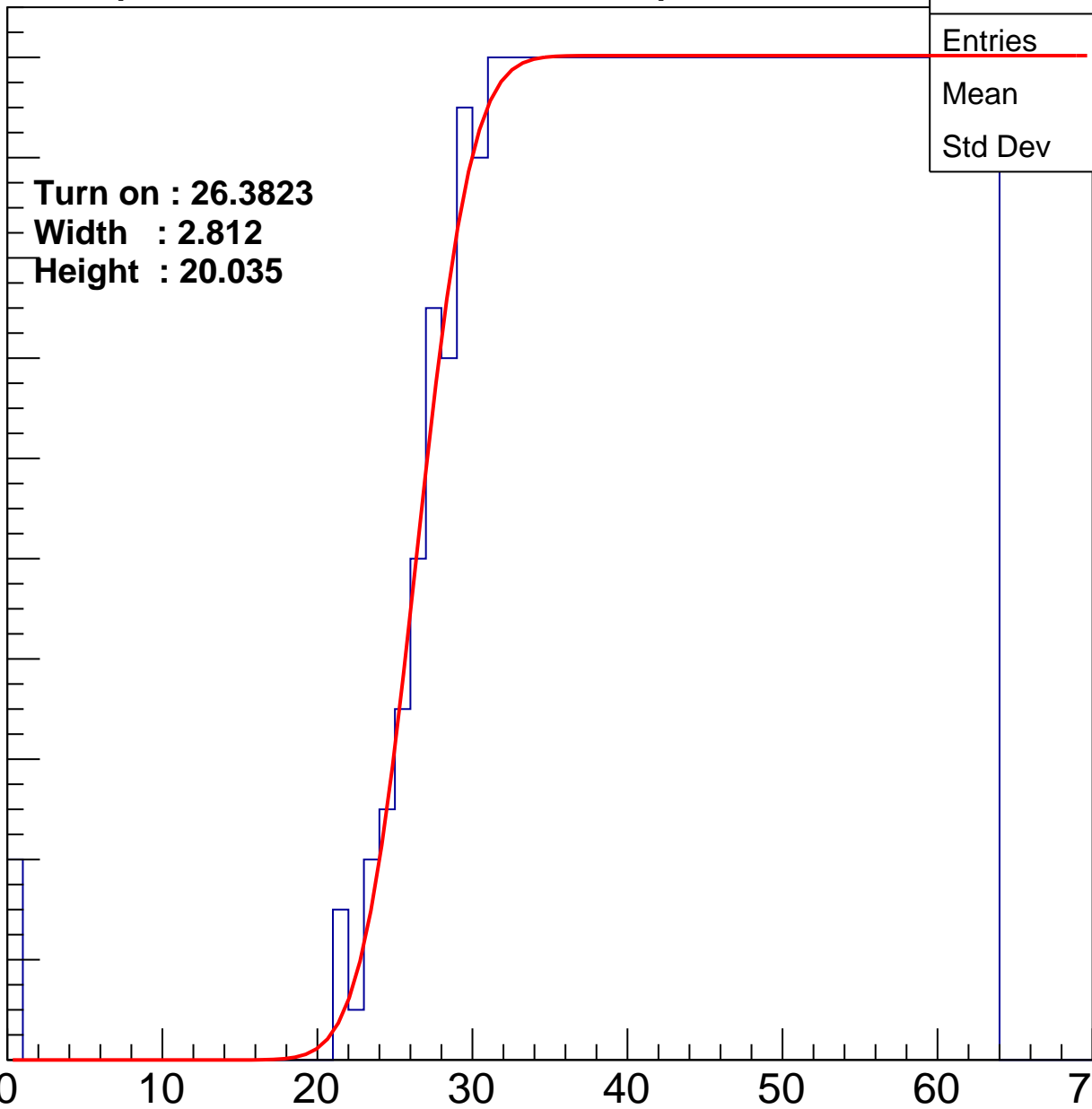
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3823
Width : 2.812
Height : 20.035

Entries	760
Mean	44.26
Std Dev	11.52

ampl



B0L101S, U14-ch21

calib_packv5_042523_0143.root, FC#1, port C1

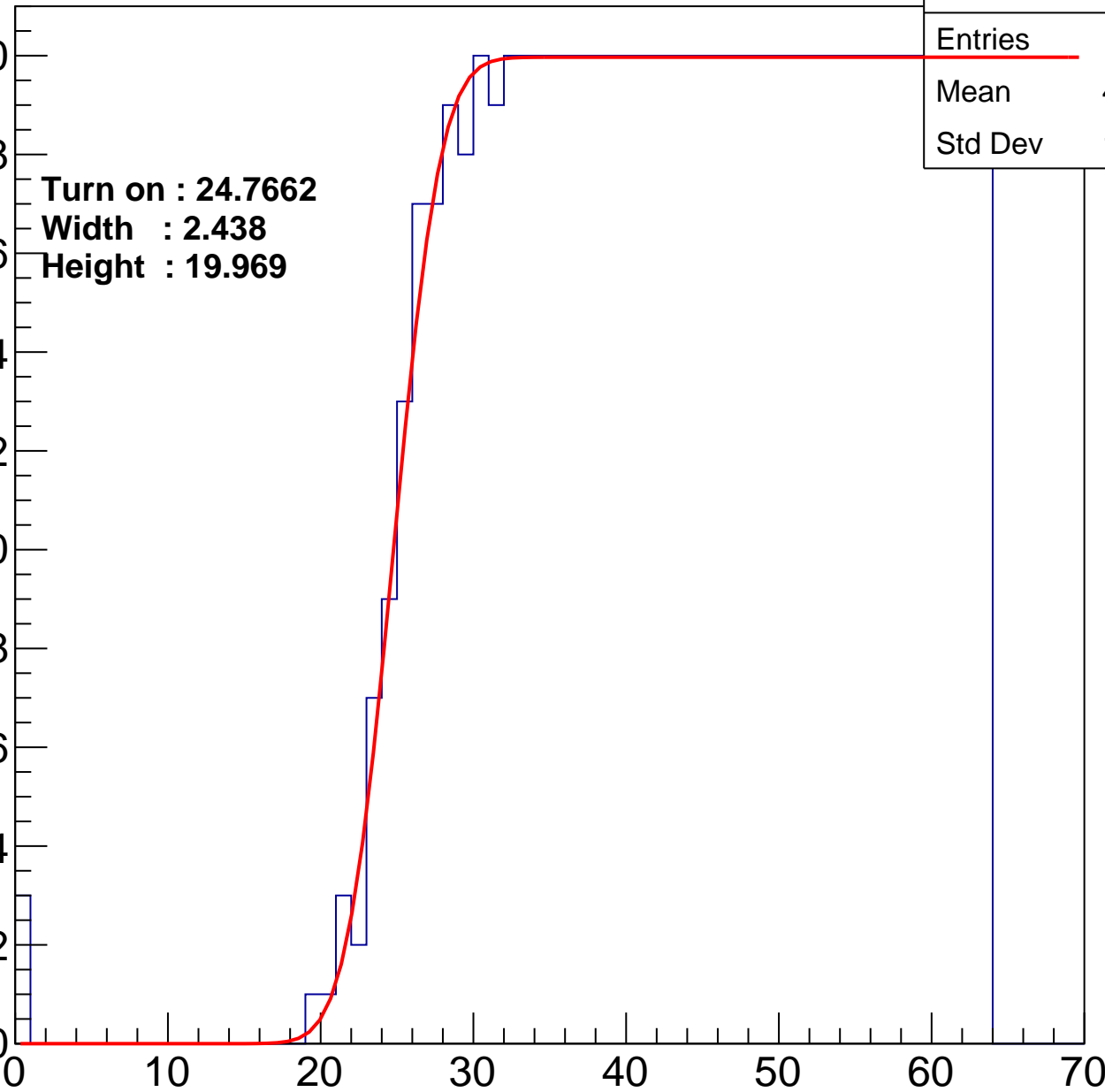
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7662
Width : 2.438
Height : 19.969

Entries	789
Mean	43.59
Std Dev	11.79

ampl



B0L101S, U14-ch22

calib_packv5_042523_0143.root, FC#1, port C1

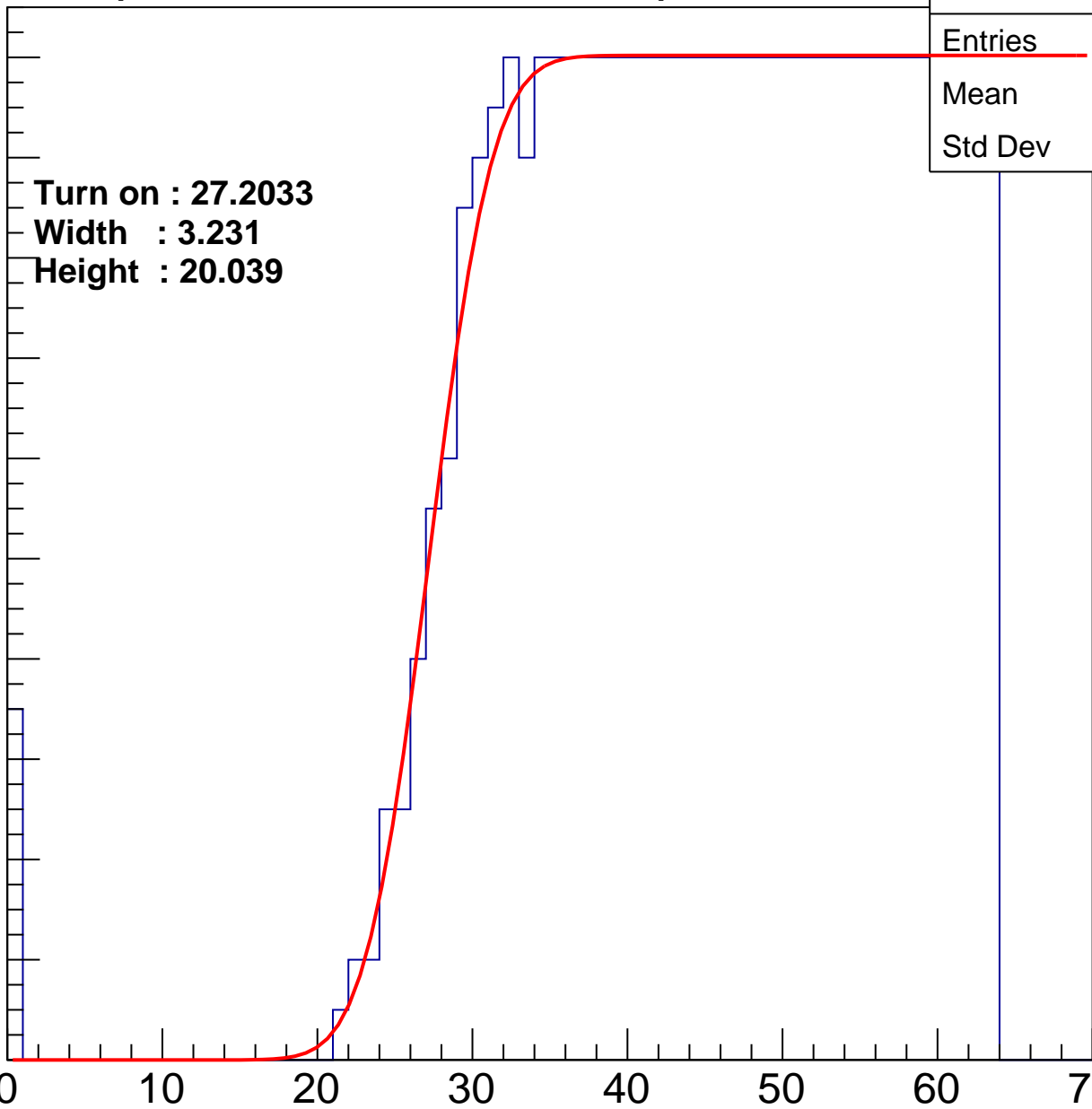
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.2033
Width : 3.231
Height : 20.039

Entries	745
Mean	44.5
Std Dev	11.65

ampl



B0L101S, U14-ch23

calib_packv5_042523_0143.root, FC#1, port C1

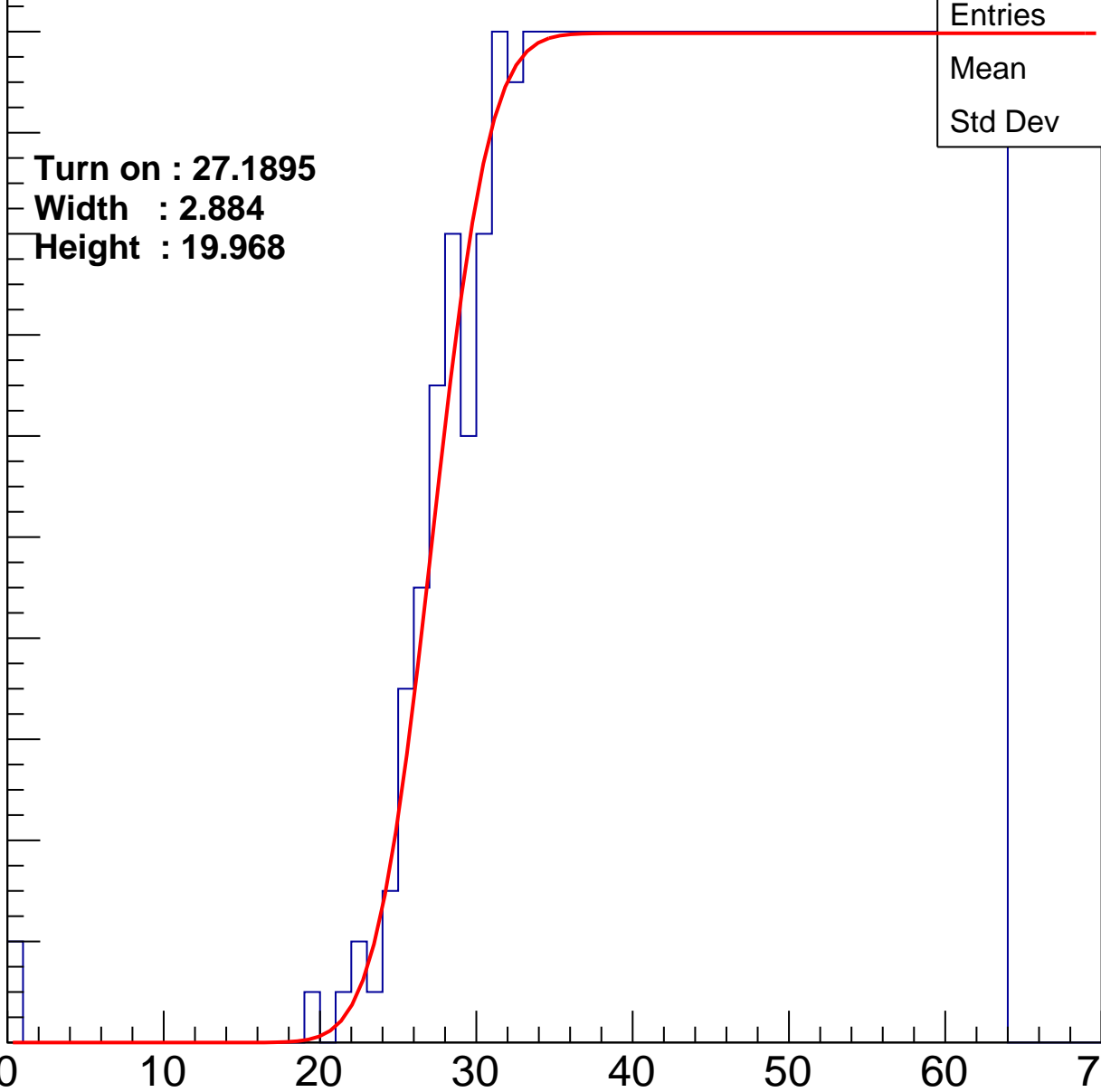
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1895
Width : 2.884
Height : 19.968

Entries	742
Mean	44.75
Std Dev	11.13

ampl



B0L101S, U14-ch24

calib_packv5_042523_0143.root, FC#1, port C1

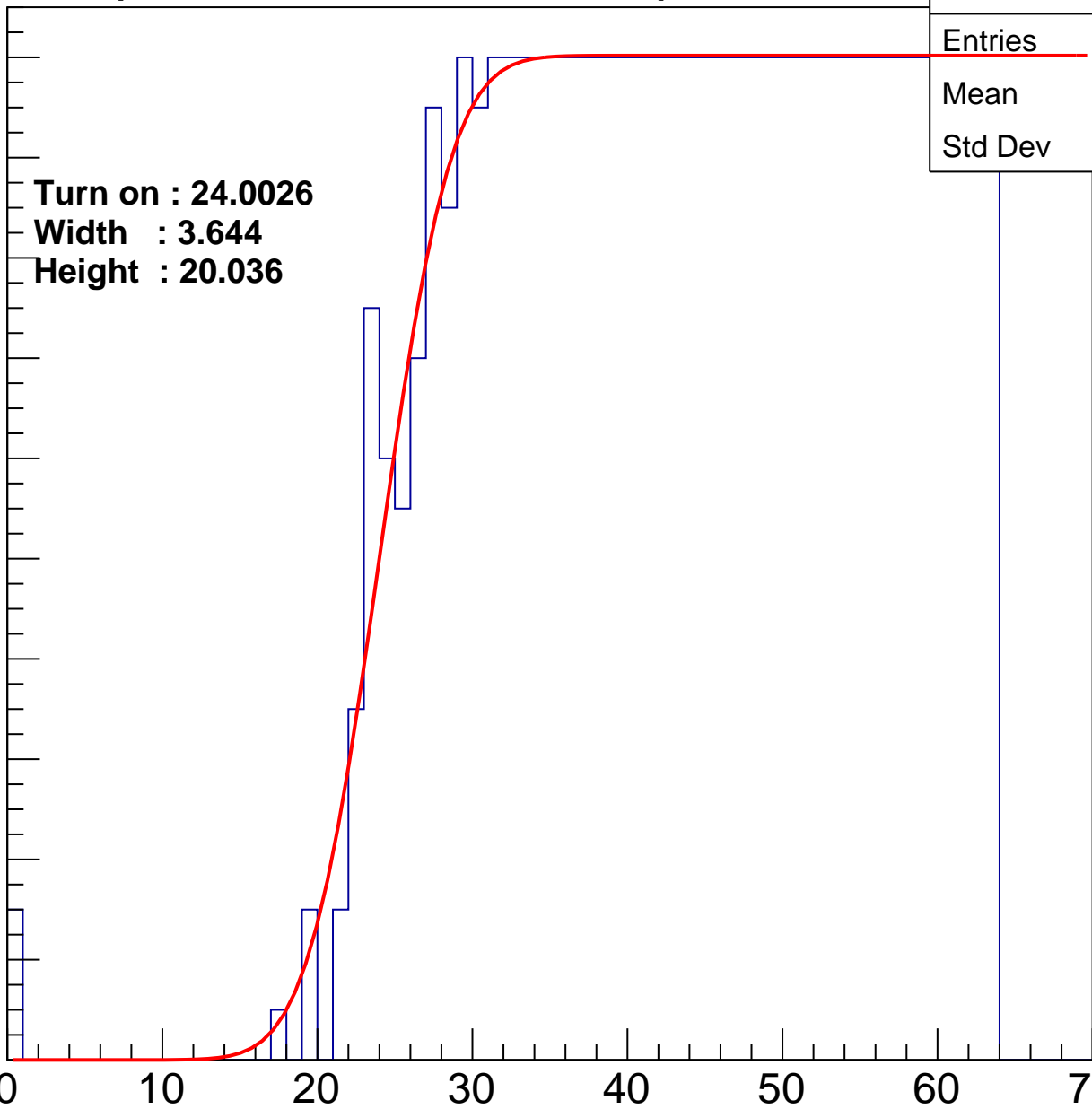
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.0026
Width : 3.644
Height : 20.036

Entries	804
Mean	43.19
Std Dev	12.04

ampl



B0L101S, U14-ch25

calib_packv5_042523_0143.root, FC#1, port C1

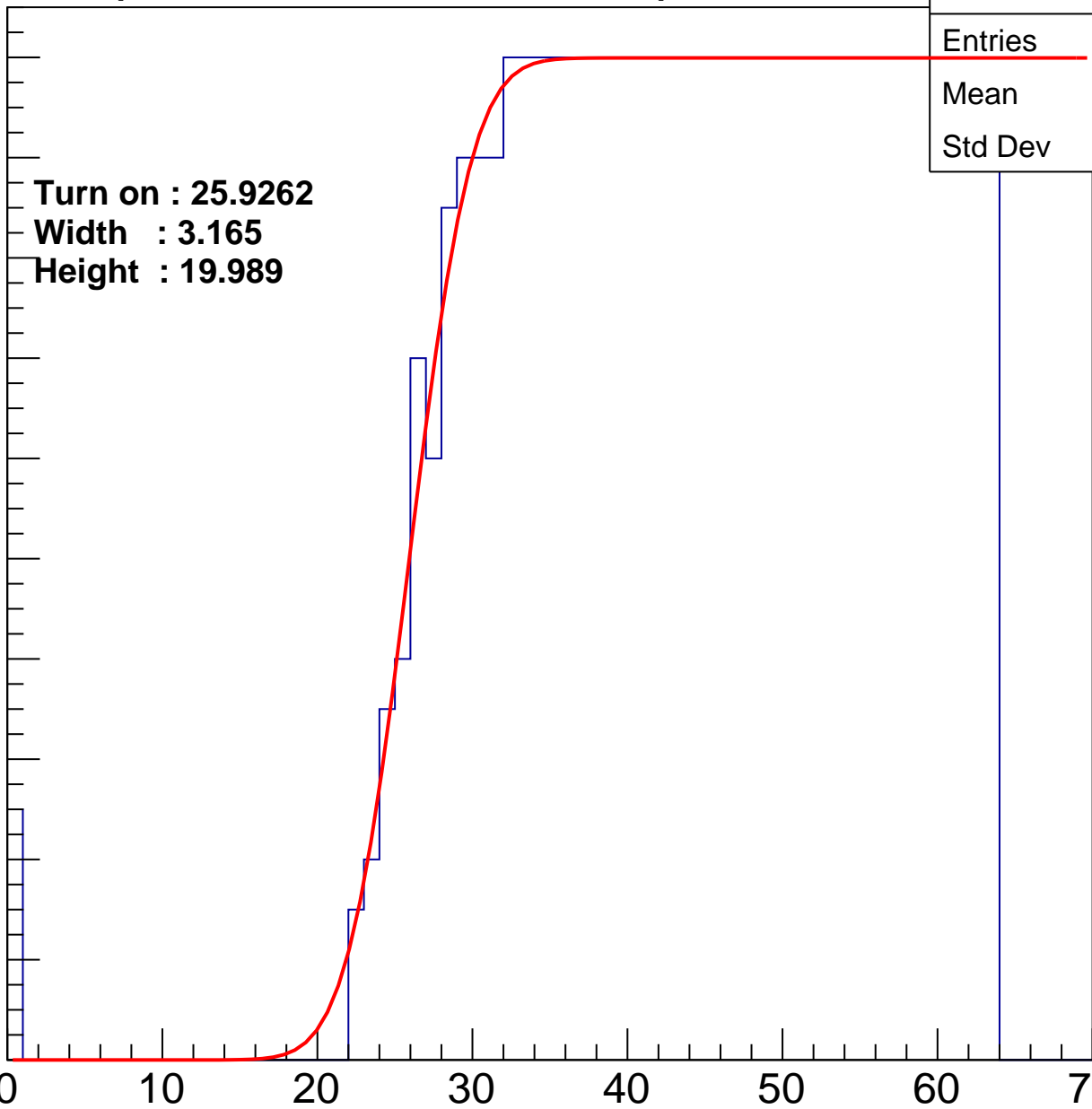
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9262
Width : 3.165
Height : 19.989

Entries	764
Mean	44.12
Std Dev	11.66

ampl



B0L101S, U14-ch26

calib_packv5_042523_0143.root, FC#1, port C1

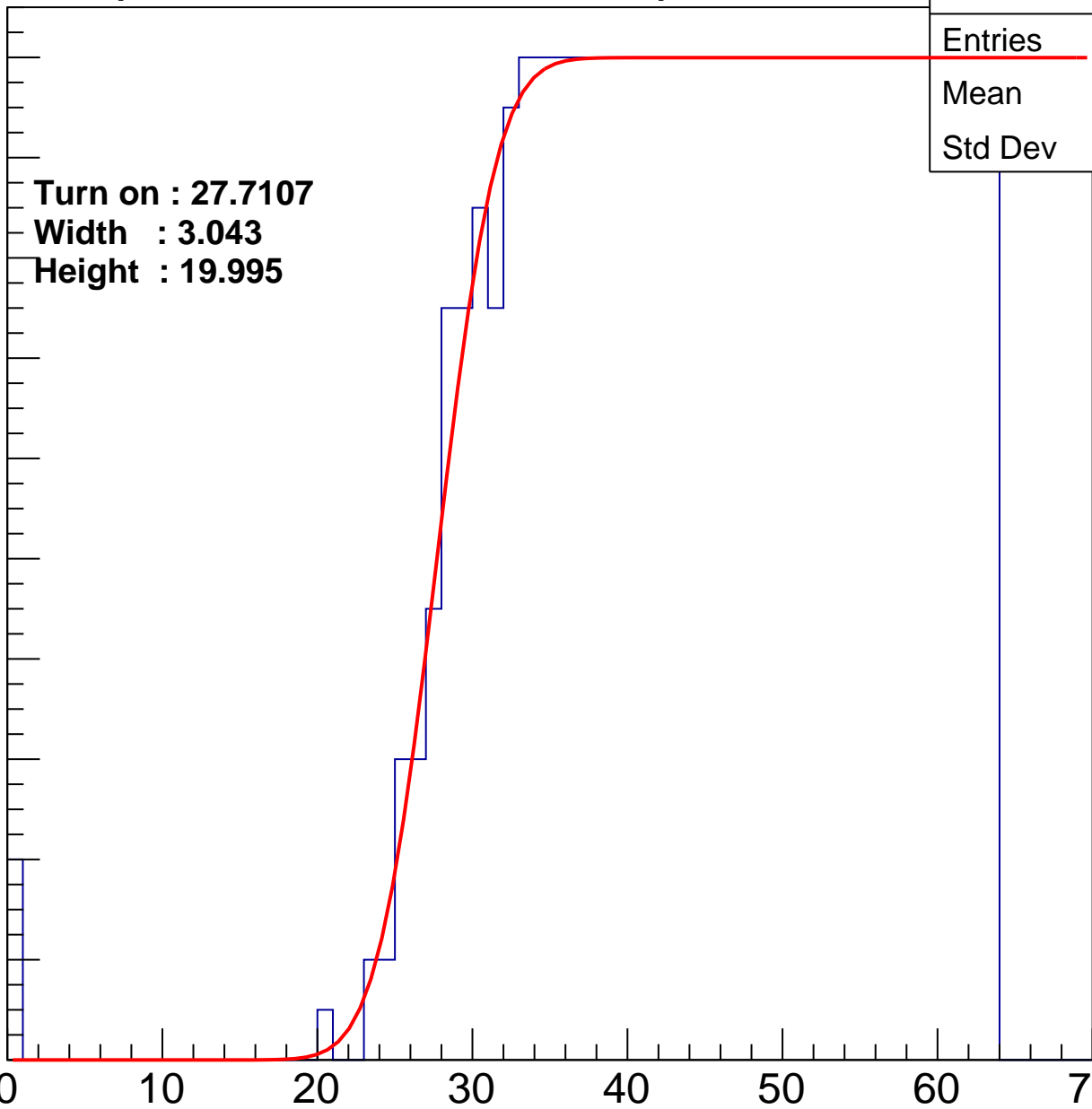
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7107
Width : 3.043
Height : 19.995

Entries	731
Mean	44.95
Std Dev	11.18

ampl



B0L101S, U14-ch27

calib_packv5_042523_0143.root, FC#1, port C1

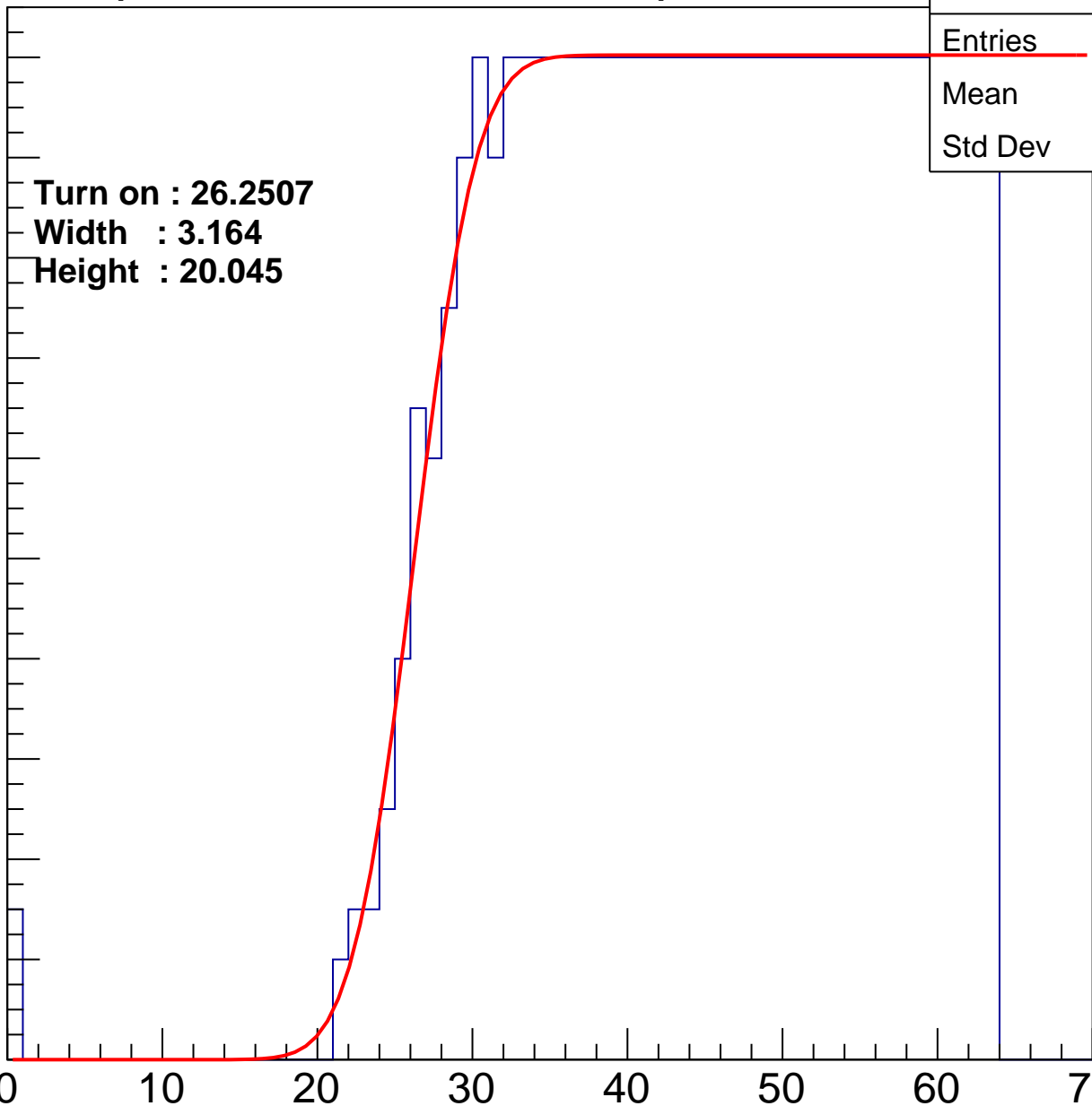
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2507
Width : 3.164
Height : 20.045

Entries	760
Mean	44.29
Std Dev	11.44

ampl



B0L101S, U14-ch28

calib_packv5_042523_0143.root, FC#1, port C1

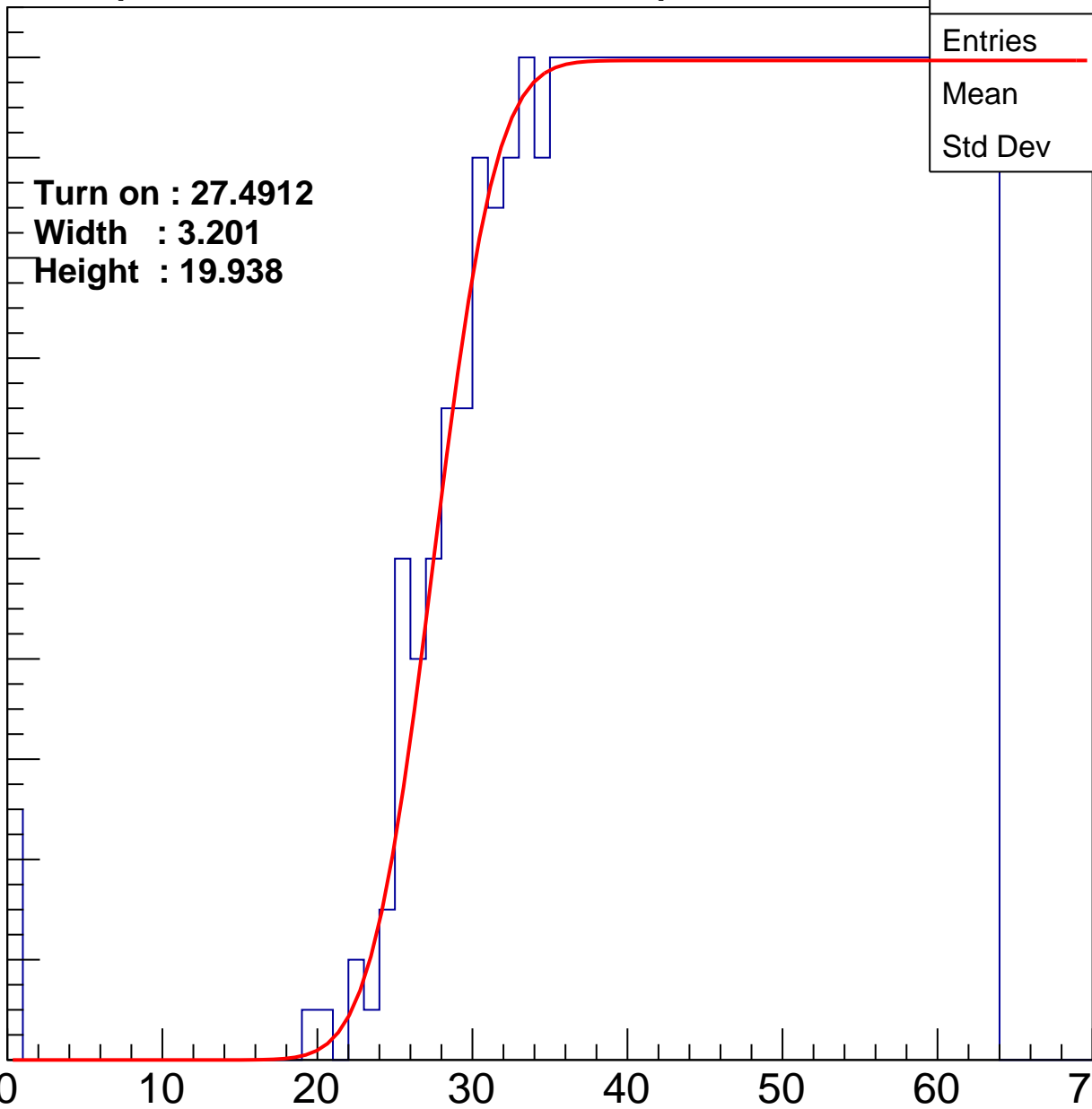
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4912
Width : 3.201
Height : 19.938

Entries	738
Mean	44.69
Std Dev	11.45

ampl



B0L101S, U14-ch29

calib_packv5_042523_0143.root, FC#1, port C1

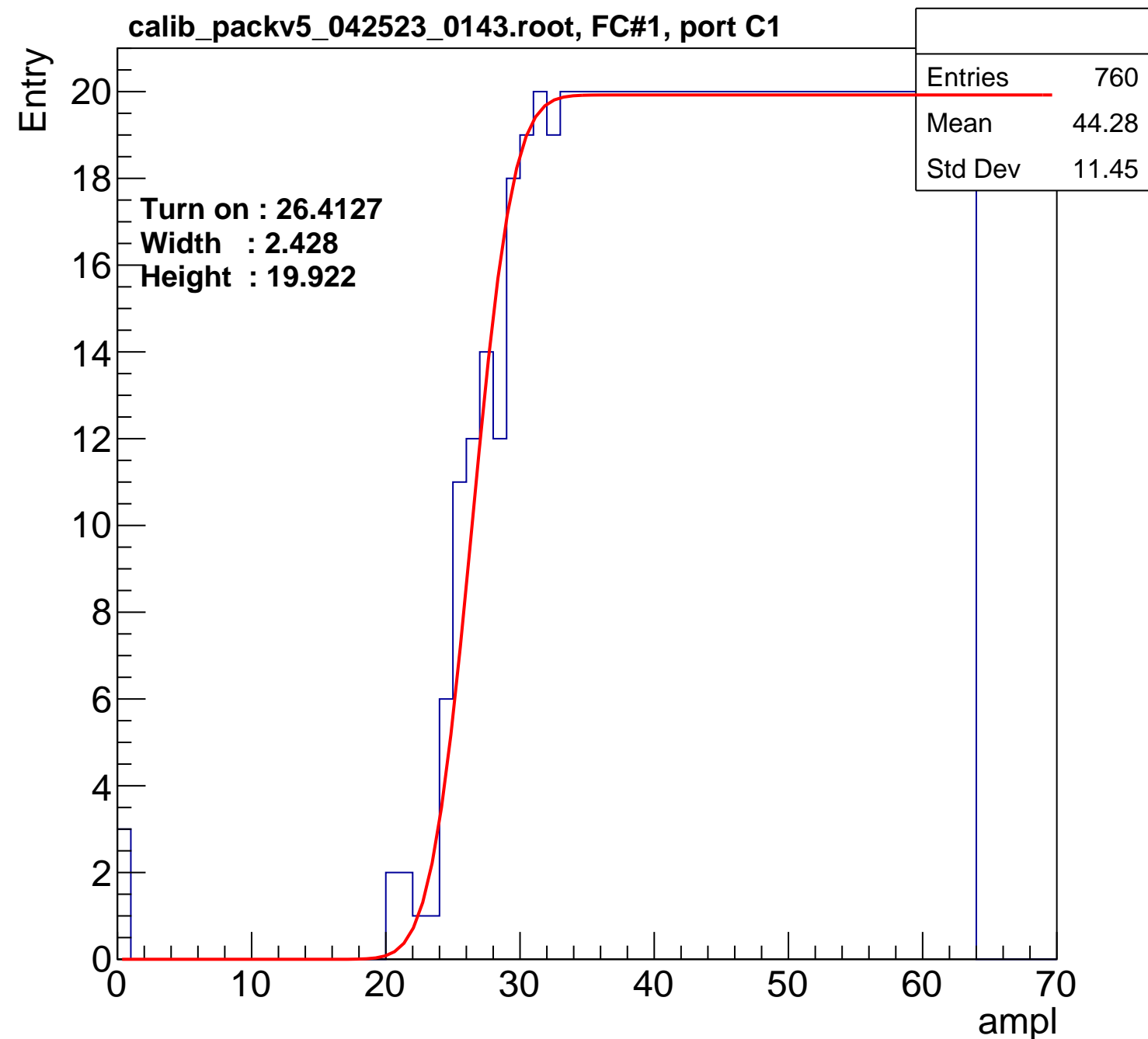
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4127
Width : 2.428
Height : 19.922

Entries	760
Mean	44.28
Std Dev	11.45

ampl



B0L101S, U14-ch30

calib_packv5_042523_0143.root, FC#1, port C1

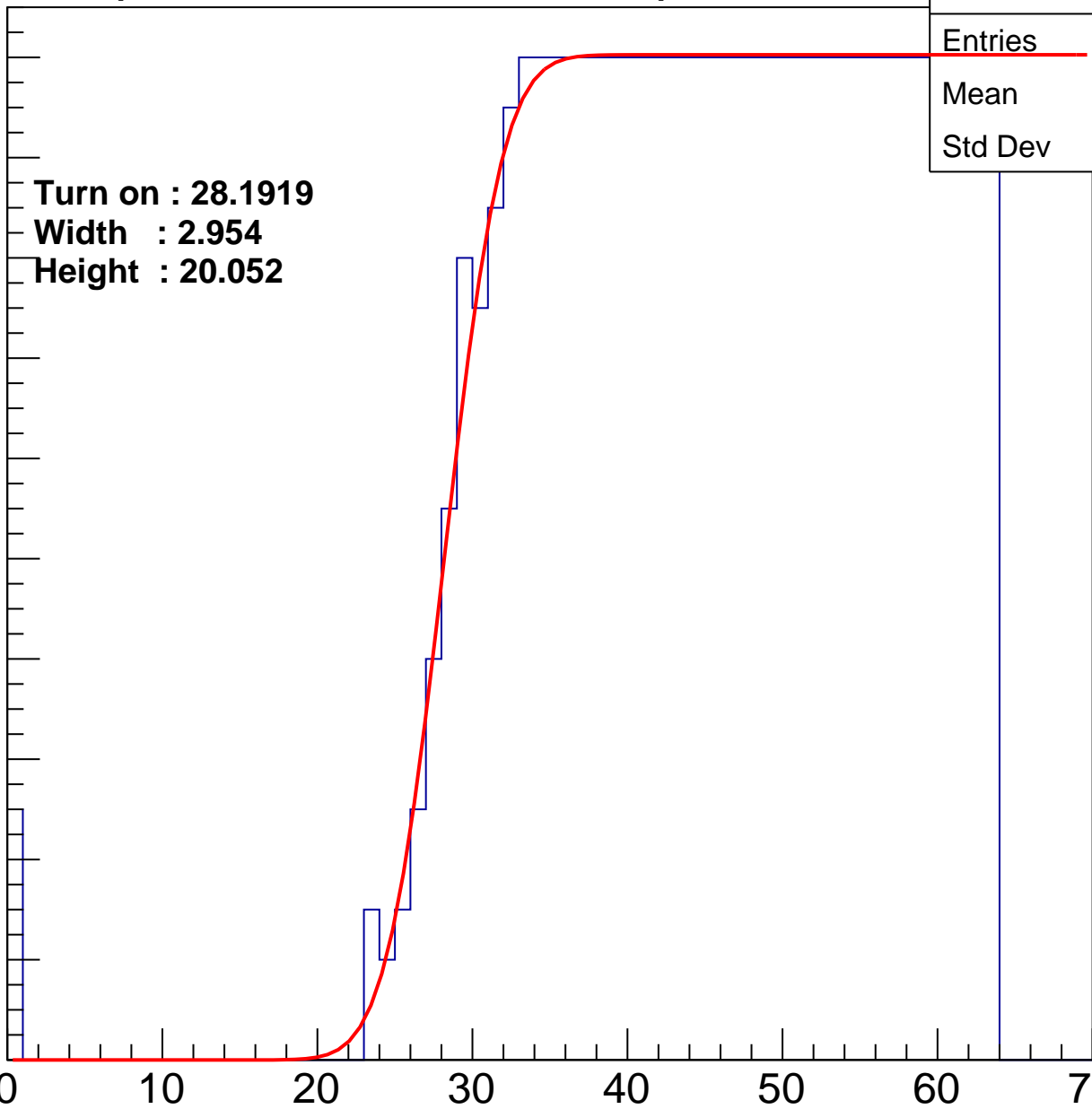
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.1919
Width : 2.954
Height : 20.052

Entries	724
Mean	45.1
Std Dev	11.18

ampl



B0L101S, U14-ch31

calib_packv5_042523_0143.root, FC#1, port C1

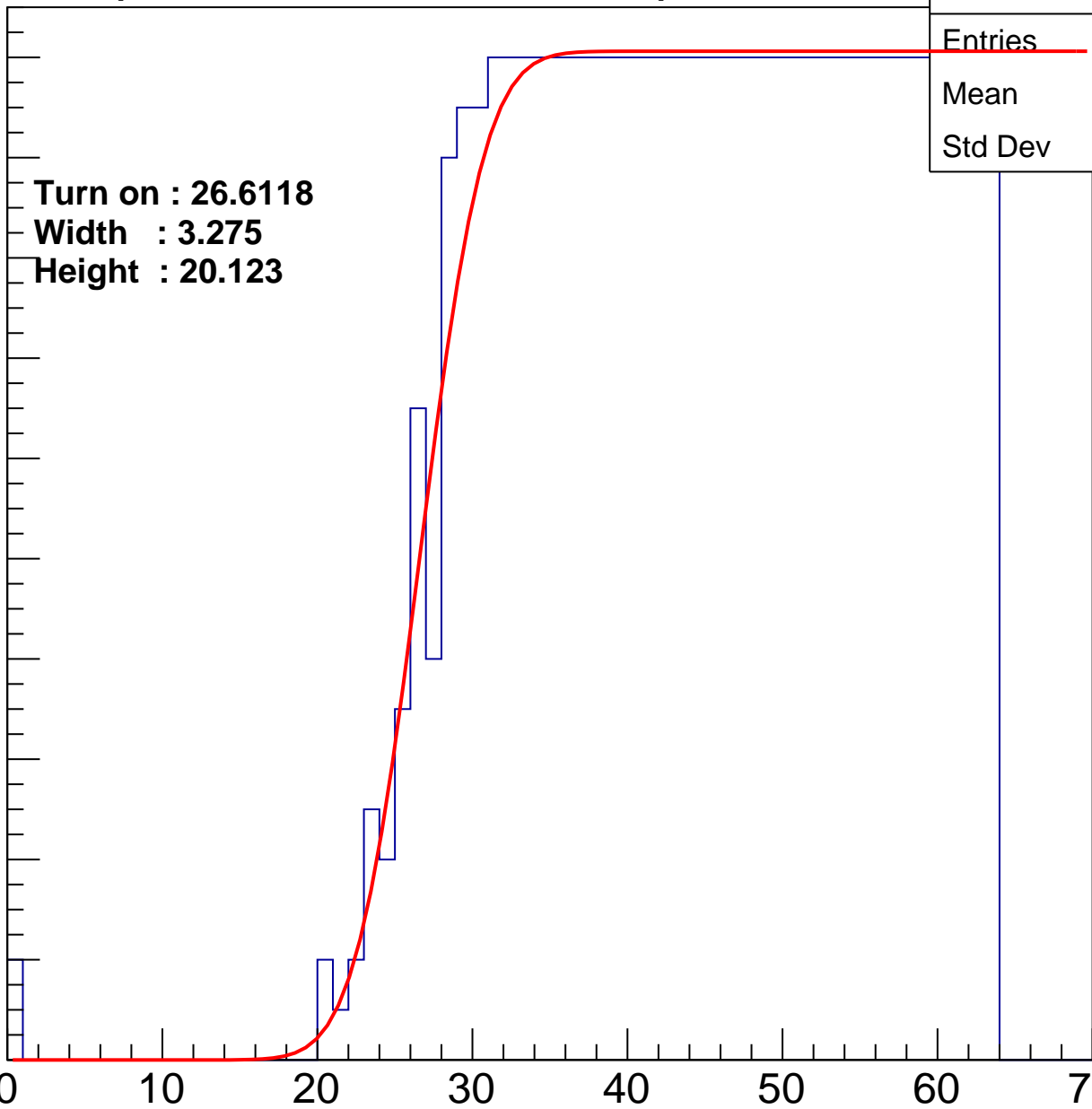
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6118
Width : 3.275
Height : 20.123

Entries	760
Mean	44.33
Std Dev	11.34

ampl



B0L101S, U14-ch32

calib_packv5_042523_0143.root, FC#1, port C1

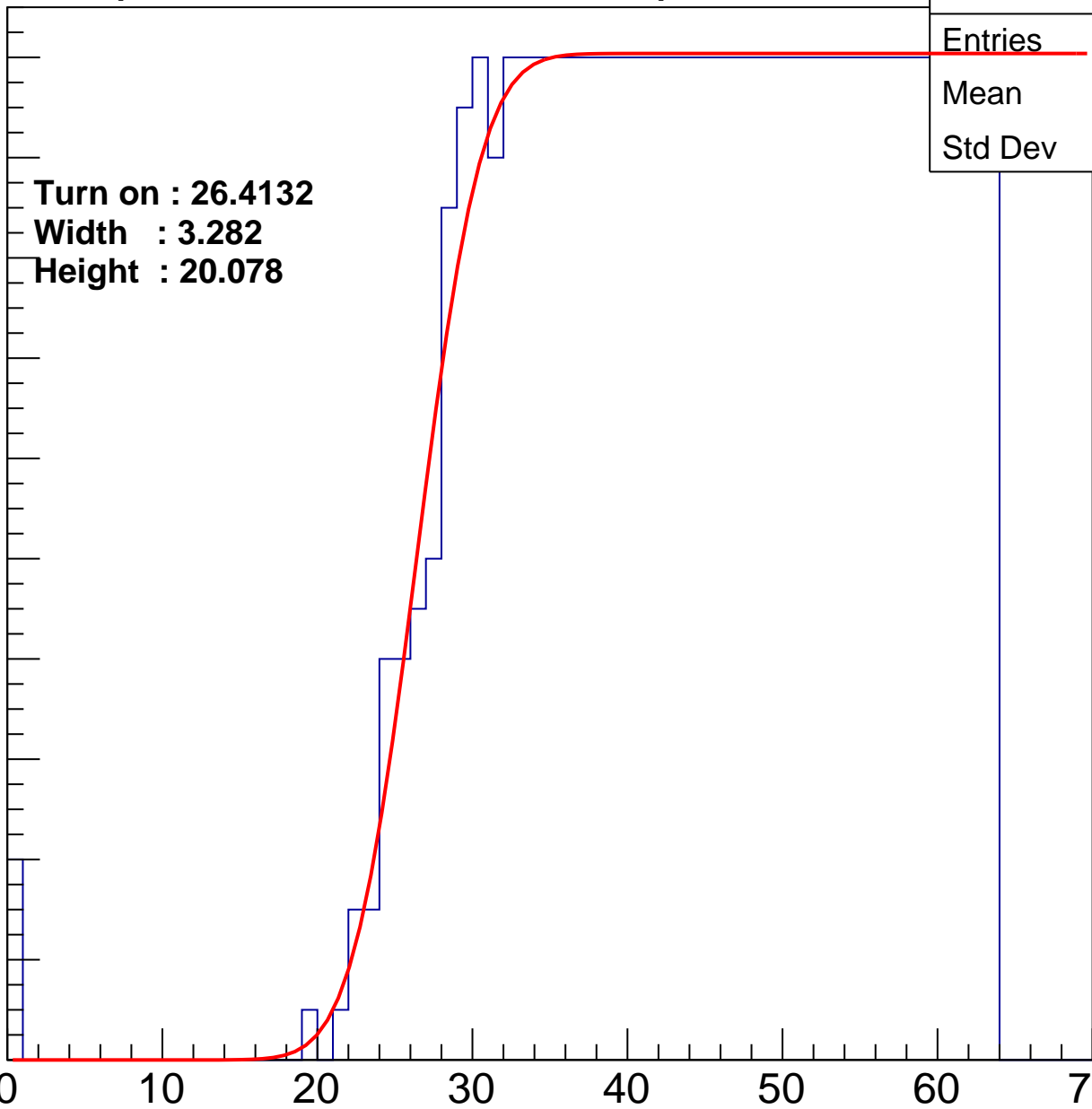
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4132
Width : 3.282
Height : 20.078

Entries	761
Mean	44.23
Std Dev	11.55

ampl



B0L101S, U14-ch33

calib_packv5_042523_0143.root, FC#1, port C1

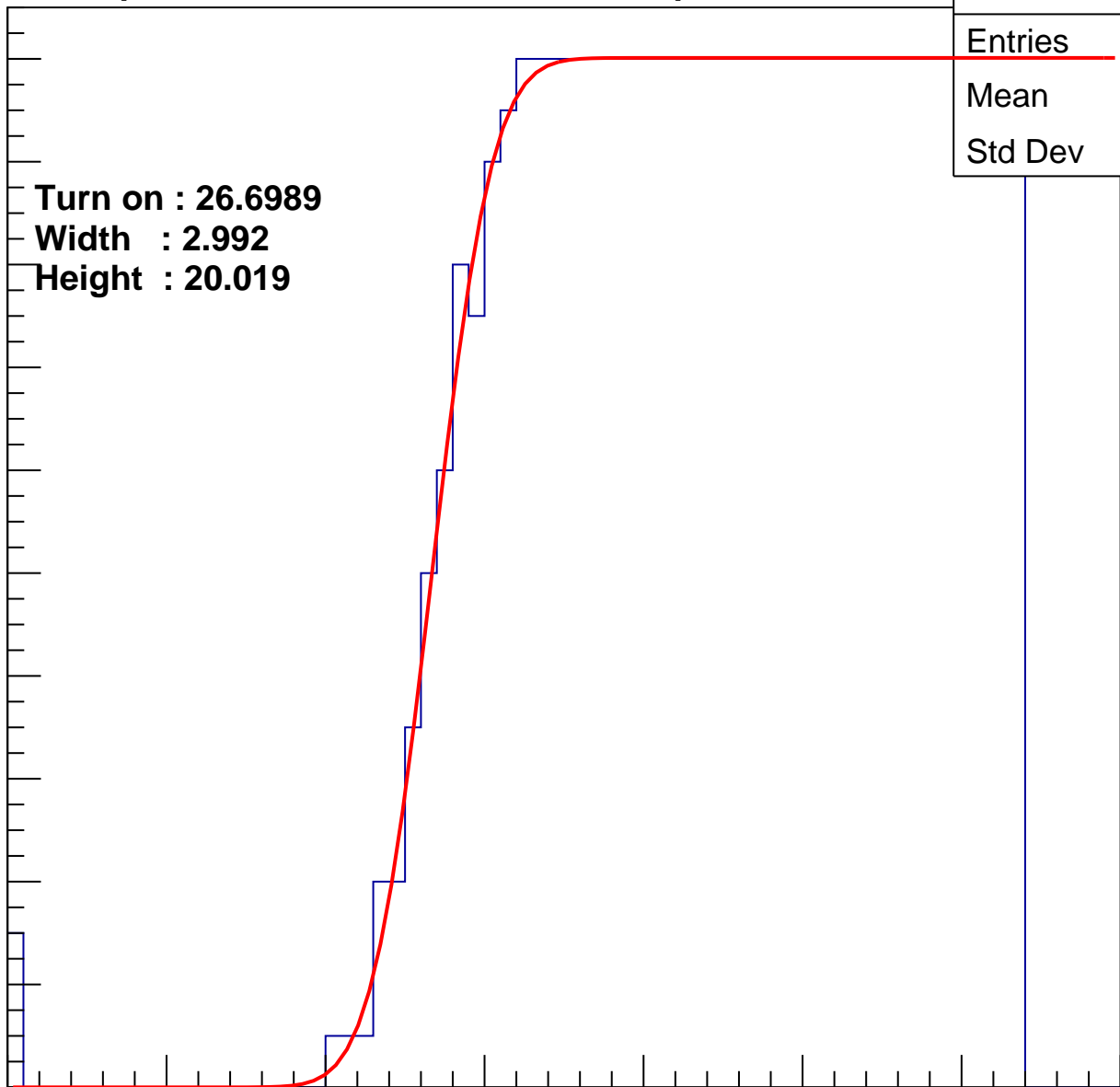
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6989
Width : 2.992
Height : 20.019

Entries	751
Mean	44.5
Std Dev	11.33

ampl



B0L101S, U14-ch34

calib_packv5_042523_0143.root, FC#1, port C1

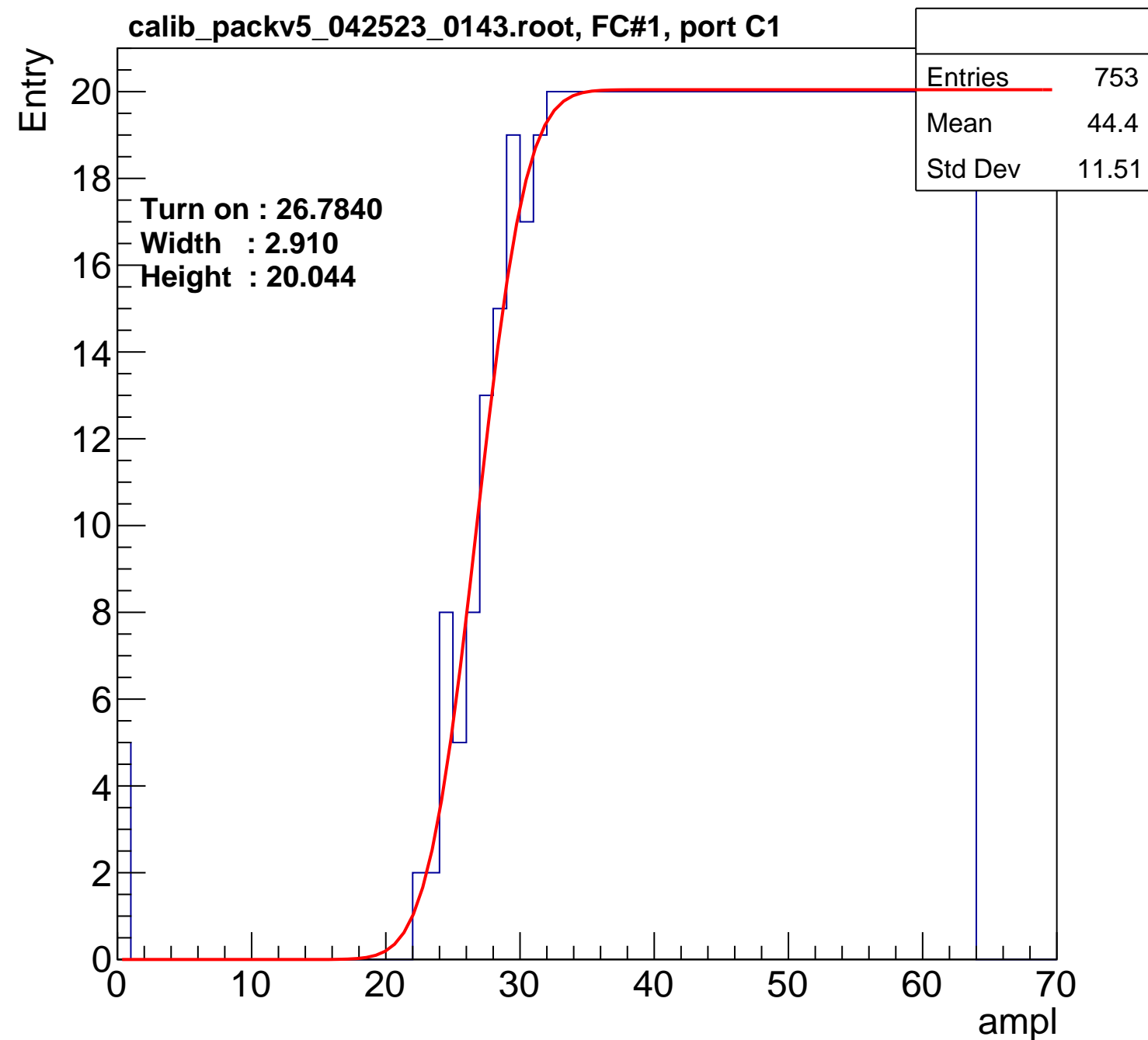
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7840
Width : 2.910
Height : 20.044

Entries	753
Mean	44.4
Std Dev	11.51

ampl



B0L101S, U14-ch35

calib_packv5_042523_0143.root, FC#1, port C1

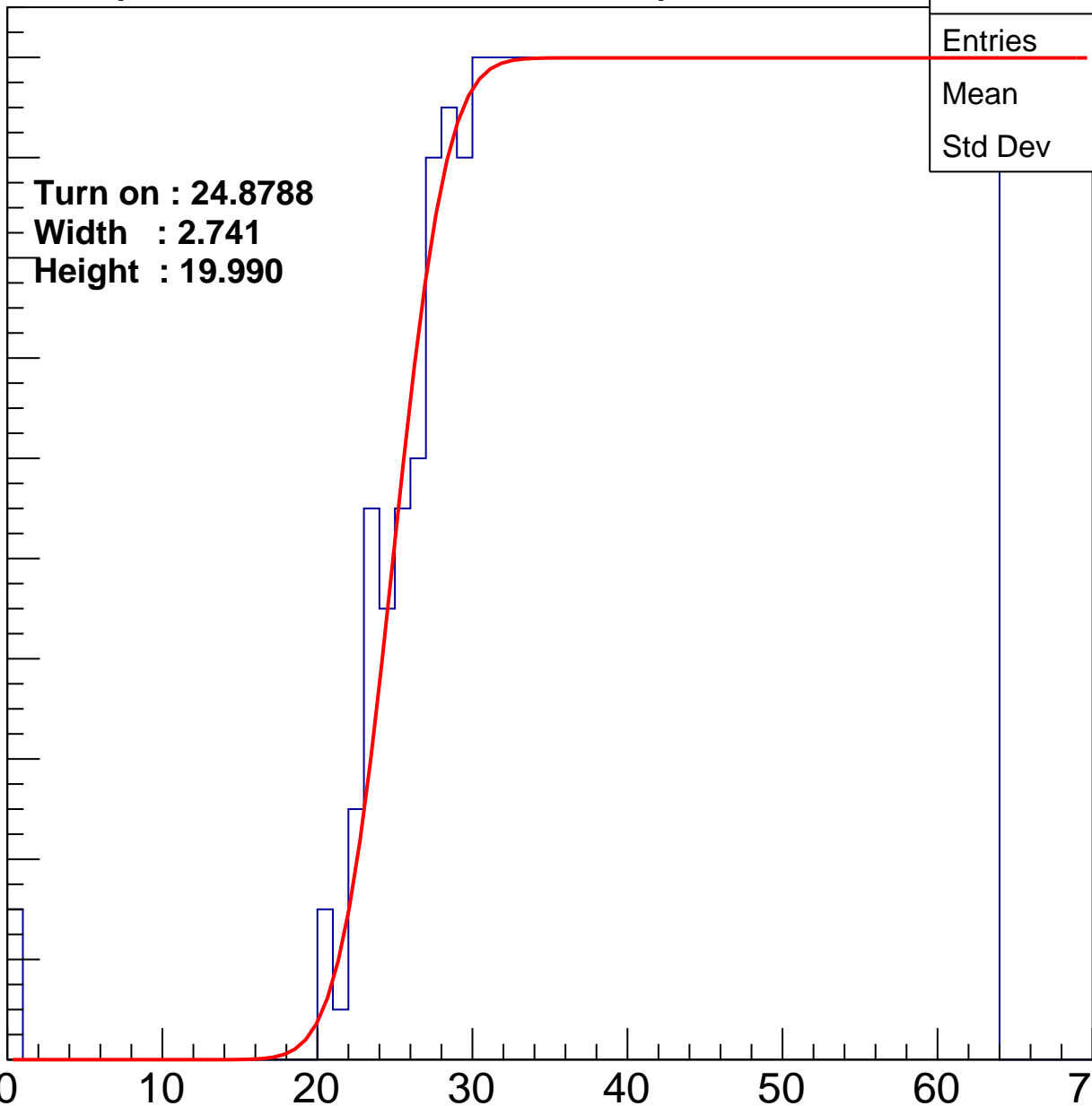
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8788
Width : 2.741
Height : 19.990

Entries	790
Mean	43.55
Std Dev	11.82

ampl



B0L101S, U14-ch36

calib_packv5_042523_0143.root, FC#1, port C1

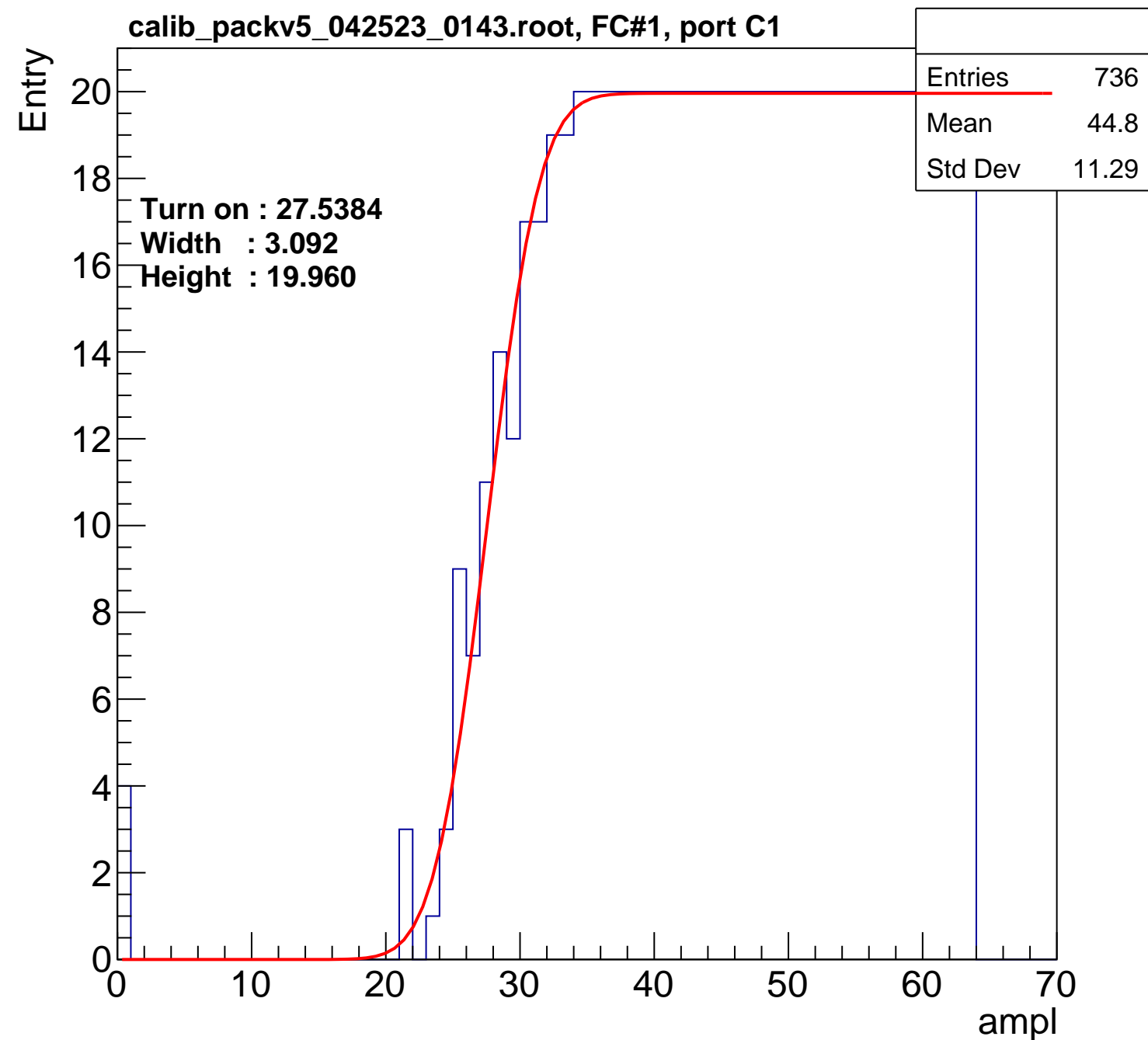
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5384
Width : 3.092
Height : 19.960

Entries	736
Mean	44.8
Std Dev	11.29

ampl



B0L101S, U14-ch37

calib_packv5_042523_0143.root, FC#1, port C1

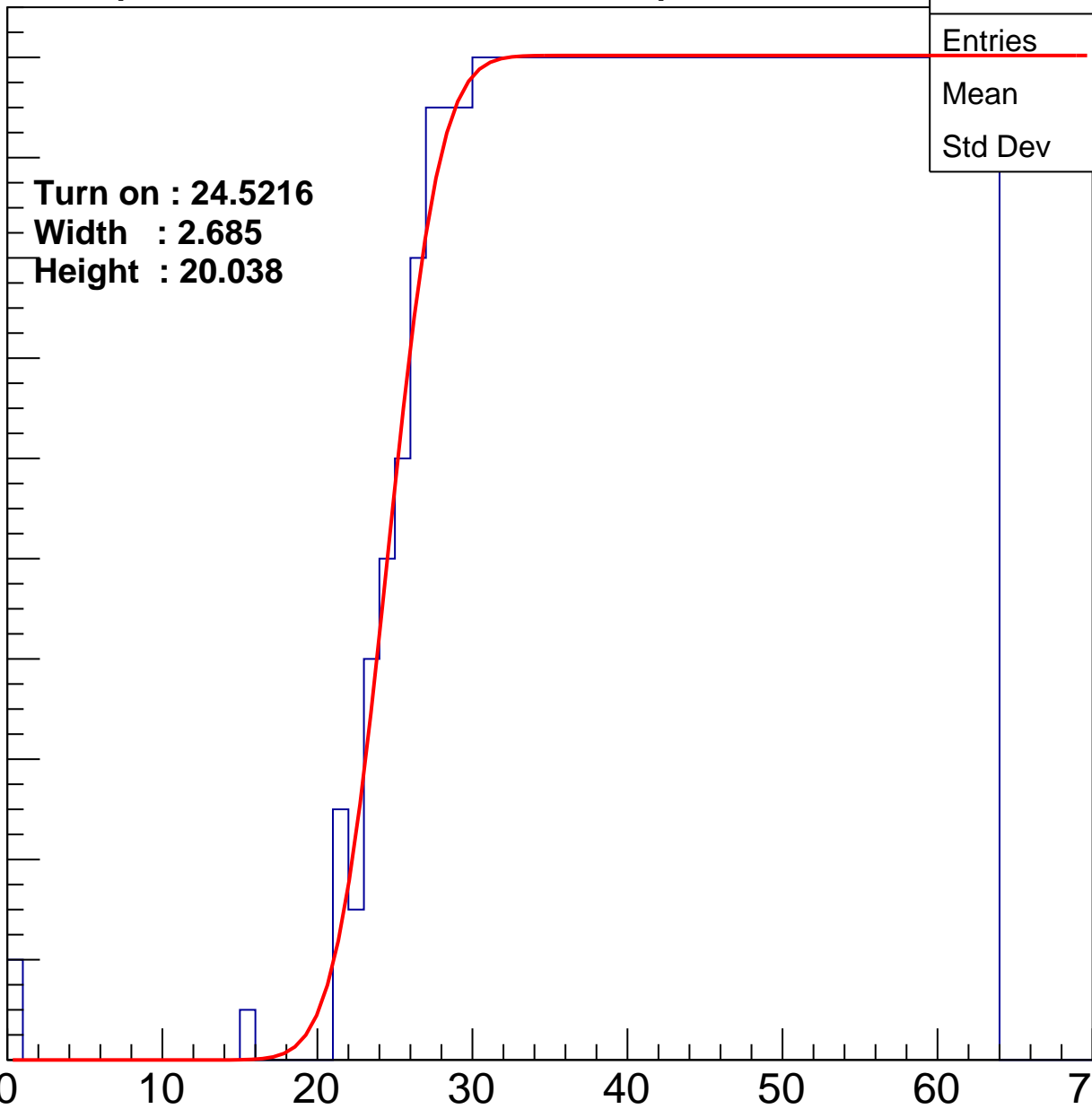
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.5216
Width : 2.685
Height : 20.038

Entries	794
Mean	43.5
Std Dev	11.77

ampl



B0L101S, U14-ch38

calib_packv5_042523_0143.root, FC#1, port C1

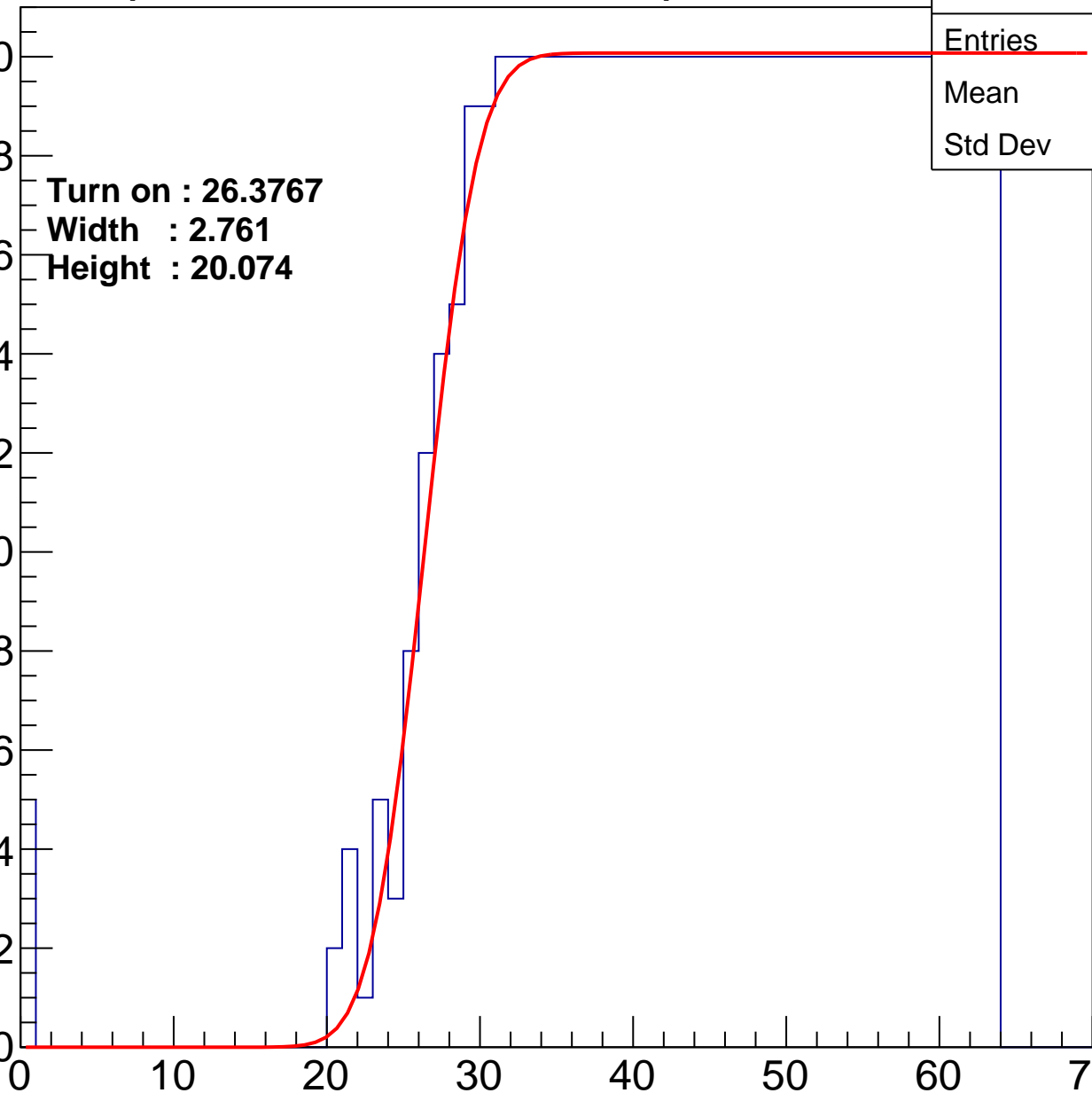
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3767
Width : 2.761
Height : 20.074

Entries	767
Mean	44.05
Std Dev	11.72

ampl



B0L101S, U14-ch39

calib_packv5_042523_0143.root, FC#1, port C1

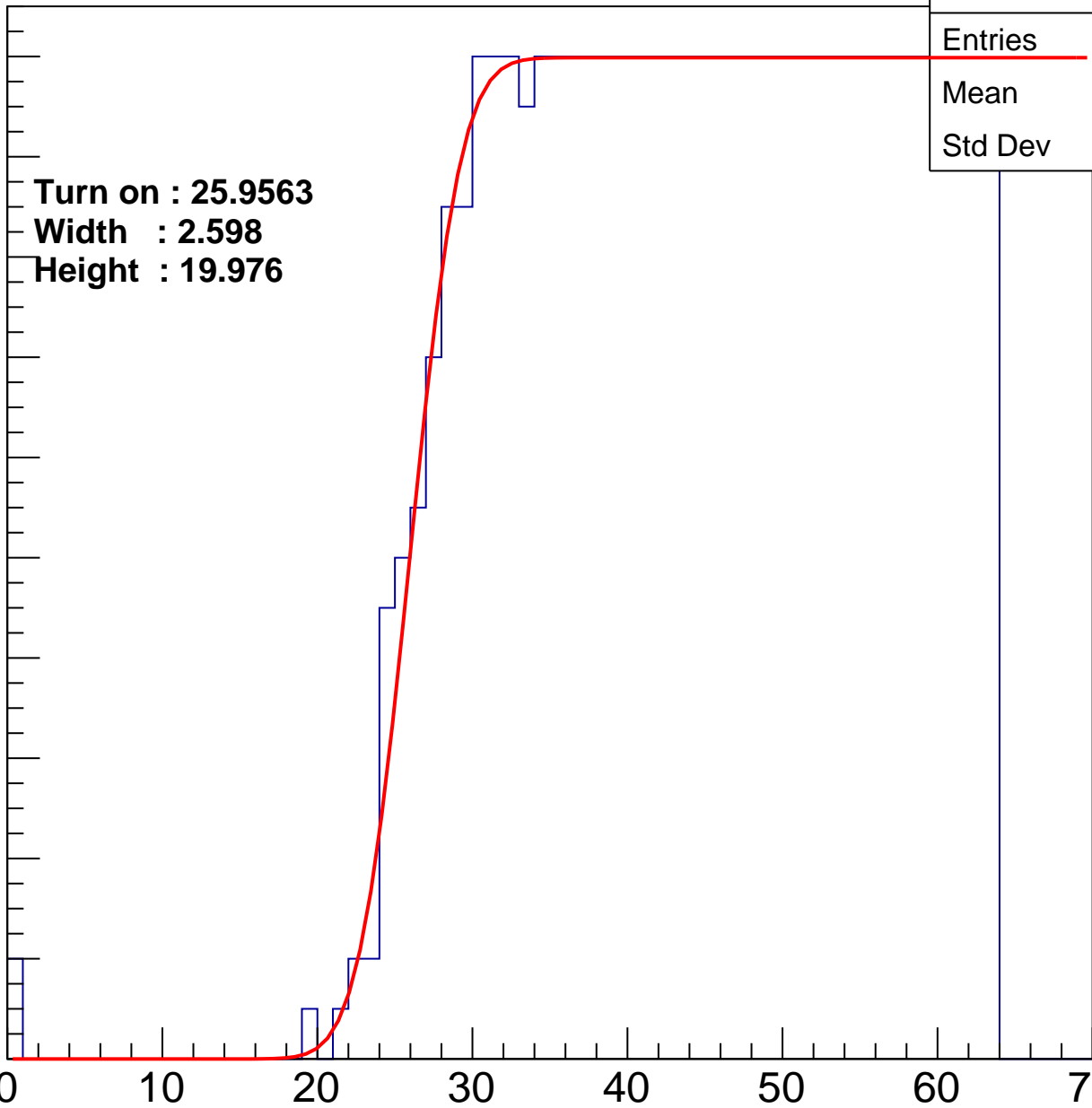
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9563
Width : 2.598
Height : 19.976

Entries	765
Mean	44.2
Std Dev	11.4

ampl



B0L101S, U14-ch40

calib_packv5_042523_0143.root, FC#1, port C1

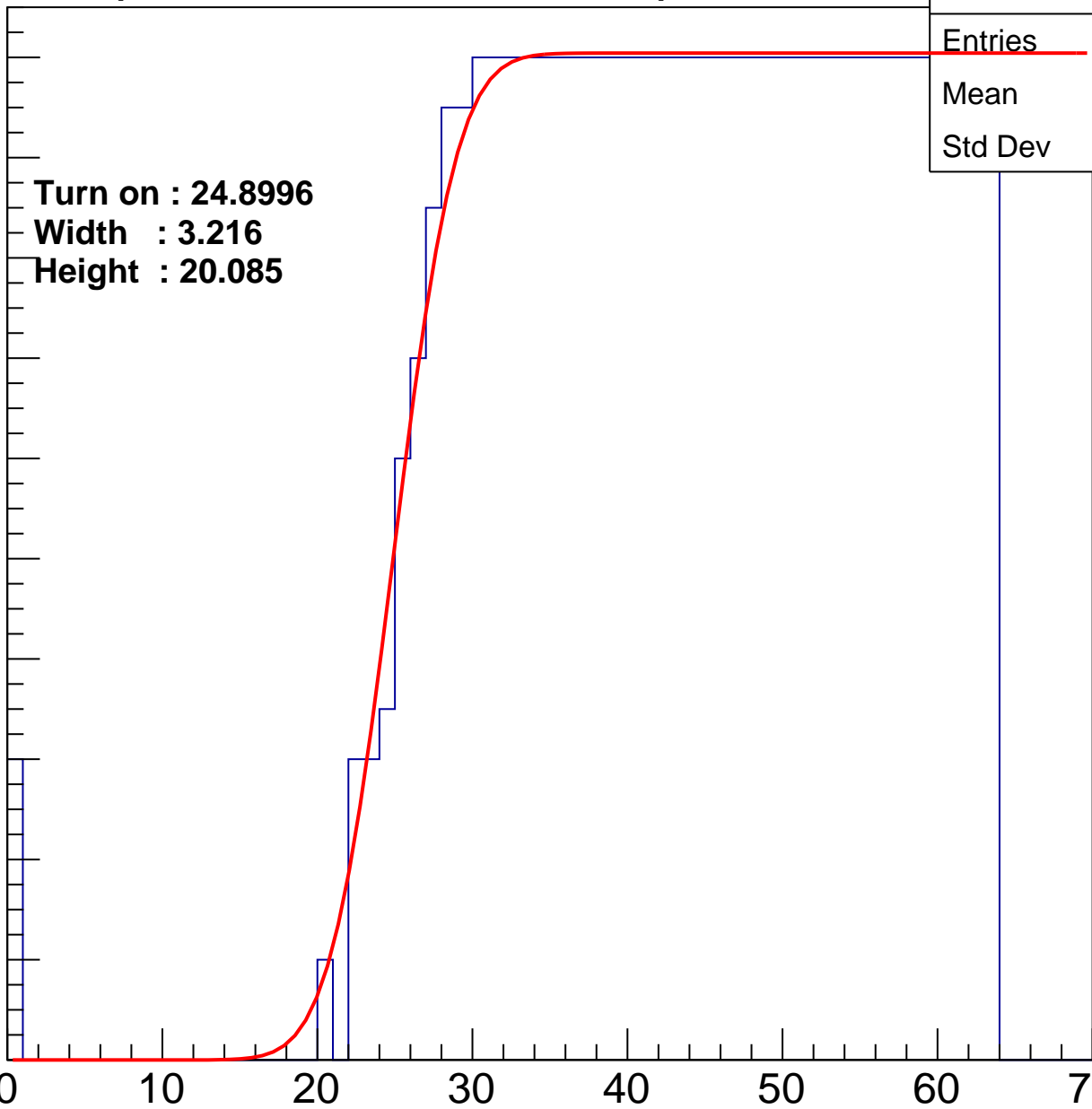
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.8996
Width : 3.216
Height : 20.085

Entries	788
Mean	43.53
Std Dev	12

ampl



B0L101S, U14-ch41

calib_packv5_042523_0143.root, FC#1, port C1

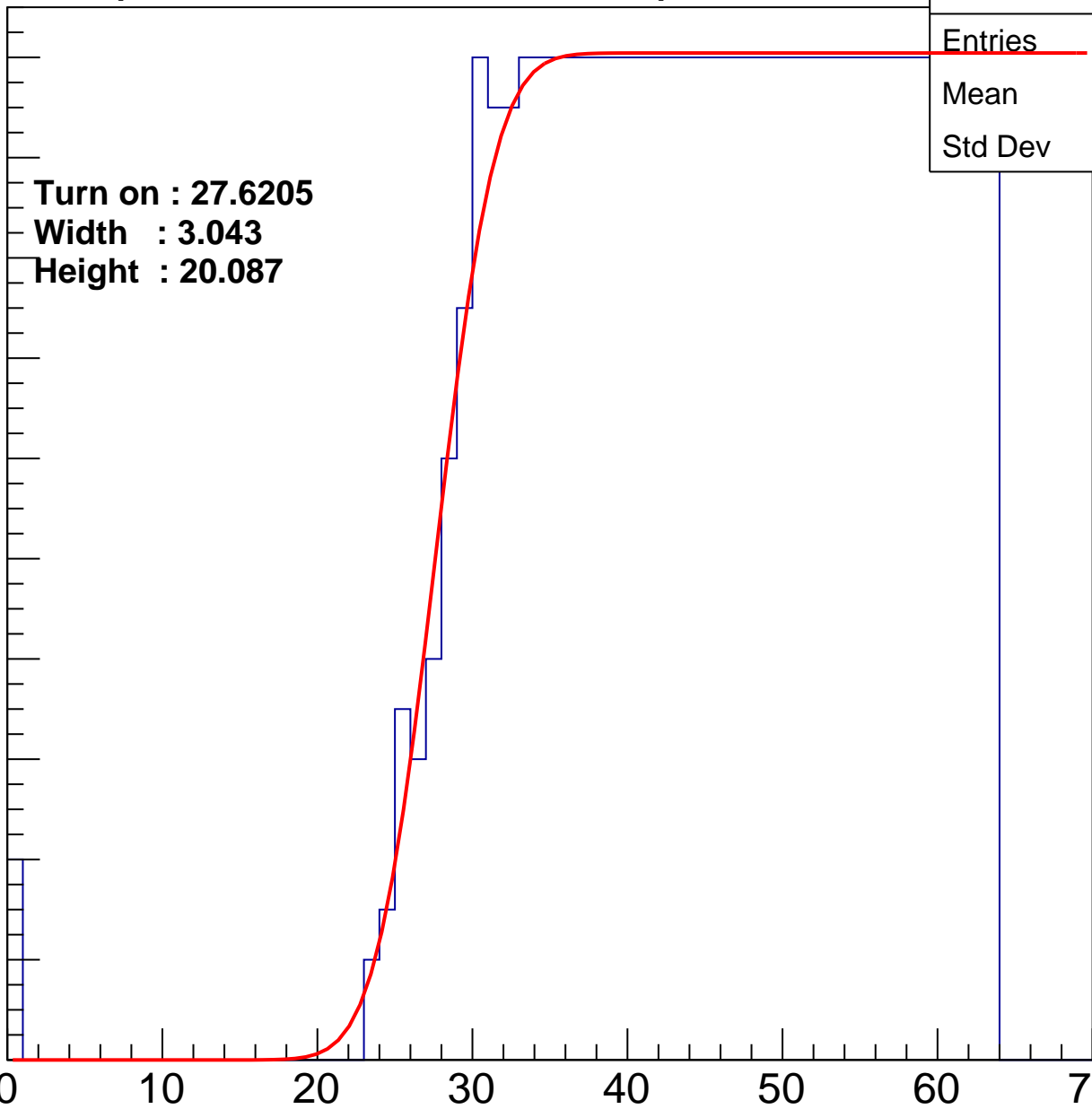
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6205
Width : 3.043
Height : 20.087

Entries	735
Mean	44.89
Std Dev	11.18

ampl



B0L101S, U14-ch42

calib_packv5_042523_0143.root, FC#1, port C1

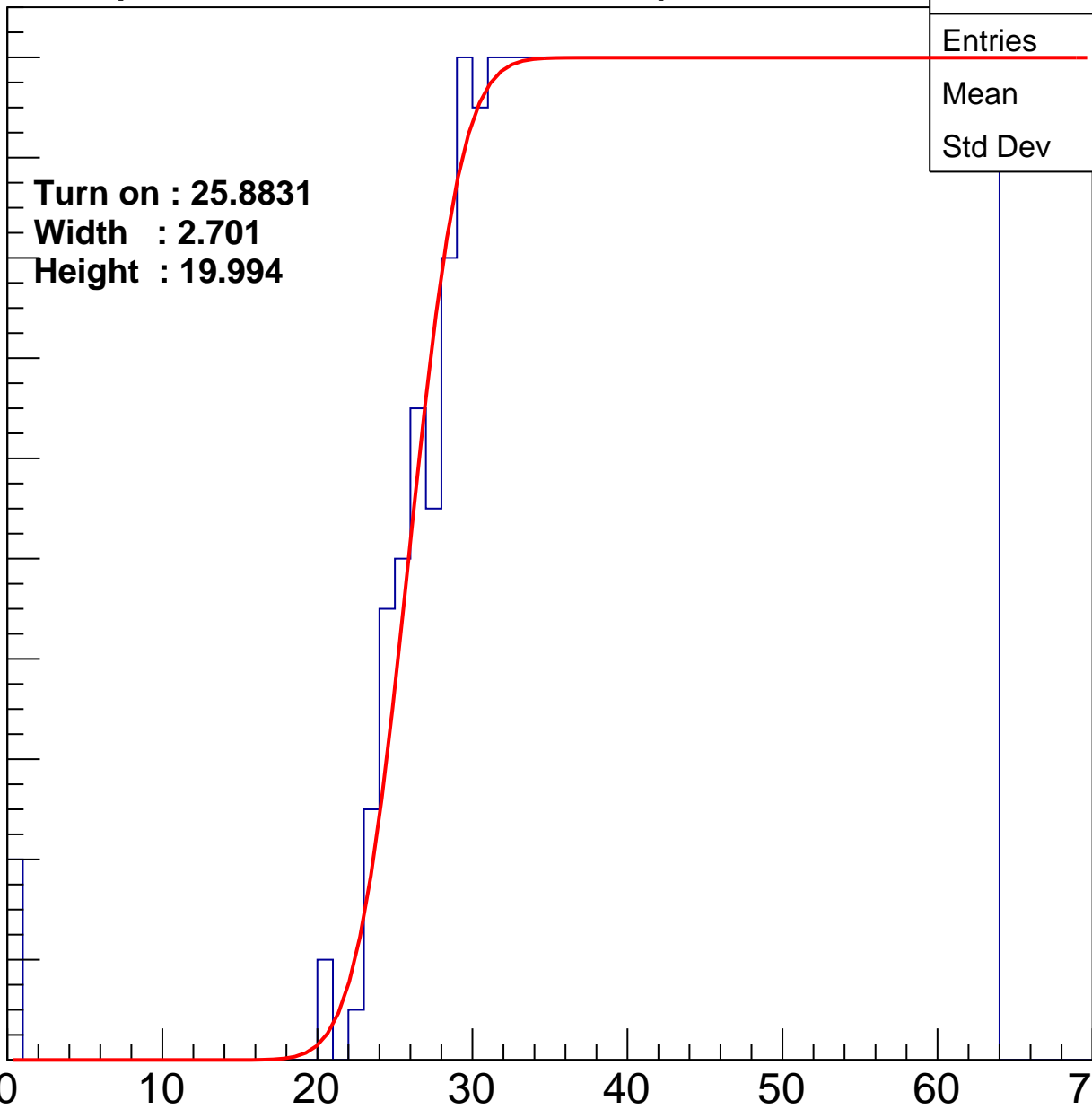
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8831
Width : 2.701
Height : 19.994

Entries	770
Mean	44.02
Std Dev	11.64

ampl



B0L101S, U14-ch43

calib_packv5_042523_0143.root, FC#1, port C1

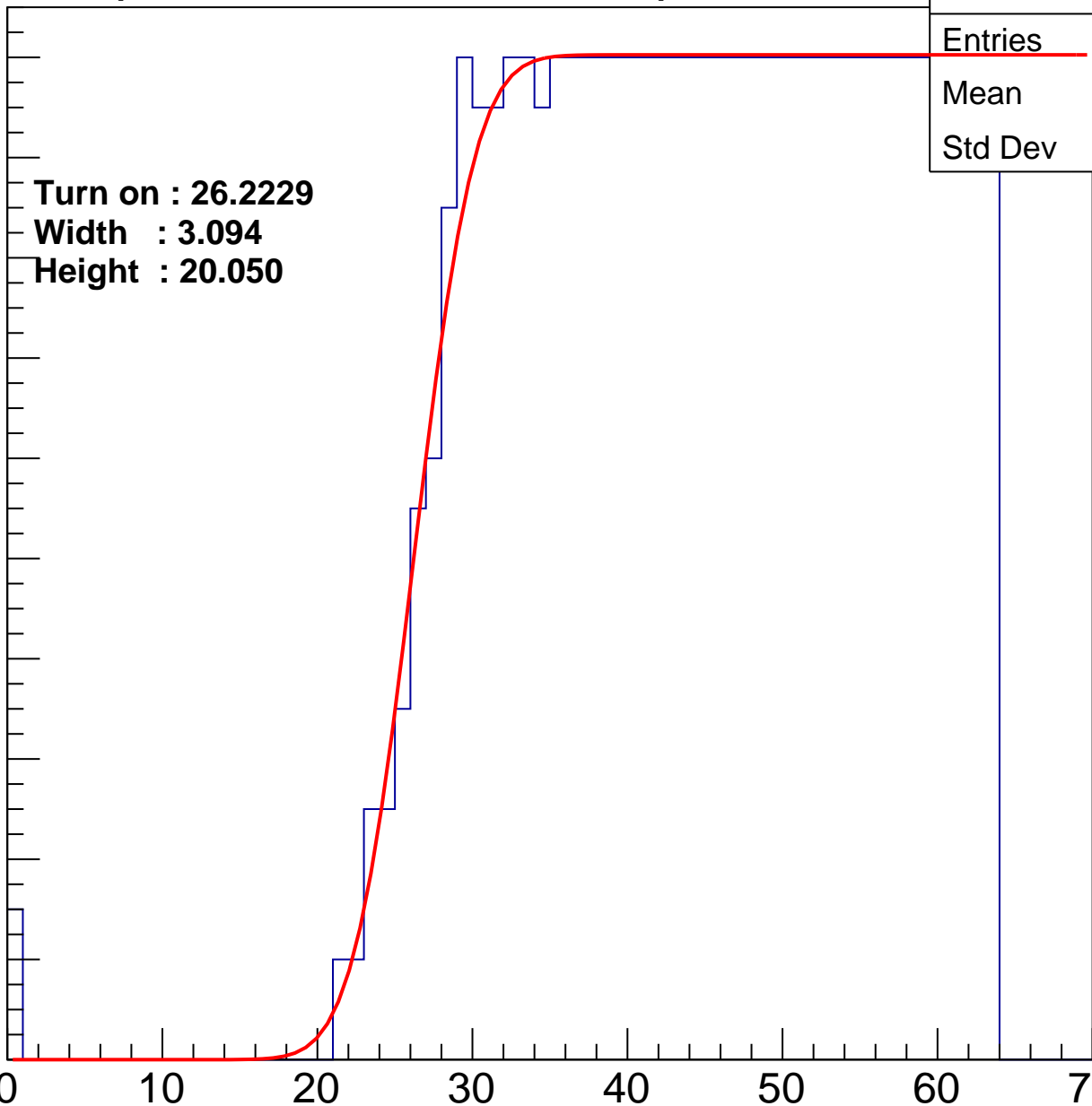
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2229
Width : 3.094
Height : 20.050

Entries	761
Mean	44.27
Std Dev	11.44

ampl



B0L101S, U14-ch44

calib_packv5_042523_0143.root, FC#1, port C1

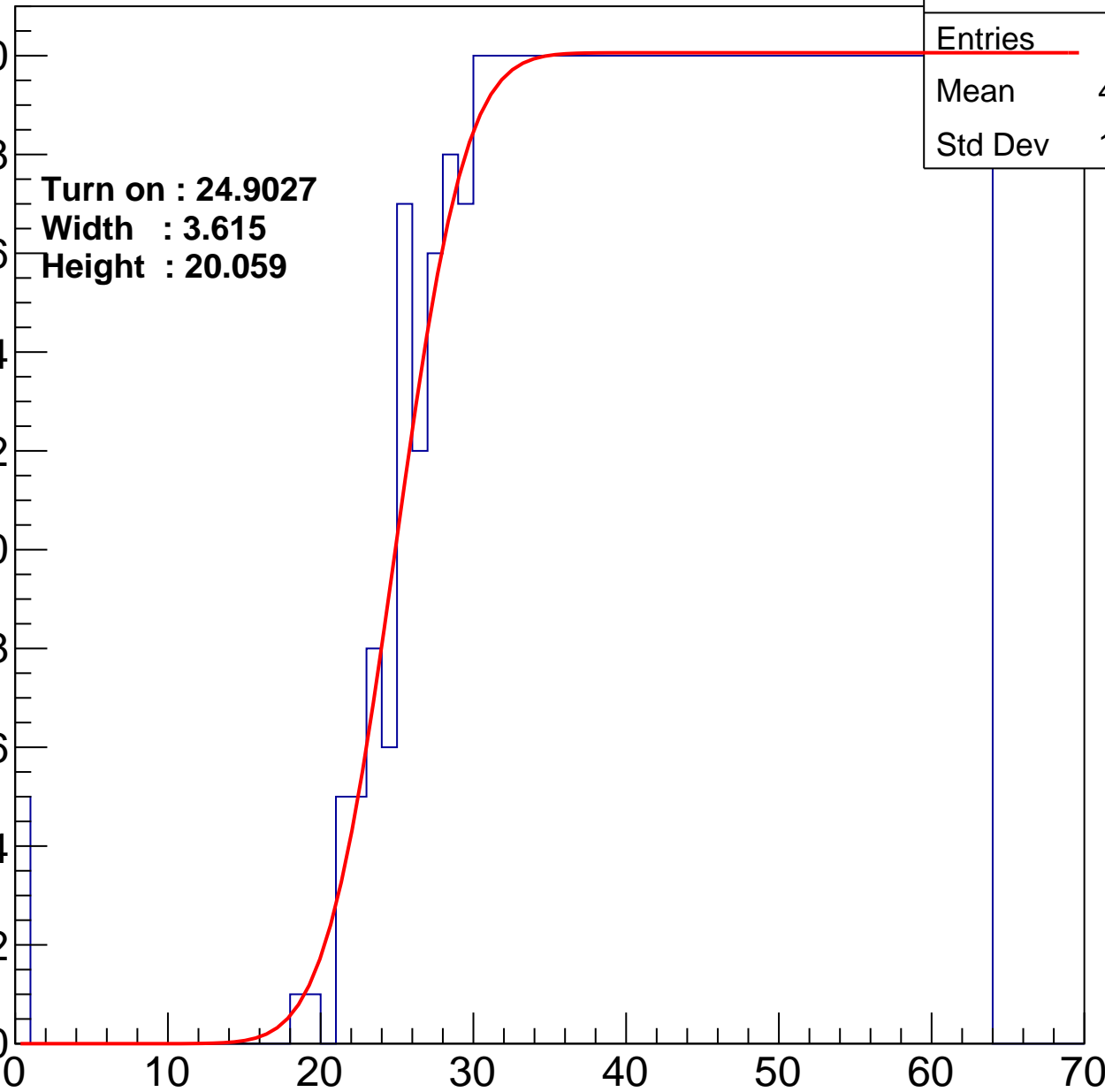
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.9027
Width : 3.615
Height : 20.059

Entries	791
Mean	43.45
Std Dev	12.03

ampl



B0L101S, U14-ch45

calib_packv5_042523_0143.root, FC#1, port C1

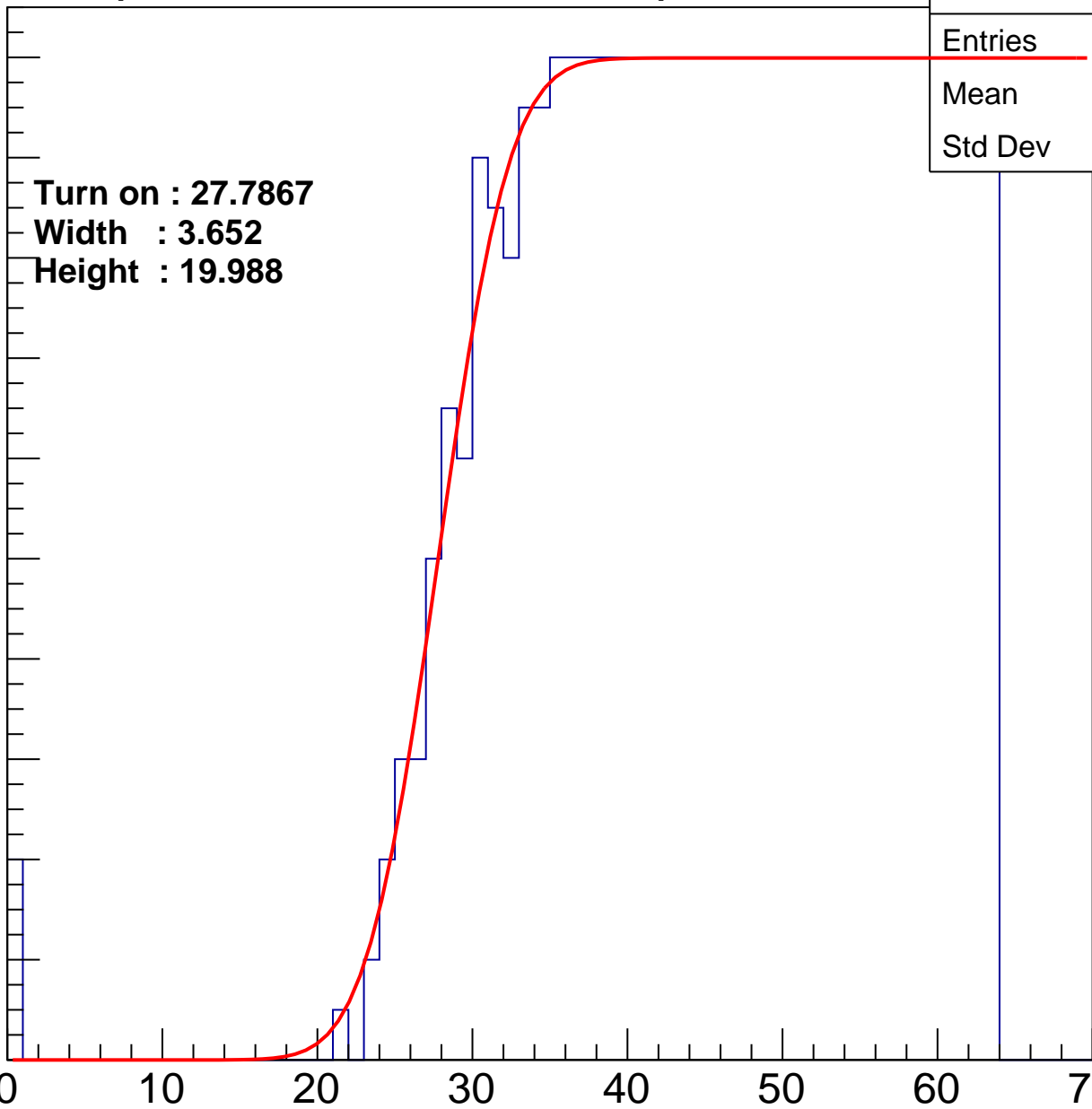
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7867
Width : 3.652
Height : 19.988

Entries	727
Mean	45.01
Std Dev	11.19

ampl



B0L101S, U14-ch46

calib_packv5_042523_0143.root, FC#1, port C1

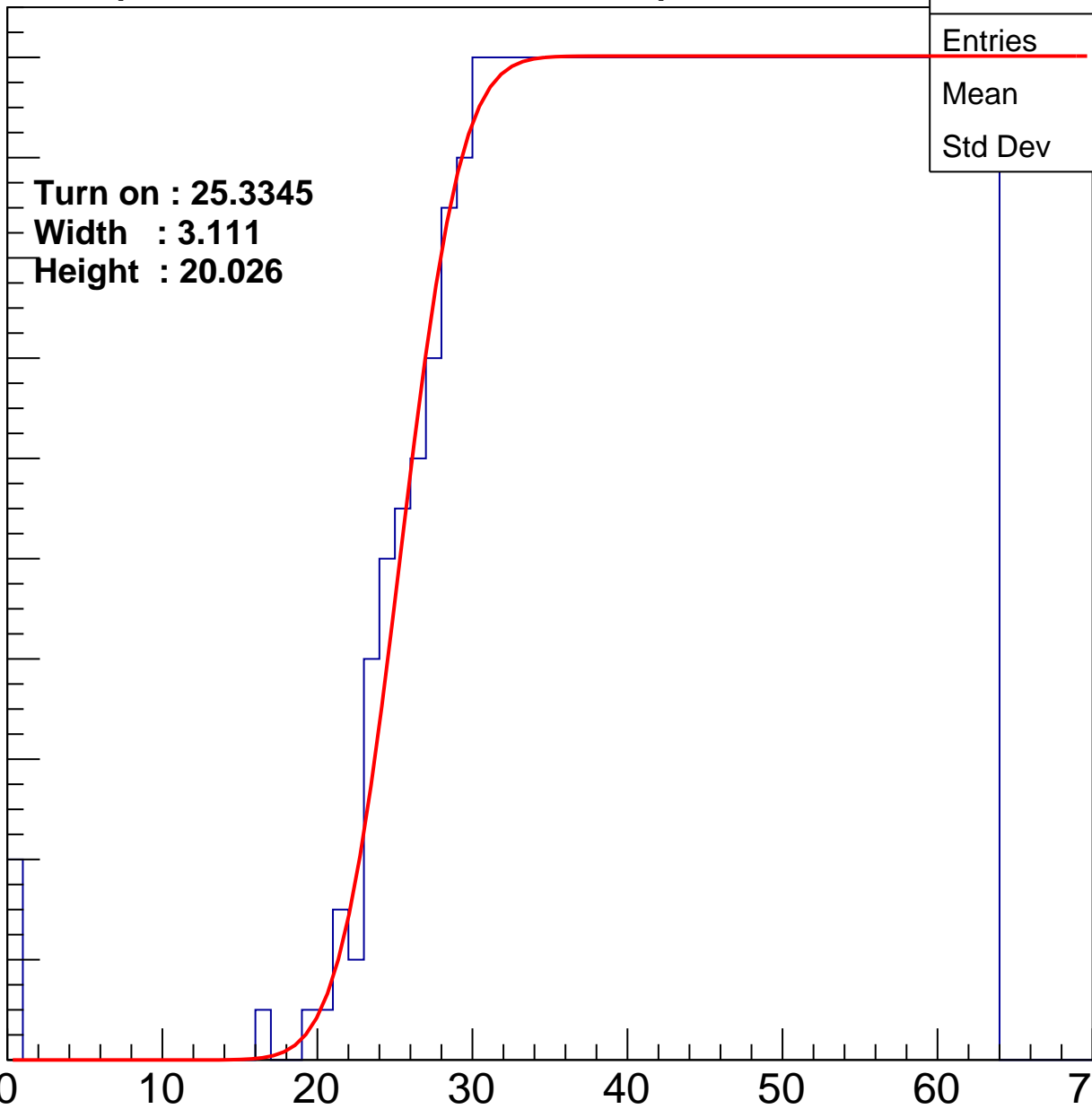
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3345
Width : 3.111
Height : 20.026

Entries	782
Mean	43.69
Std Dev	11.85

ampl



B0L101S, U14-ch47

calib_packv5_042523_0143.root, FC#1, port C1

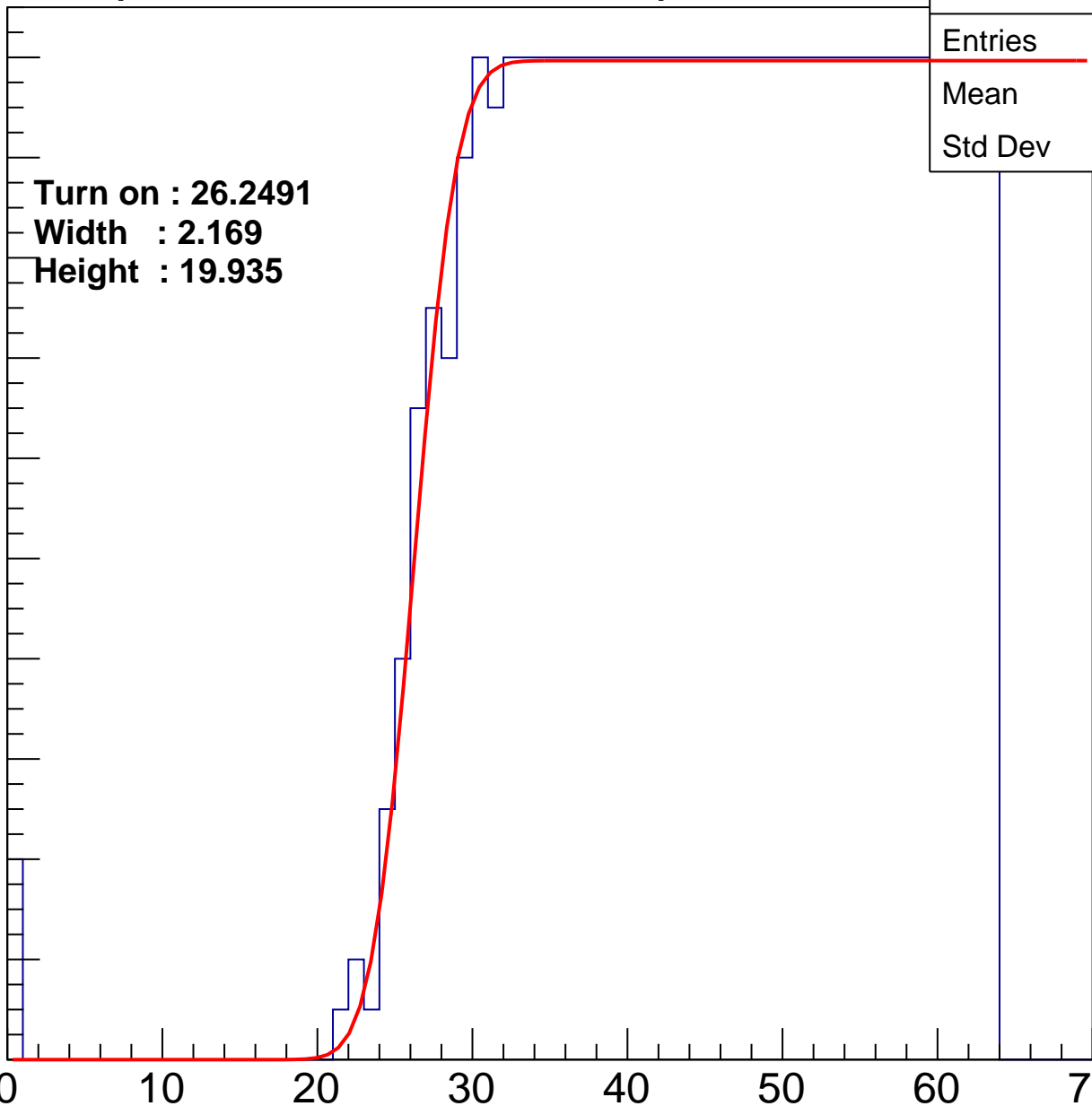
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.2491
Width : 2.169
Height : 19.935

Entries	760
Mean	44.28
Std Dev	11.48

ampl



B0L101S, U14-ch48

calib_packv5_042523_0143.root, FC#1, port C1

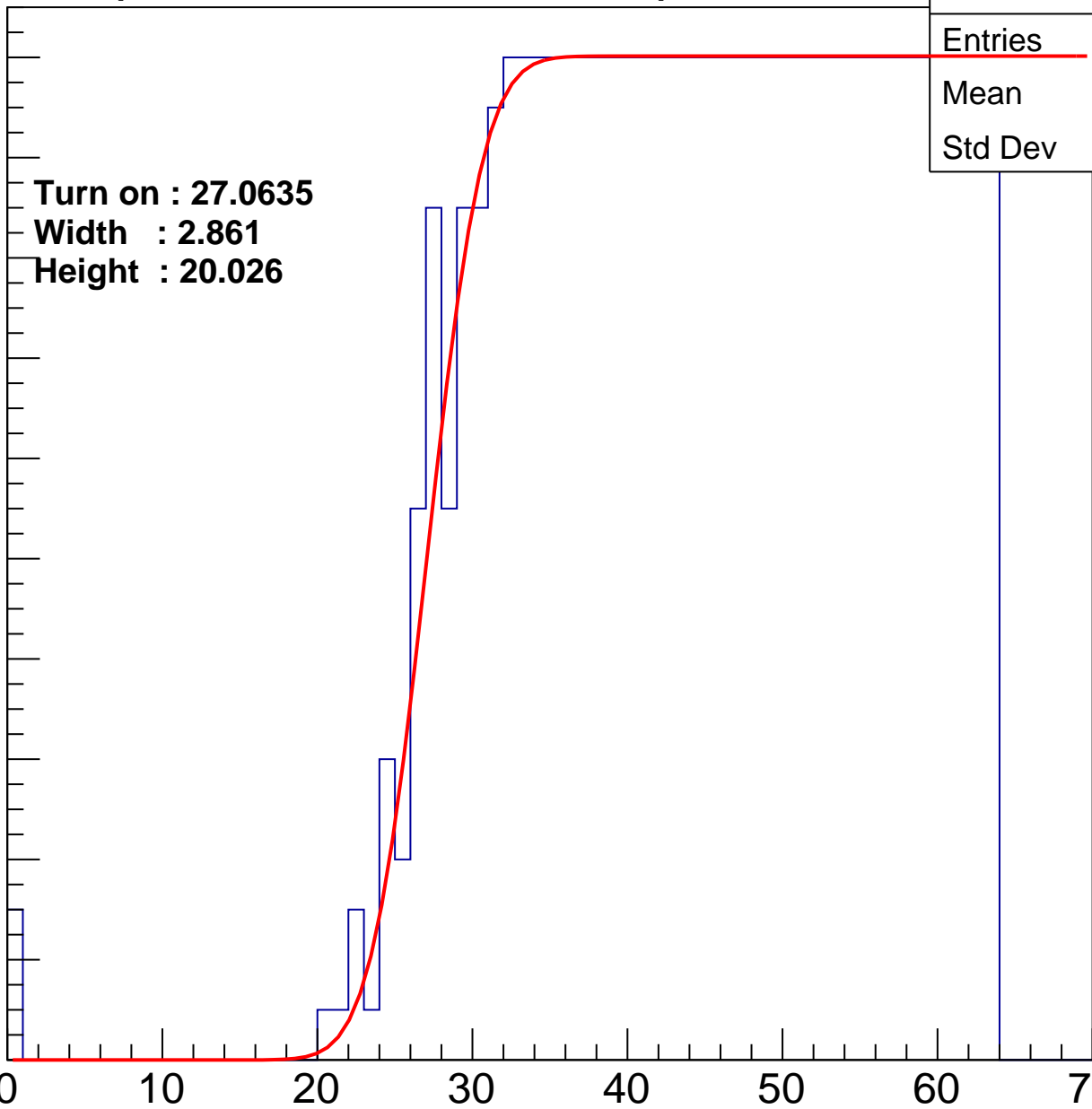
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0635
Width : 2.861
Height : 20.026

Entries	751
Mean	44.5
Std Dev	11.34

ampl



B0L101S, U14-ch49

calib_packv5_042523_0143.root, FC#1, port C1

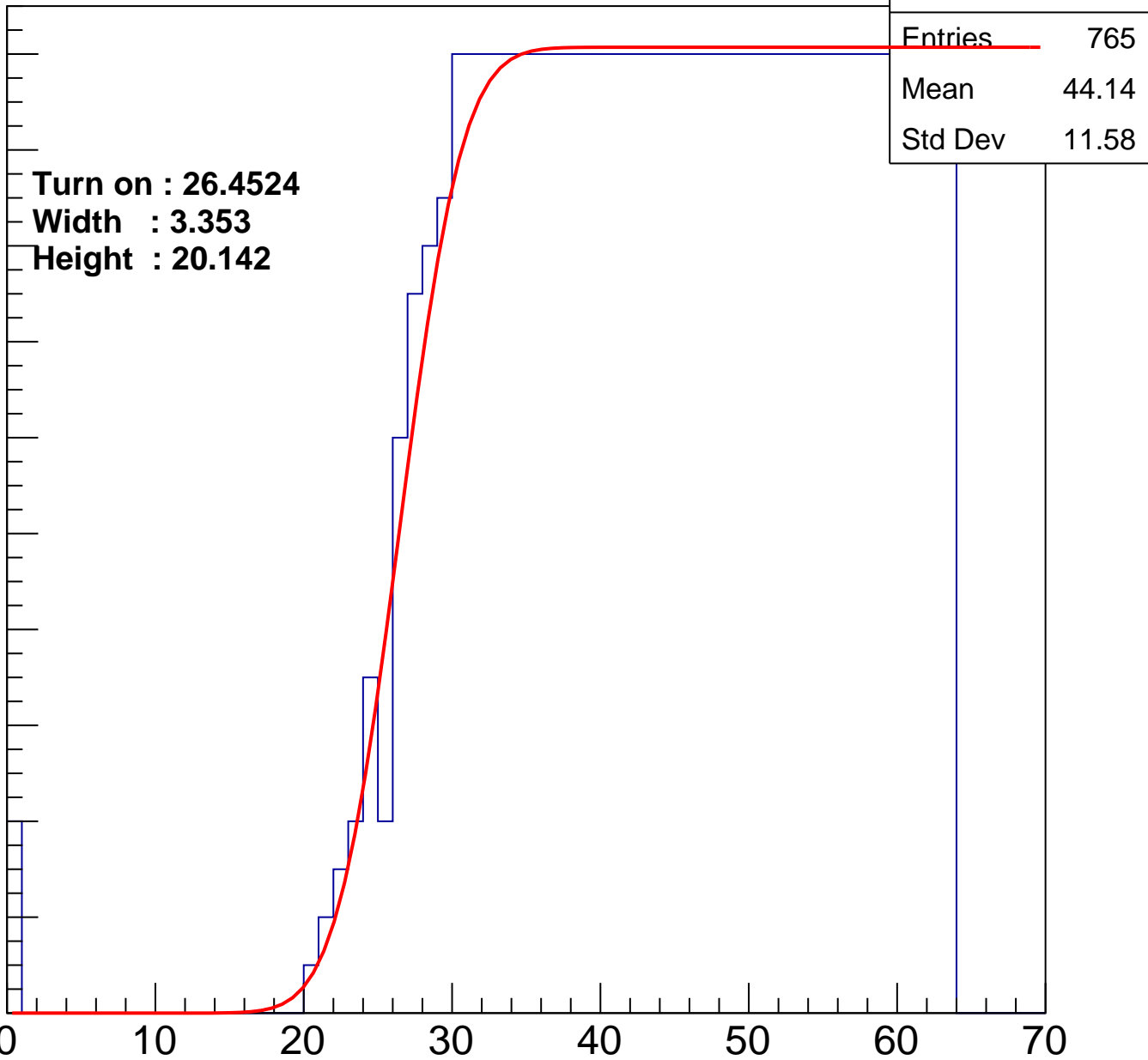
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4524
Width : 3.353
Height : 20.142

Entries	765
Mean	44.14
Std Dev	11.58

ampl



B0L101S, U14-ch50

calib_packv5_042523_0143.root, FC#1, port C1

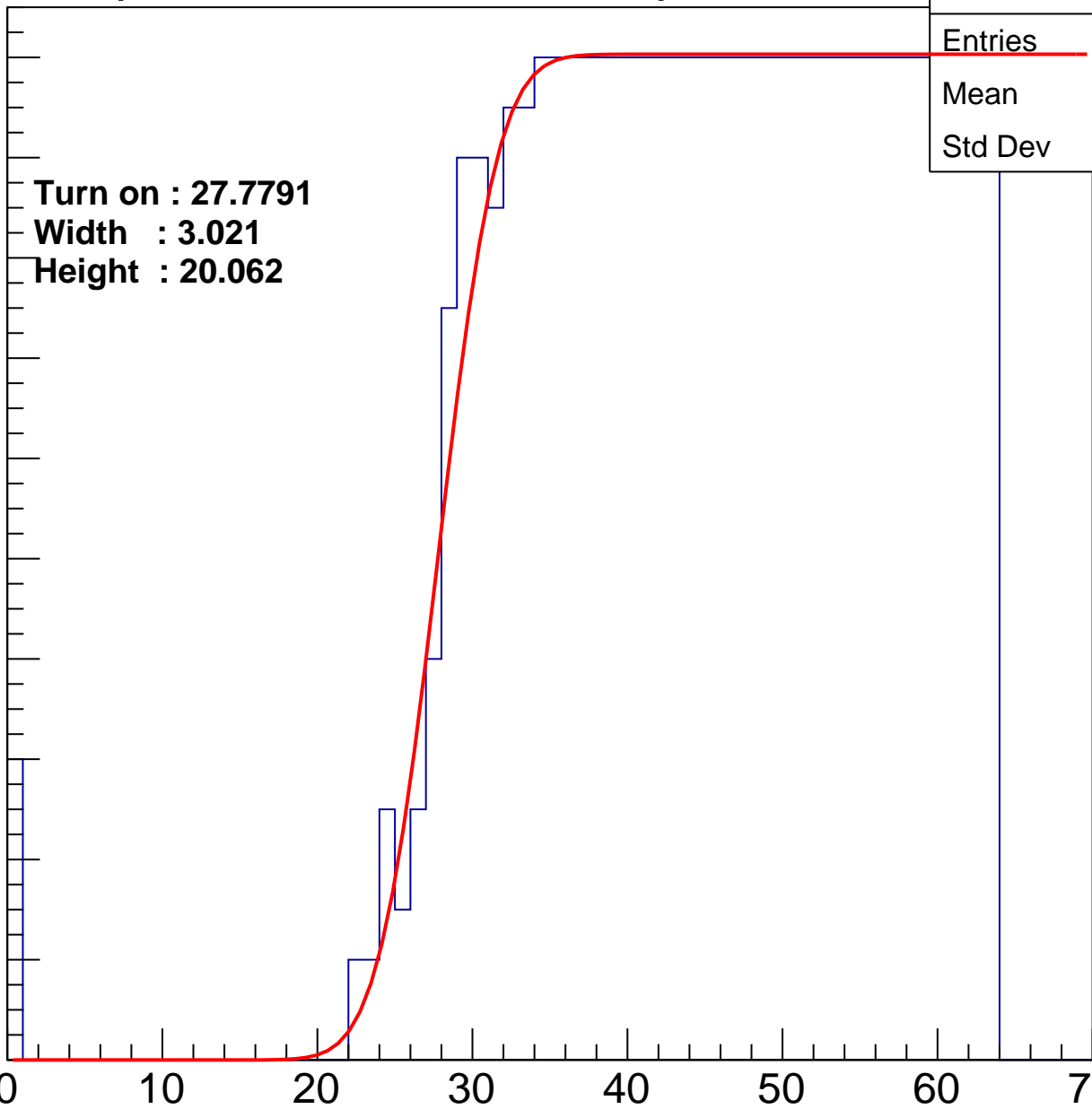
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7791
Width : 3.021
Height : 20.062

Entries	737
Mean	44.74
Std Dev	11.44

ampl



B0L101S, U14-ch51

calib_packv5_042523_0143.root, FC#1, port C1

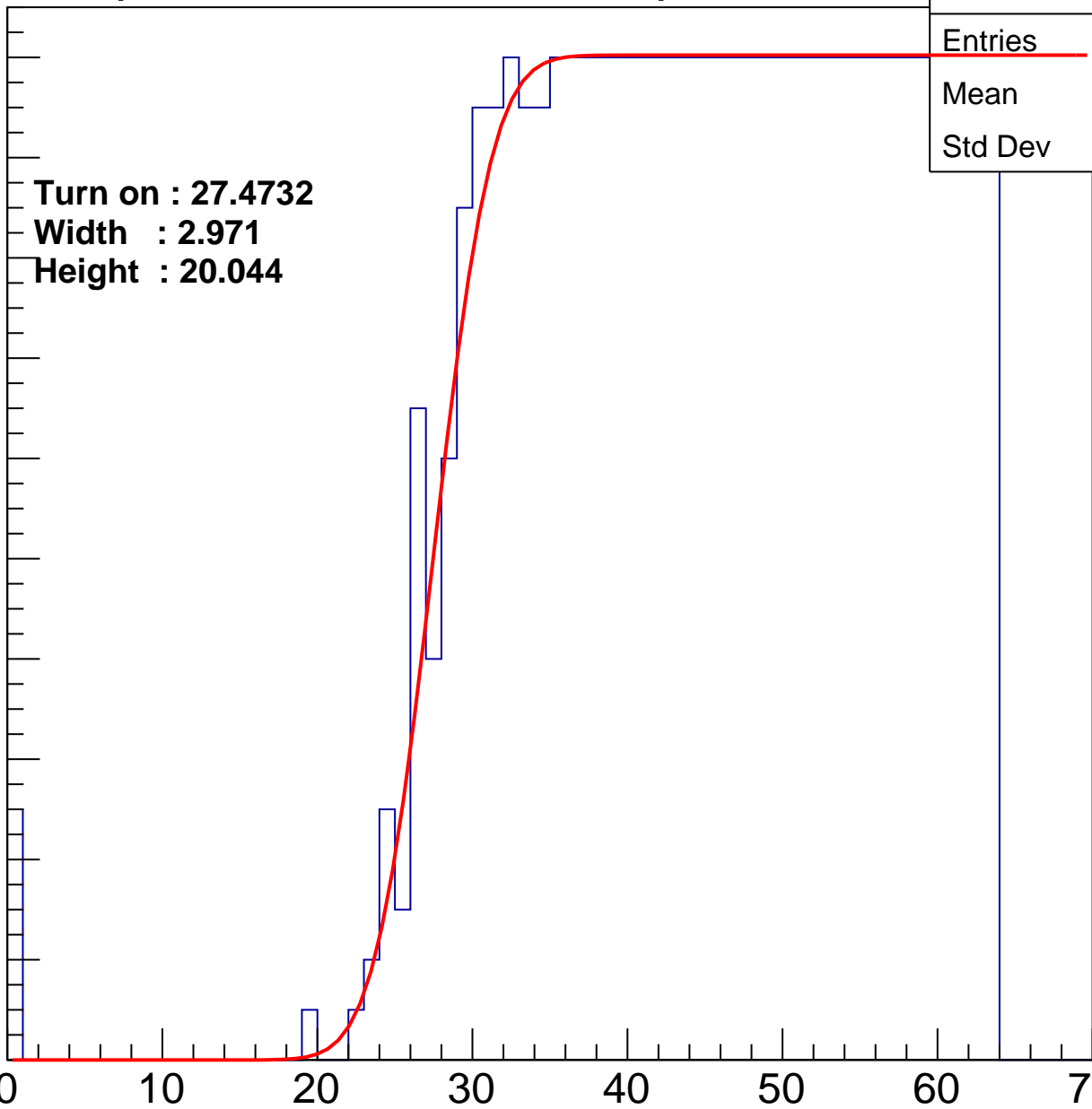
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.4732
Width : 2.971
Height : 20.044

Entries	743
Mean	44.63
Std Dev	11.43

ampl



B0L101S, U14-ch52

calib_packv5_042523_0143.root, FC#1, port C1

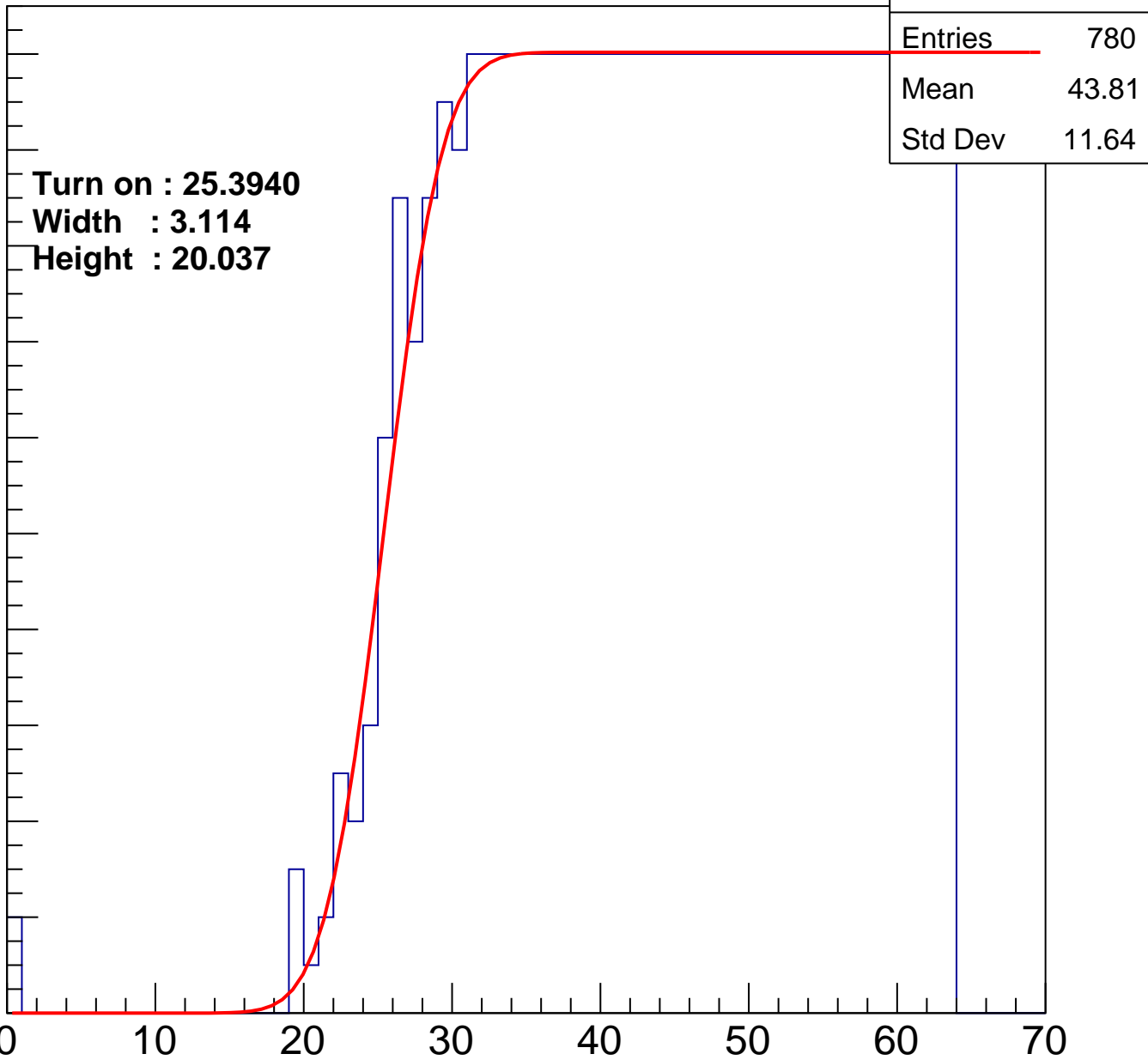
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3940
Width : 3.114
Height : 20.037

Entries	780
Mean	43.81
Std Dev	11.64

ampl



B0L101S, U14-ch53

calib_packv5_042523_0143.root, FC#1, port C1

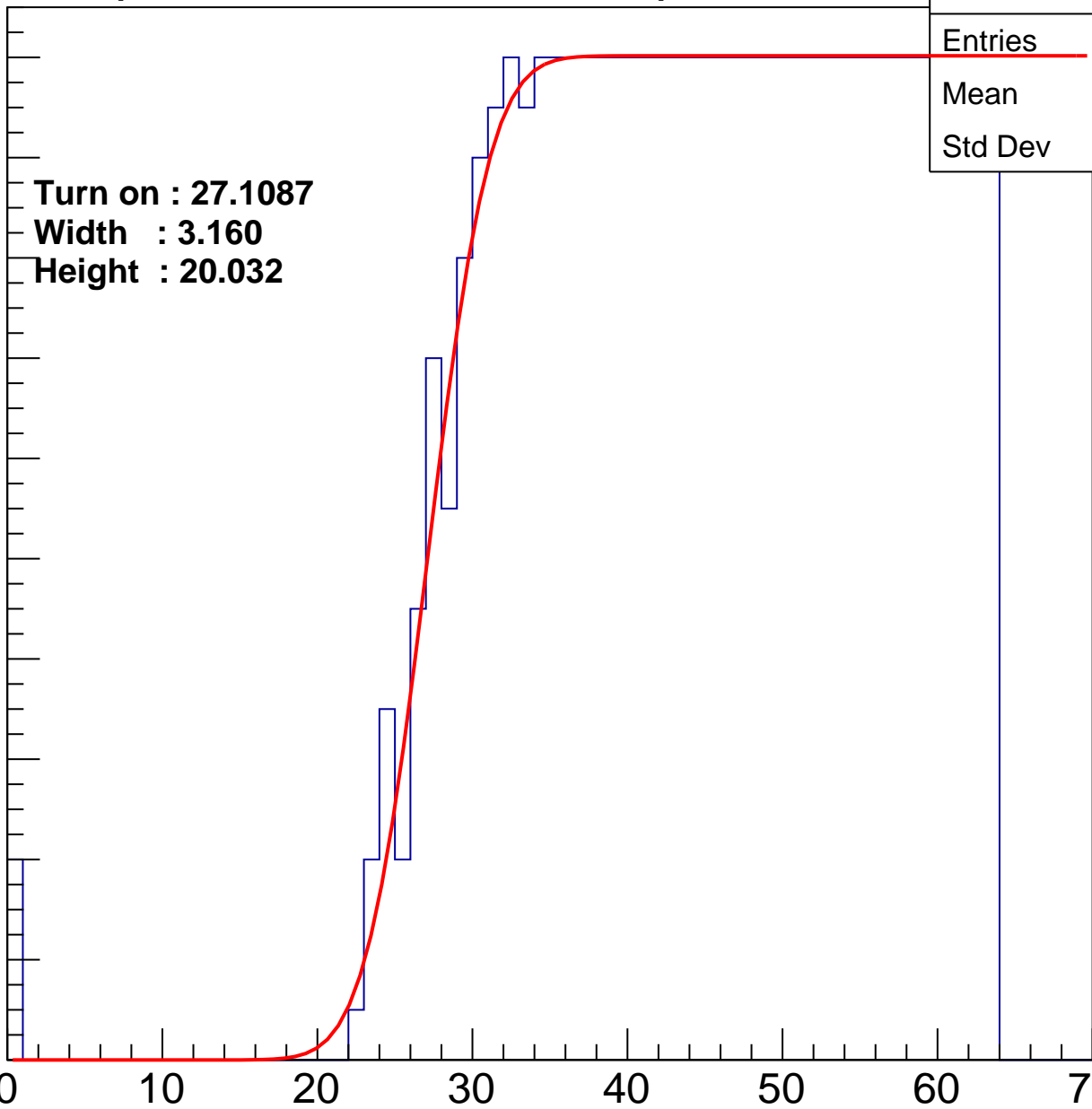
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1087
Width : 3.160
Height : 20.032

Entries	746
Mean	44.59
Std Dev	11.37

ampl

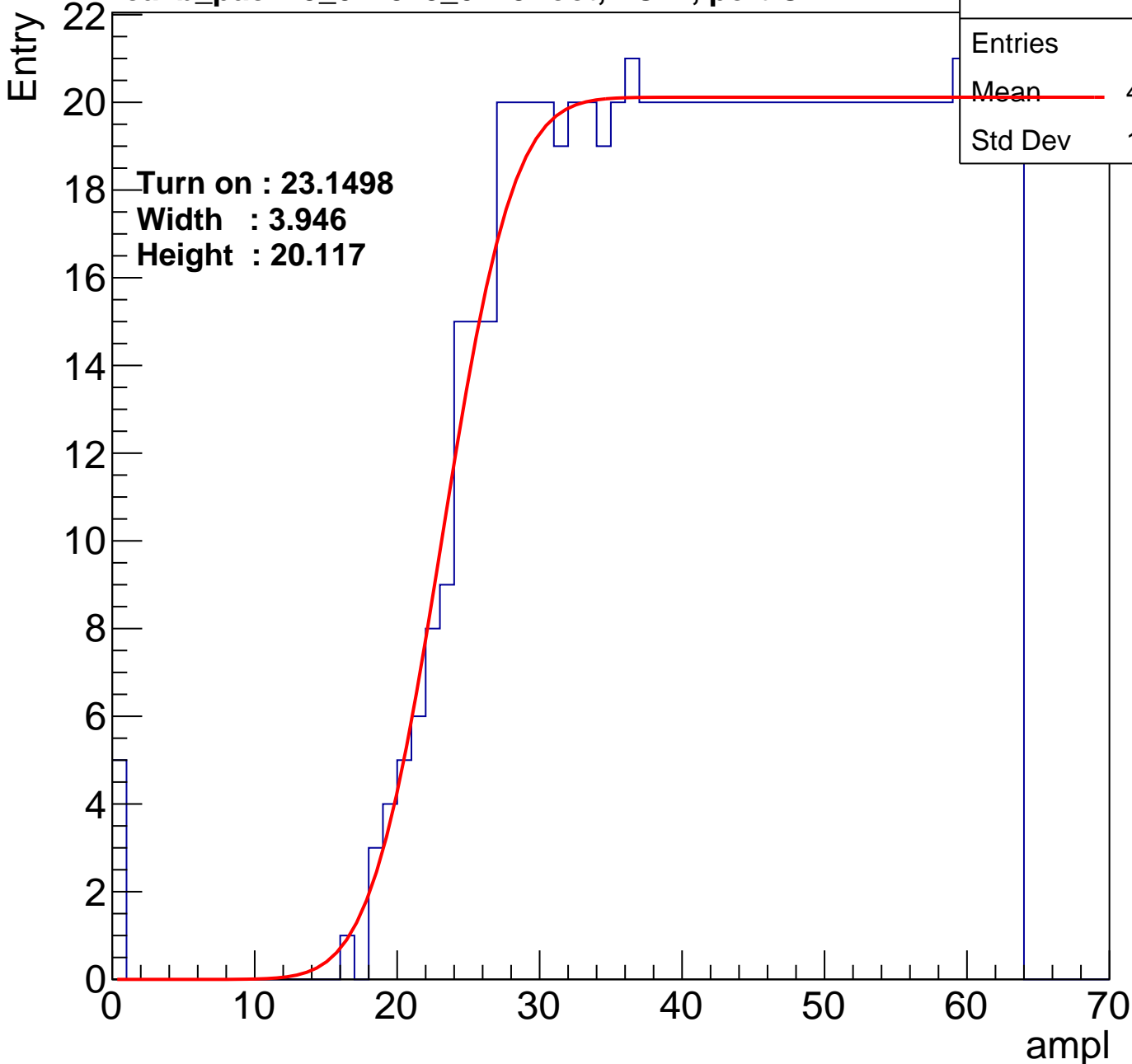


B0L101S, U14-ch54

calib_packv5_042523_0143.root, FC#1, port C1

Entries	826
Mean	42.63
Std Dev	12.48

Turn on : 23.1498
Width : 3.946
Height : 20.117



B0L101S, U14-ch55

calib_packv5_042523_0143.root, FC#1, port C1

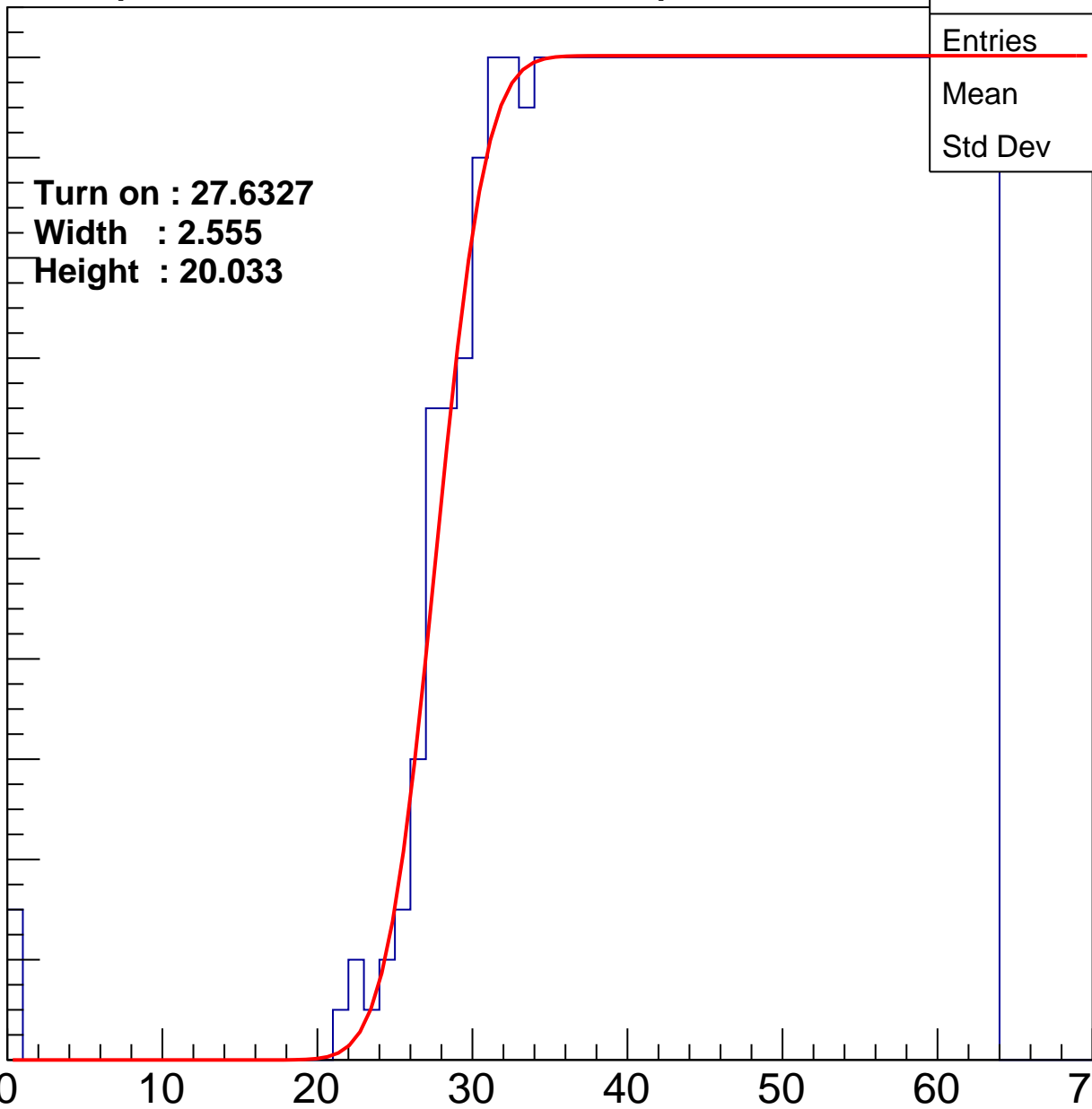
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.6327
Width : 2.555
Height : 20.033

Entries	735
Mean	44.92
Std Dev	11.09

ampl



B0L101S, U14-ch56

calib_packv5_042523_0143.root, FC#1, port C1

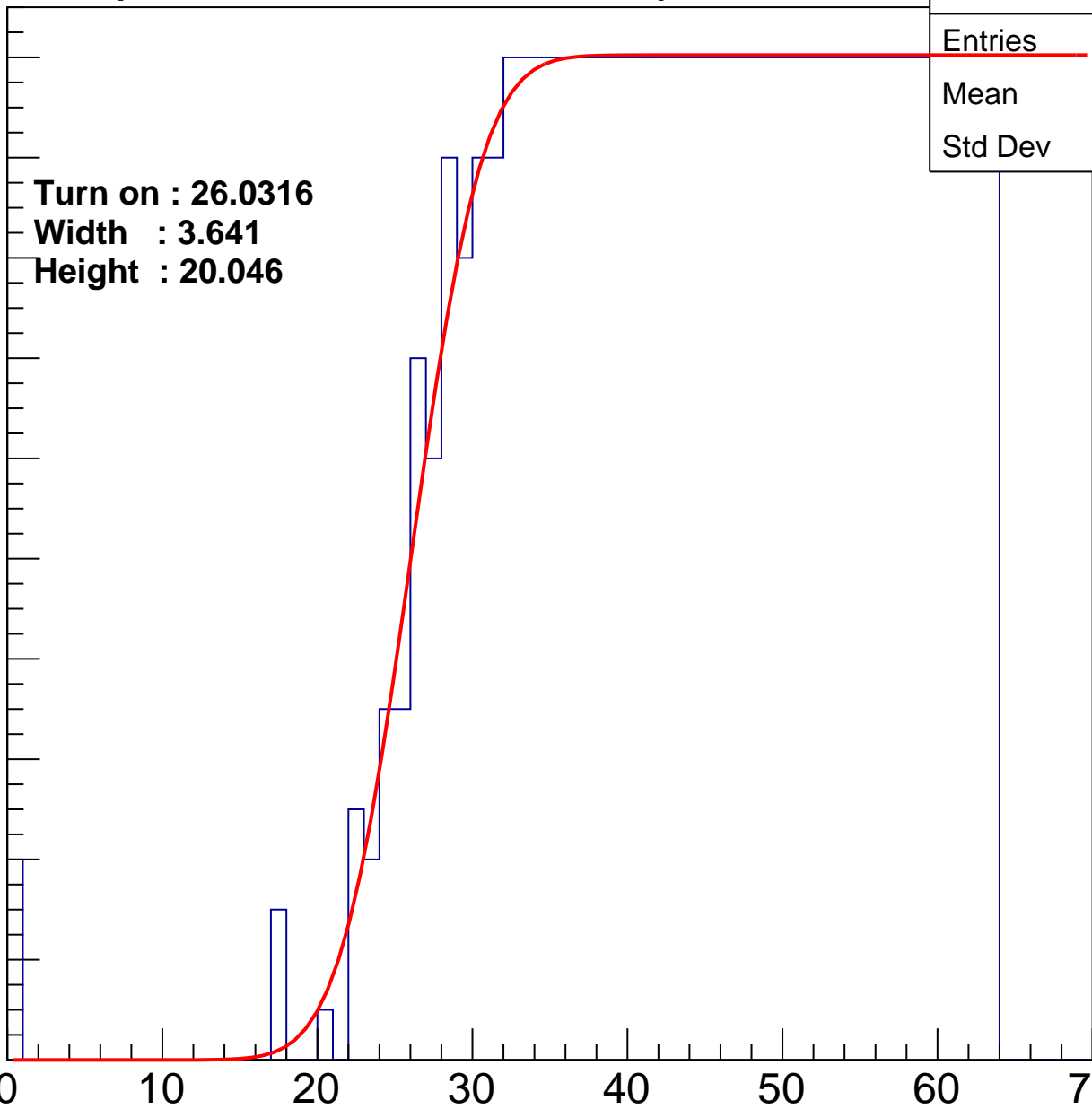
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0316
Width : 3.641
Height : 20.046

Entries	767
Mean	44.03
Std Dev	11.71

ampl



B0L101S, U14-ch57

calib_packv5_042523_0143.root, FC#1, port C1

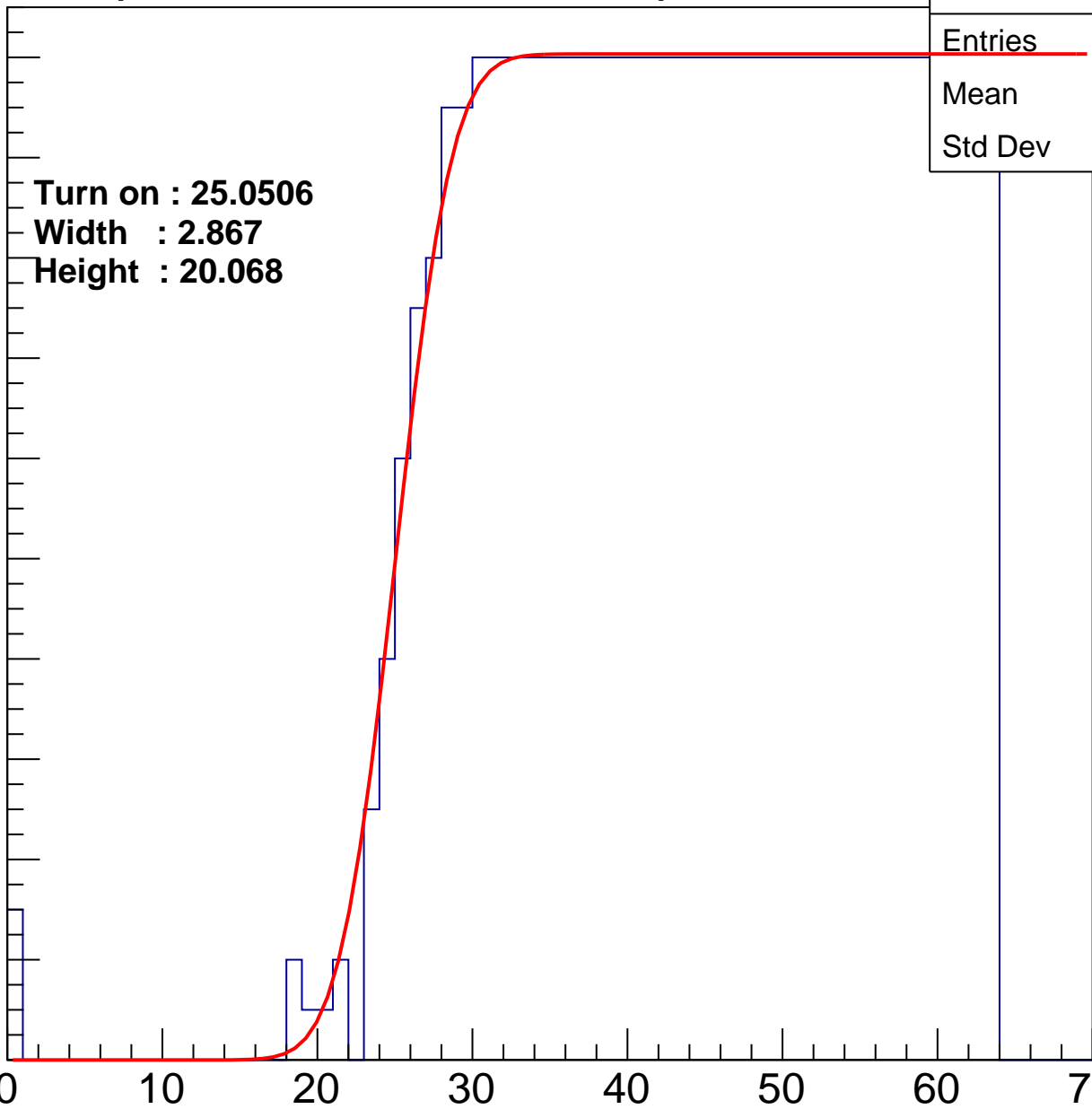
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.0506
Width : 2.867
Height : 20.068

Entries	783
Mean	43.74
Std Dev	11.71

ampl



B0L101S, U14-ch58

calib_packv5_042523_0143.root, FC#1, port C1

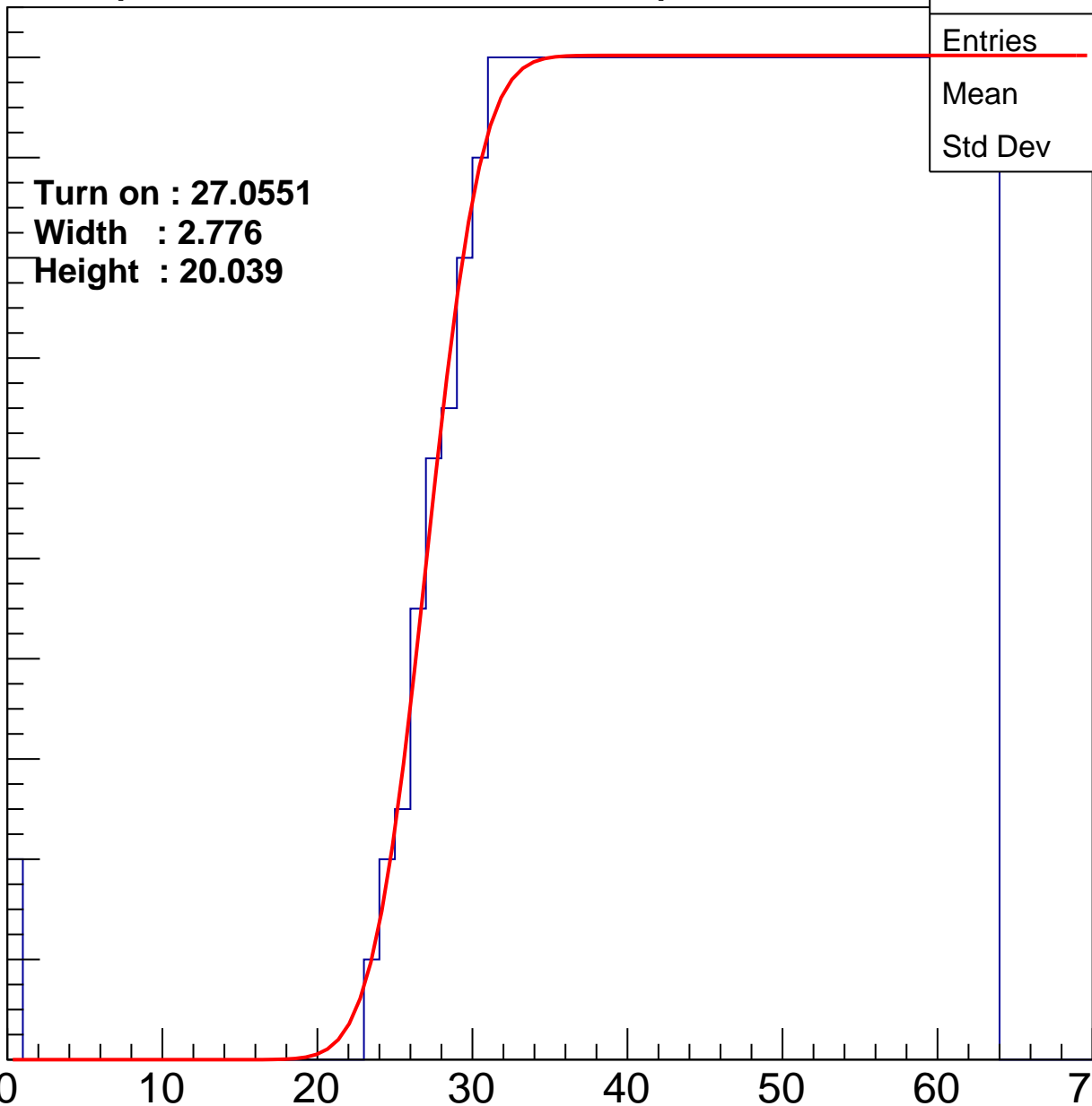
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0551
Width : 2.776
Height : 20.039

Entries	743
Mean	44.7
Std Dev	11.26

ampl



B0L101S, U14-ch59

calib_packv5_042523_0143.root, FC#1, port C1

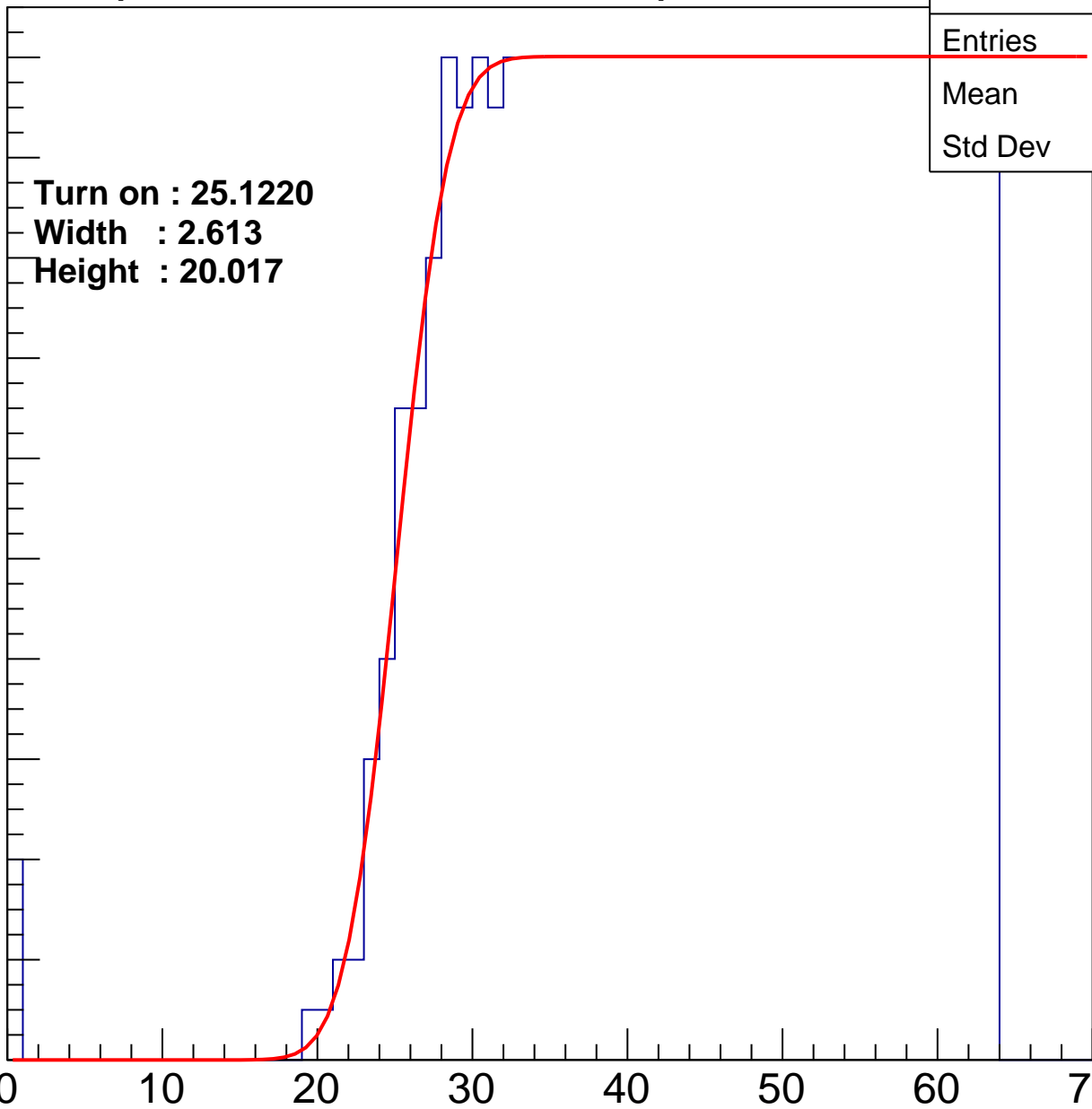
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.1220
Width : 2.613
Height : 20.017

Entries	784
Mean	43.69
Std Dev	11.8

ampl



B0L101S, U14-ch60

calib_packv5_042523_0143.root, FC#1, port C1

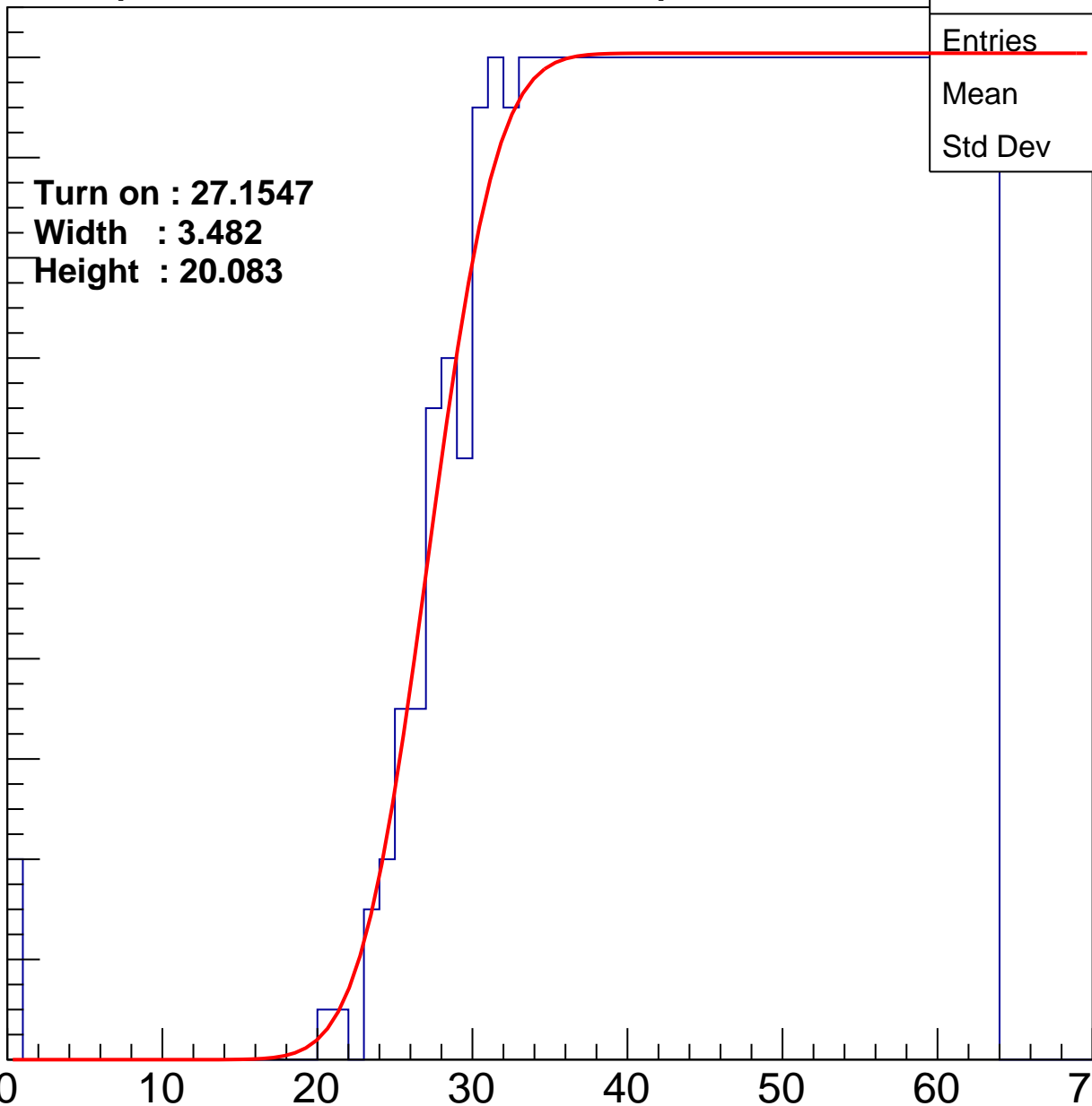
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.1547
Width : 3.482
Height : 20.083

Entries	744
Mean	44.64
Std Dev	11.34

ampl



B0L101S, U14-ch61

calib_packv5_042523_0143.root, FC#1, port C1

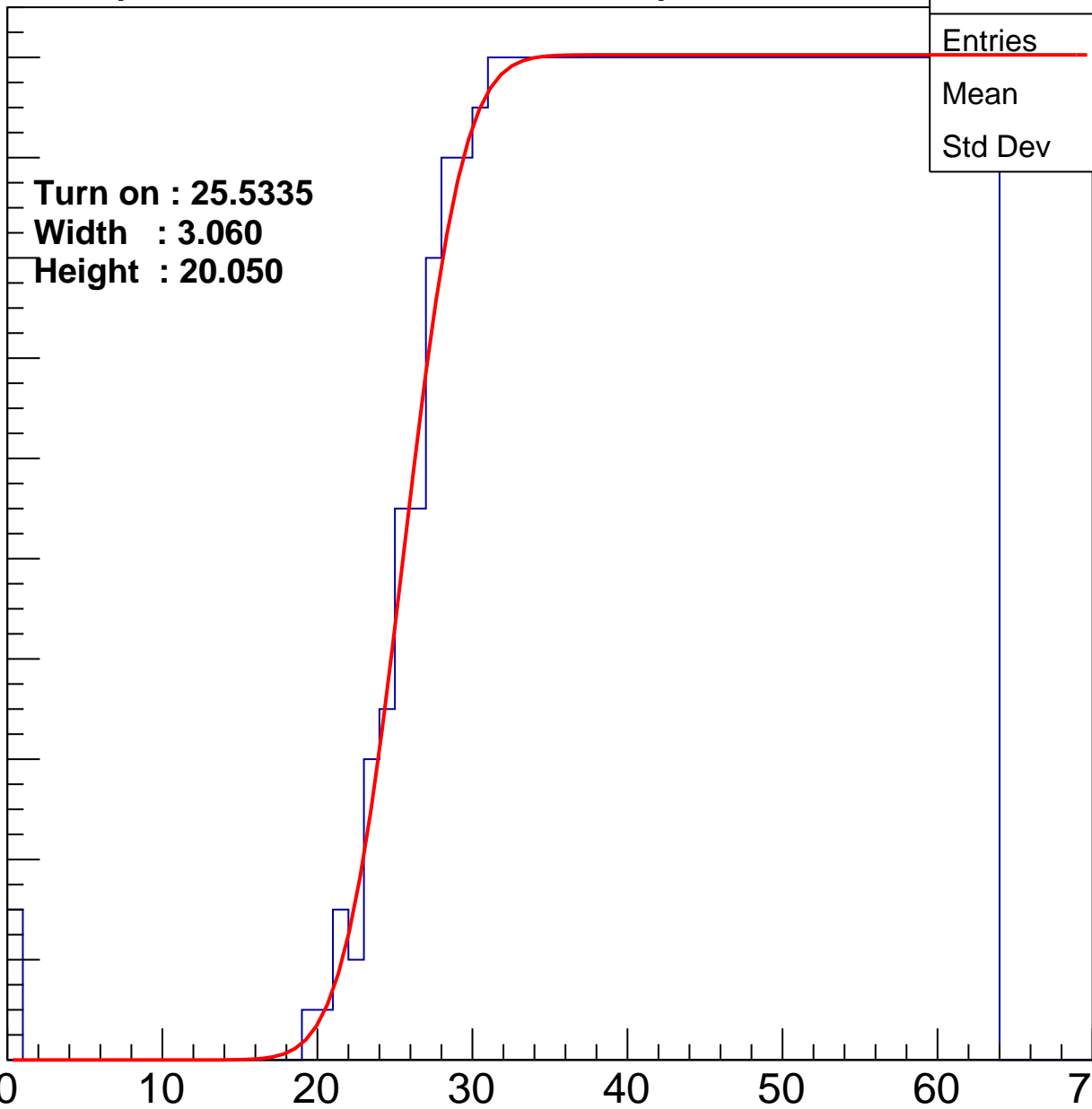
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5335
Width : 3.060
Height : 20.050

Entries	776
Mean	43.89
Std Dev	11.65

ampl



B0L101S, U14-ch62

calib_packv5_042523_0143.root, FC#1, port C1

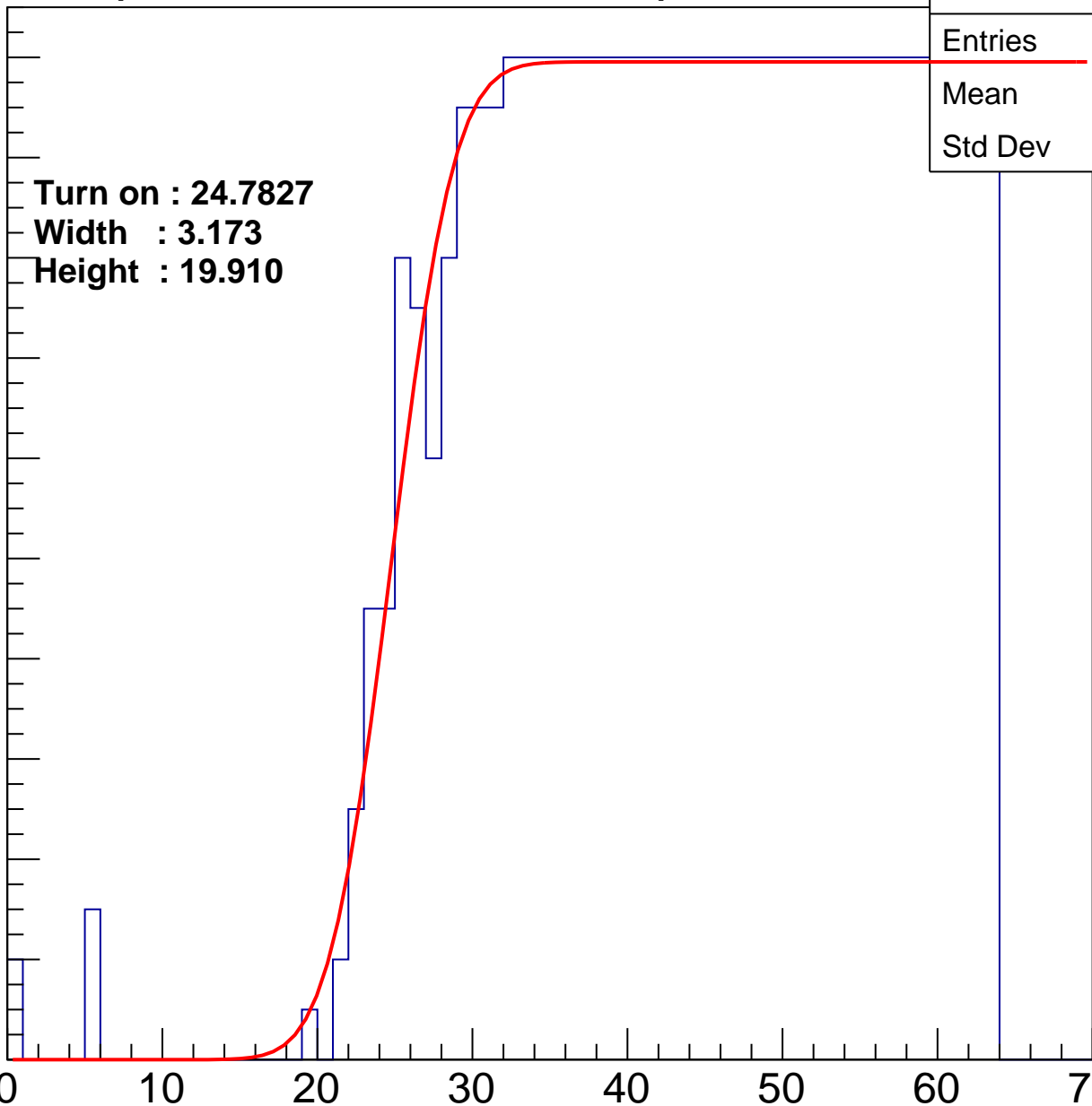
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.7827
Width : 3.173
Height : 19.910

Entries	787
Mean	43.56
Std Dev	11.92

ampl



B0L101S, U14-ch63

calib_packv5_042523_0143.root, FC#1, port C1

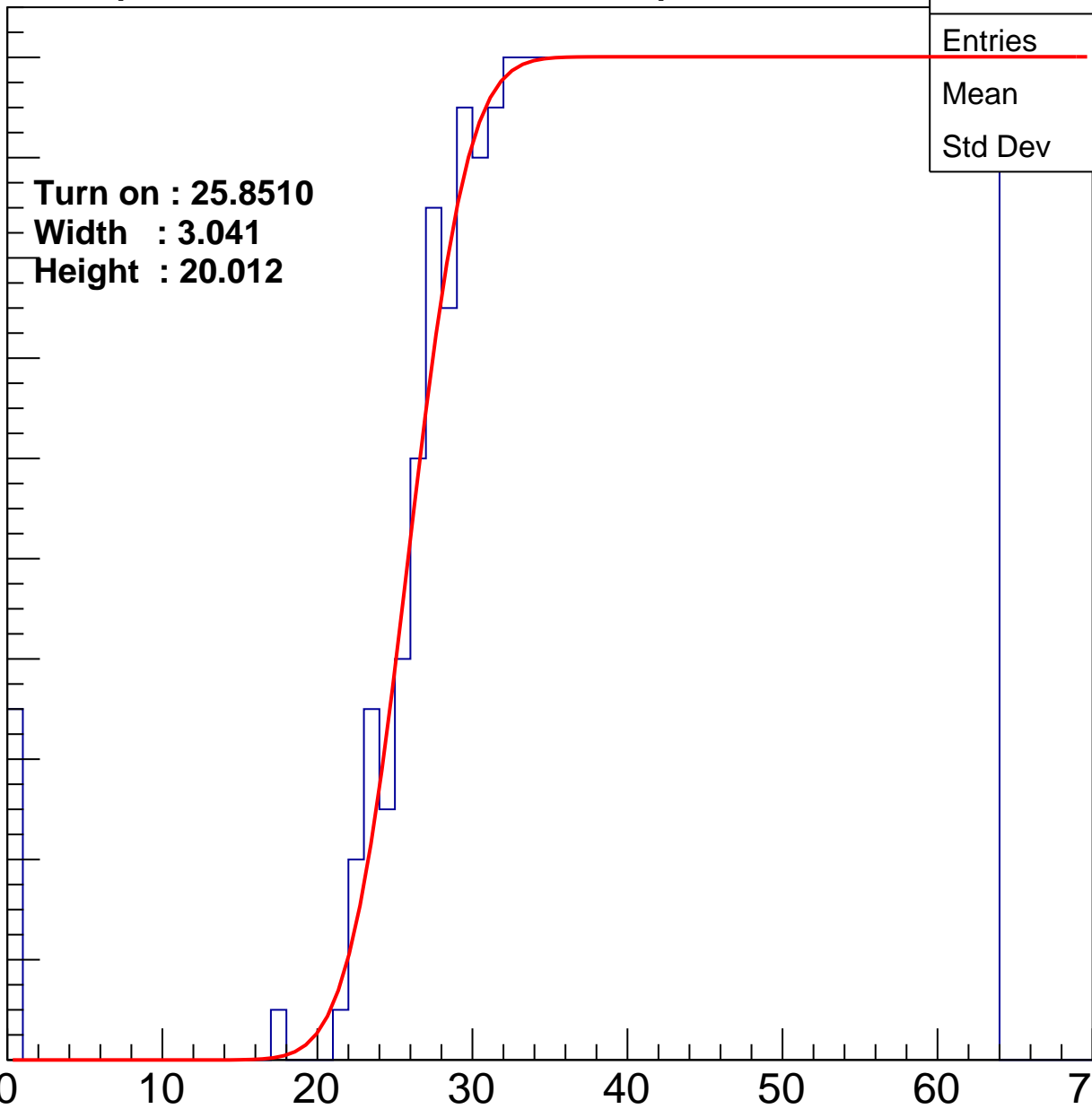
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8510
Width : 3.041
Height : 20.012

Entries	773
Mean	43.83
Std Dev	11.97

ampl



B0L101S, U14-ch64

calib_packv5_042523_0143.root, FC#1, port C1

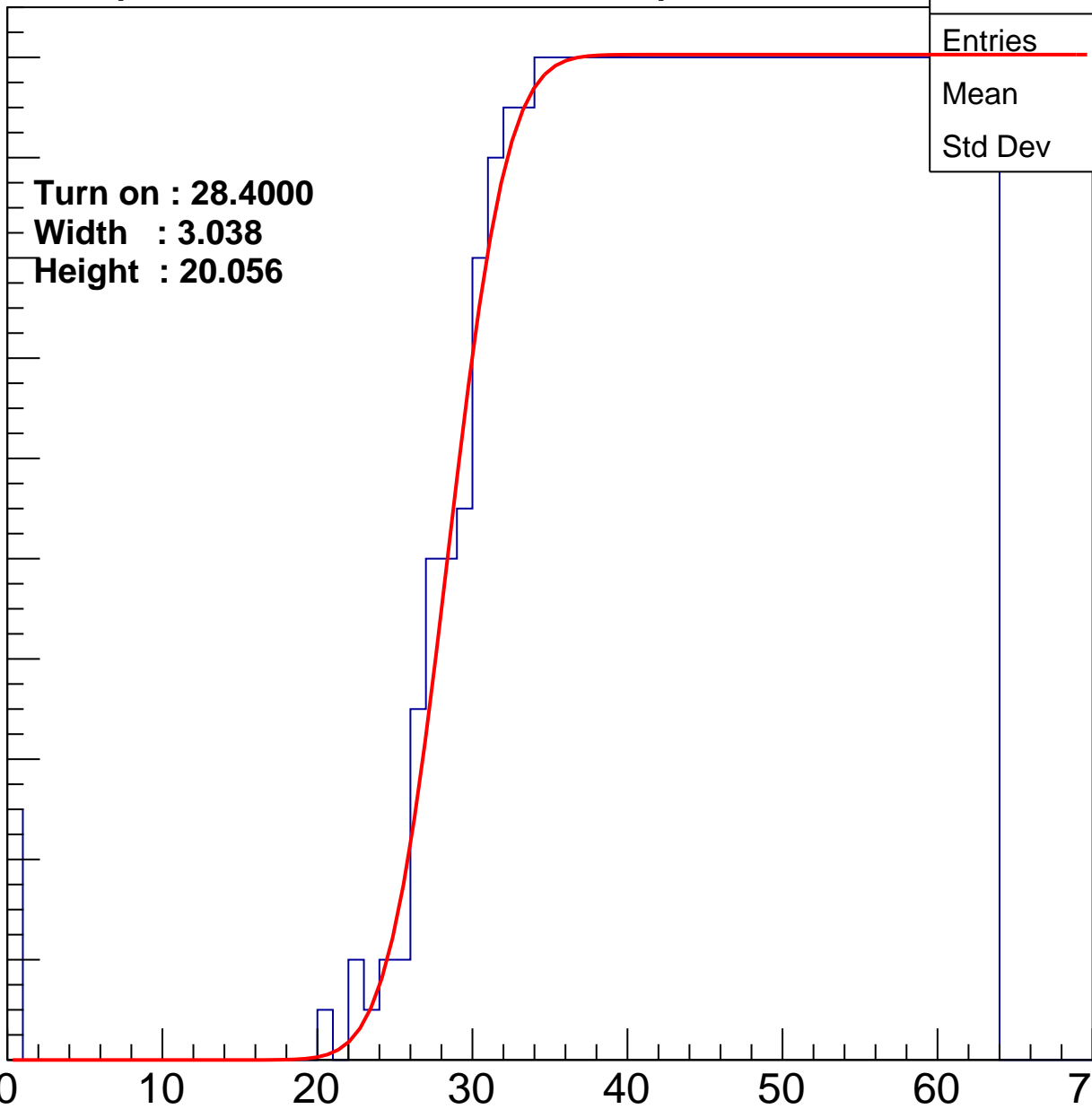
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.4000
Width : 3.038
Height : 20.056

Entries	723
Mean	45.1
Std Dev	11.21

ampl



B0L101S, U14-ch65

calib_packv5_042523_0143.root, FC#1, port C1

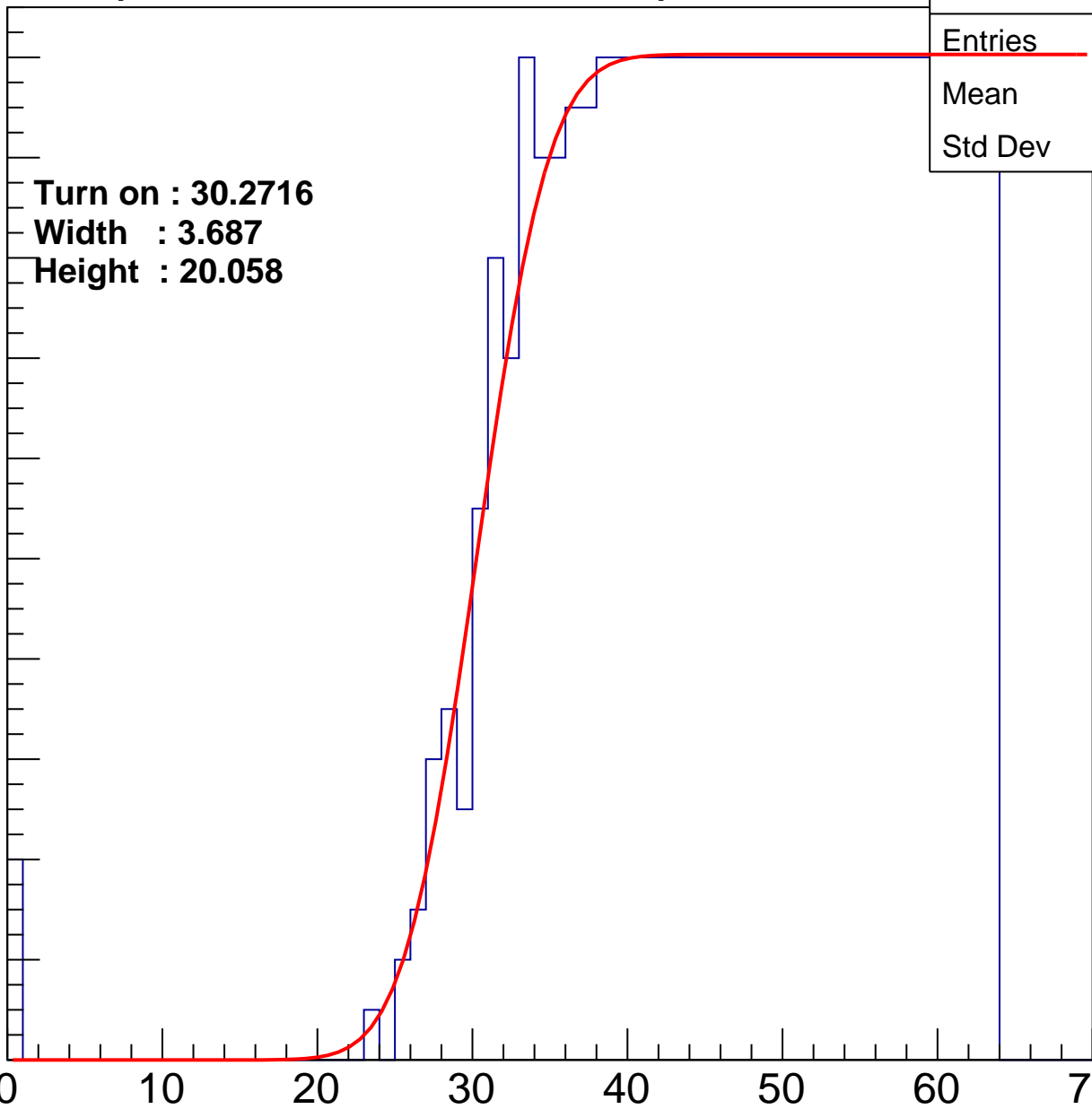
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 30.2716
Width : 3.687
Height : 20.058

Entries	683
Mean	46.09
Std Dev	10.65

ampl



B0L101S, U14-ch66

calib_packv5_042523_0143.root, FC#1, port C1

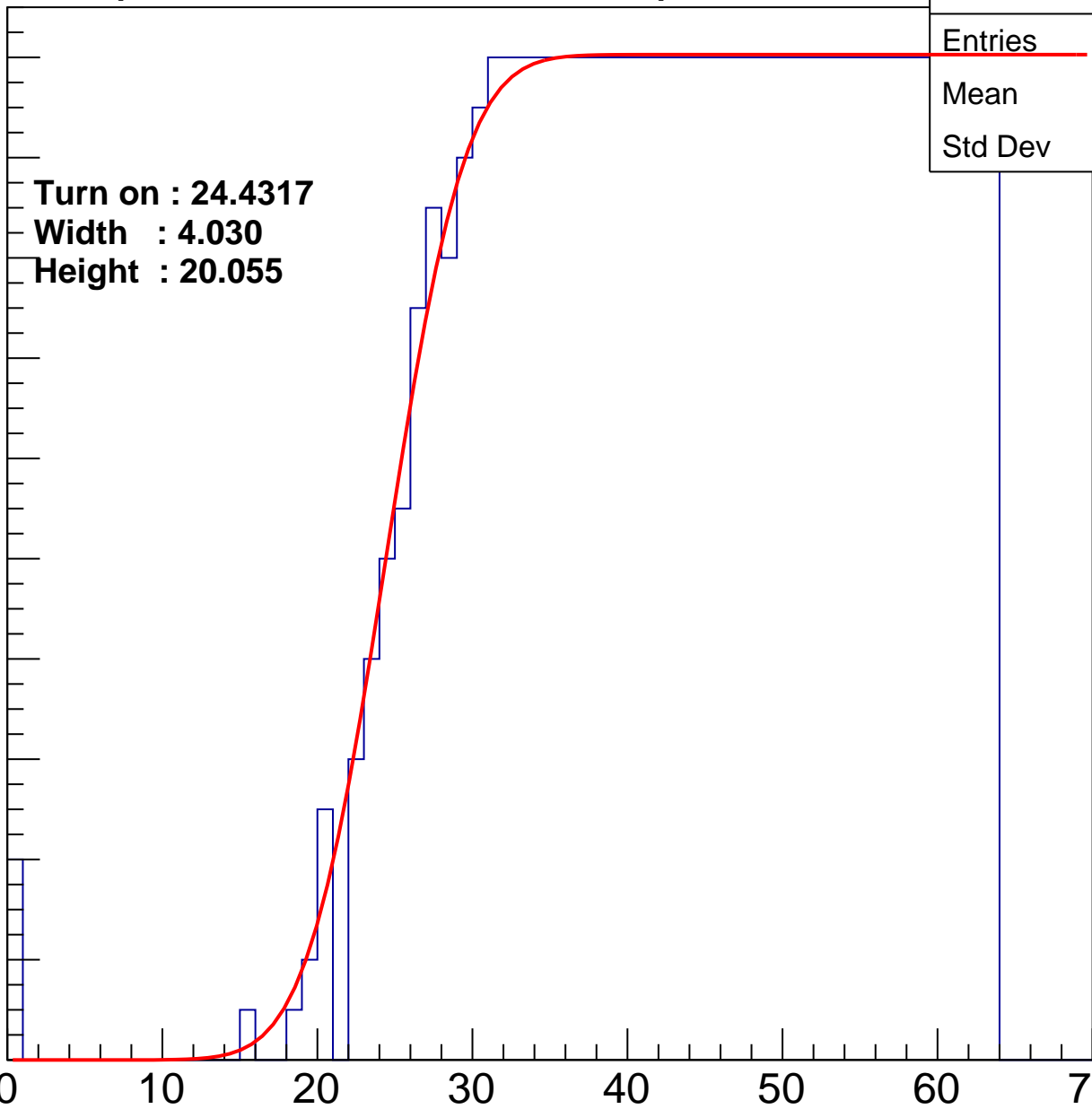
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4317
Width : 4.030
Height : 20.055

Entries	793
Mean	43.39
Std Dev	12.04

ampl



B0L101S, U14-ch67

calib_packv5_042523_0143.root, FC#1, port C1

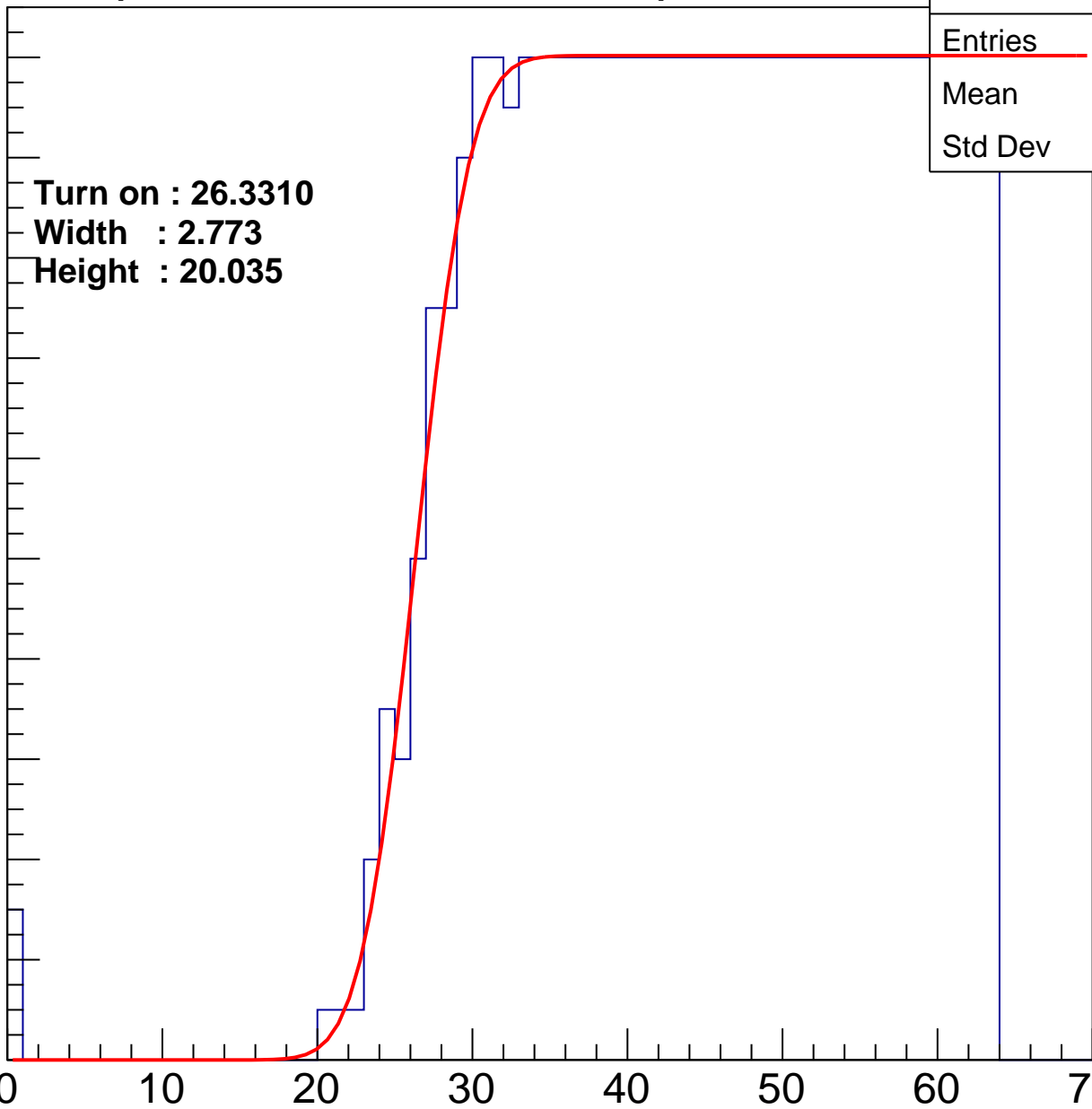
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3310
Width : 2.773
Height : 20.035

Entries	760
Mean	44.3
Std Dev	11.42

ampl



B0L101S, U14-ch68

calib_packv5_042523_0143.root, FC#1, port C1

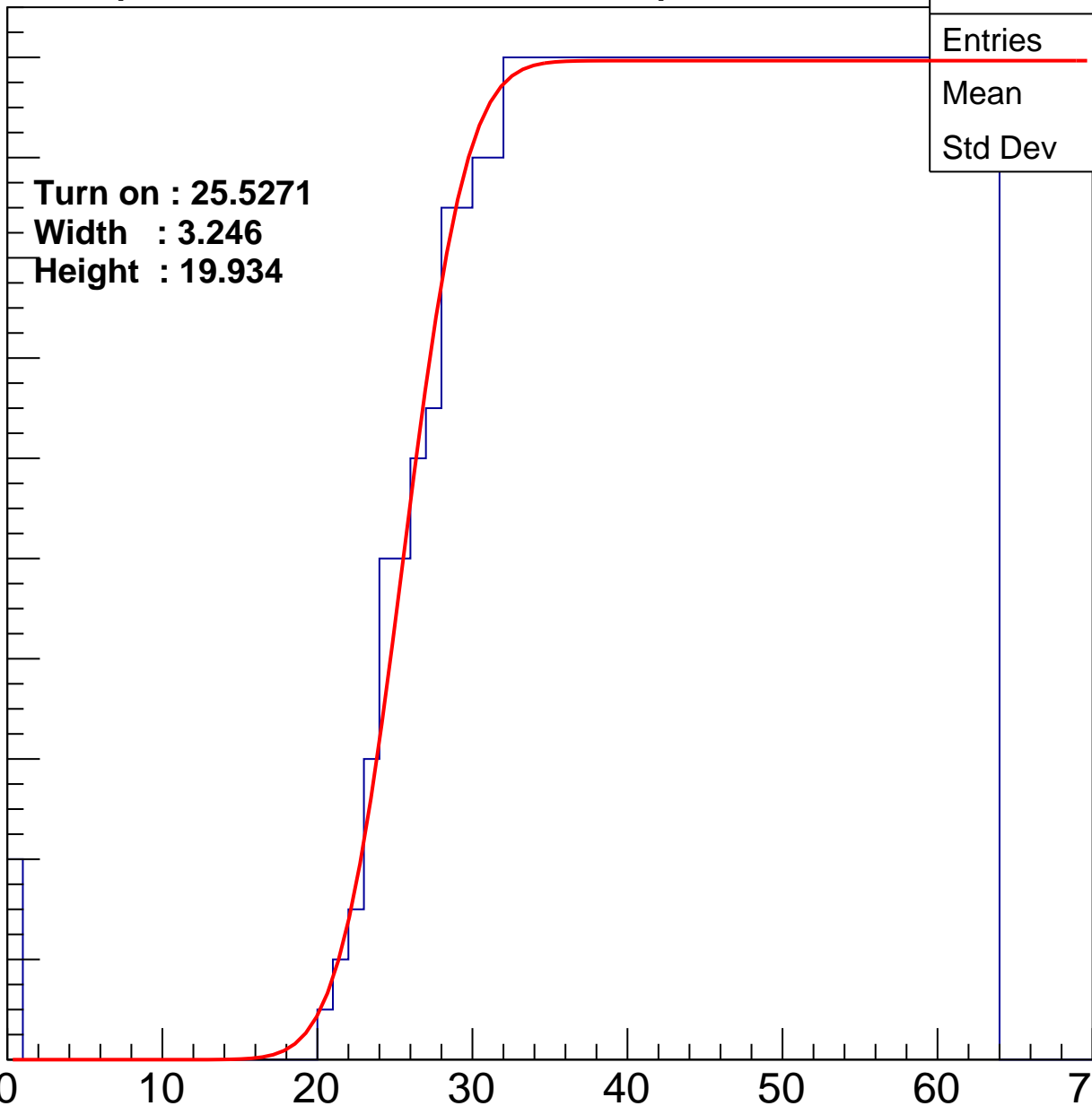
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5271
Width : 3.246
Height : 19.934

Entries	771
Mean	43.95
Std Dev	11.72

ampl



B0L101S, U14-ch69

calib_packv5_042523_0143.root, FC#1, port C1

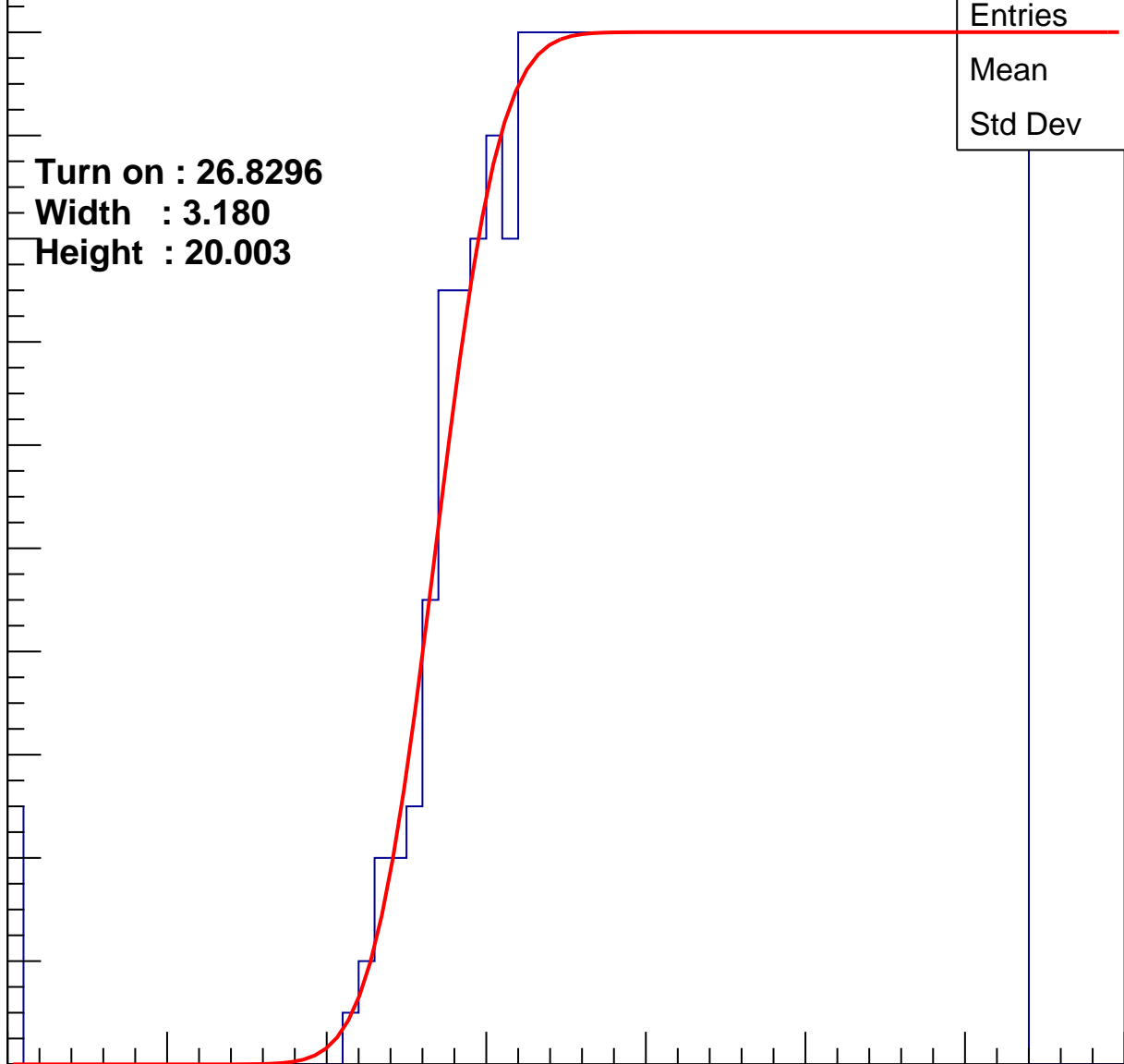
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8296
Width : 3.180
Height : 20.003

Entries	750
Mean	44.45
Std Dev	11.52

ampl



B0L101S, U14-ch70

calib_packv5_042523_0143.root, FC#1, port C1

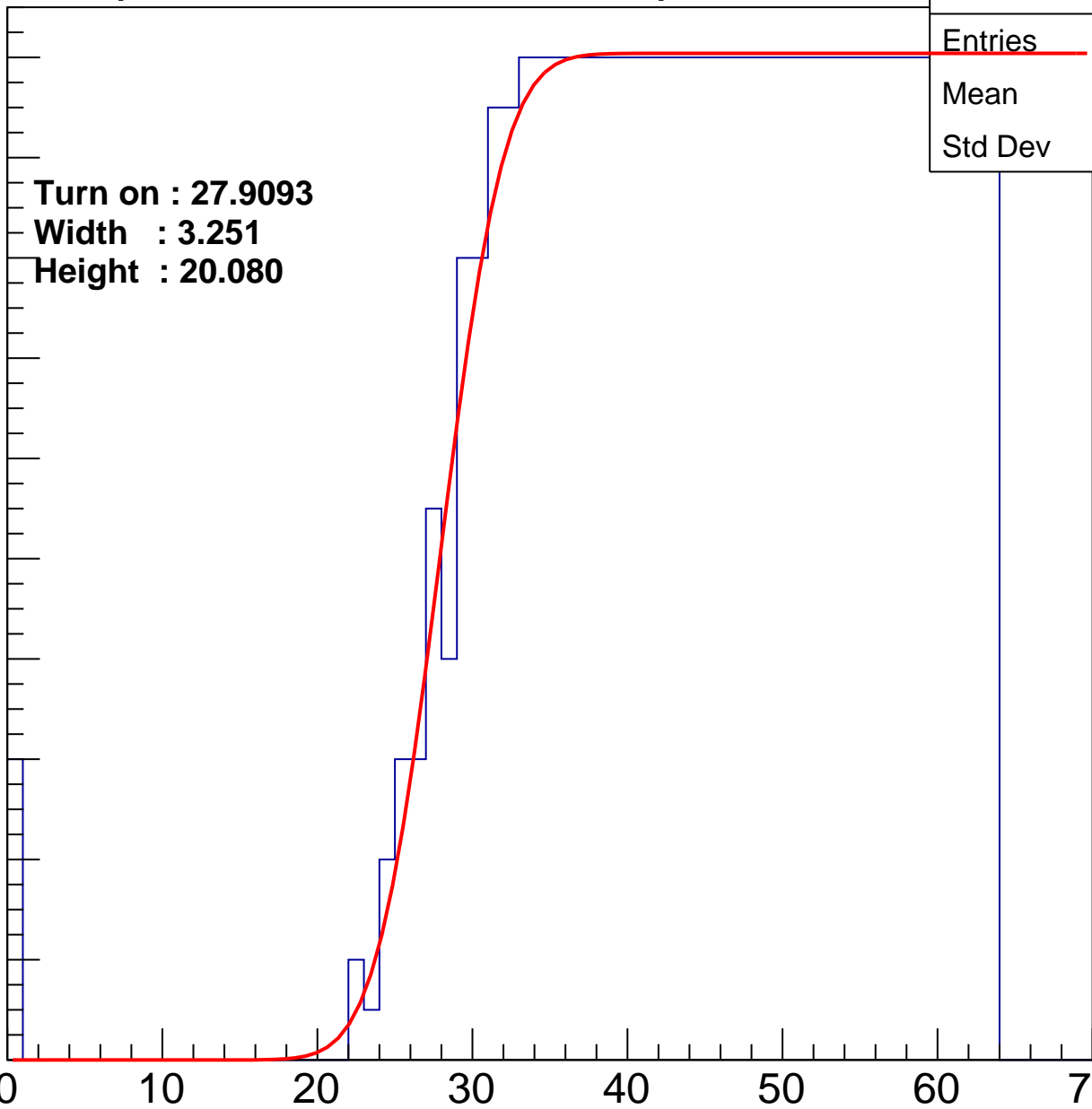
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9093
Width : 3.251
Height : 20.080

Entries	734
Mean	44.81
Std Dev	11.42

ampl



B0L101S, U14-ch71

calib_packv5_042523_0143.root, FC#1, port C1

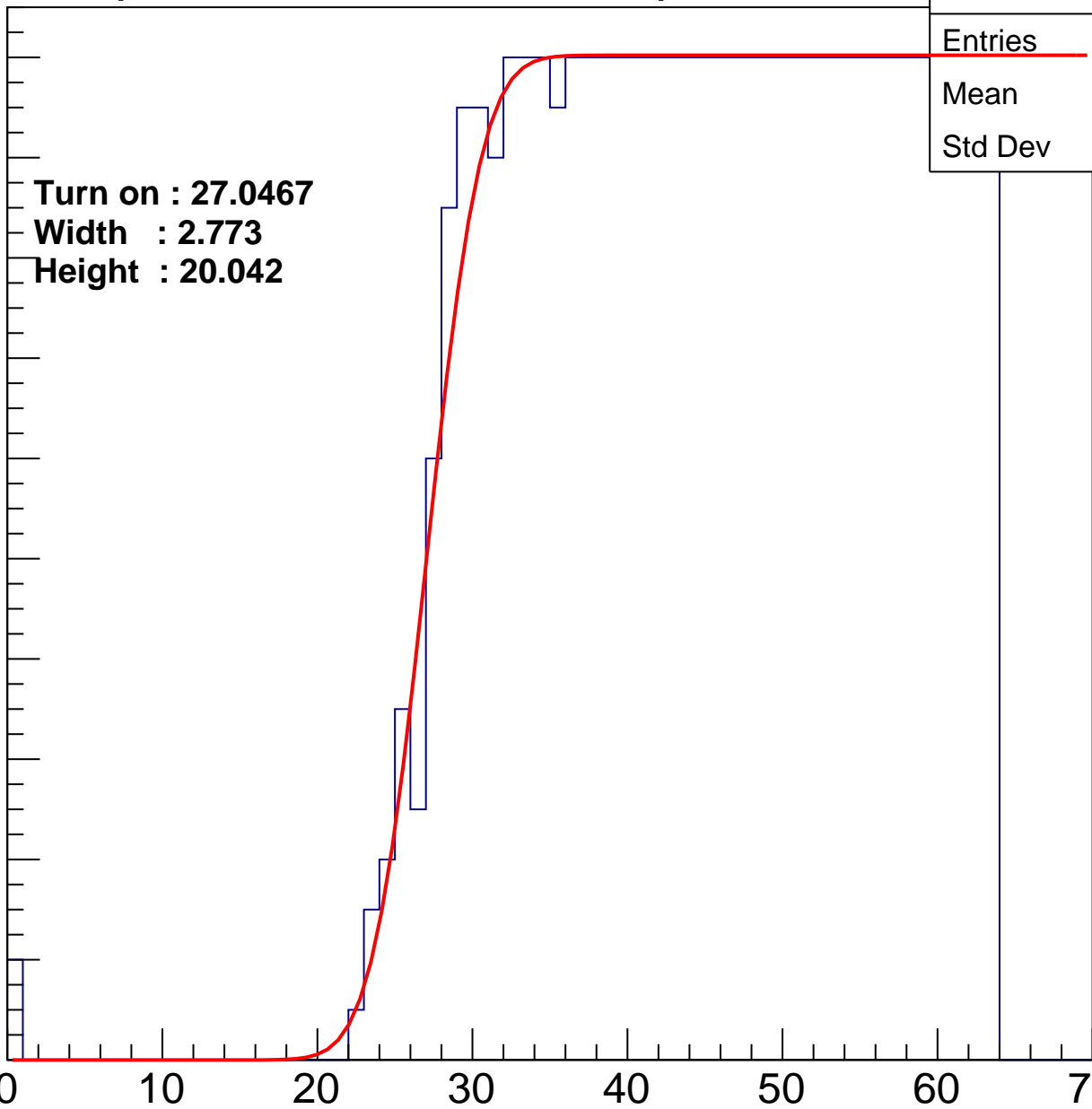
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0467
Width : 2.773
Height : 20.042

Entries	746
Mean	44.69
Std Dev	11.12

ampl



B0L101S, U14-ch72

calib_packv5_042523_0143.root, FC#1, port C1

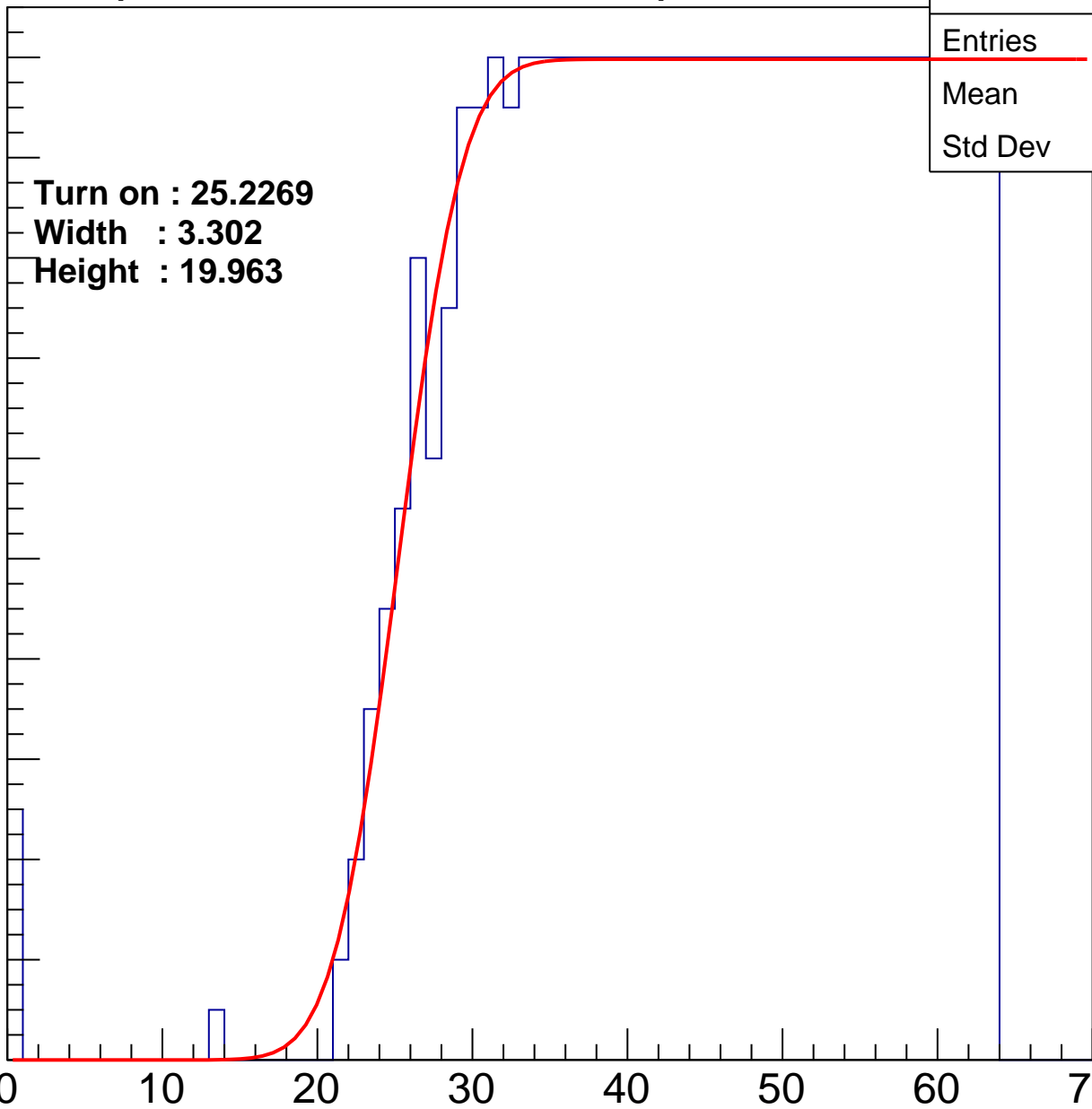
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2269
Width : 3.302
Height : 19.963

Entries	779
Mean	43.73
Std Dev	11.9

ampl



B0L101S, U14-ch73

calib_packv5_042523_0143.root, FC#1, port C1

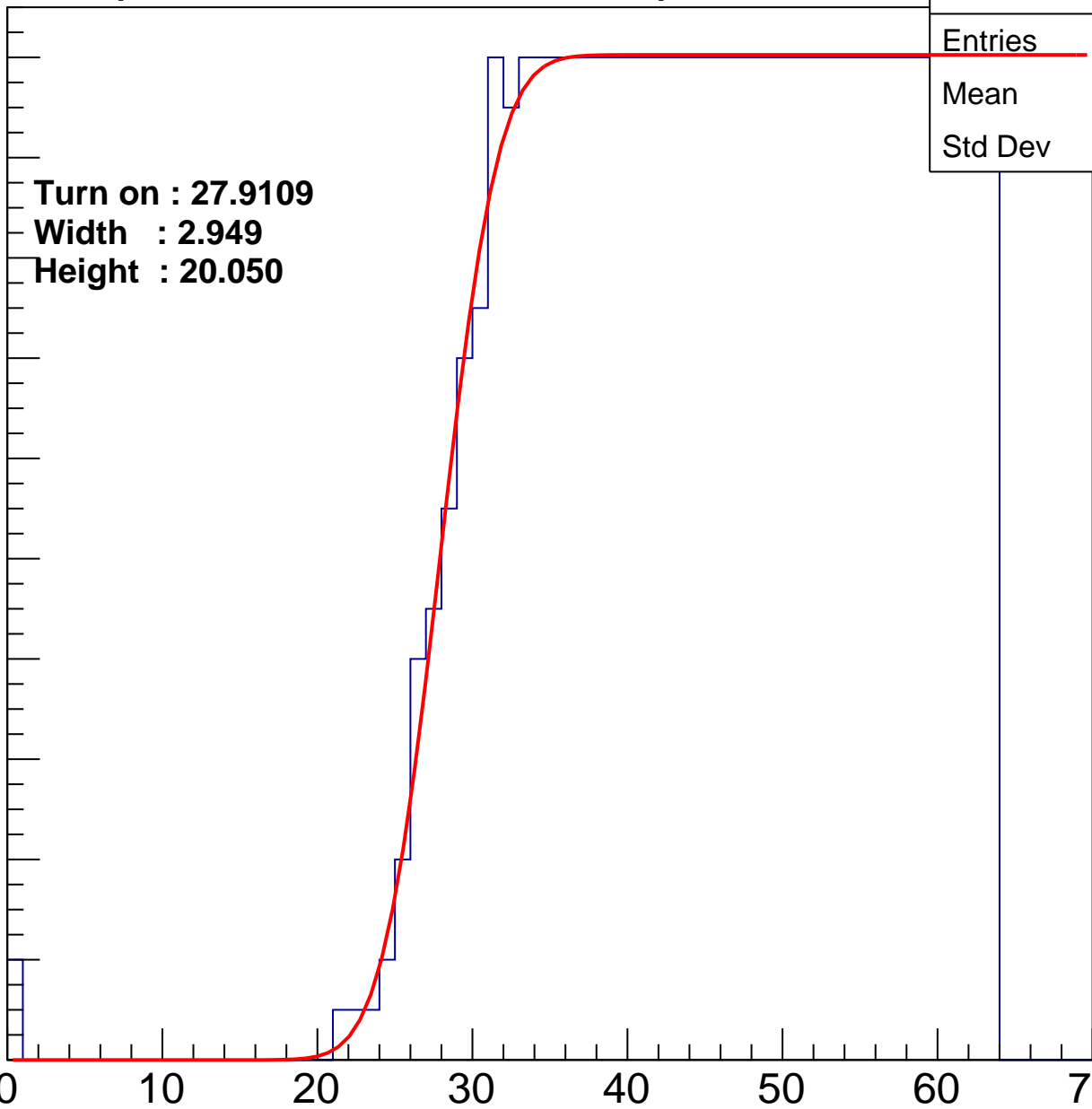
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.9109
Width : 2.949
Height : 20.050

Entries	727
Mean	45.14
Std Dev	10.9

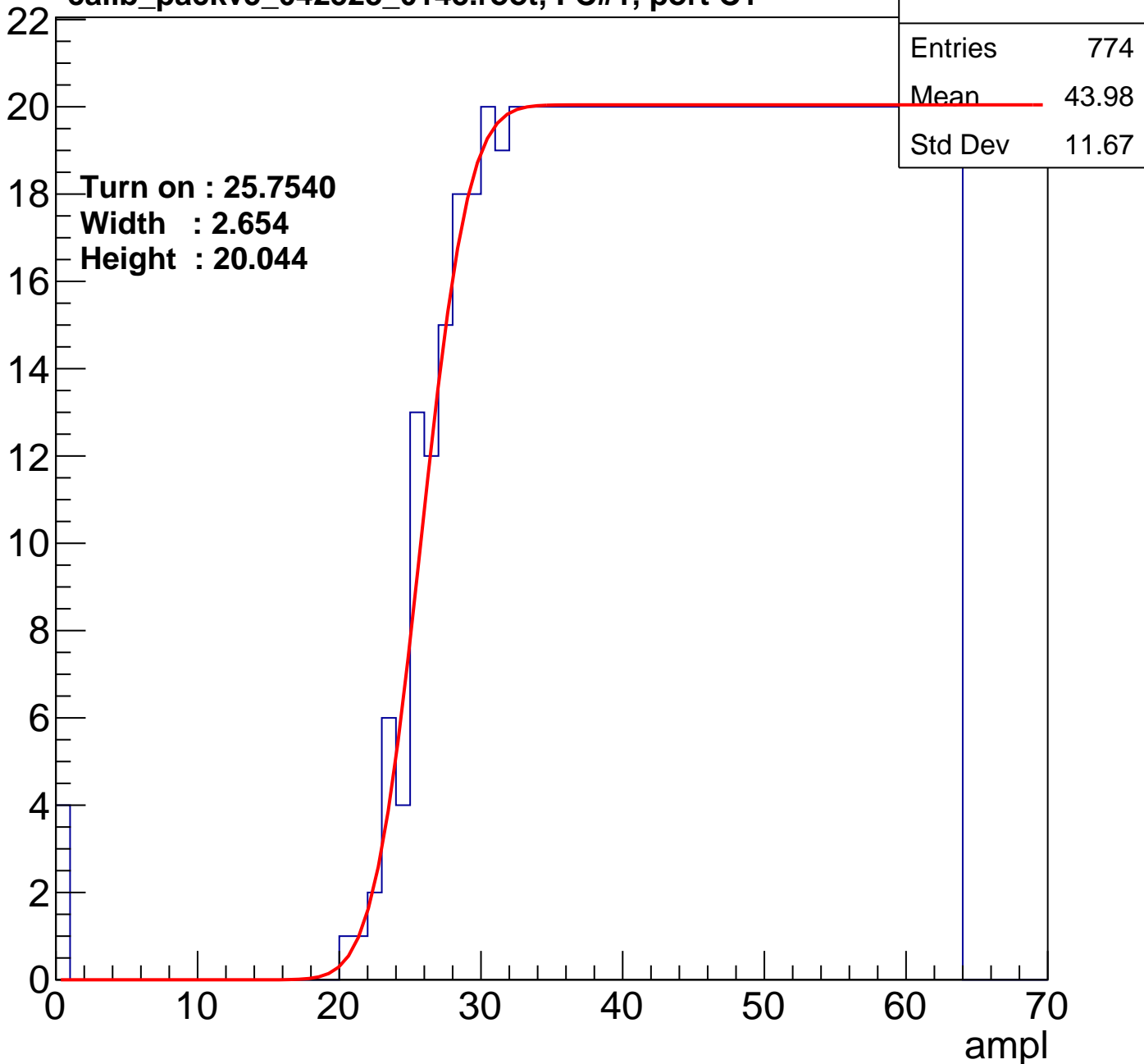
ampl



B0L101S, U14-ch74

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch75

calib_packv5_042523_0143.root, FC#1, port C1

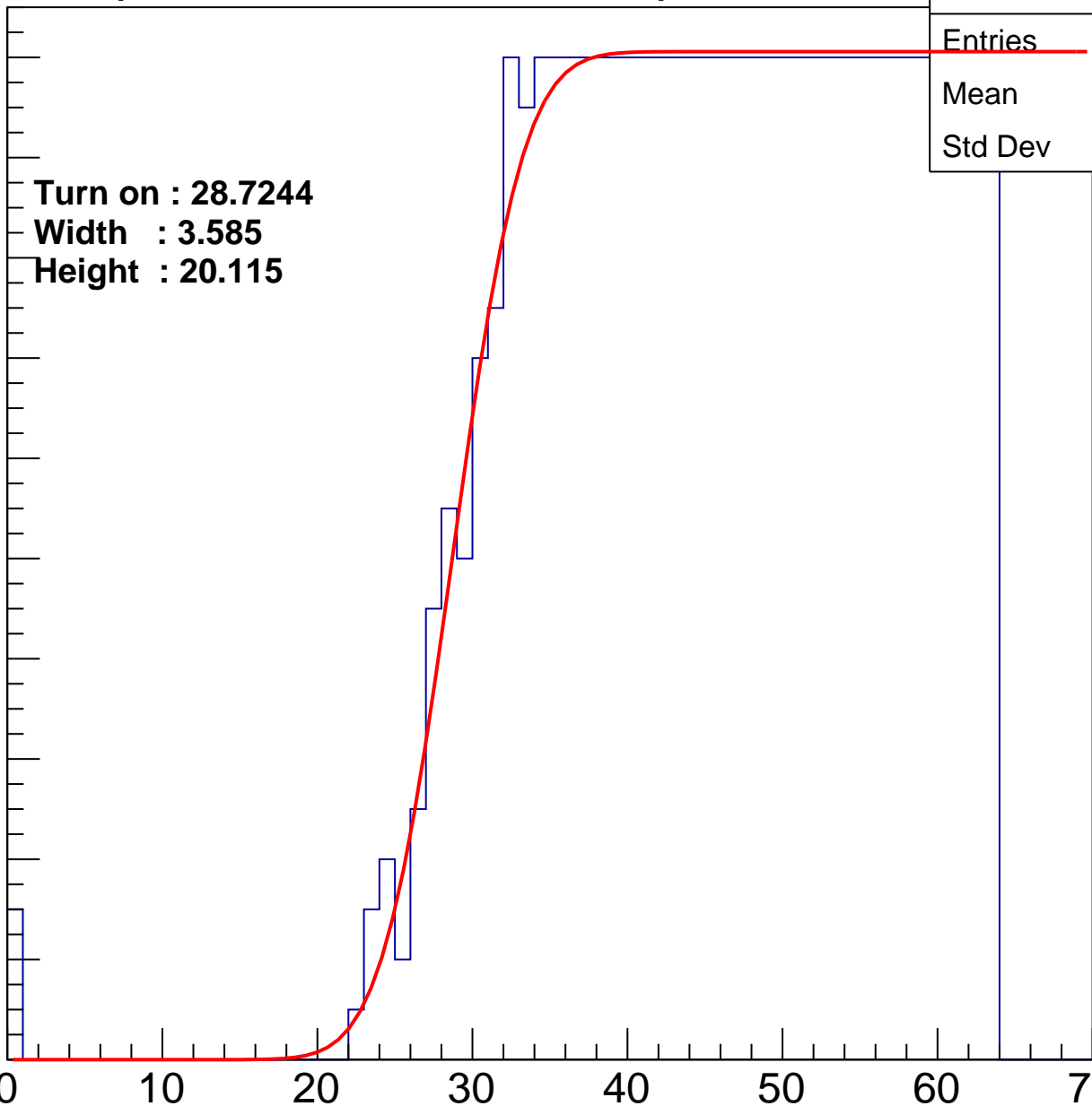
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 28.7244
Width : 3.585
Height : 20.115

Entries	716
Mean	45.34
Std Dev	10.93

ampl



B0L101S, U14-ch76

calib_packv5_042523_0143.root, FC#1, port C1

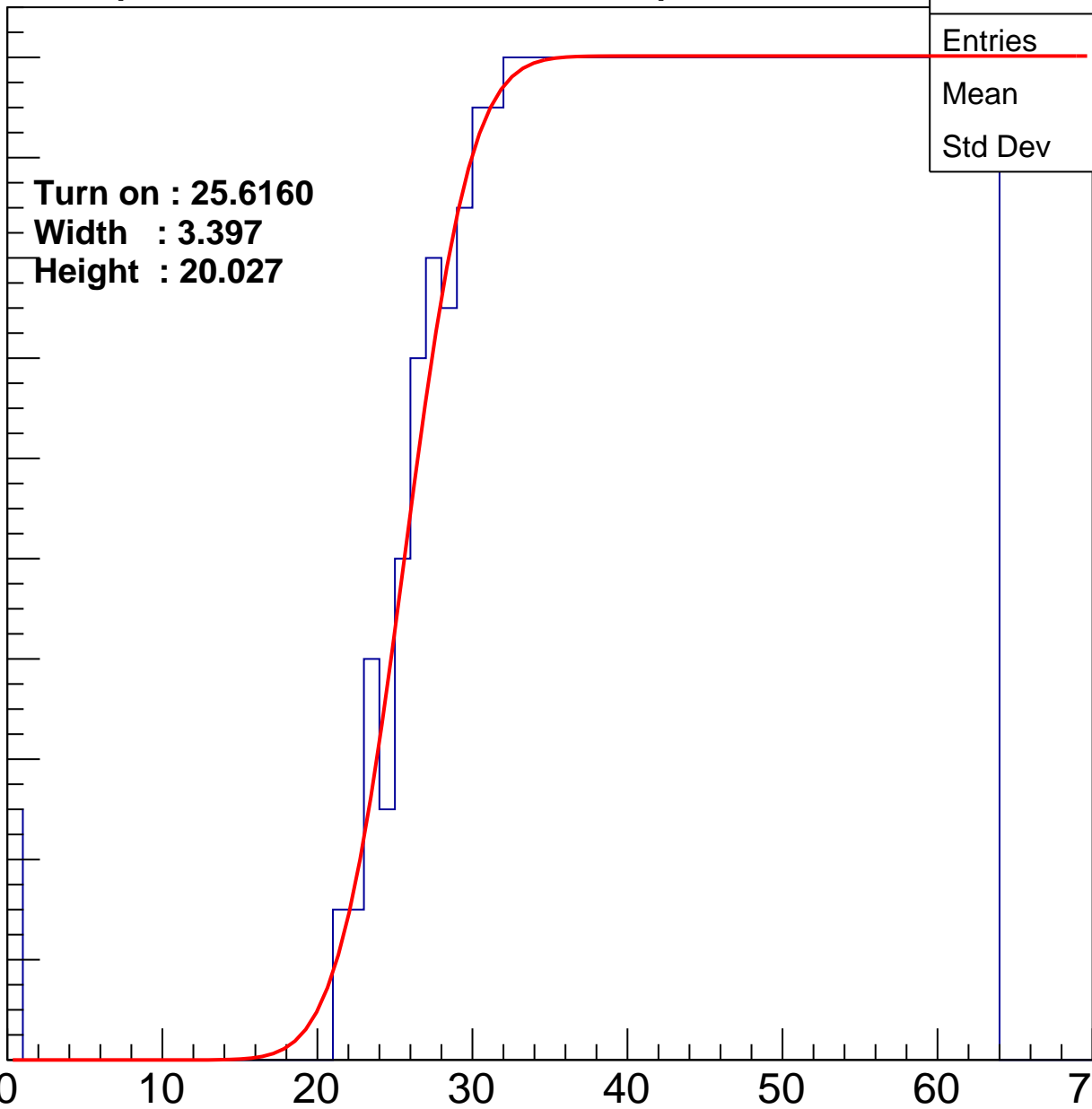
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6160
Width : 3.397
Height : 20.027

Entries	774
Mean	43.86
Std Dev	11.81

ampl



B0L101S, U14-ch77

calib_packv5_042523_0143.root, FC#1, port C1

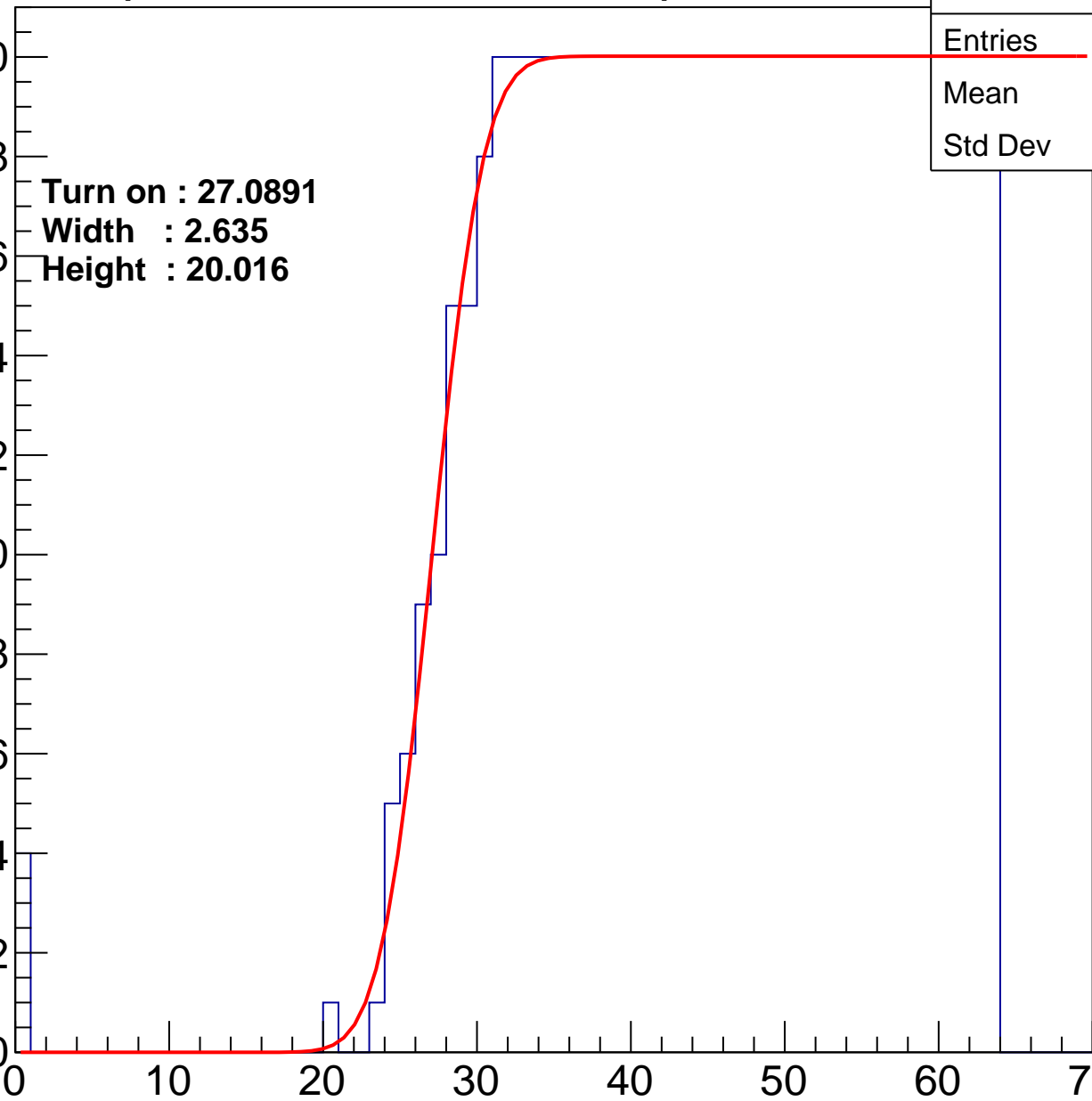
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0891
Width : 2.635
Height : 20.016

Entries	744
Mean	44.67
Std Dev	11.3

ampl



B0L101S, U14-ch78

calib_packv5_042523_0143.root, FC#1, port C1

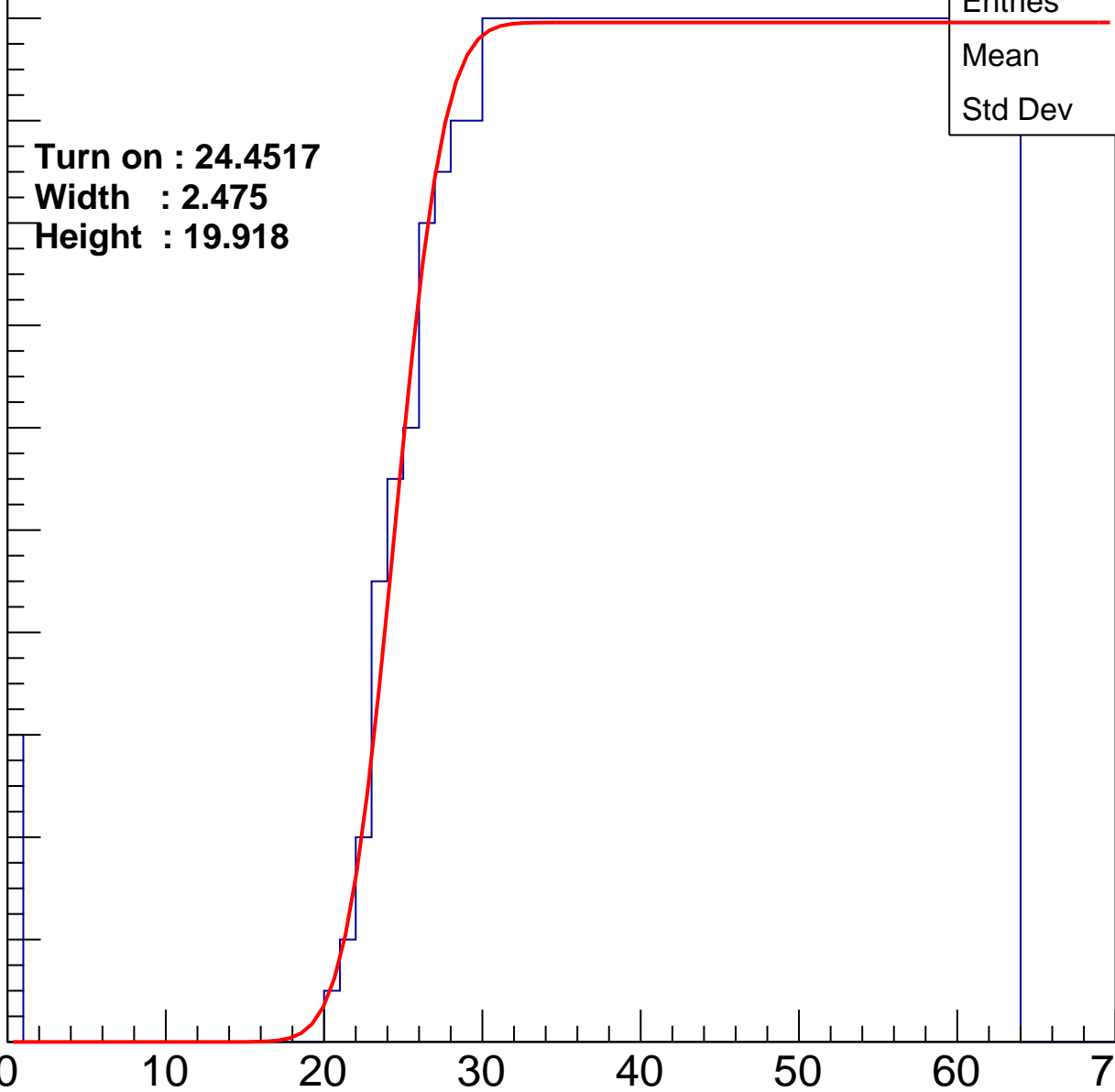
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4517
Width : 2.475
Height : 19.918

Entries	794
Mean	43.38
Std Dev	12.09

ampl



B0L101S, U14-ch79

calib_packv5_042523_0143.root, FC#1, port C1

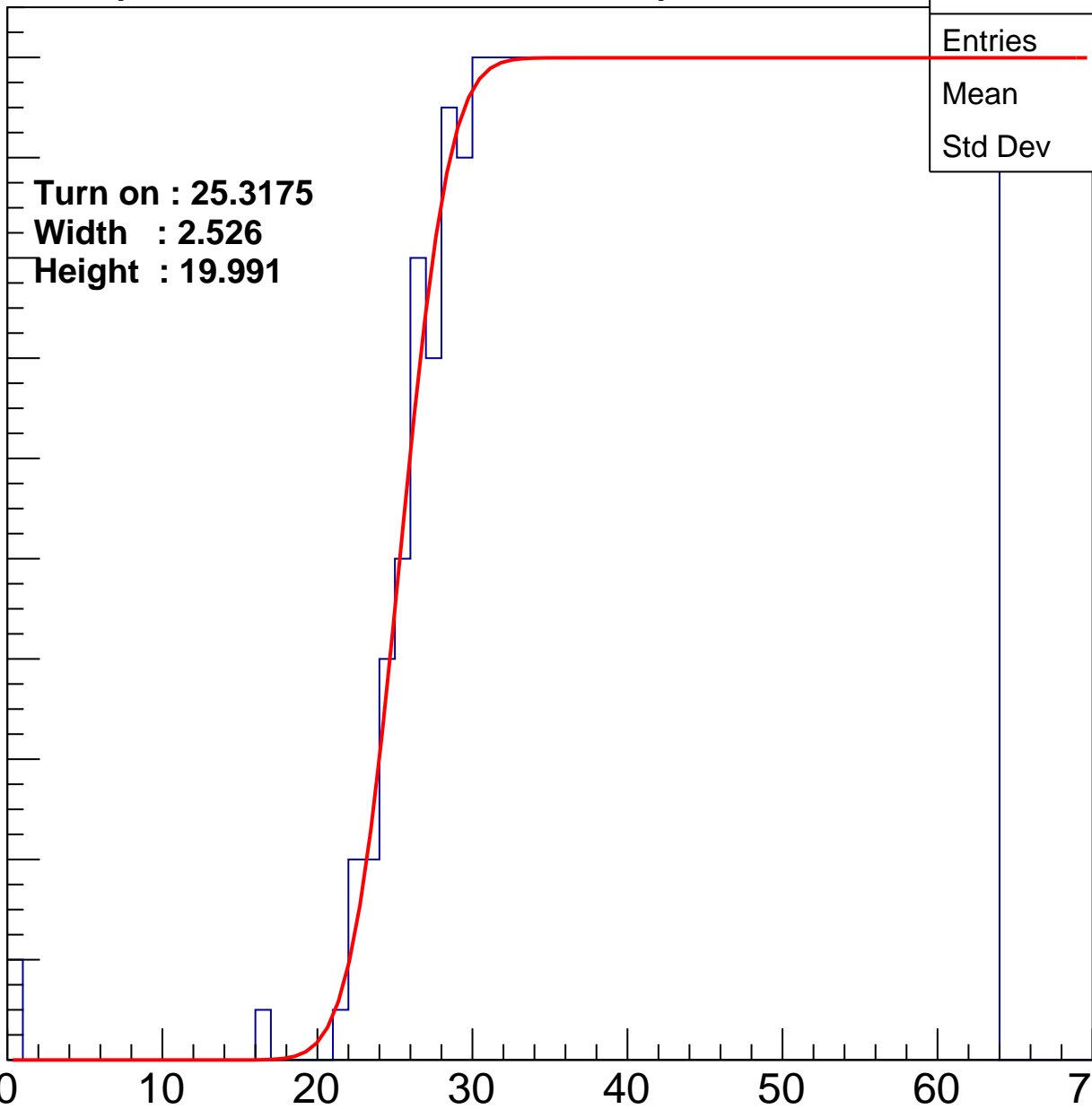
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3175
Width : 2.526
Height : 19.991

Entries	777
Mean	43.92
Std Dev	11.54

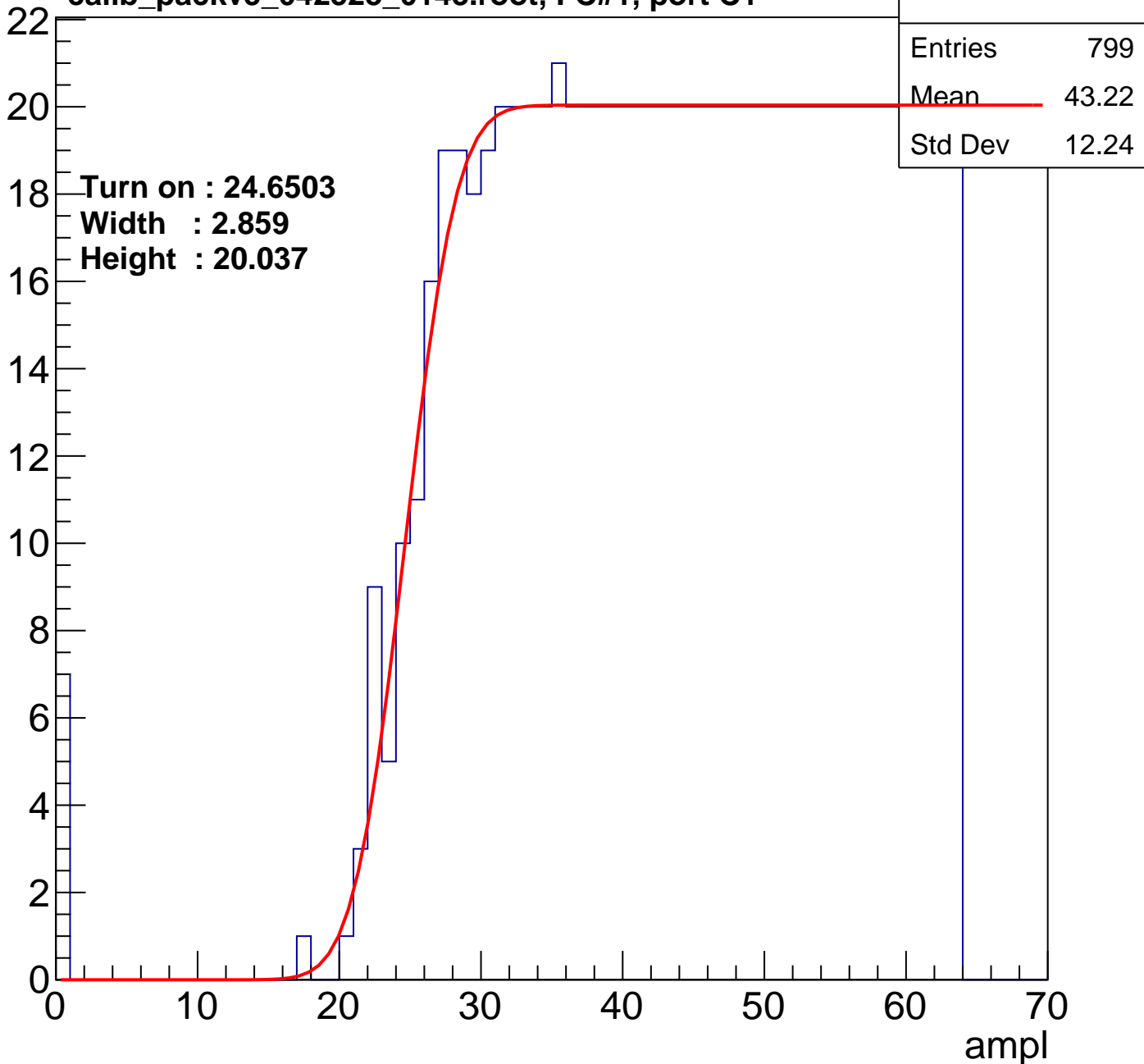
ampl



B0L101S, U14-ch80

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch81

calib_packv5_042523_0143.root, FC#1, port C1

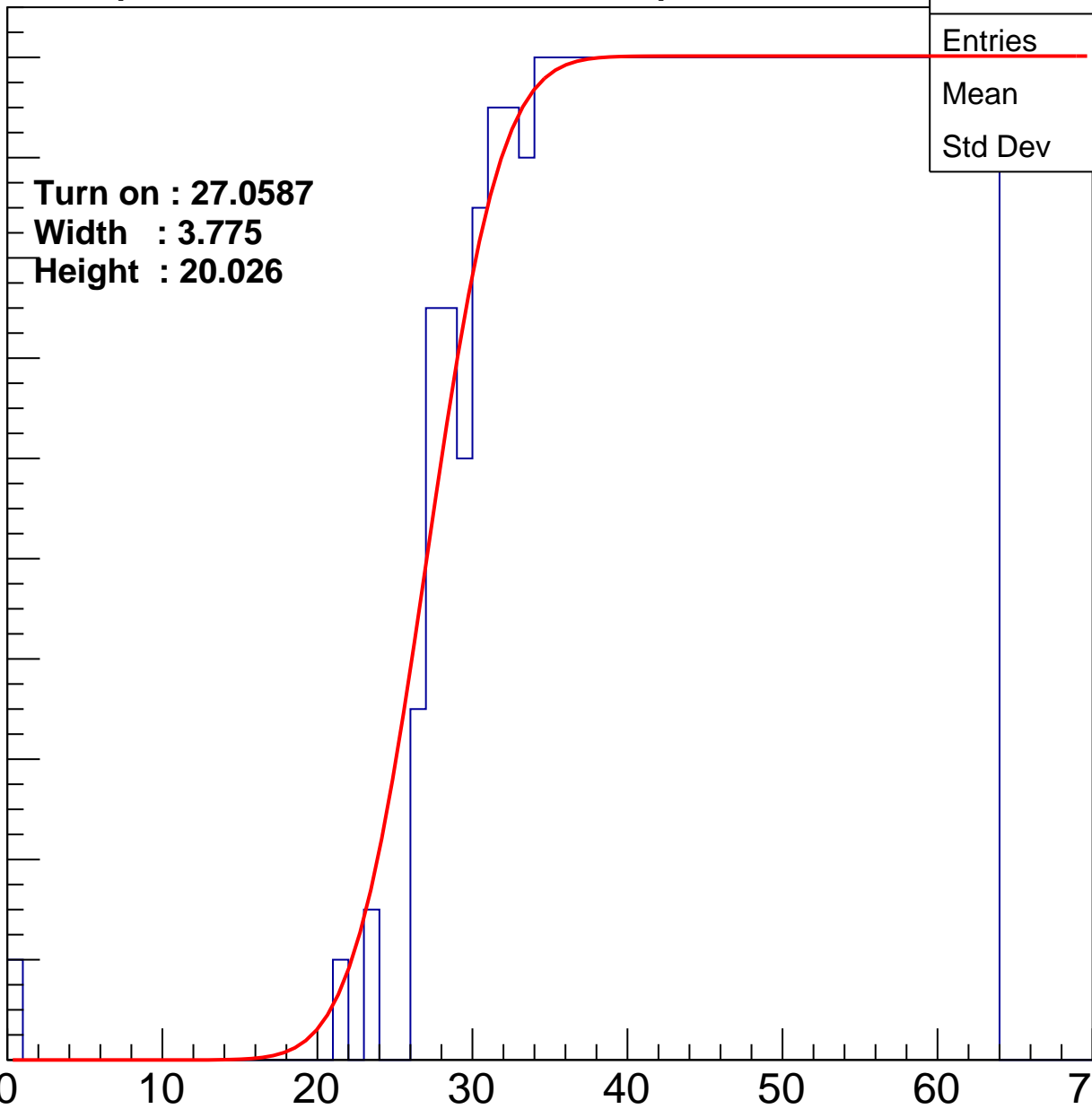
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0587
Width : 3.775
Height : 20.026

Entries	729
Mean	45.09
Std Dev	10.93

ampl



B0L101S, U14-ch82

calib_packv5_042523_0143.root, FC#1, port C1

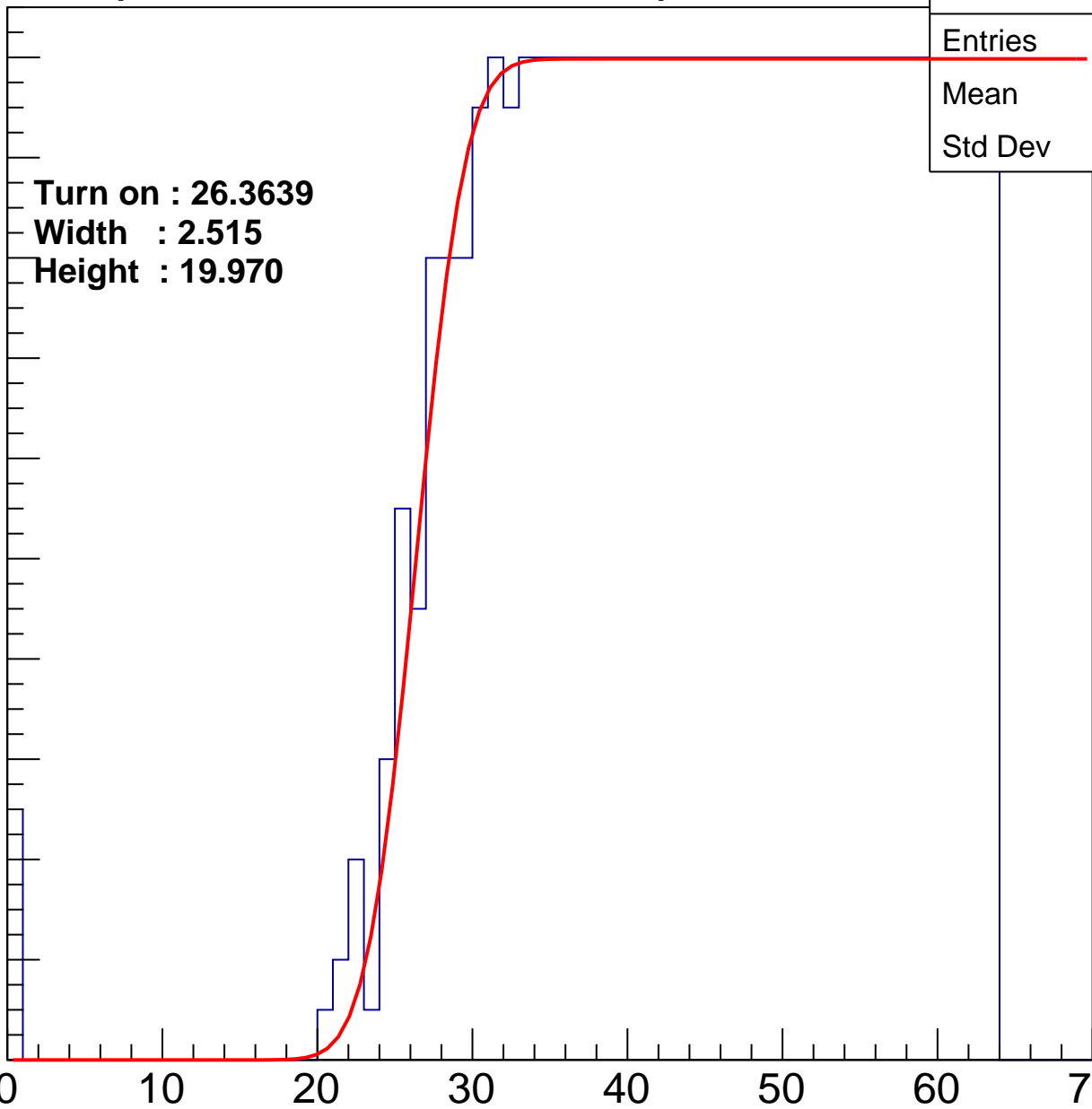
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.3639
Width : 2.515
Height : 19.970

Entries	765
Mean	44.09
Std Dev	11.7

ampl



B0L101S, U14-ch83

calib_packv5_042523_0143.root, FC#1, port C1

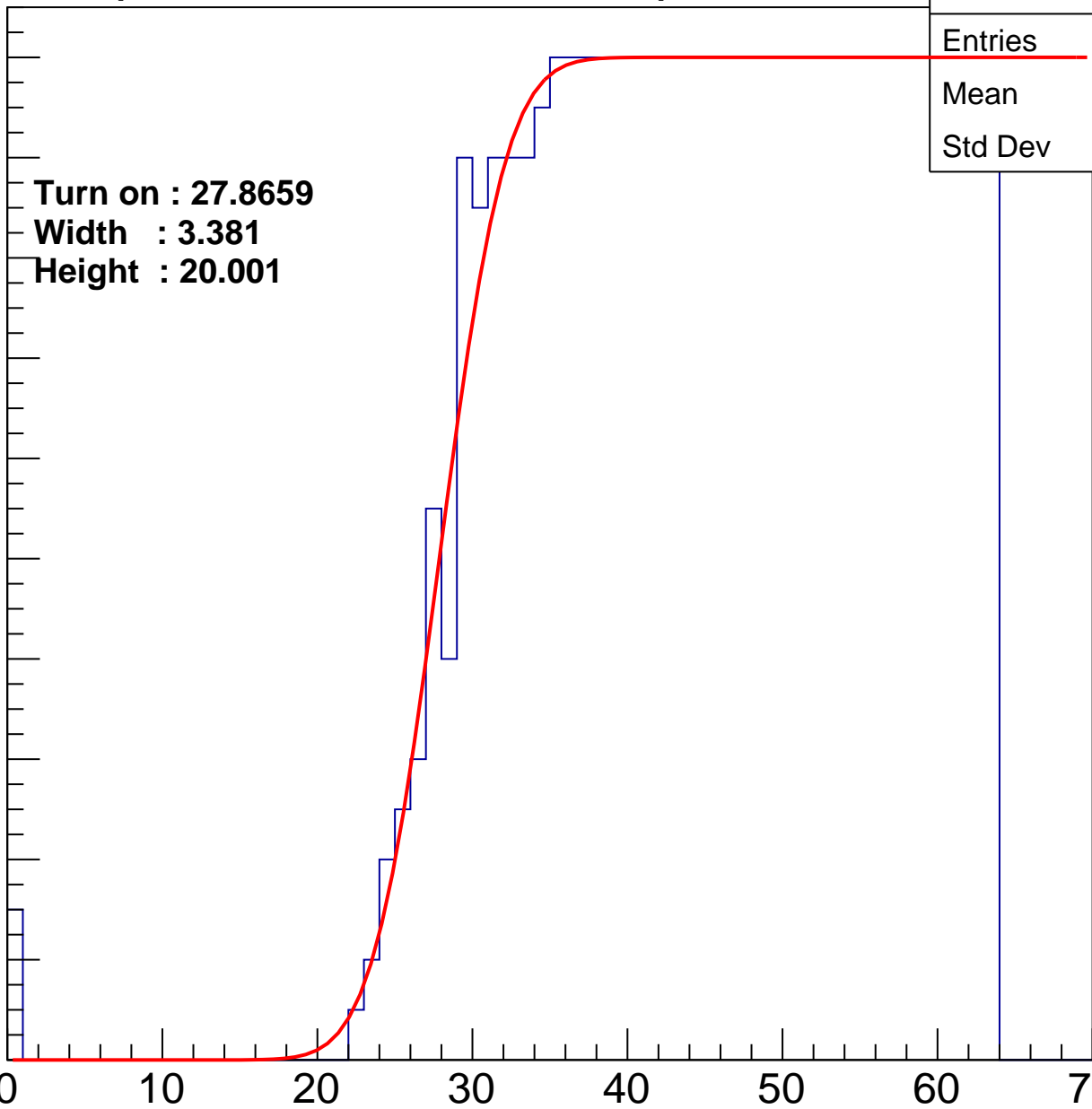
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8659
Width : 3.381
Height : 20.001

Entries	728
Mean	45.04
Std Dev	11.07

ampl



B0L101S, U14-ch84

calib_packv5_042523_0143.root, FC#1, port C1

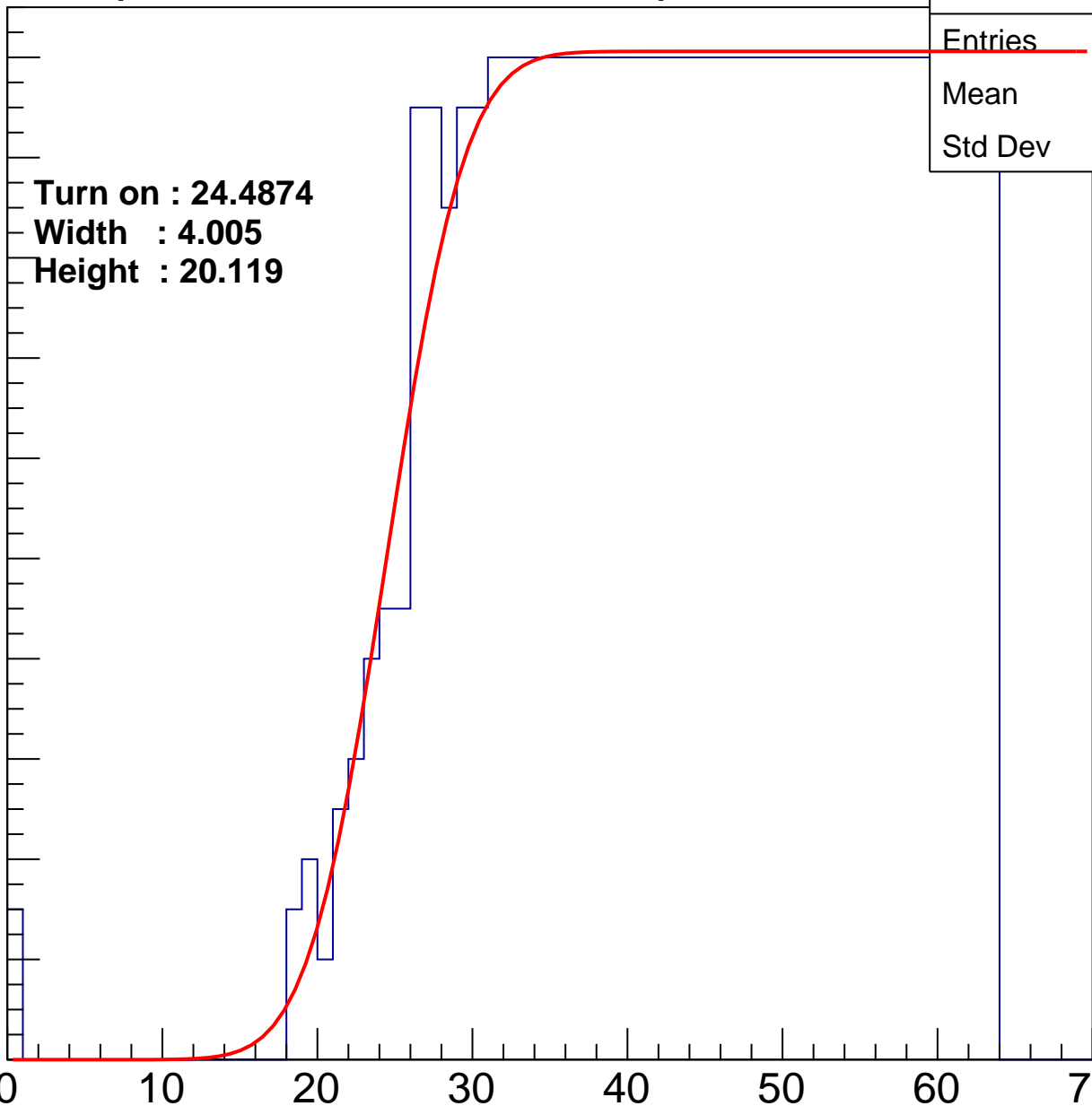
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.4874
Width : 4.005
Height : 20.119

Entries	802
Mean	43.21
Std Dev	12.06

ampl



B0L101S, U14-ch85

calib_packv5_042523_0143.root, FC#1, port C1

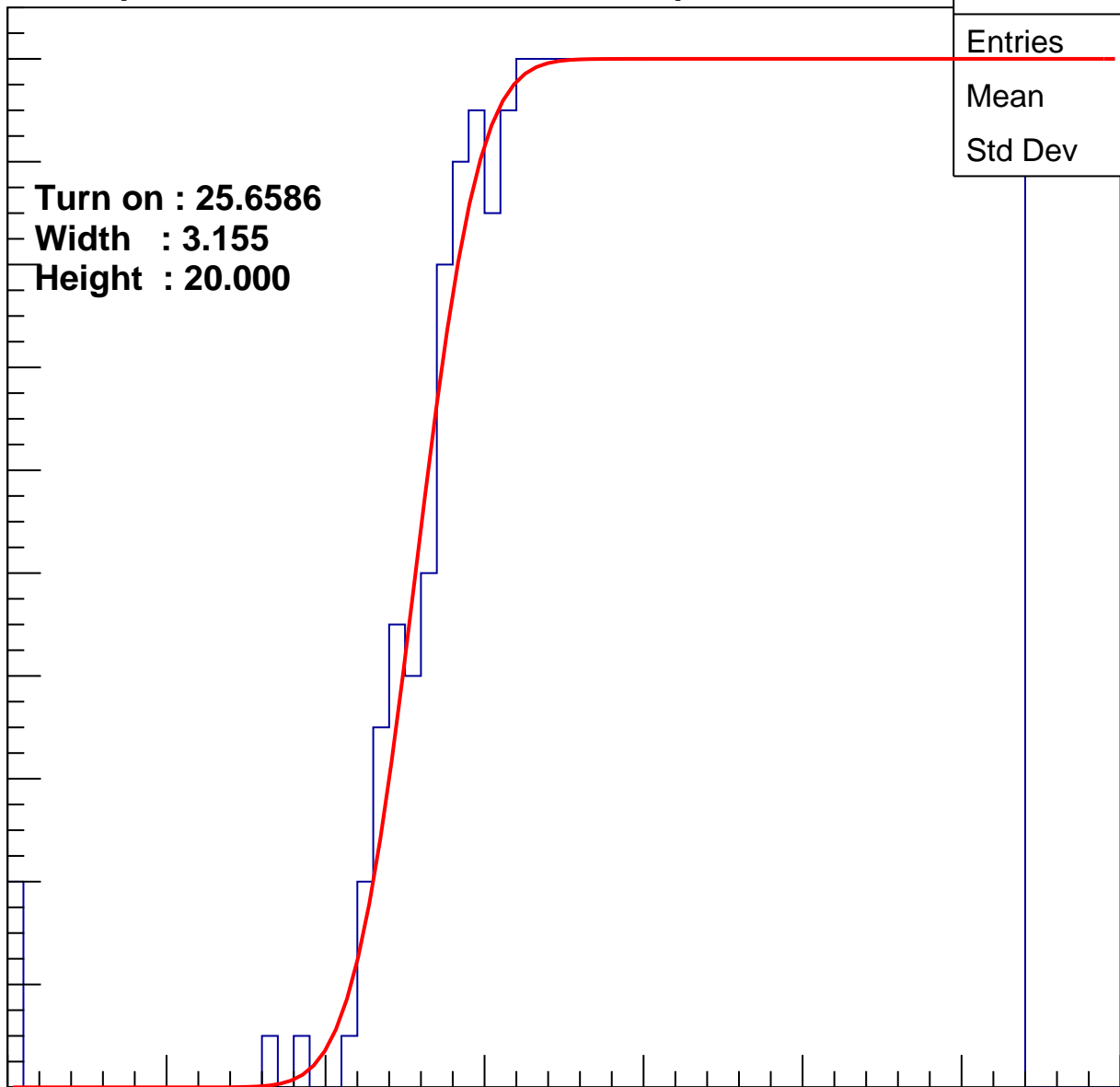
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6586
Width : 3.155
Height : 20.000

Entries	774
Mean	43.88
Std Dev	11.75

ampl



B0L101S, U14-ch86

calib_packv5_042523_0143.root, FC#1, port C1

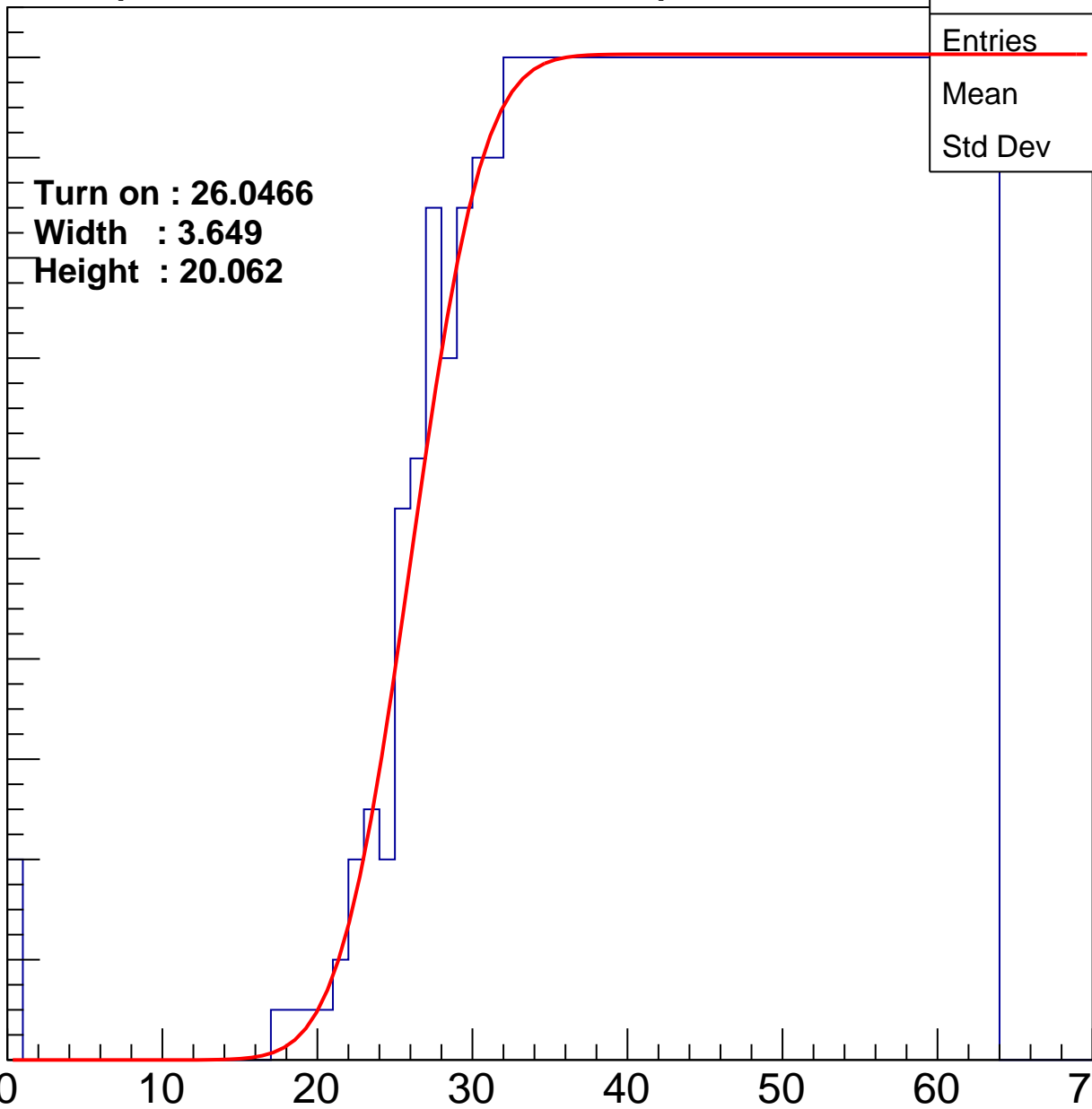
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0466
Width : 3.649
Height : 20.062

Entries	770
Mean	43.95
Std Dev	11.75

ampl



B0L101S, U14-ch87

calib_packv5_042523_0143.root, FC#1, port C1

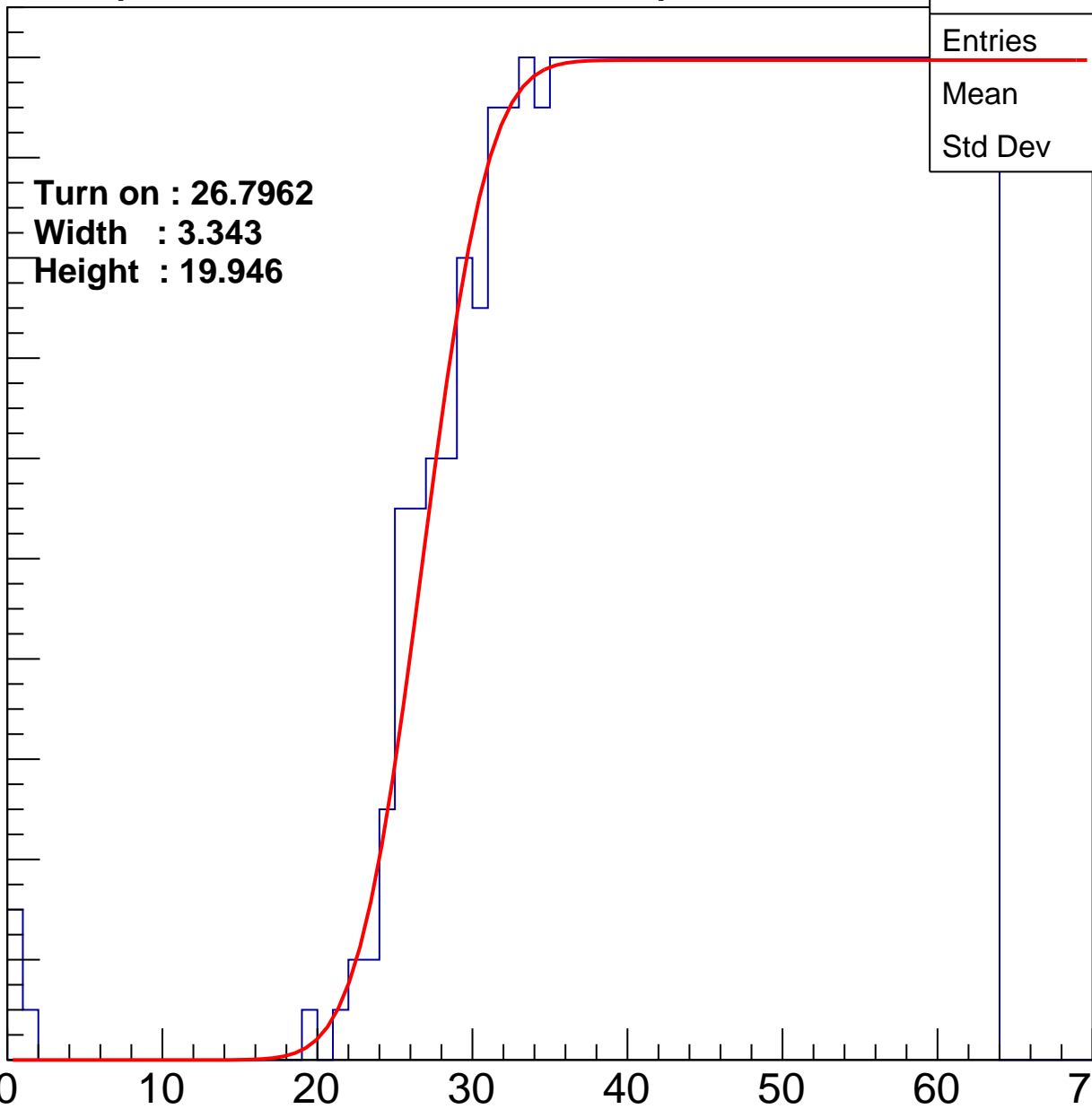
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7962
Width : 3.343
Height : 19.946

Entries	749
Mean	44.47
Std Dev	11.47

ampl



B0L101S, U14-ch88

calib_packv5_042523_0143.root, FC#1, port C1

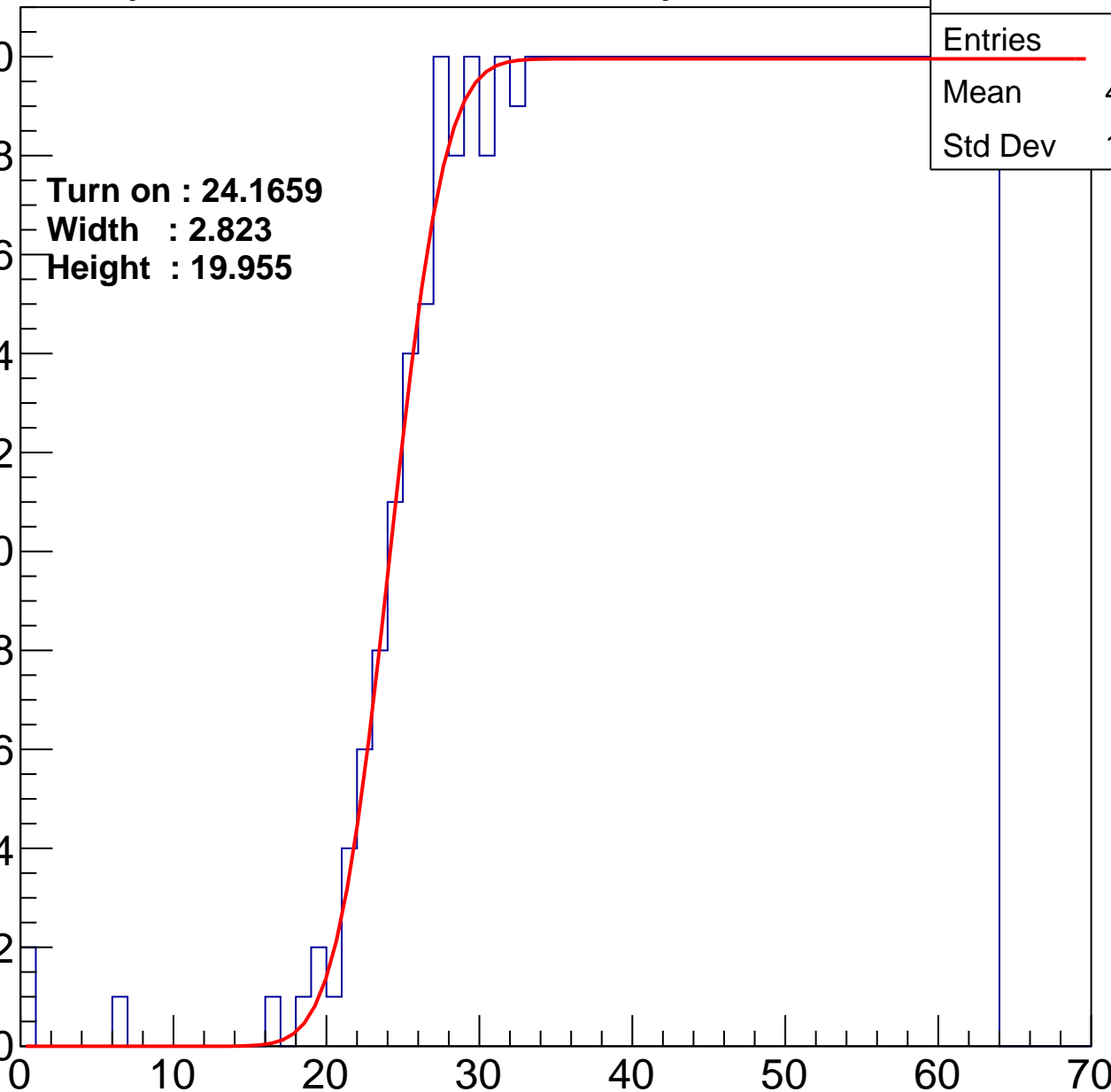
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 24.1659
Width : 2.823
Height : 19.955

Entries	801
Mean	43.26
Std Dev	11.99

ampl



B0L101S, U14-ch89

calib_packv5_042523_0143.root, FC#1, port C1

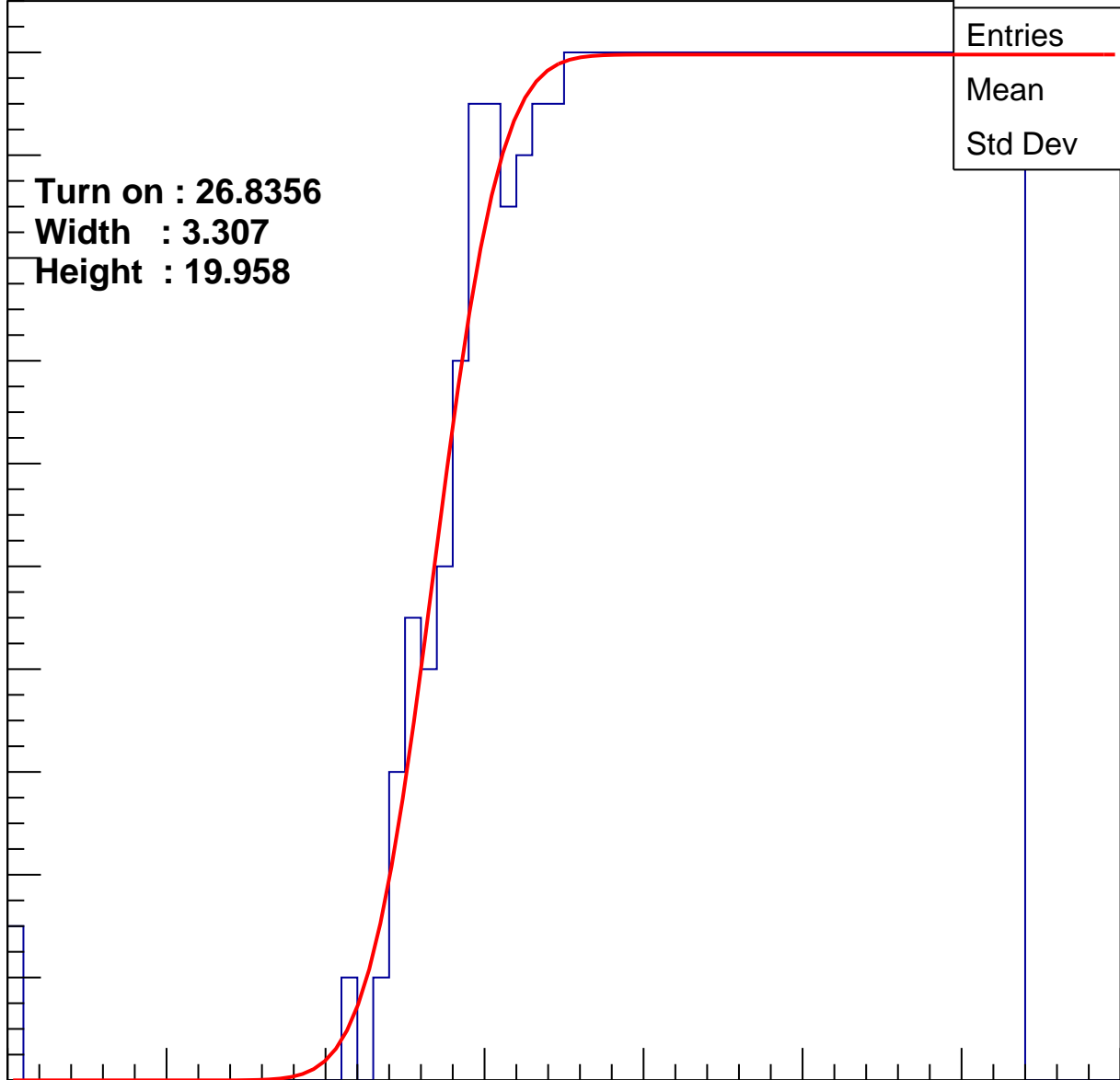
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8356
Width : 3.307
Height : 19.958

Entries	745
Mean	44.62
Std Dev	11.29

ampl



B0L101S, U14-ch90

calib_packv5_042523_0143.root, FC#1, port C1

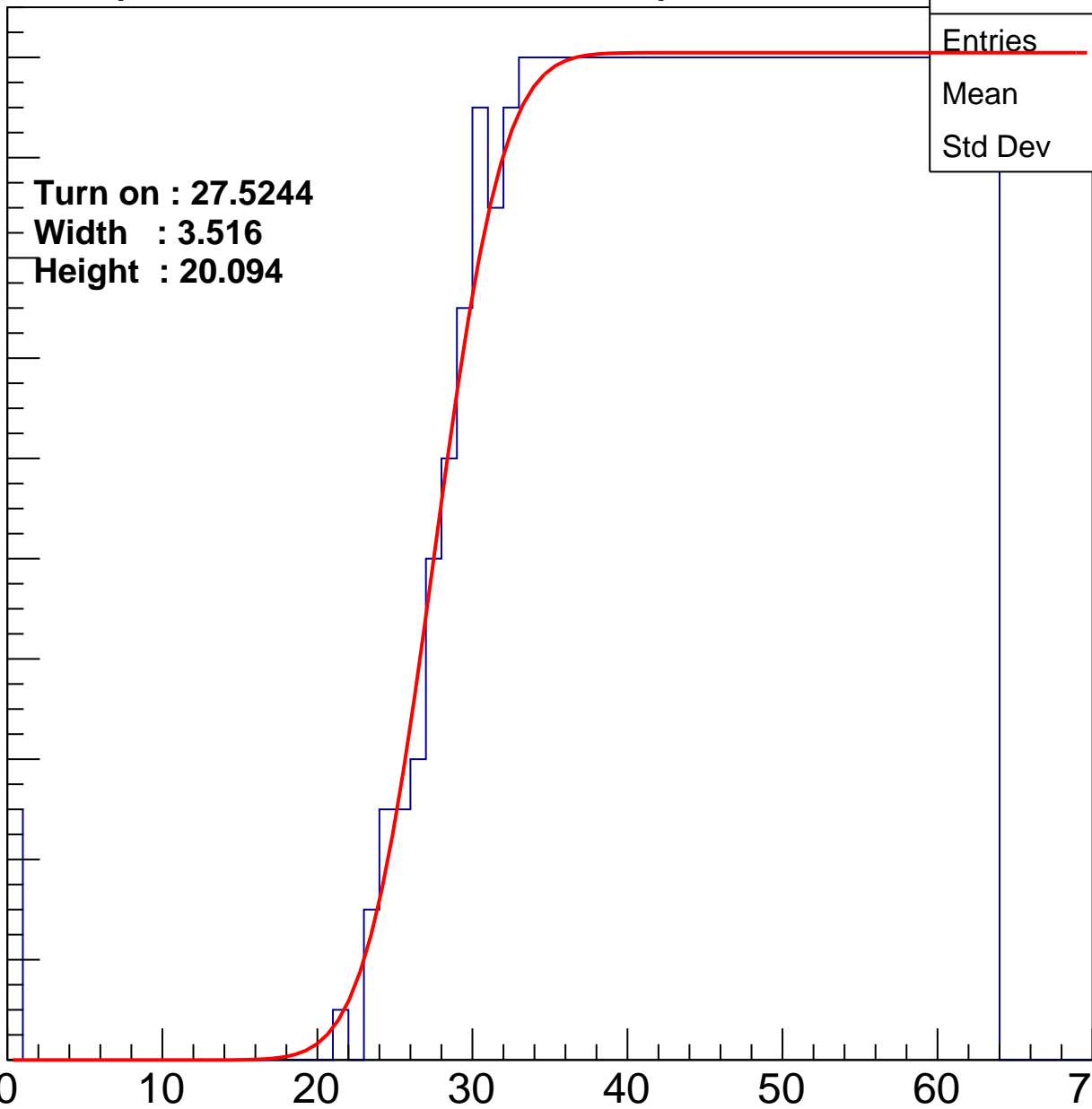
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5244
Width : 3.516
Height : 20.094

Entries	737
Mean	44.77
Std Dev	11.36

ampl



B0L101S, U14-ch91

calib_packv5_042523_0143.root, FC#1, port C1

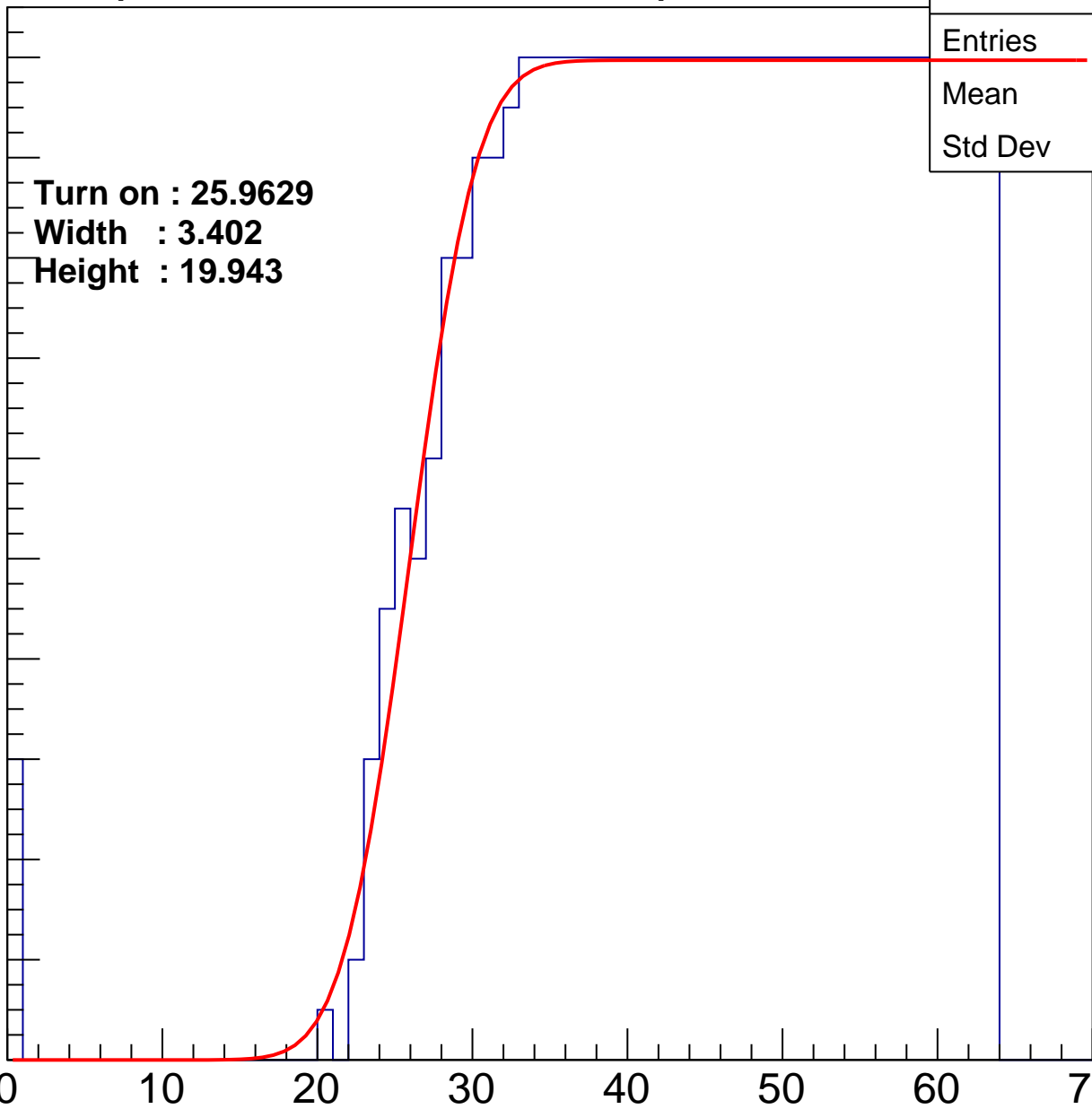
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.9629
Width : 3.402
Height : 19.943

Entries	764
Mean	44.05
Std Dev	11.81

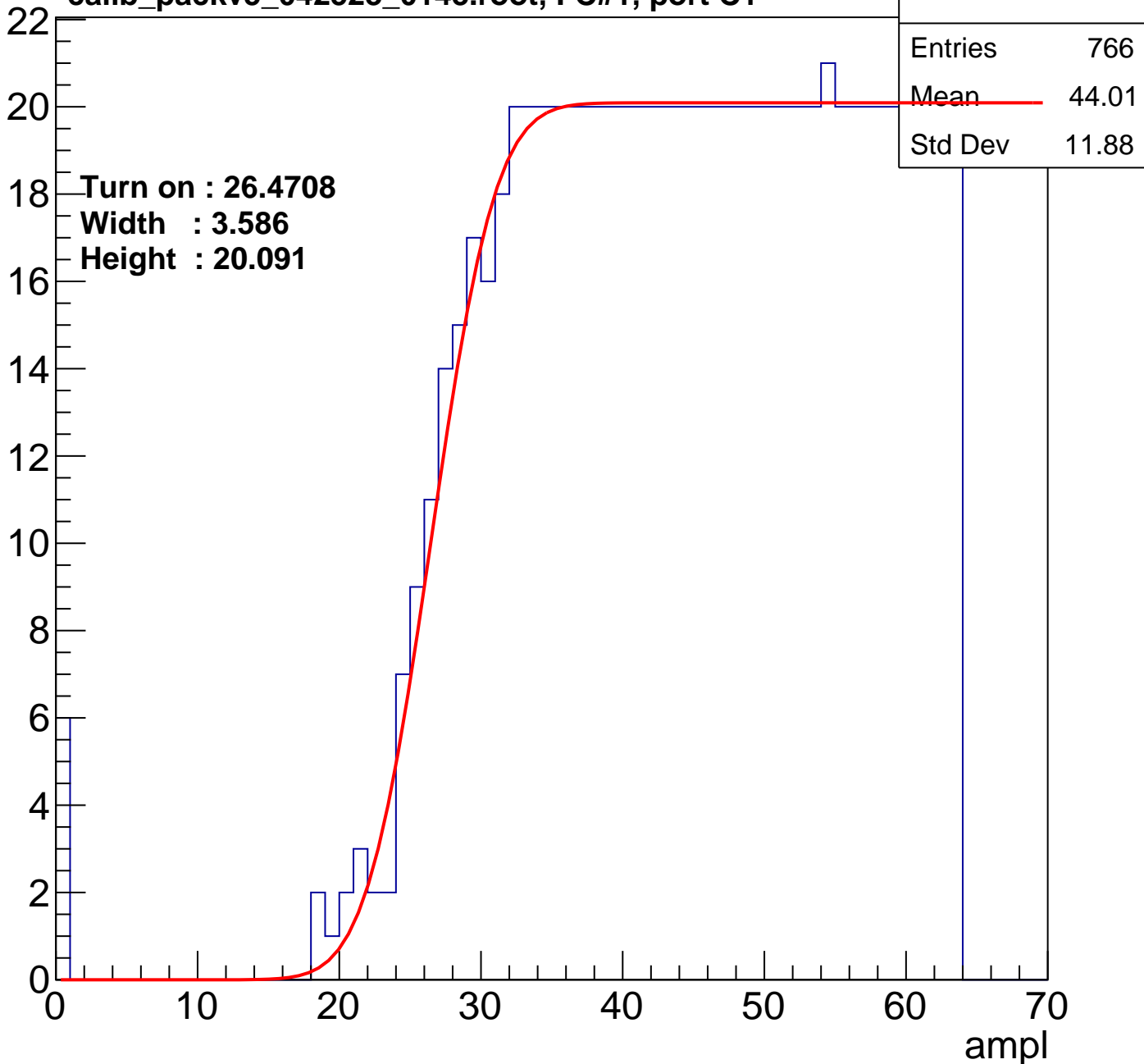
ampl



B0L101S, U14-ch92

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch93

calib_packv5_042523_0143.root, FC#1, port C1

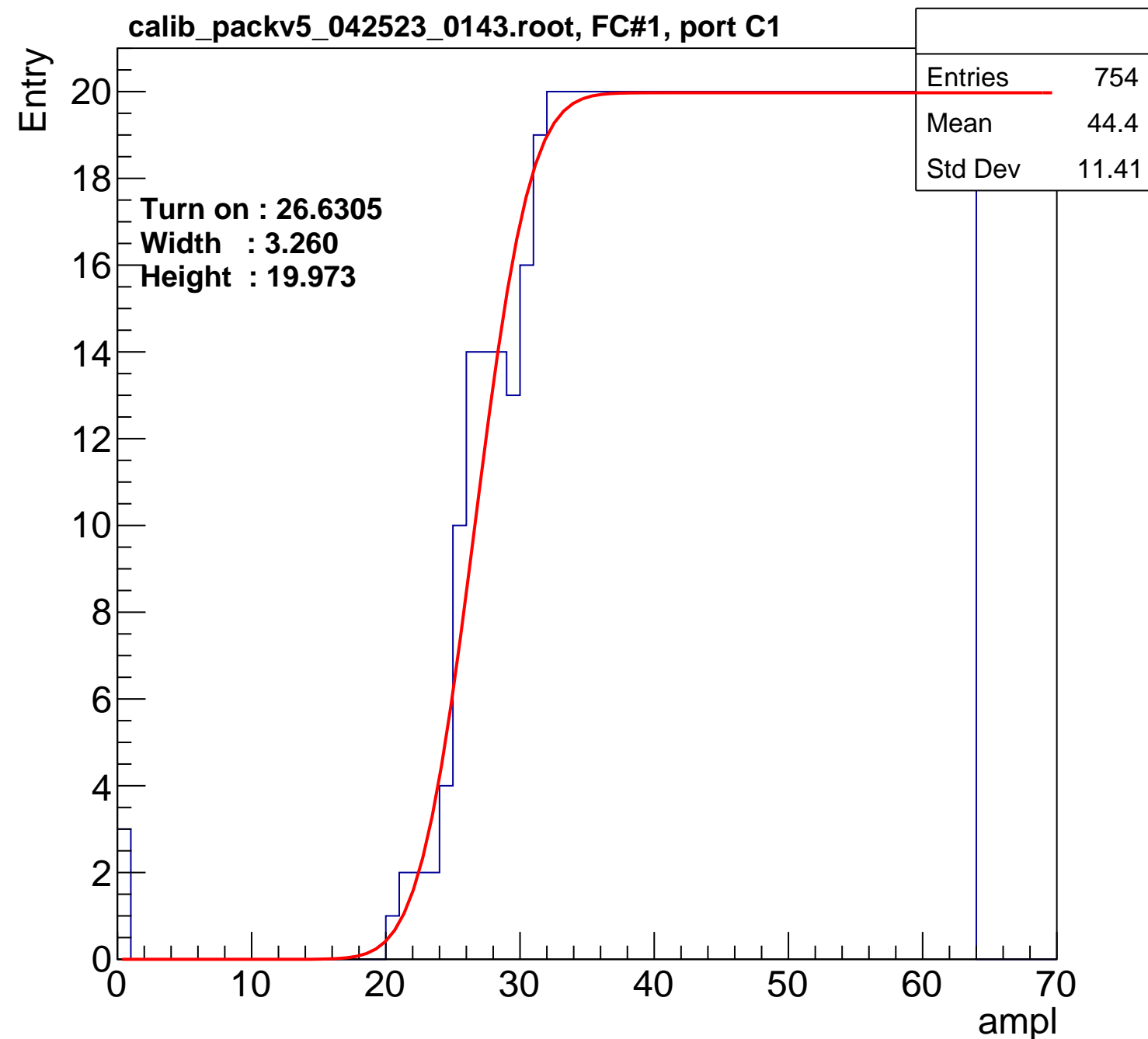
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6305
Width : 3.260
Height : 19.973

Entries	754
Mean	44.4
Std Dev	11.41

ampl



B0L101S, U14-ch94

calib_packv5_042523_0143.root, FC#1, port C1

Entry

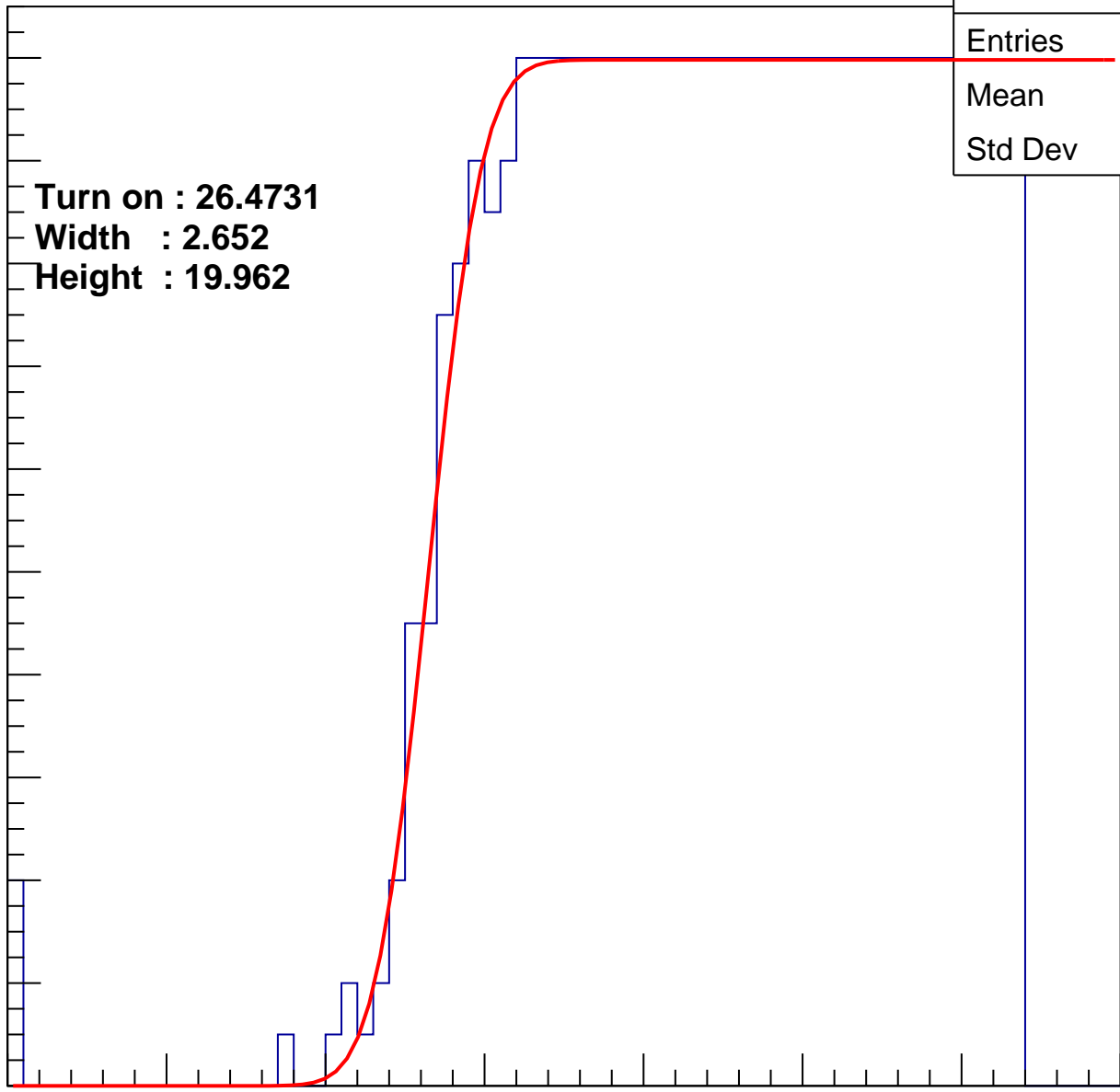
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.4731
Width : 2.652
Height : 19.962

Entries	757
Mean	44.31
Std Dev	11.52

ampl

0 10 20 30 40 50 60 70



B0L101S, U14-ch95

calib_packv5_042523_0143.root, FC#1, port C1

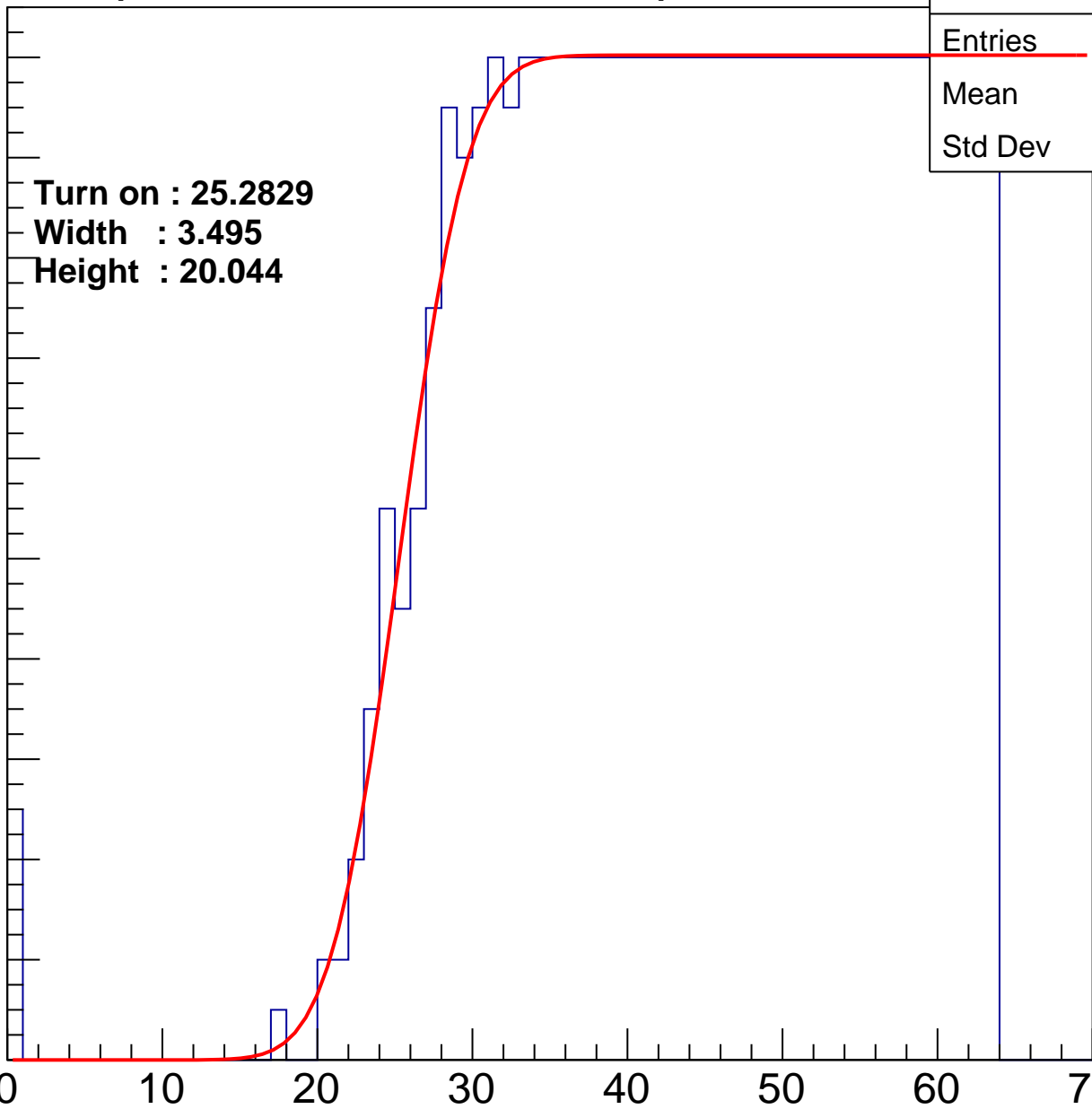
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2829
Width : 3.495
Height : 20.044

Entries	782
Mean	43.66
Std Dev	11.93

ampl



B0L101S, U14-ch96

calib_packv5_042523_0143.root, FC#1, port C1

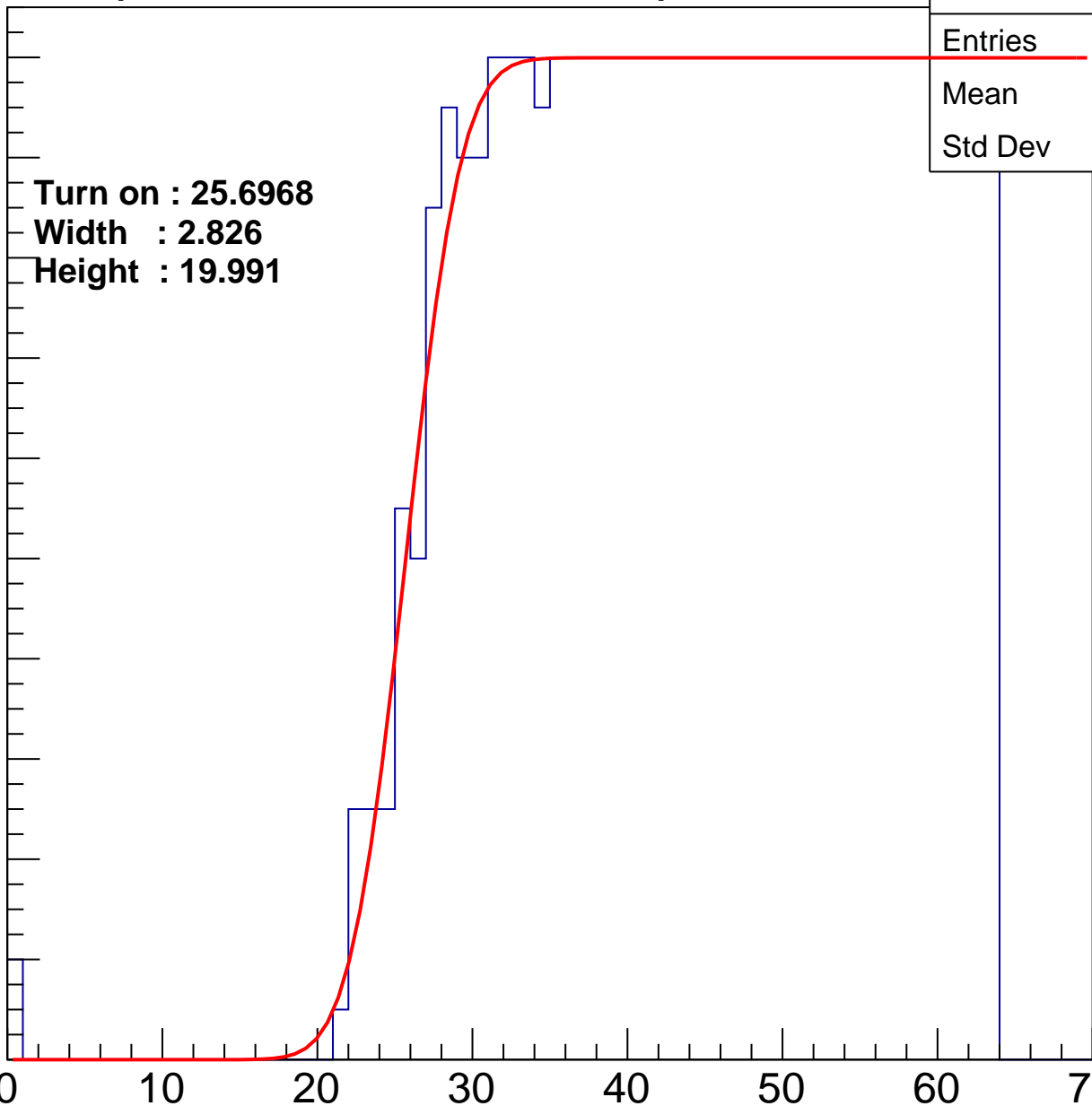
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.6968
Width : 2.826
Height : 19.991

Entries	770
Mean	44.08
Std Dev	11.47

ampl



B0L101S, U14-ch97

calib_packv5_042523_0143.root, FC#1, port C1

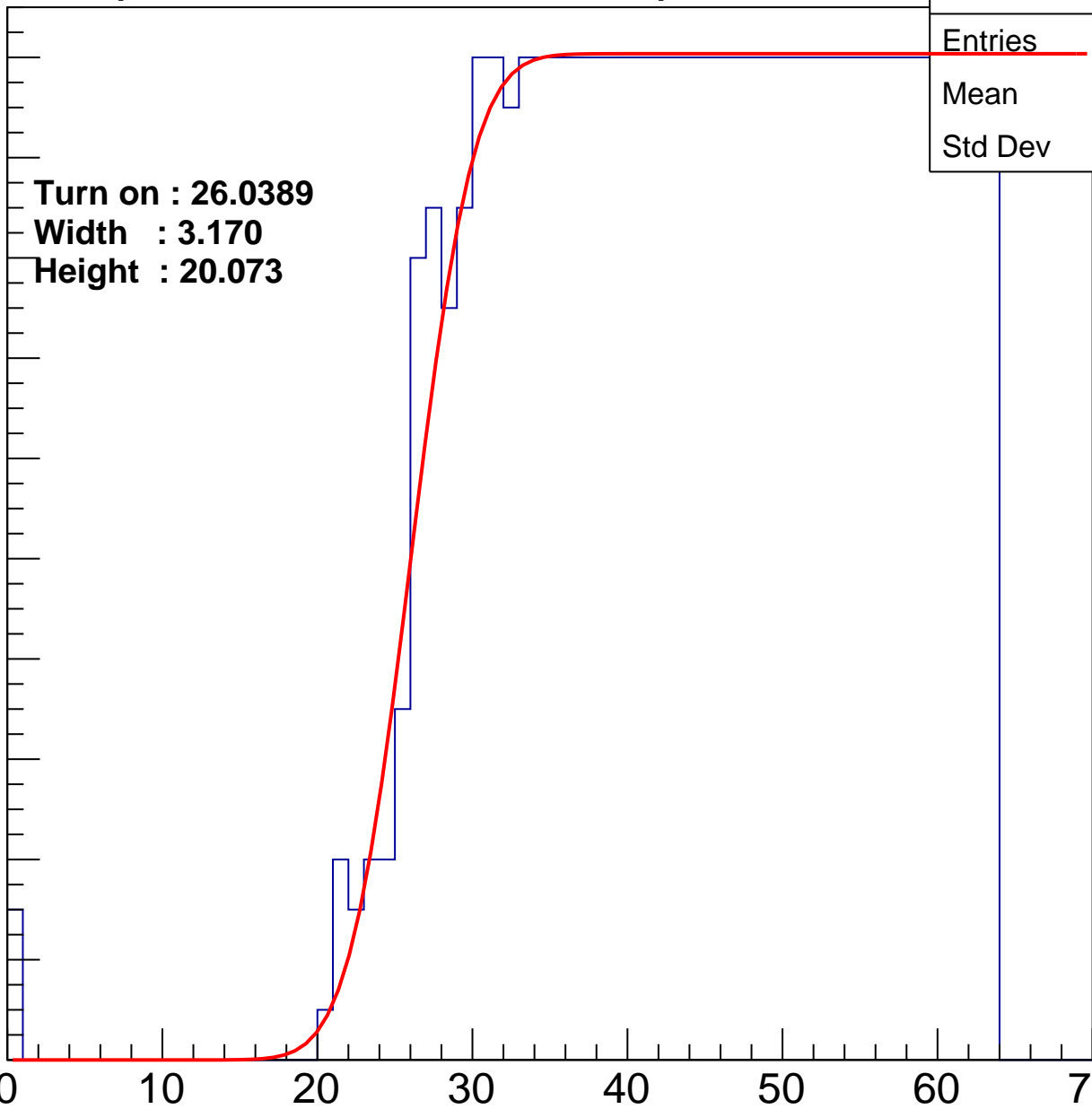
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.0389
Width : 3.170
Height : 20.073

Entries	770
Mean	44.04
Std Dev	11.57

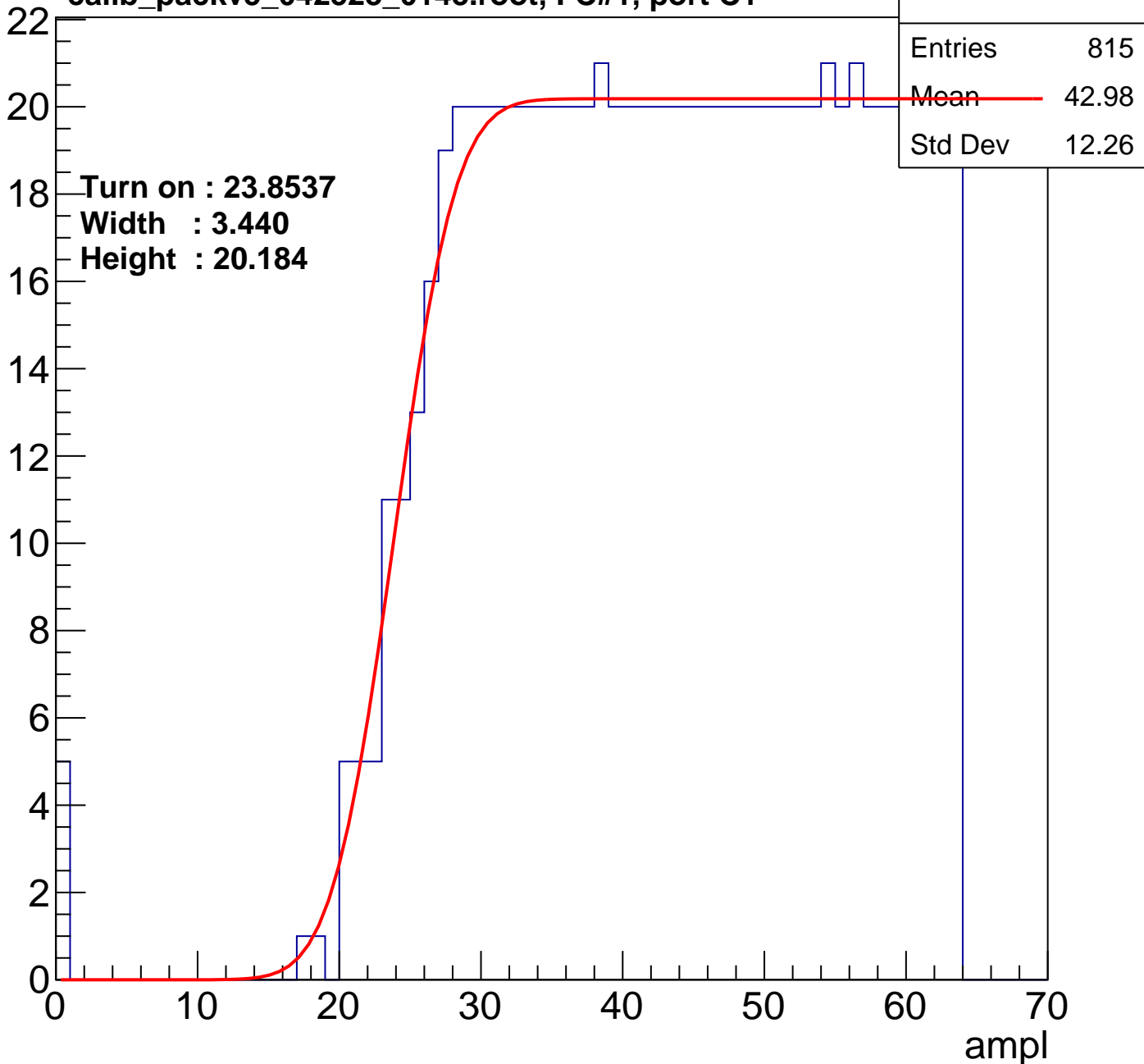
ampl



B0L101S, U14-ch98

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch99

calib_packv5_042523_0143.root, FC#1, port C1

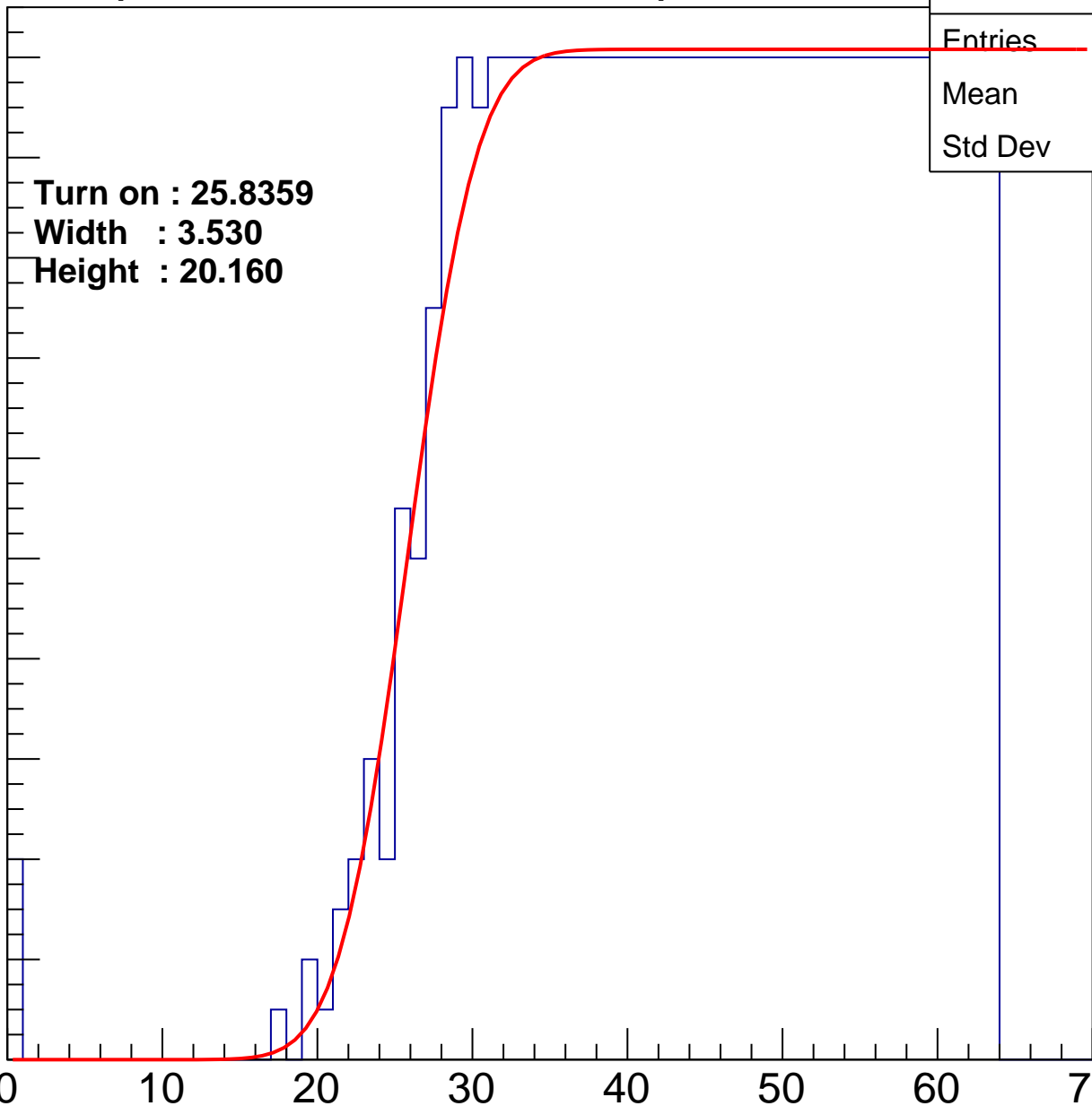
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8359
Width : 3.530
Height : 20.160

Entries	779
Mean	43.78
Std Dev	11.8

ampl



B0L101S, U14-ch100

calib_packv5_042523_0143.root, FC#1, port C1

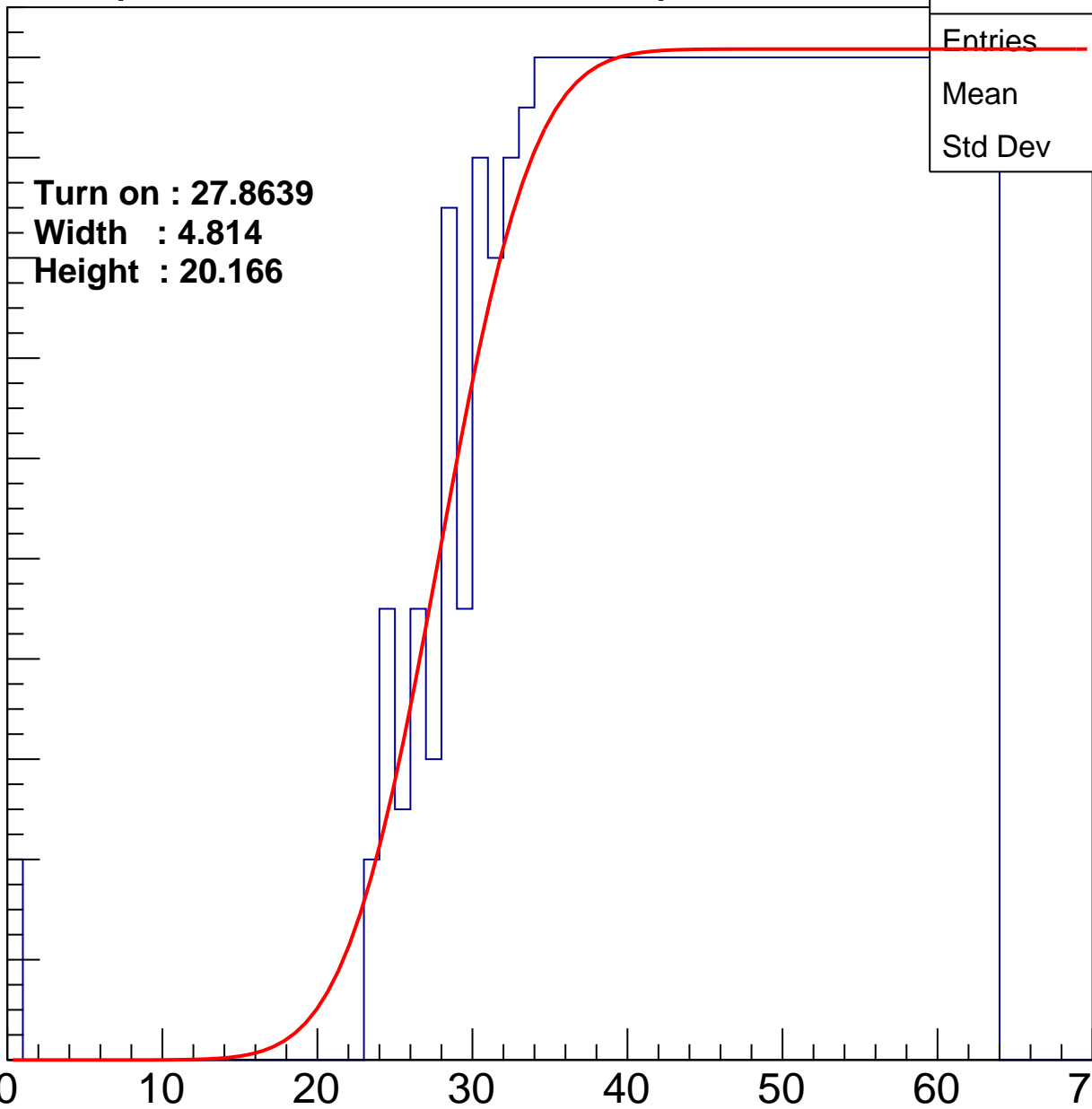
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8639
Width : 4.814
Height : 20.166

Entries	734
Mean	44.83
Std Dev	11.3

ampl



B0L101S, U14-ch101

calib_packv5_042523_0143.root, FC#1, port C1

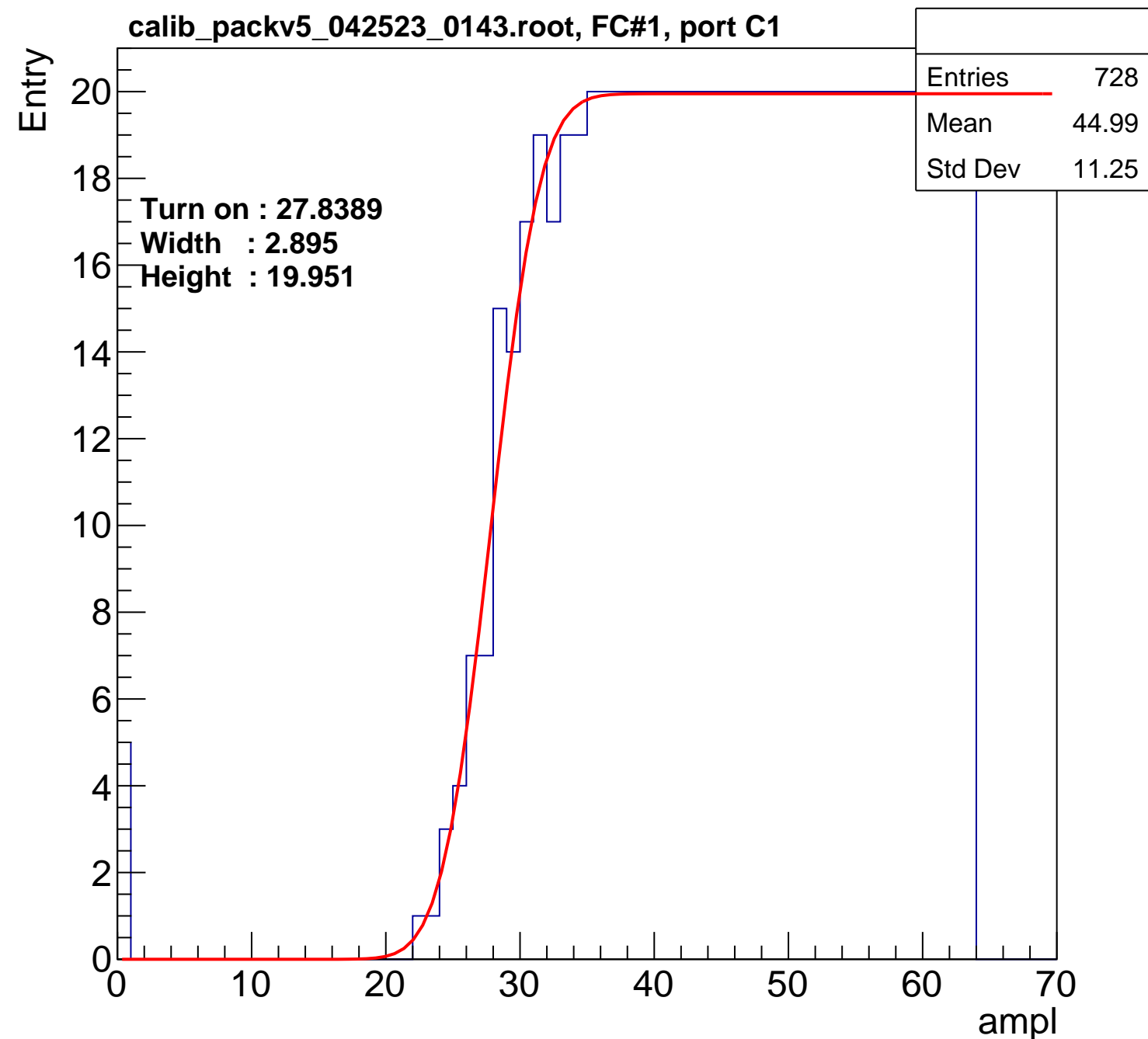
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.8389
Width : 2.895
Height : 19.951

Entries	728
Mean	44.99
Std Dev	11.25

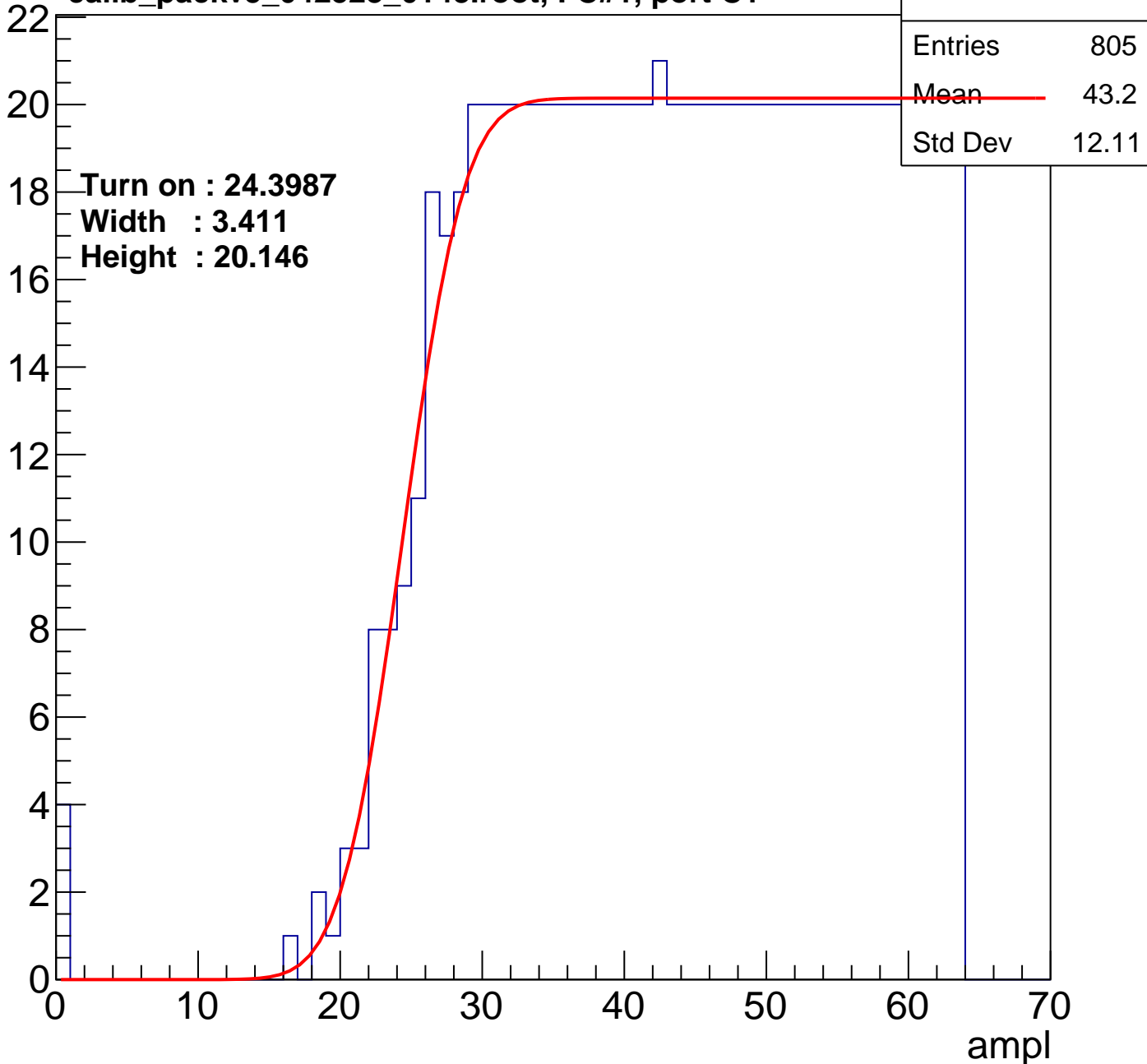
ampl



B0L101S, U14-ch102

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch103

calib_packv5_042523_0143.root, FC#1, port C1

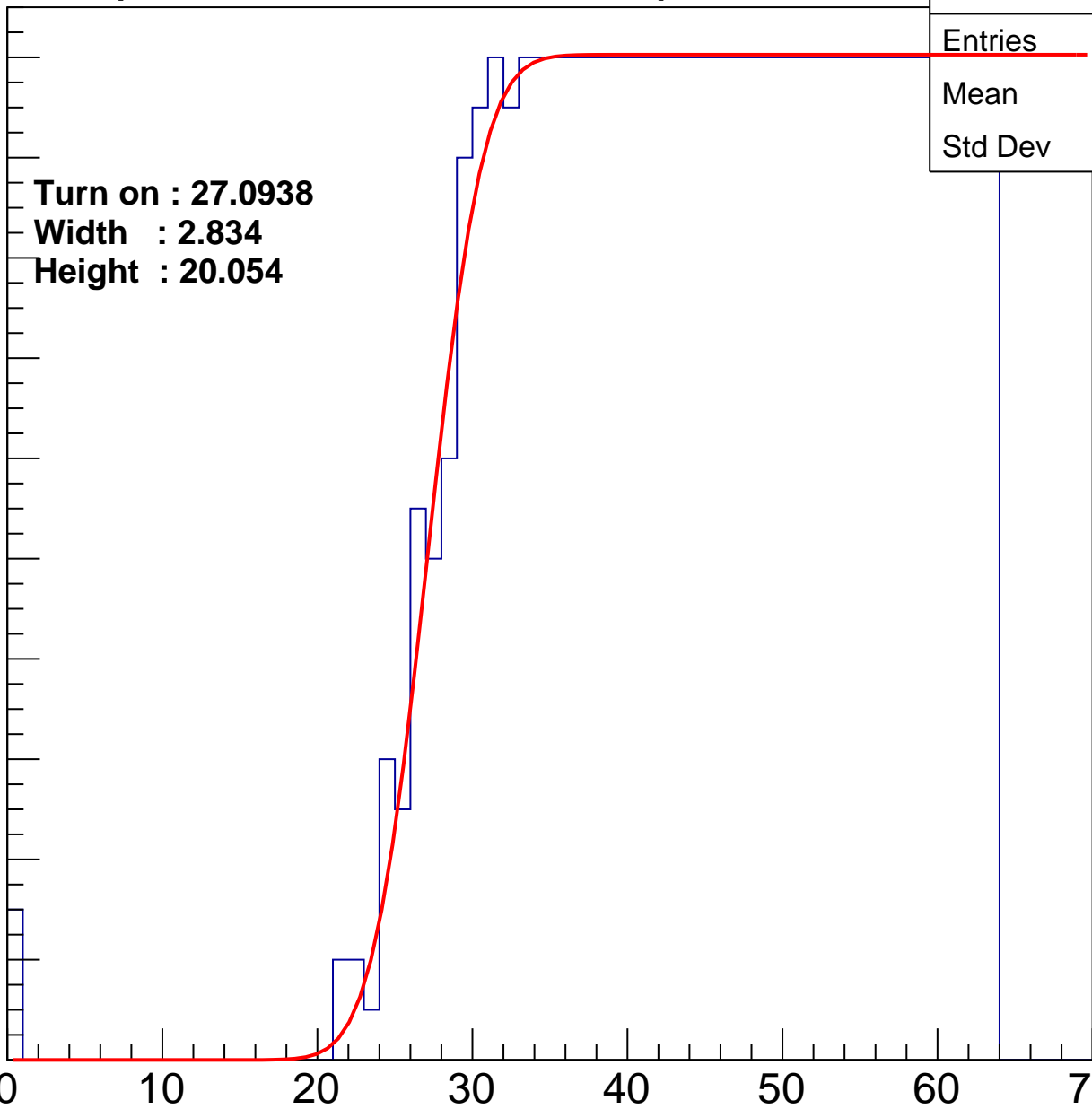
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.0938
Width : 2.834
Height : 20.054

Entries	748
Mean	44.59
Std Dev	11.28

ampl

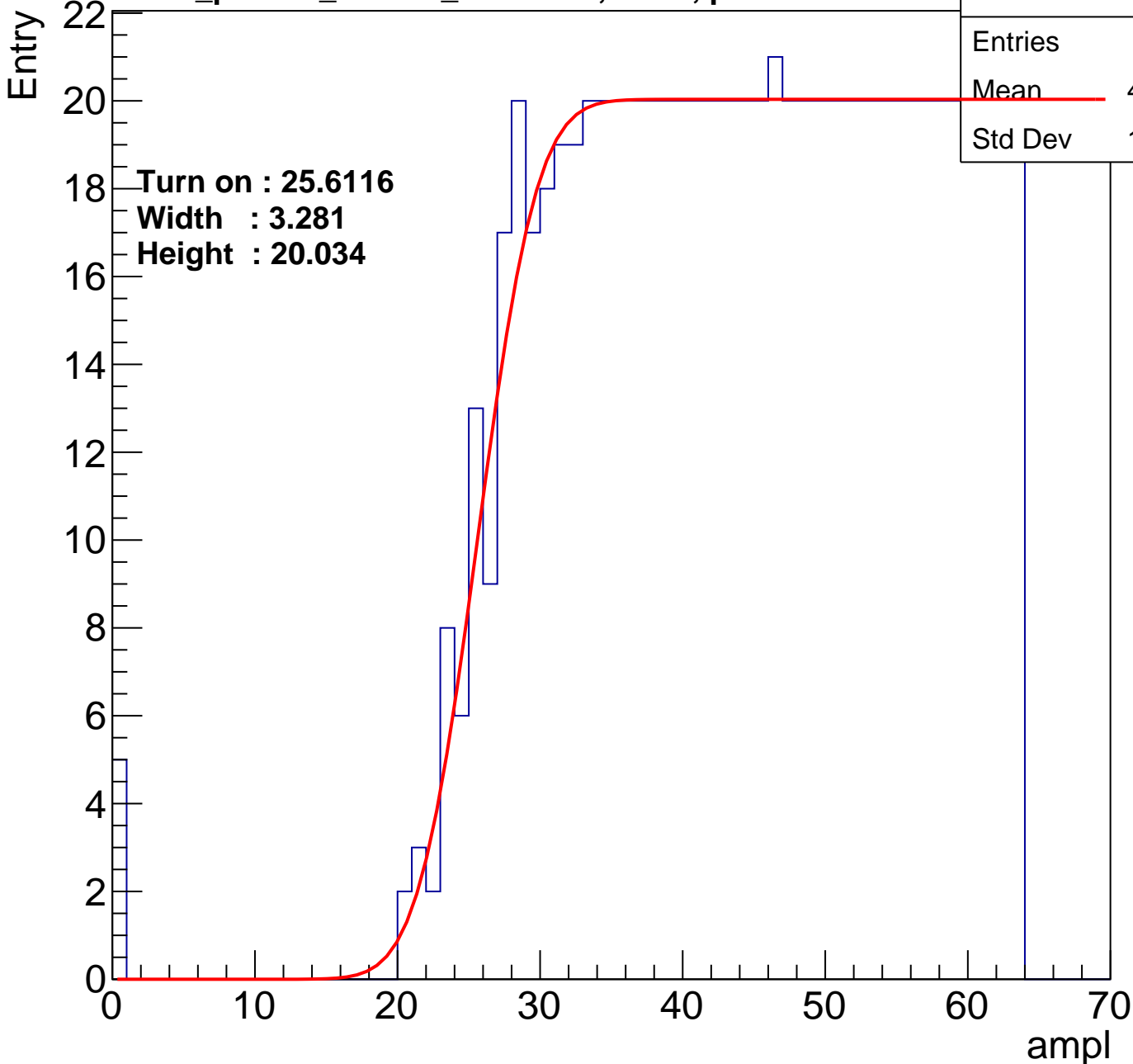


B0L101S, U14-ch104

calib_packv5_042523_0143.root, FC#1, port C1

Entries	779
Mean	43.76
Std Dev	11.87

Turn on : 25.6116
Width : 3.281
Height : 20.034



B0L101S, U14-ch105

calib_packv5_042523_0143.root, FC#1, port C1

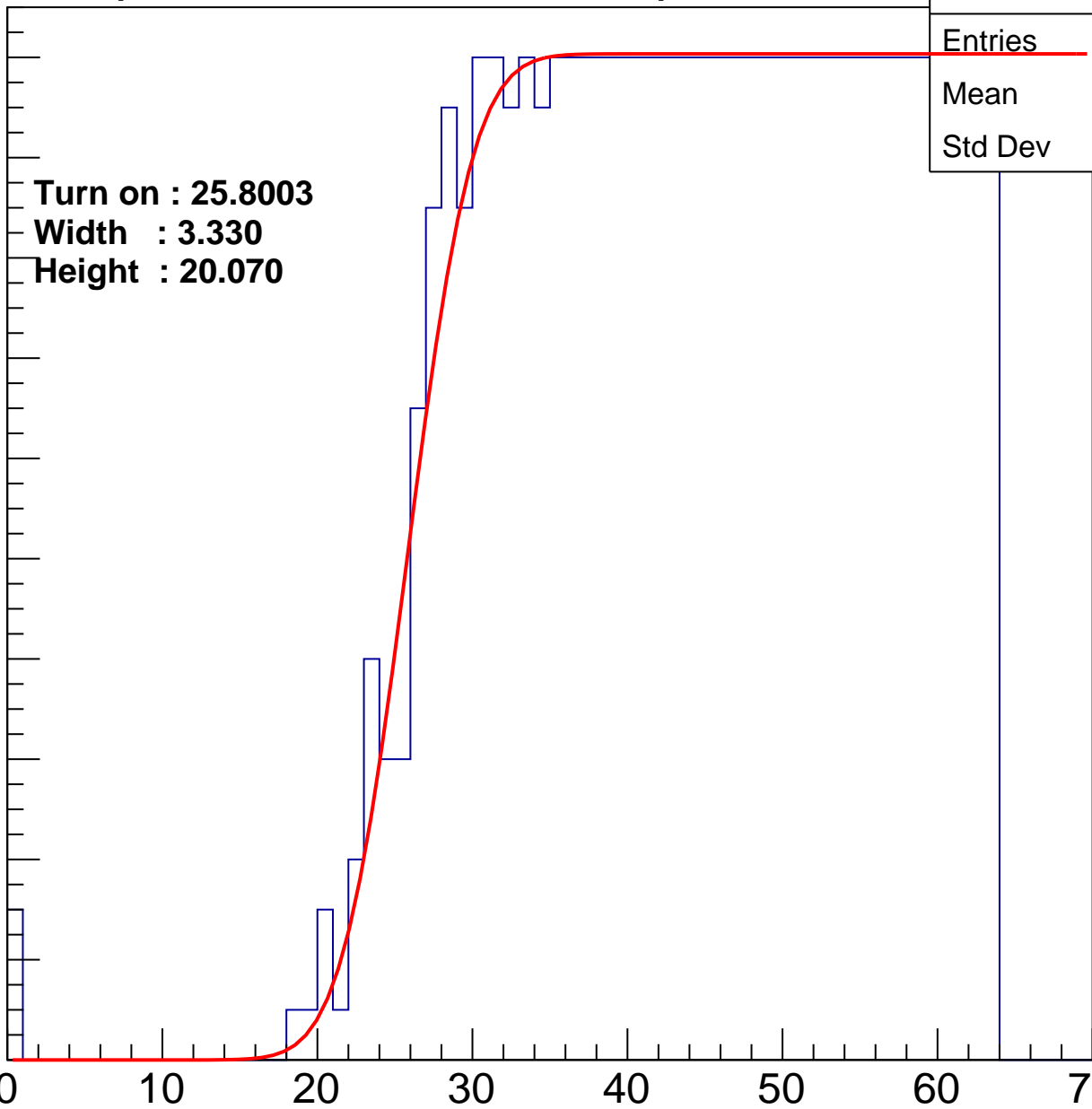
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.8003
Width : 3.330
Height : 20.070

Entries	777
Mean	43.84
Std Dev	11.72

ampl

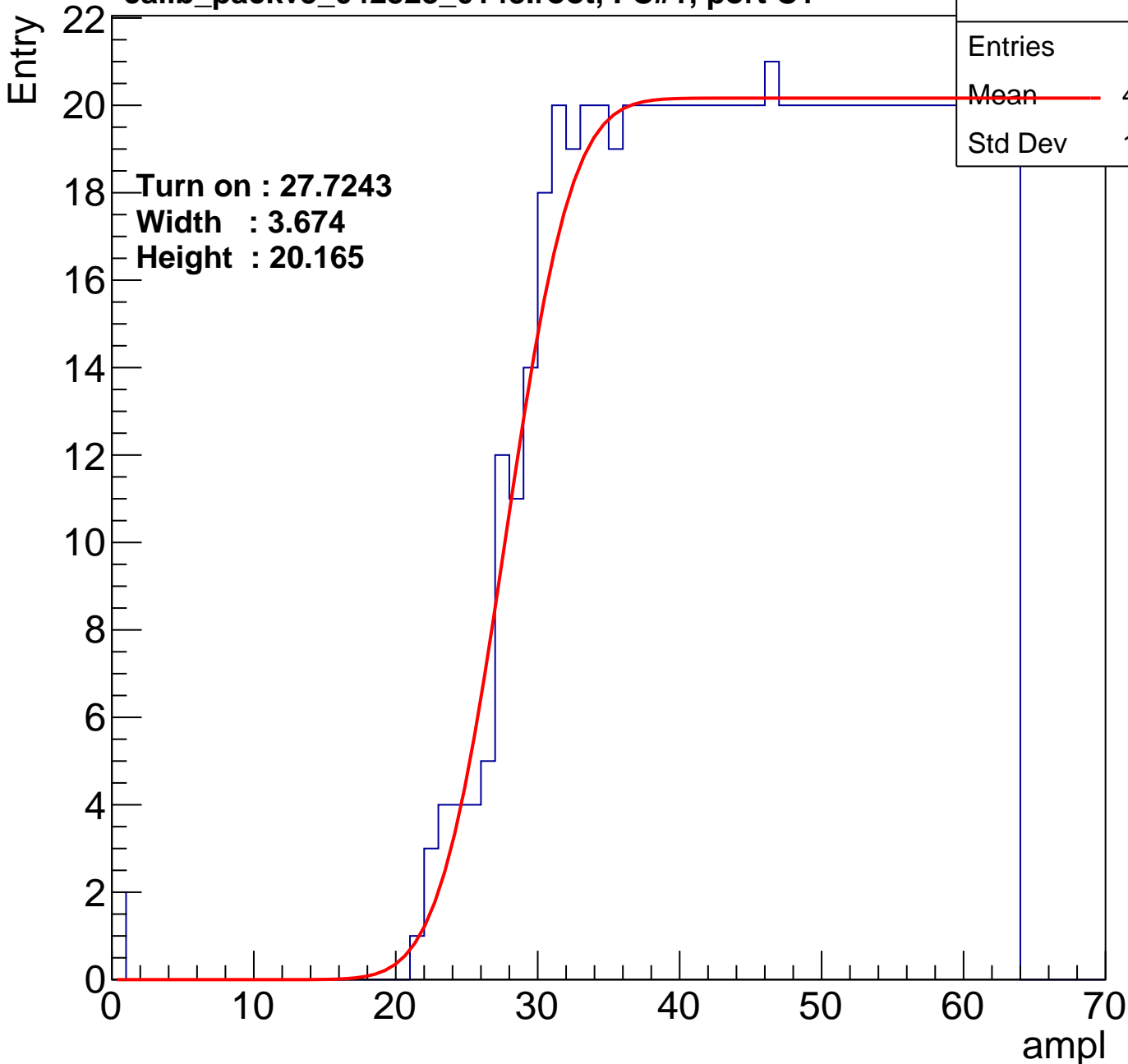


B0L101S, U14-ch106

calib_packv5_042523_0143.root, FC#1, port C1

Entries	737
Mean	44.89
Std Dev	11.06

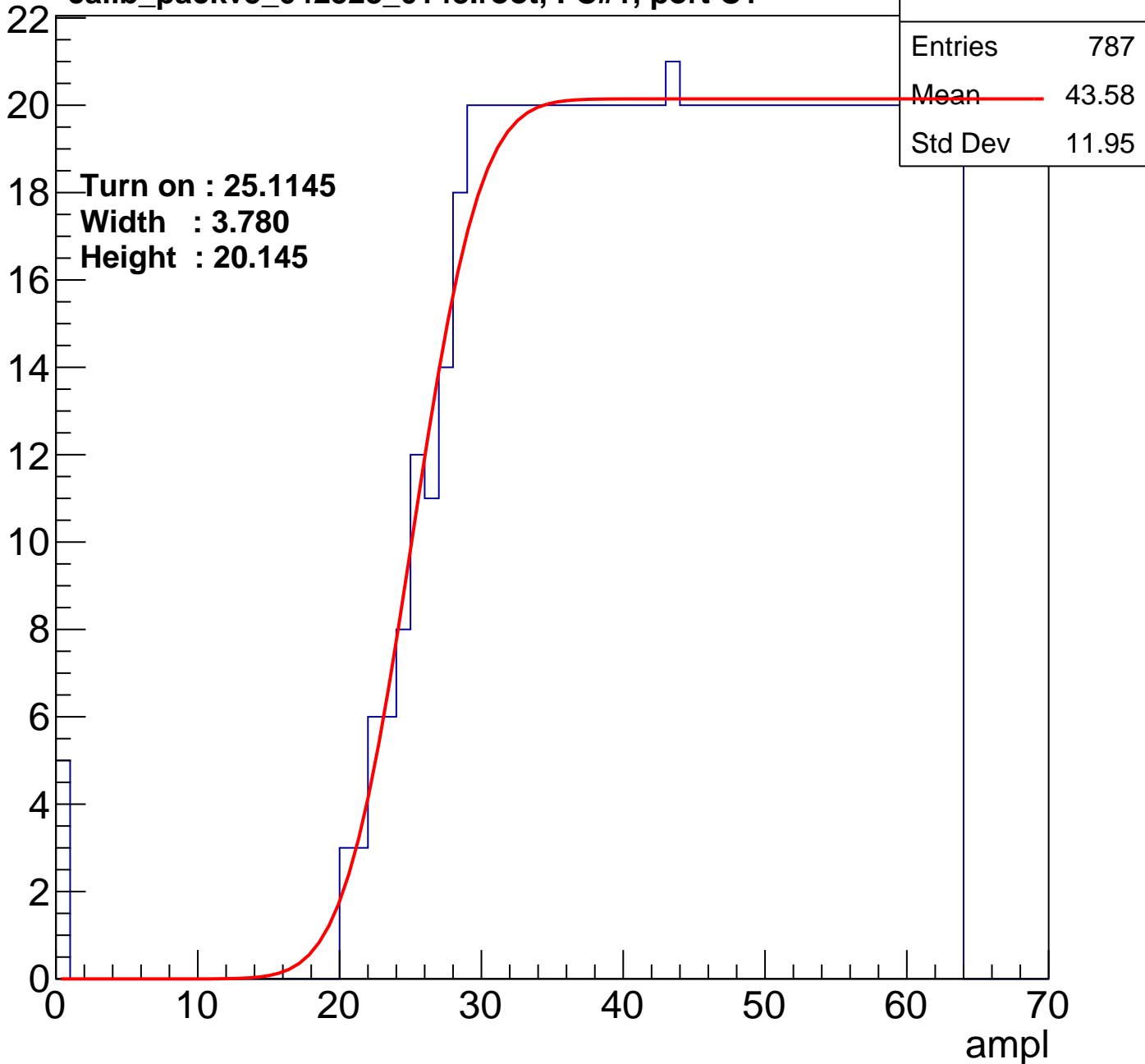
Turn on : 27.7243
Width : 3.674
Height : 20.165



B0L101S, U14-ch107

calib_packv5_042523_0143.root, FC#1, port C1

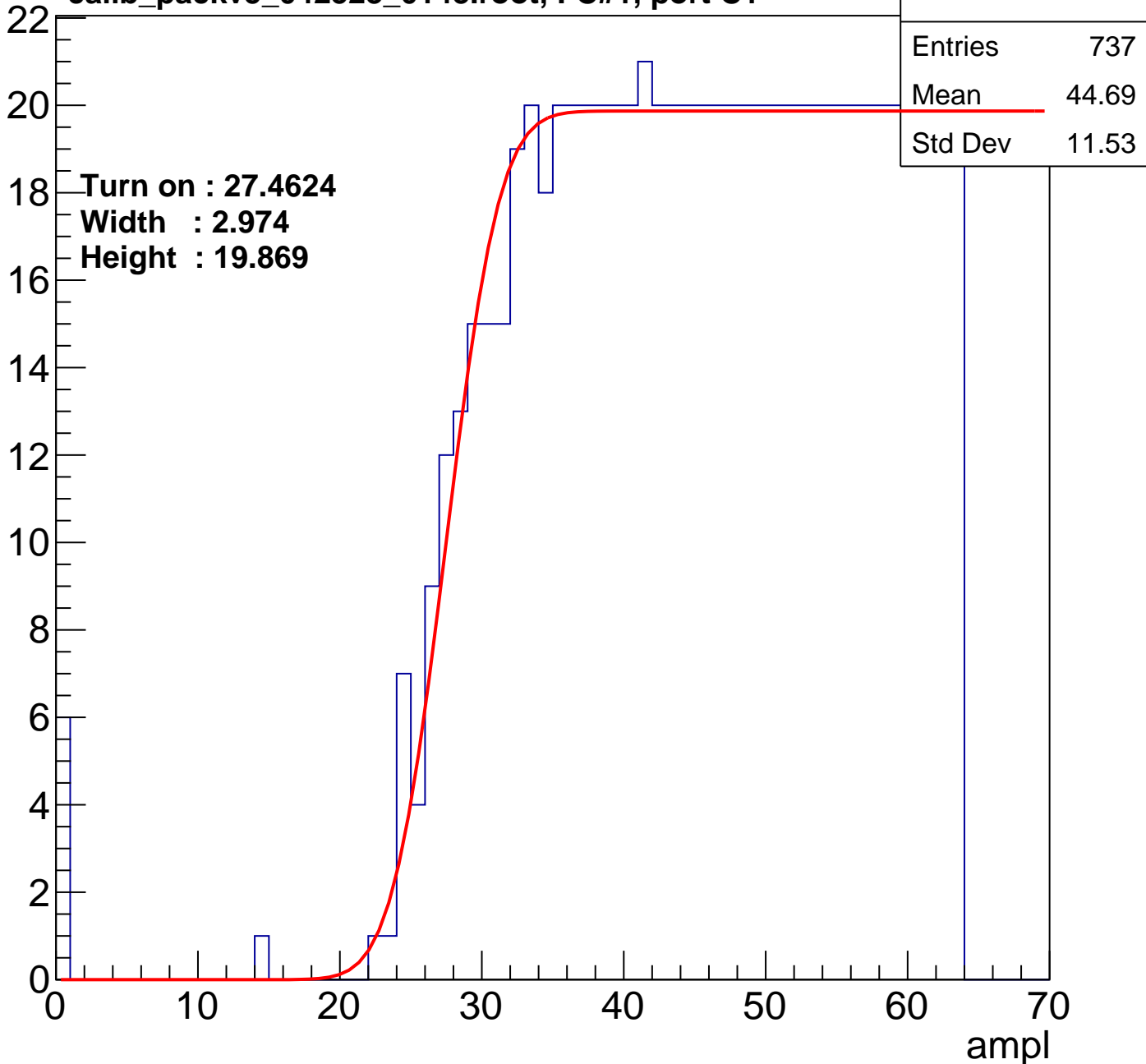
Entry



B0L101S, U14-ch108

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch109

calib_packv5_042523_0143.root, FC#1, port C1

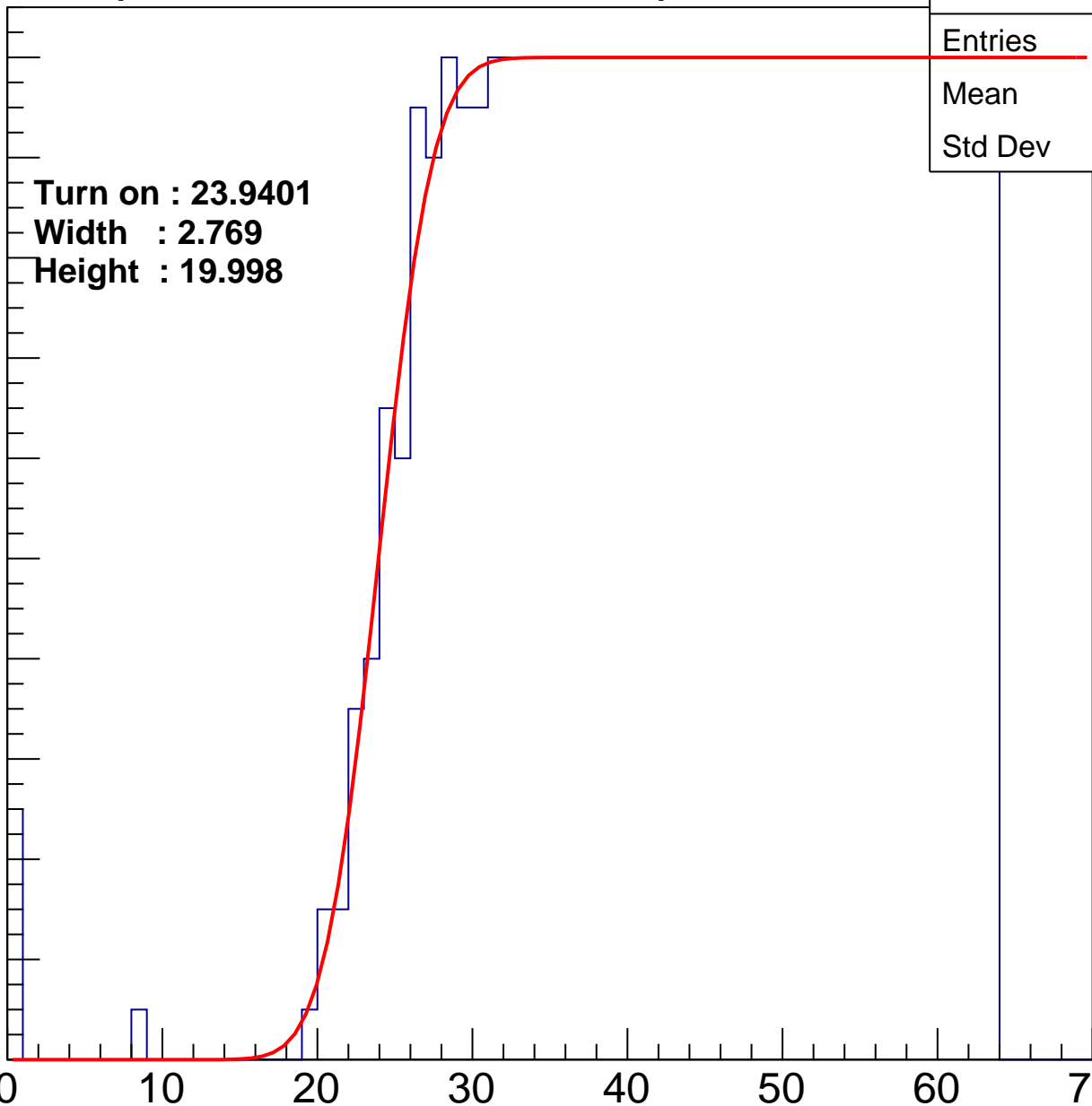
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 23.9401
Width : 2.769
Height : 19.998

Entries	808
Mean	43.05
Std Dev	12.23

ampl



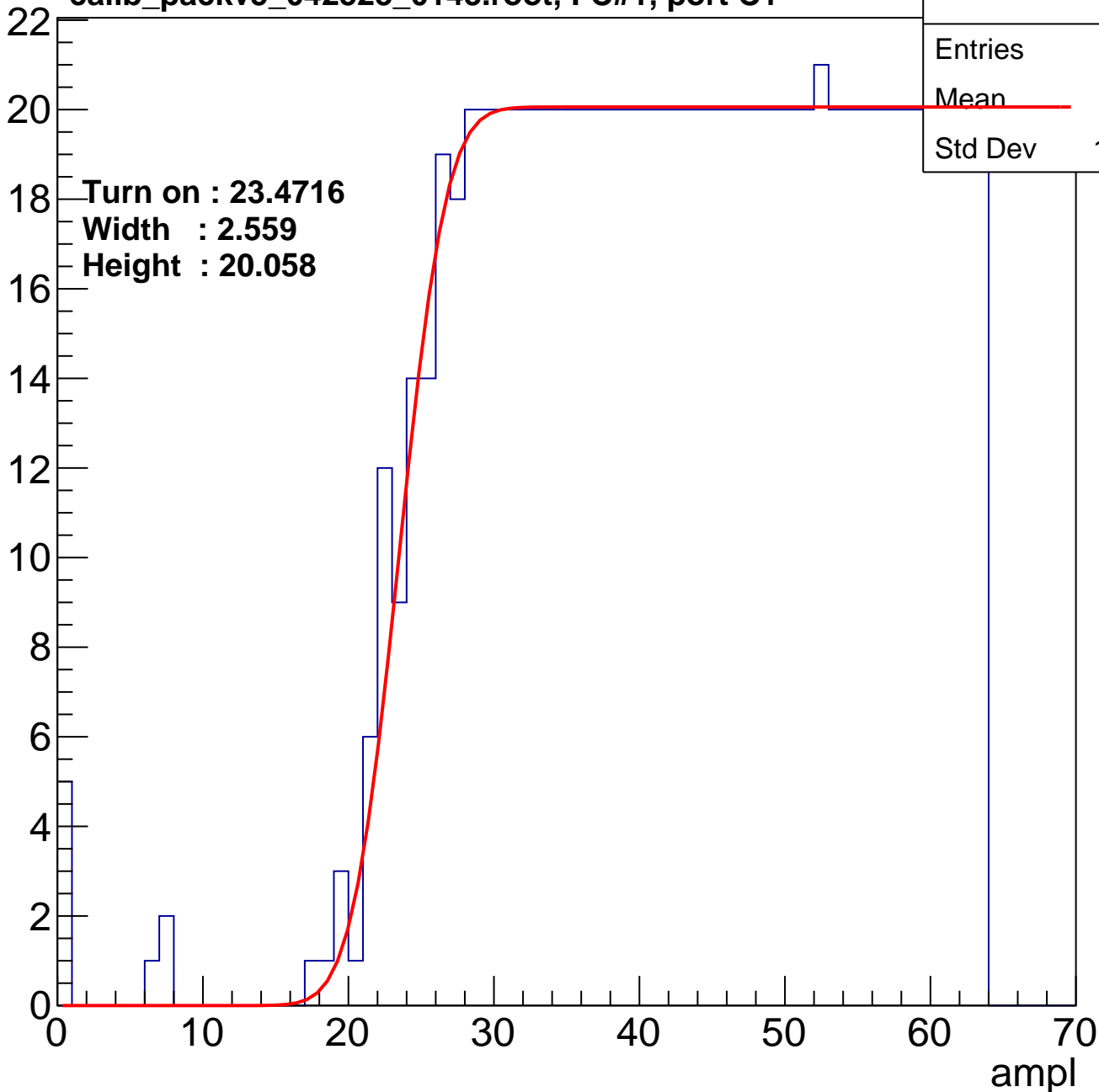
B0L101S, U14-ch110

calib_packv5_042523_0143.root, FC#1, port C1

Entries	828
Mean	42.6
Std Dev	12.57

Turn on : 23.4716
Width : 2.559
Height : 20.058

Entry



B0L101S, U14-ch111

calib_packv5_042523_0143.root, FC#1, port C1

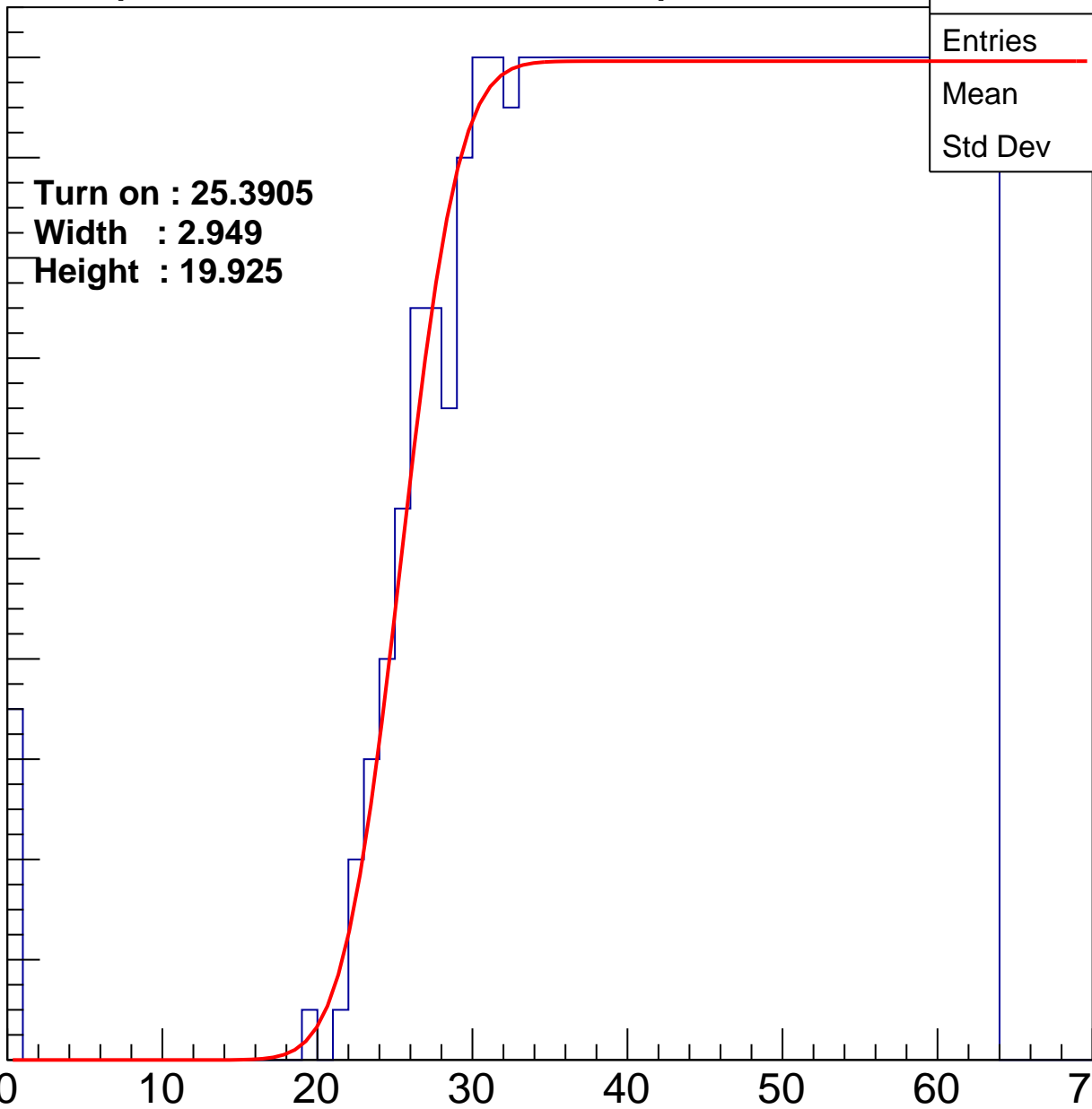
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.3905
Width : 2.949
Height : 19.925

Entries	778
Mean	43.7
Std Dev	12.03

ampl



B0L101S, U14-ch112

calib_packv5_042523_0143.root, FC#1, port C1

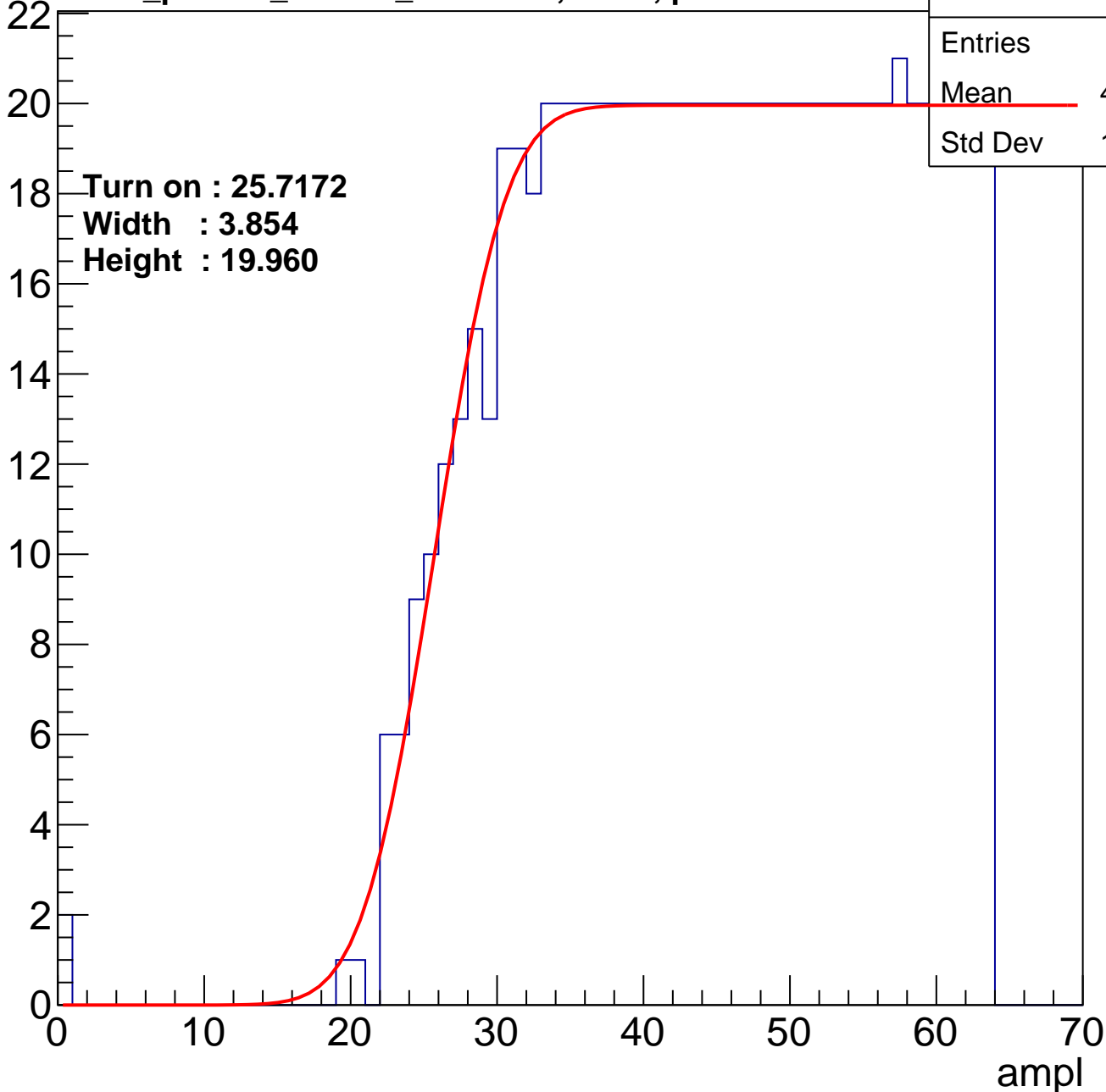
Entries	765
Mean	44.17
Std Dev	11.52

Turn on : 25.7172

Width : 3.854

Height : 19.960

Entry



B0L101S, U14-ch113

calib_packv5_042523_0143.root, FC#1, port C1

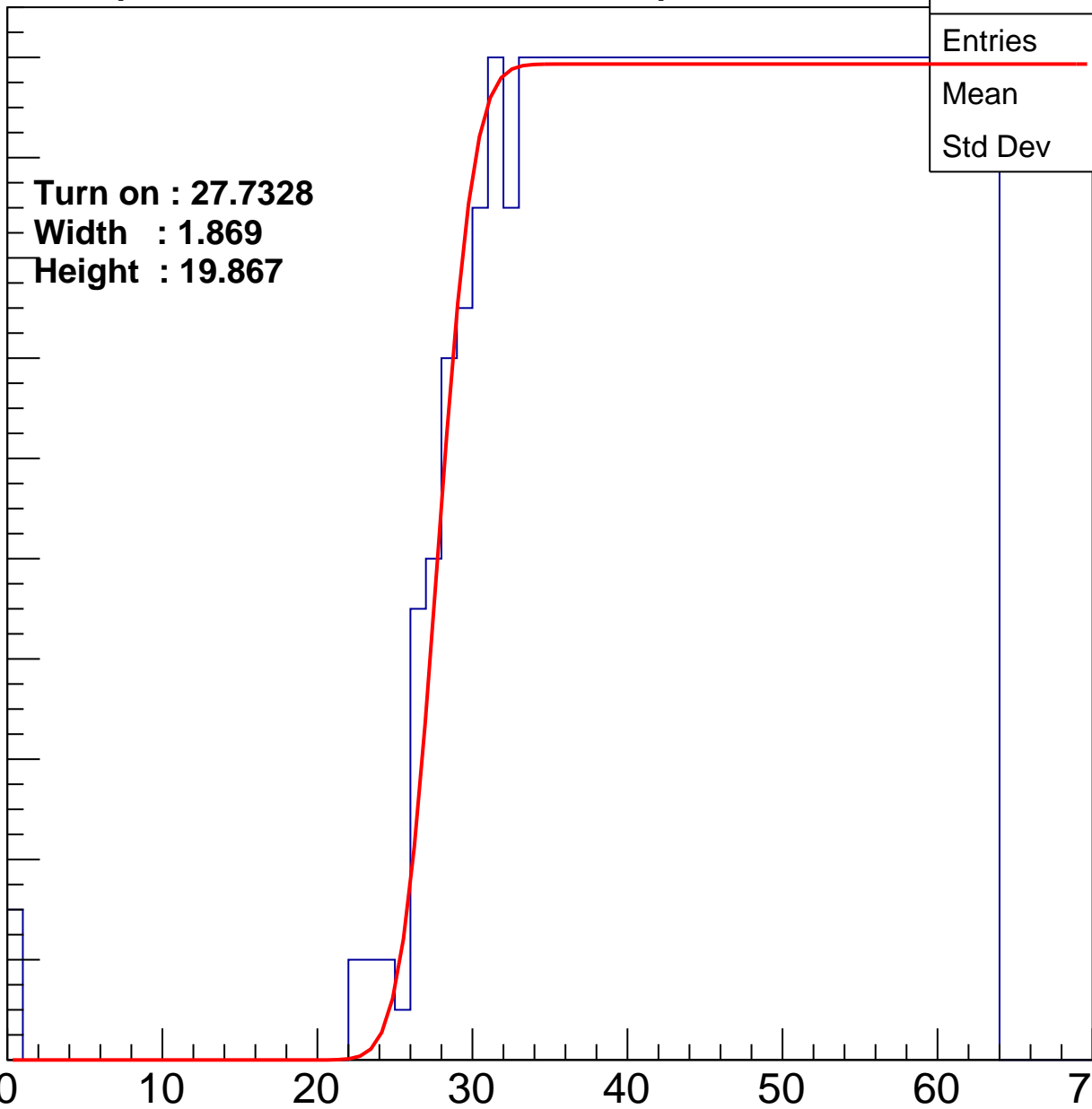
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.7328
Width : 1.869
Height : 19.867

Entries	732
Mean	44.98
Std Dev	11.06

ampl



B0L101S, U14-ch114

calib_packv5_042523_0143.root, FC#1, port C1

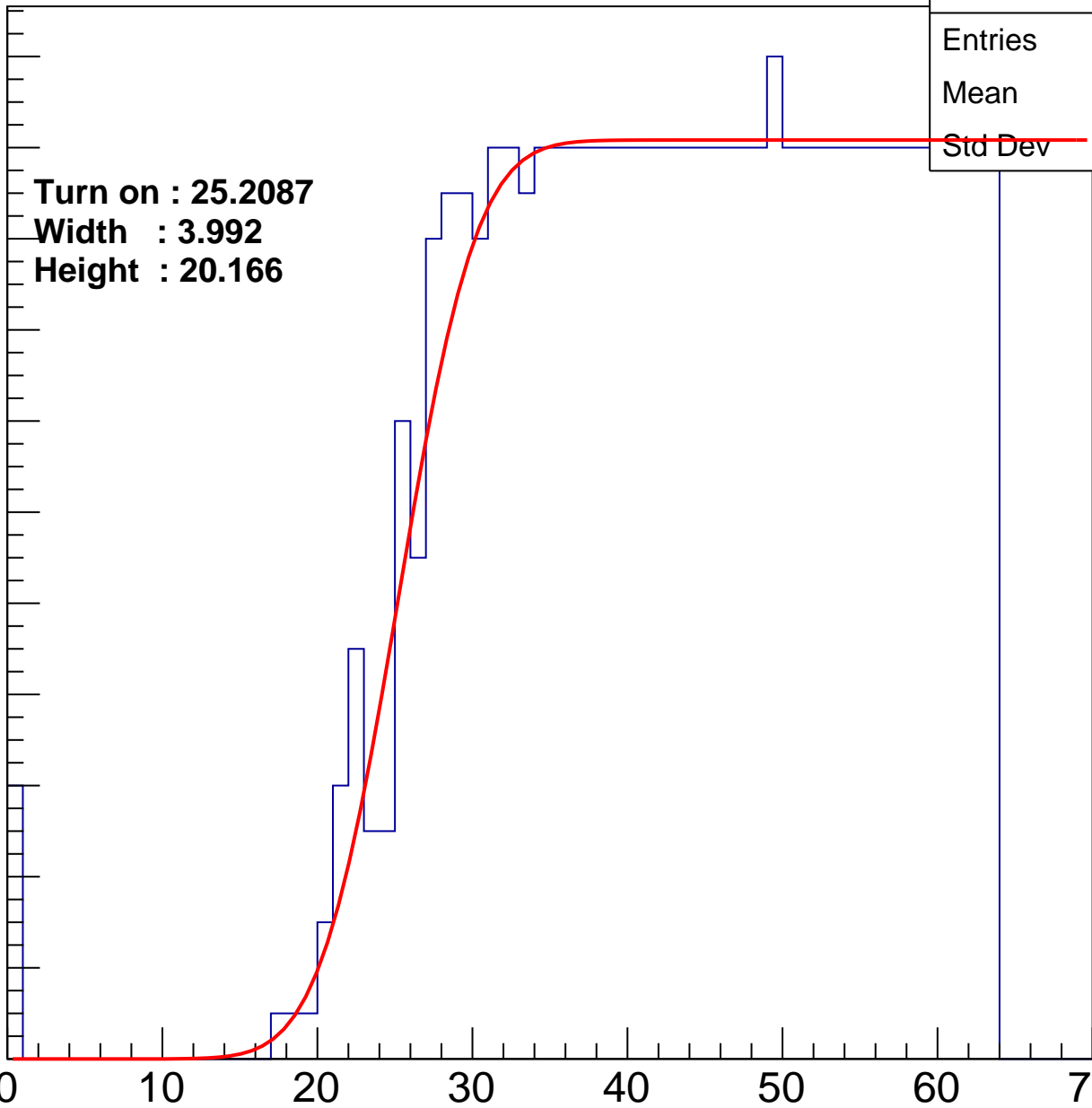
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2087
Width : 3.992
Height : 20.166

Entries	797
Mean	43.29
Std Dev	12.21

ampl



B0L101S, U14-ch115

calib_packv5_042523_0143.root, FC#1, port C1

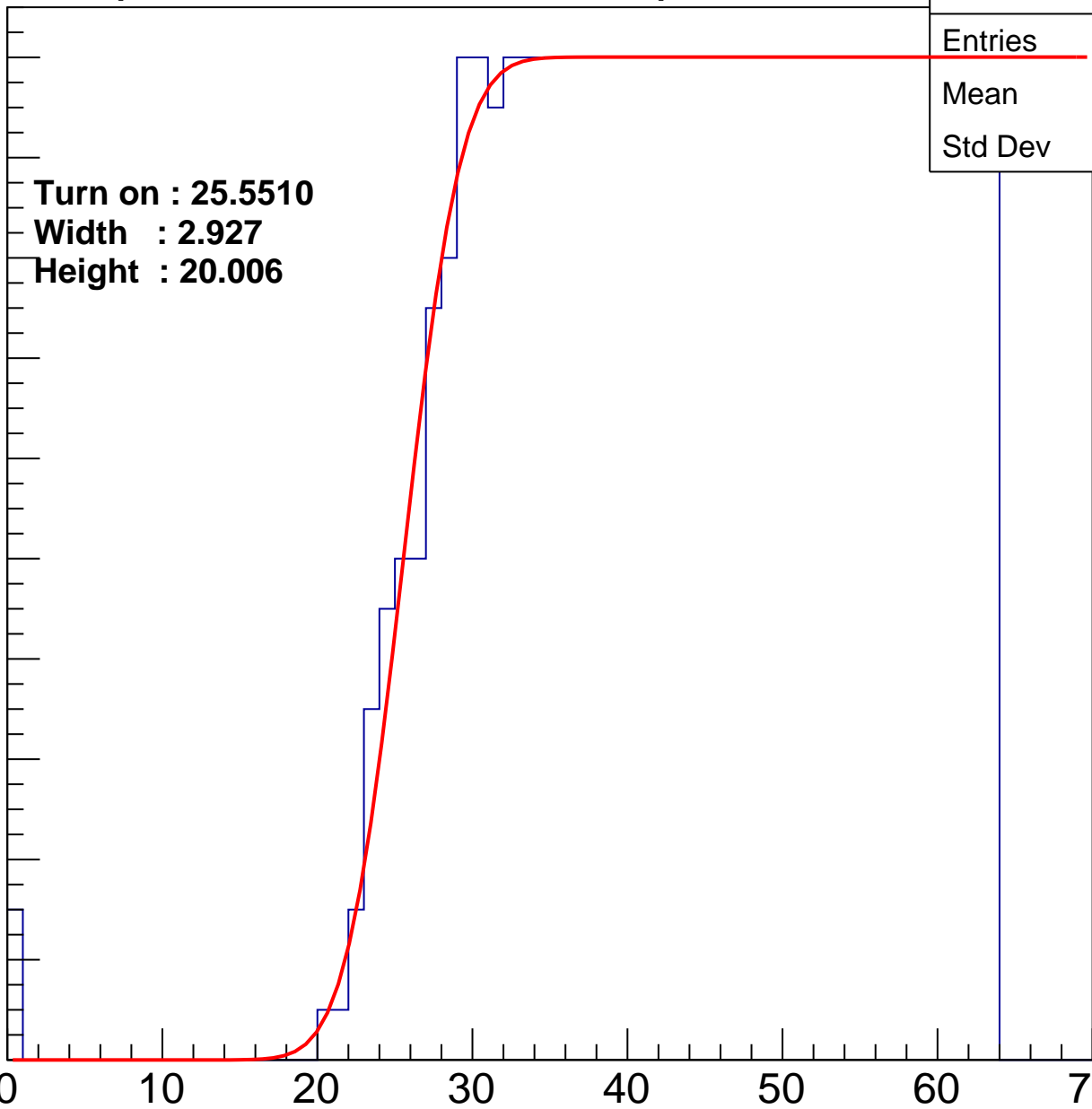
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.5510
Width : 2.927
Height : 20.006

Entries	774
Mean	43.95
Std Dev	11.61

ampl



B0L101S, U14-ch116

calib_packv5_042523_0143.root, FC#1, port C1

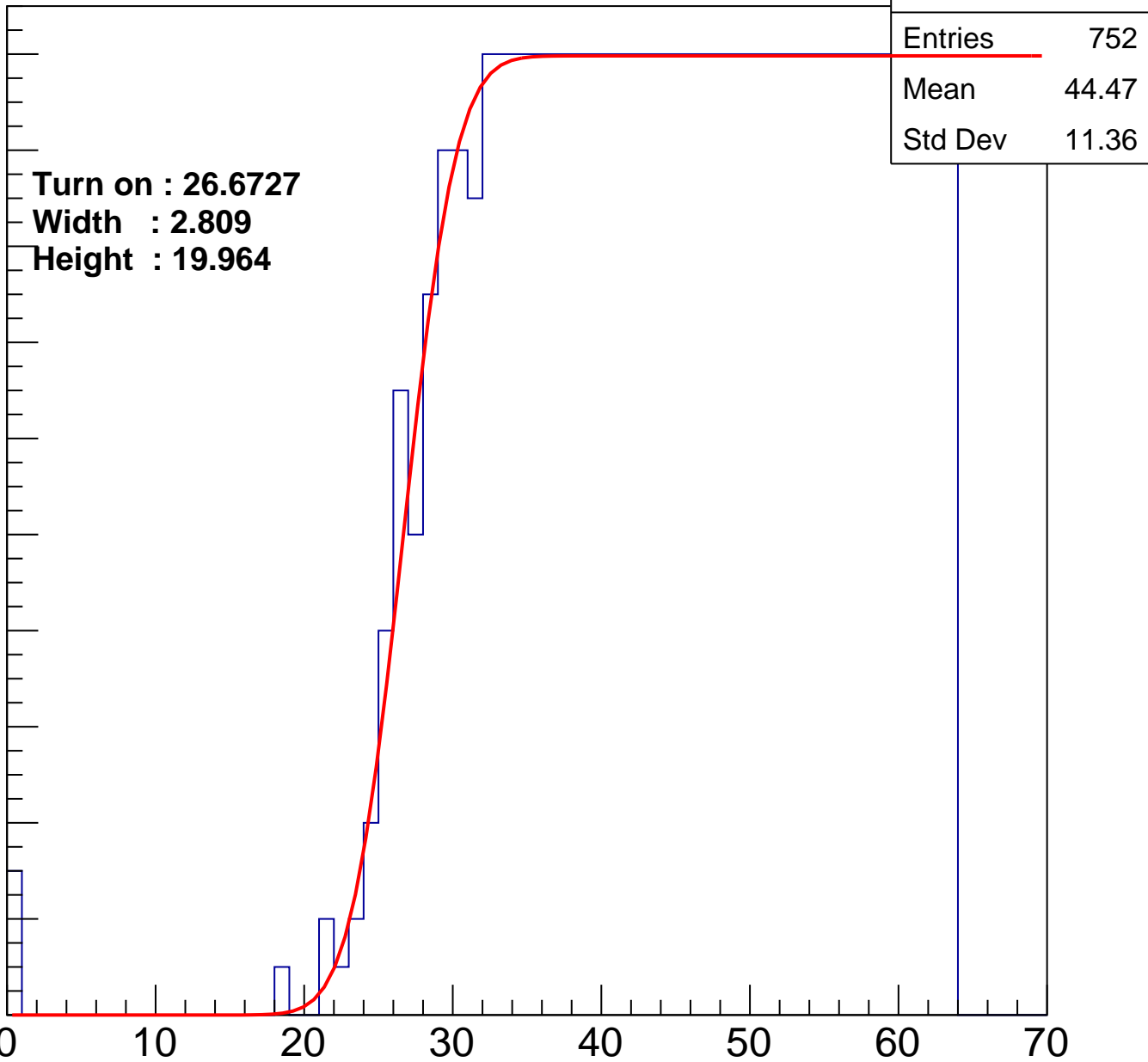
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.6727
Width : 2.809
Height : 19.964

Entries	752
Mean	44.47
Std Dev	11.36

ampl



B0L101S, U14-ch117

calib_packv5_042523_0143.root, FC#1, port C1

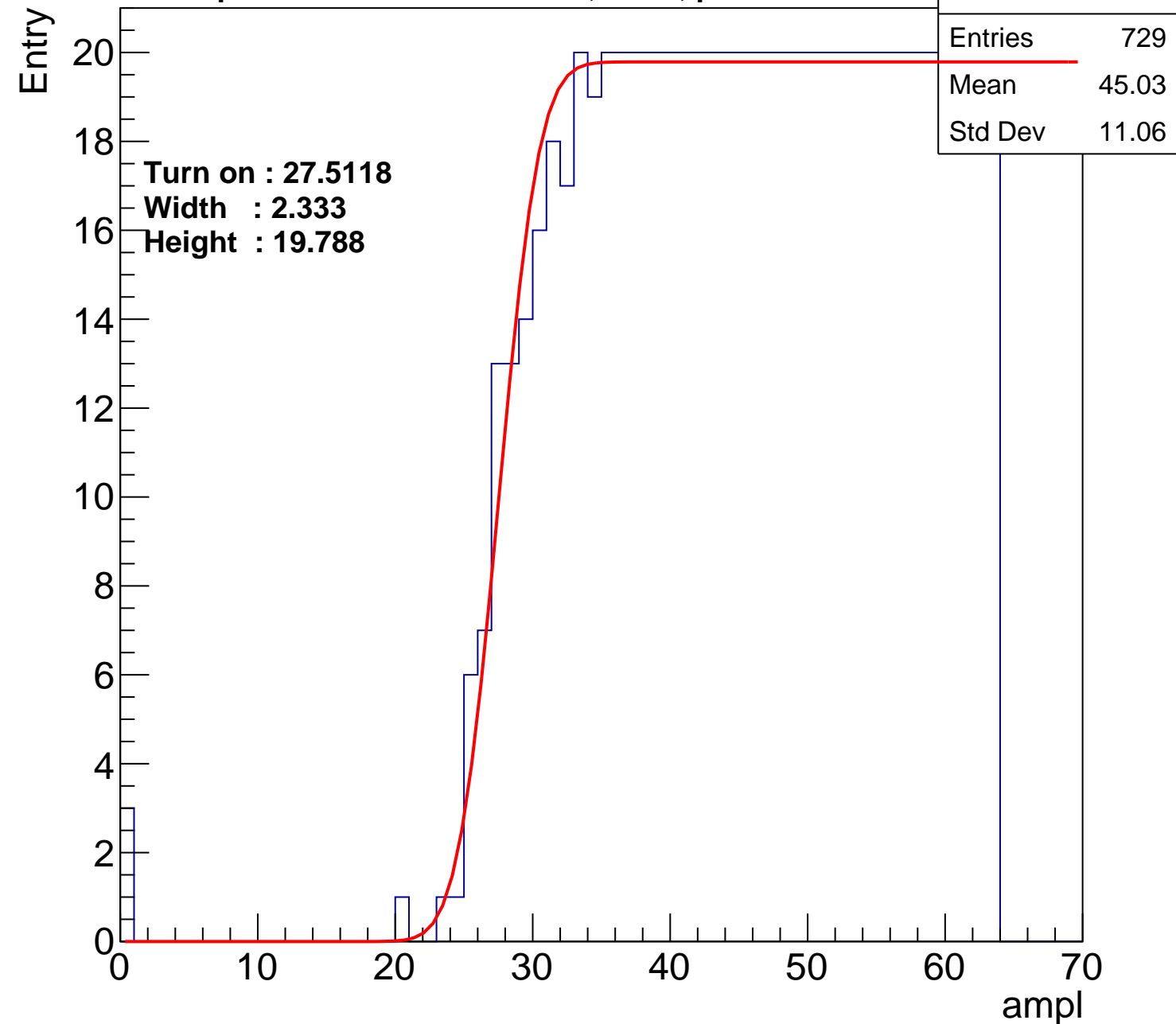
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 27.5118
Width : 2.333
Height : 19.788

Entries	729
Mean	45.03
Std Dev	11.06

ampl



B0L101S, U14-ch118

calib_packv5_042523_0143.root, FC#1, port C1

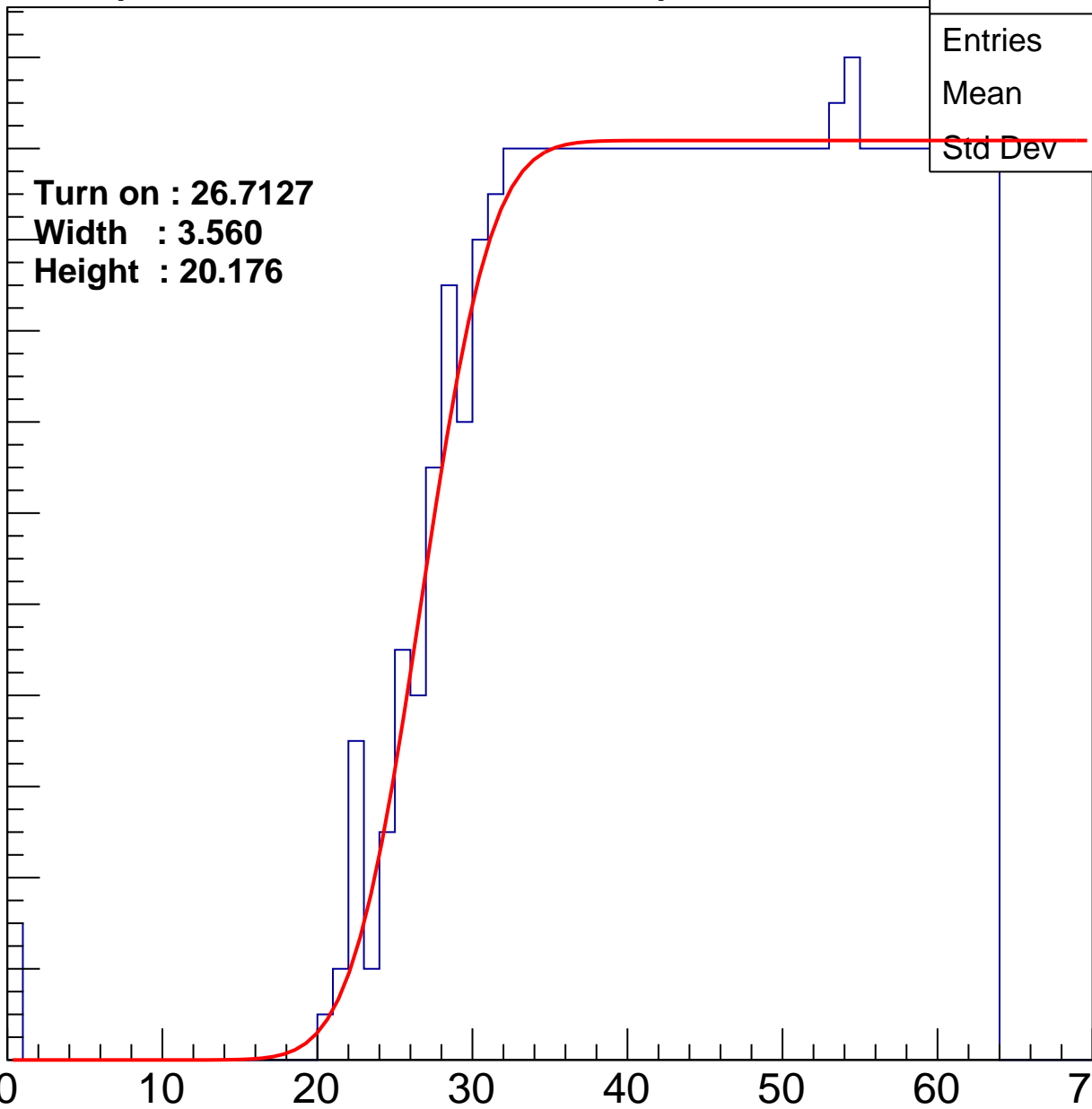
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.7127
Width : 3.560
Height : 20.176

Entries	761
Mean	44.33
Std Dev	11.47

ampl



B0L101S, U14-ch119

calib_packv5_042523_0143.root, FC#1, port C1

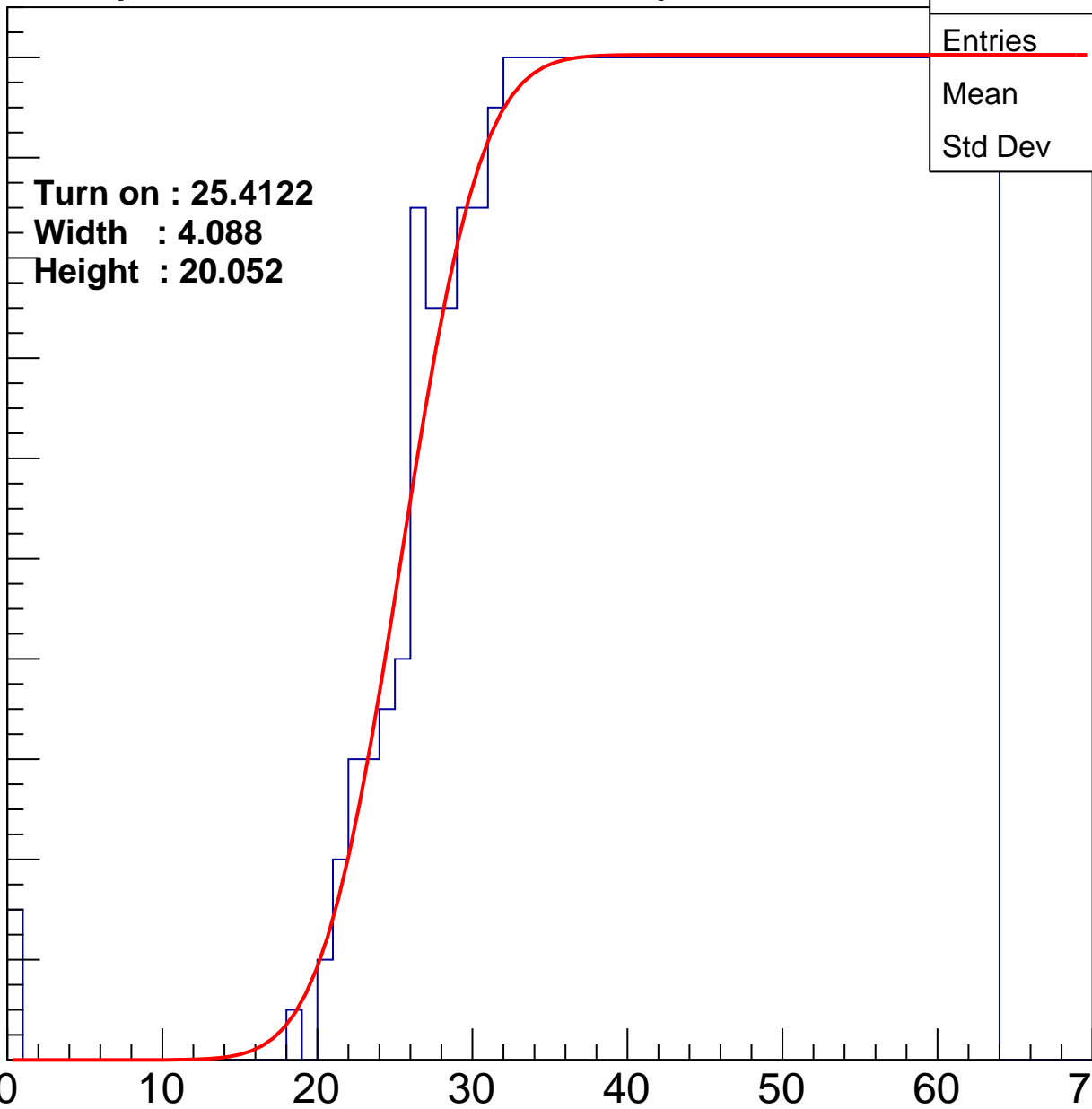
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.4122
Width : 4.088
Height : 20.052

Entries	777
Mean	43.81
Std Dev	11.76

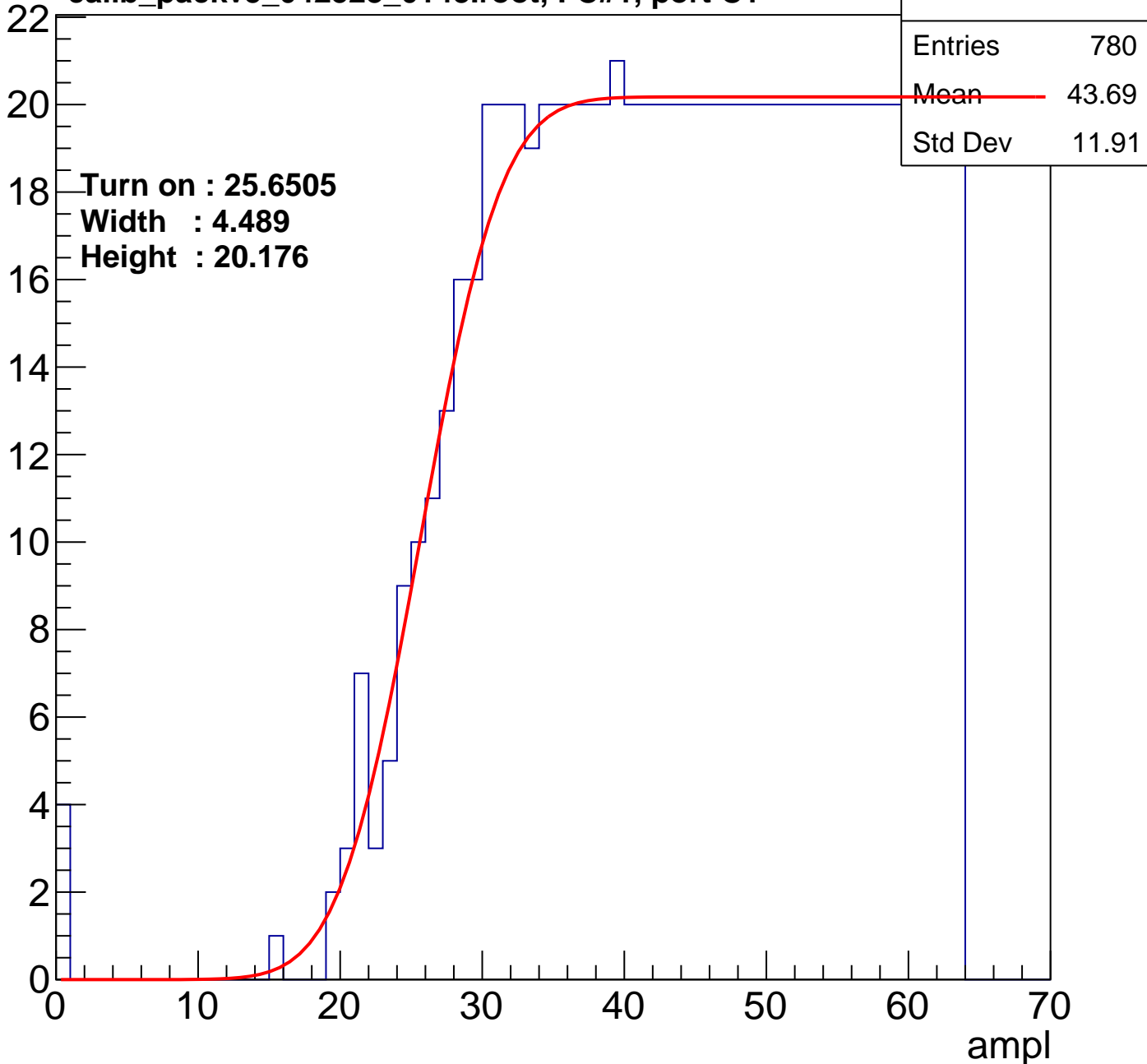
ampl



B0L101S, U14-ch120

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch121

calib_packv5_042523_0143.root, FC#1, port C1

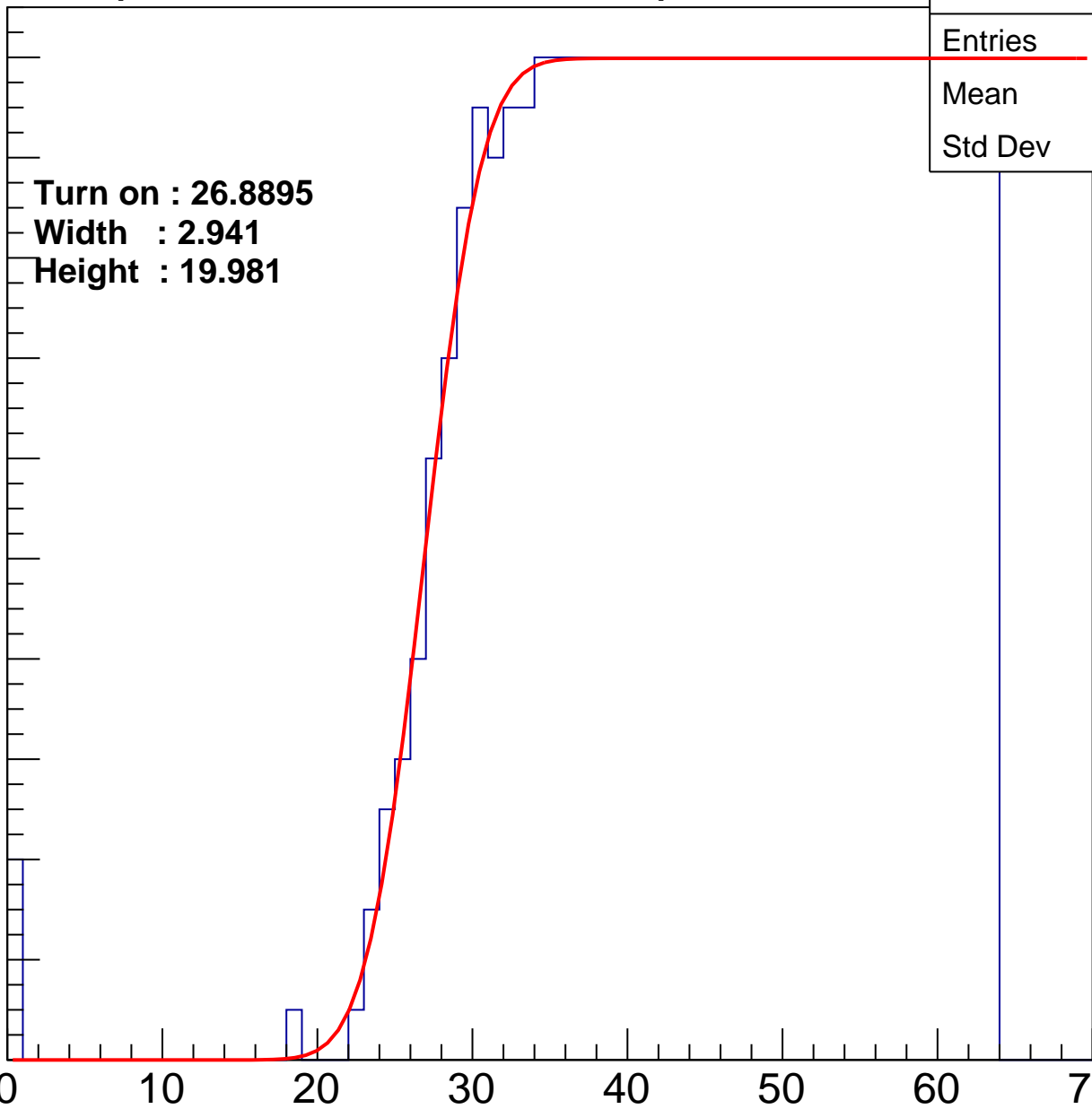
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 26.8895
Width : 2.941
Height : 19.981

Entries	746
Mean	44.58
Std Dev	11.37

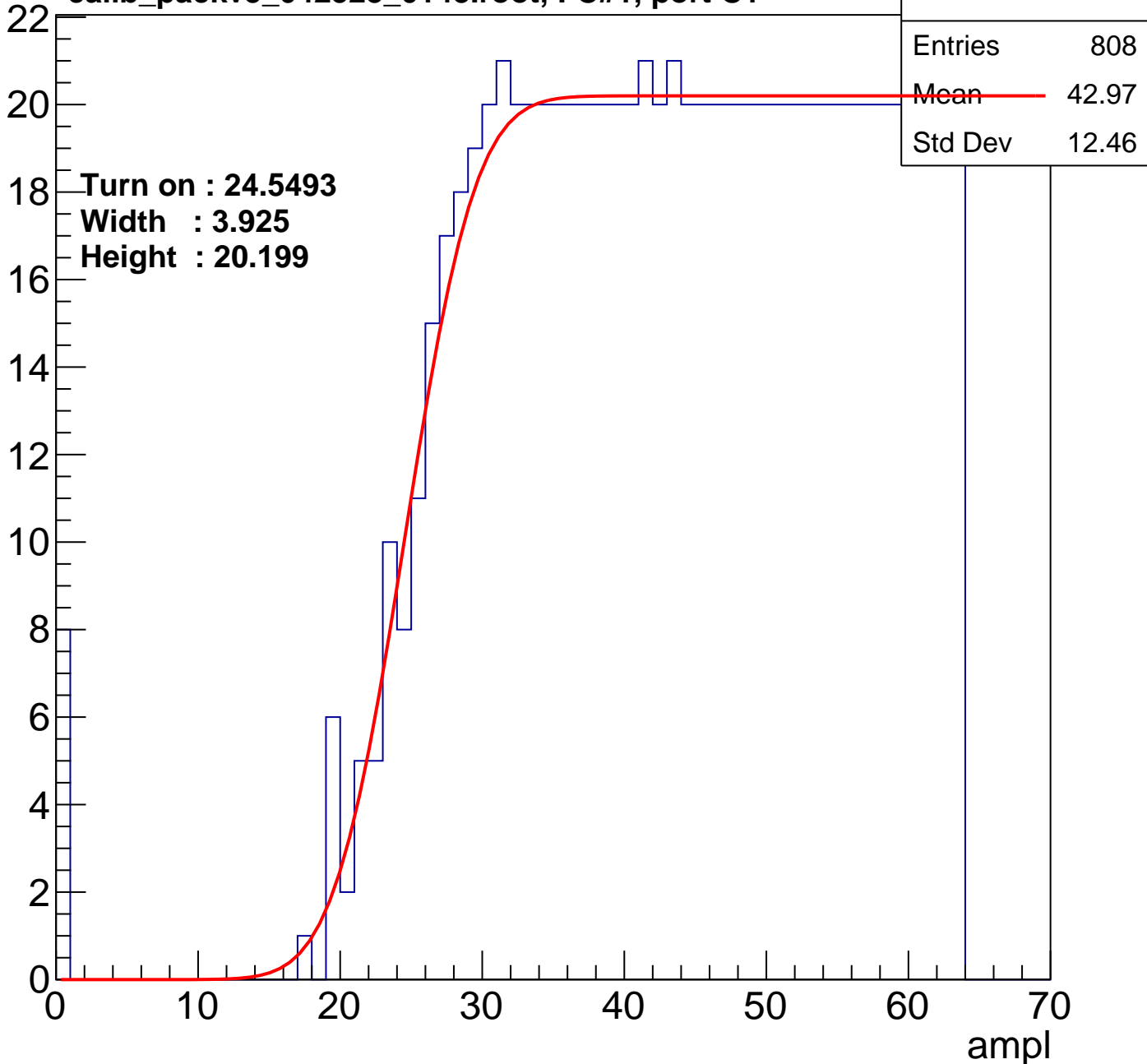
ampl



B0L101S, U14-ch122

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch123

calib_packv5_042523_0143.root, FC#1, port C1

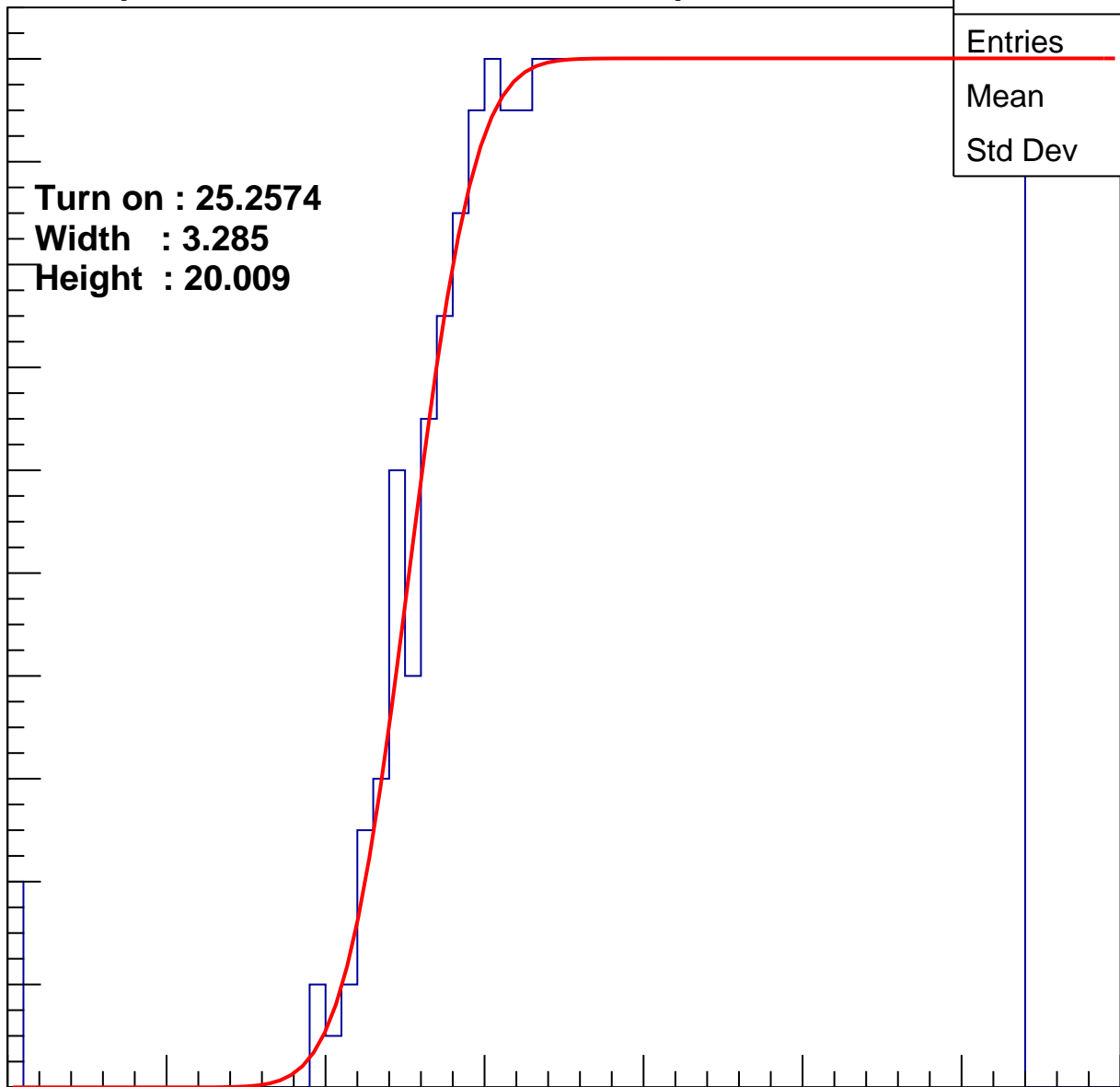
Entry

20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2574
Width : 3.285
Height : 20.009

Entries	782
Mean	43.69
Std Dev	11.85

ampl

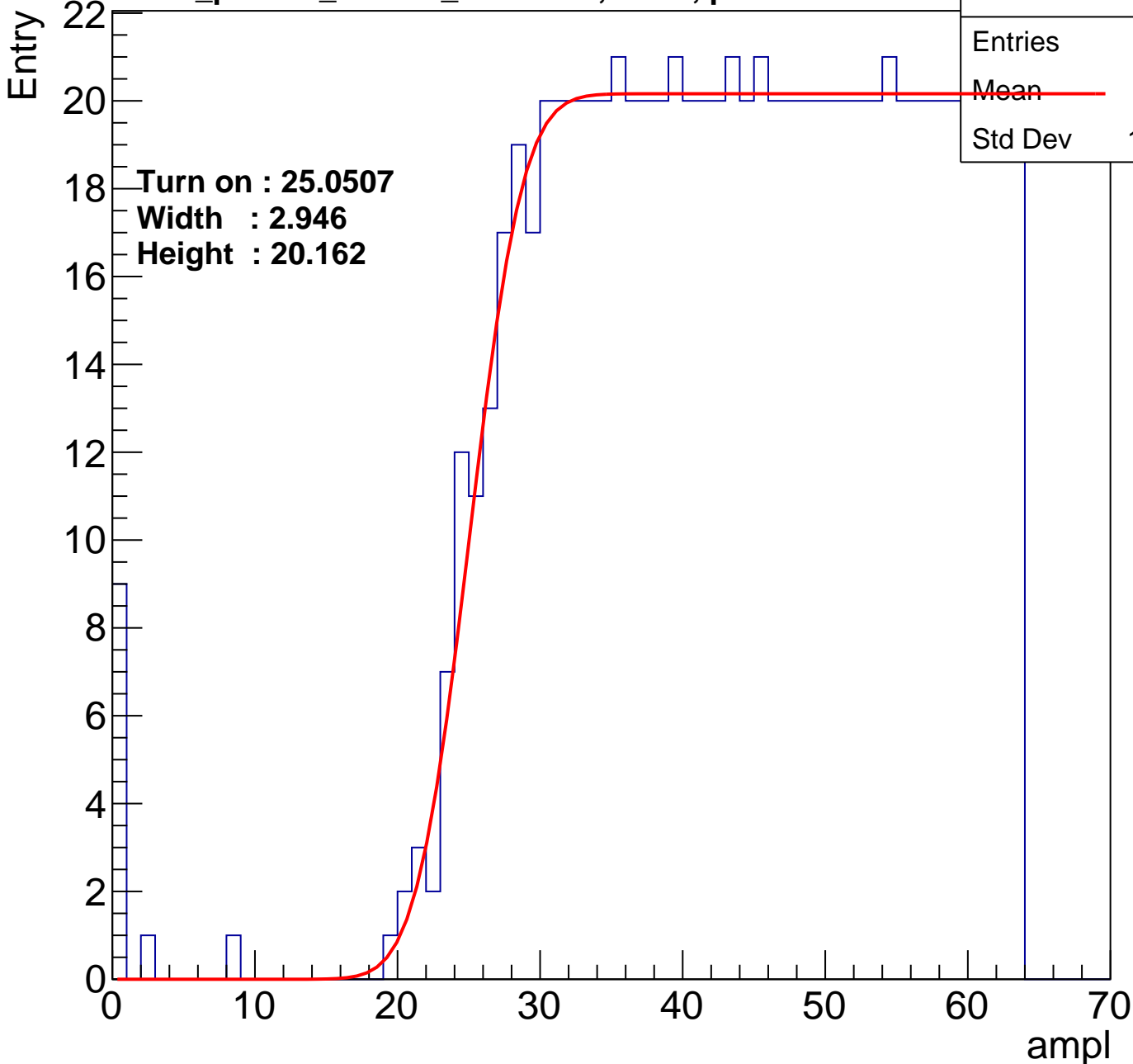


B0L101S, U14-ch124

calib_packv5_042523_0143.root, FC#1, port C1

Entries	800
Mean	43.2
Std Dev	12.44

Turn on : 25.0507
Width : 2.946
Height : 20.162



B0L101S, U14-ch125

calib_packv5_042523_0143.root, FC#1, port C1

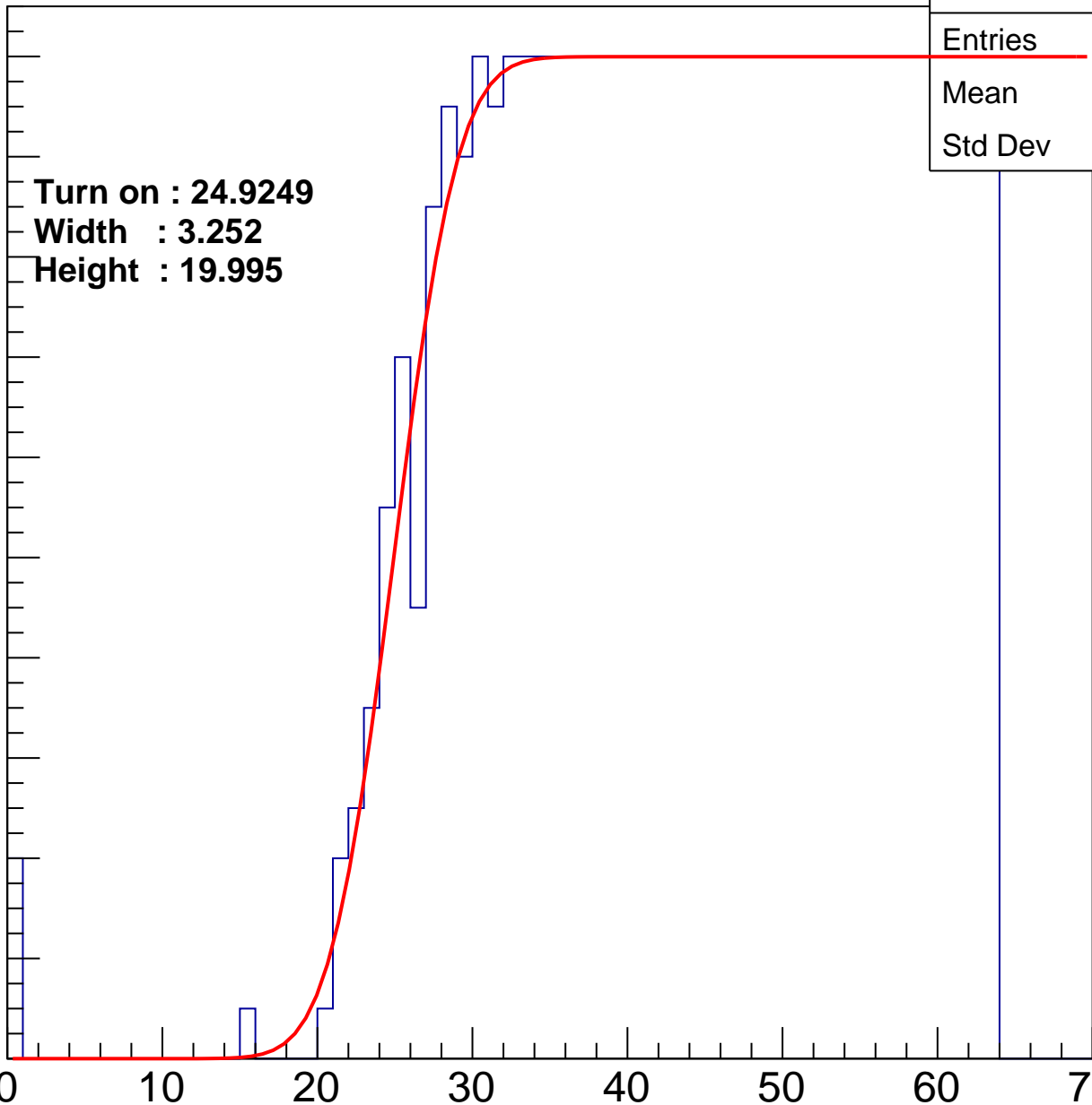
Entries	789
Mean	43.52
Std Dev	11.93

Turn on : 24.9249
Width : 3.252
Height : 19.995

Entry

20
18
16
14
12
10
8
6
4
2
0

ampl



B0L101S, U14-ch126

calib_packv5_042523_0143.root, FC#1, port C1

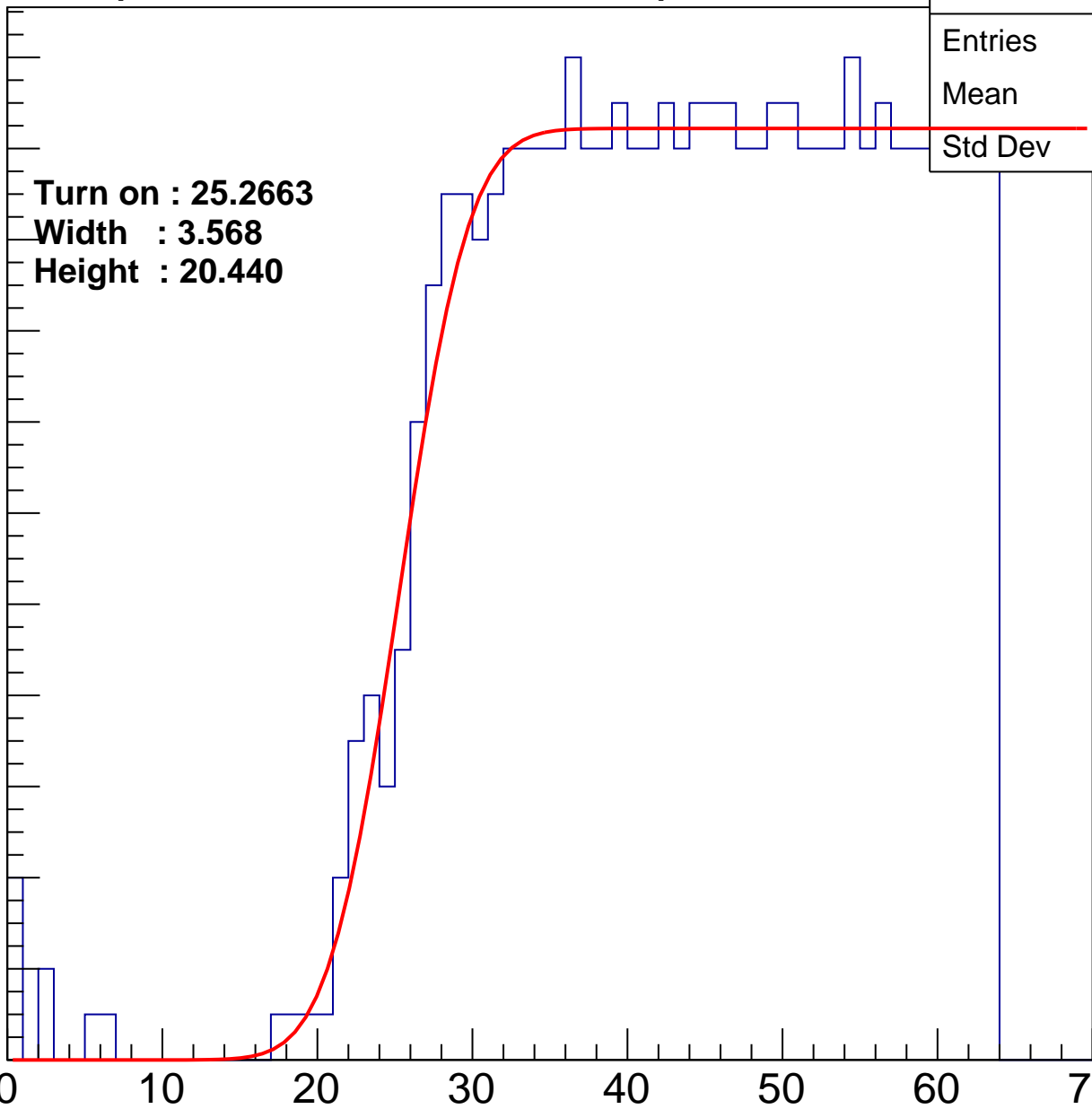
Entry

22
20
18
16
14
12
10
8
6
4
2
0

Turn on : 25.2663
Width : 3.568
Height : 20.440

Entries	805
Mean	43.39
Std Dev	12.2

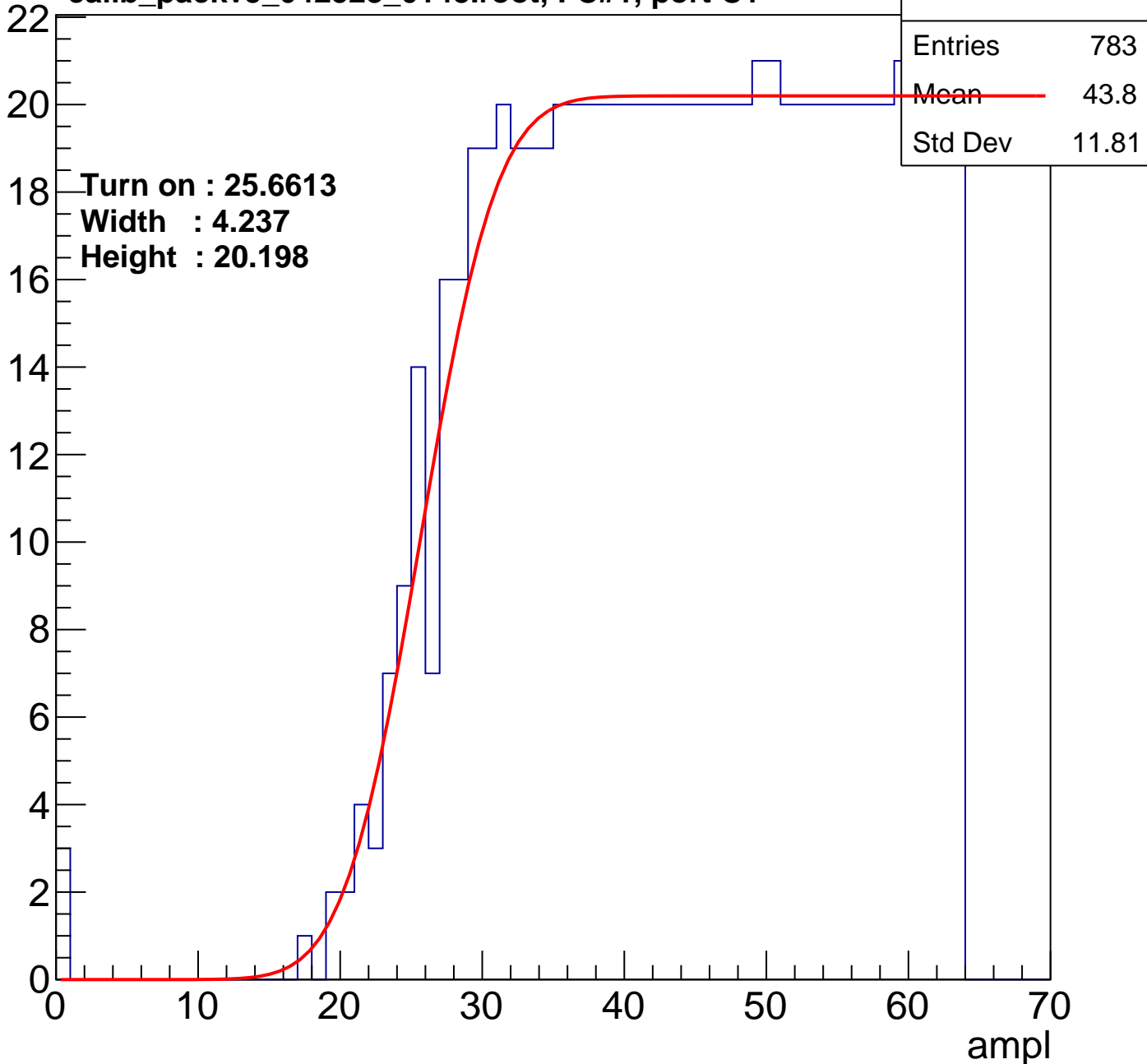
ampl



B0L101S, U14-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry



B0L101S, U14-ch127

calib_packv5_042523_0143.root, FC#1, port C1

Entry

